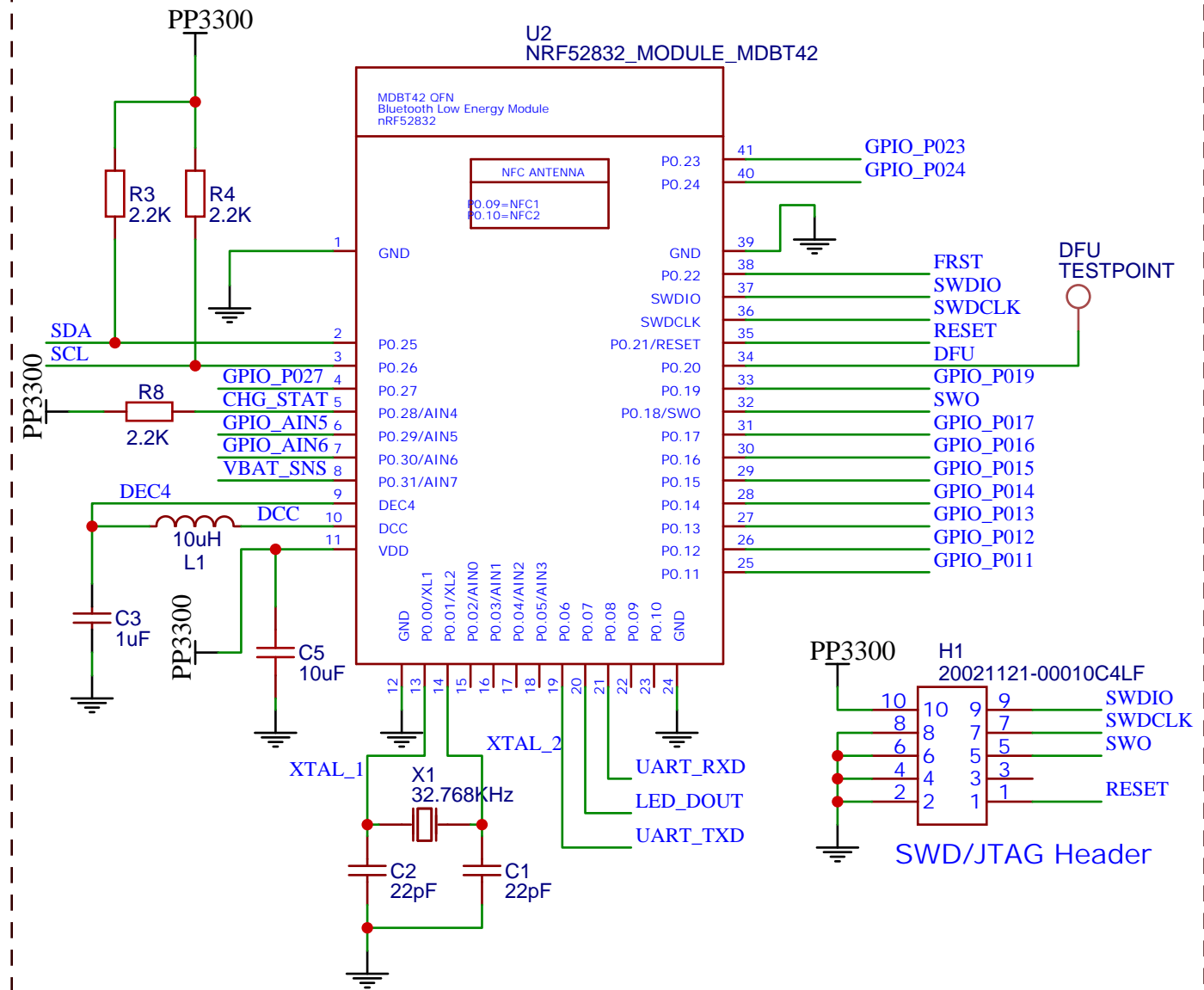
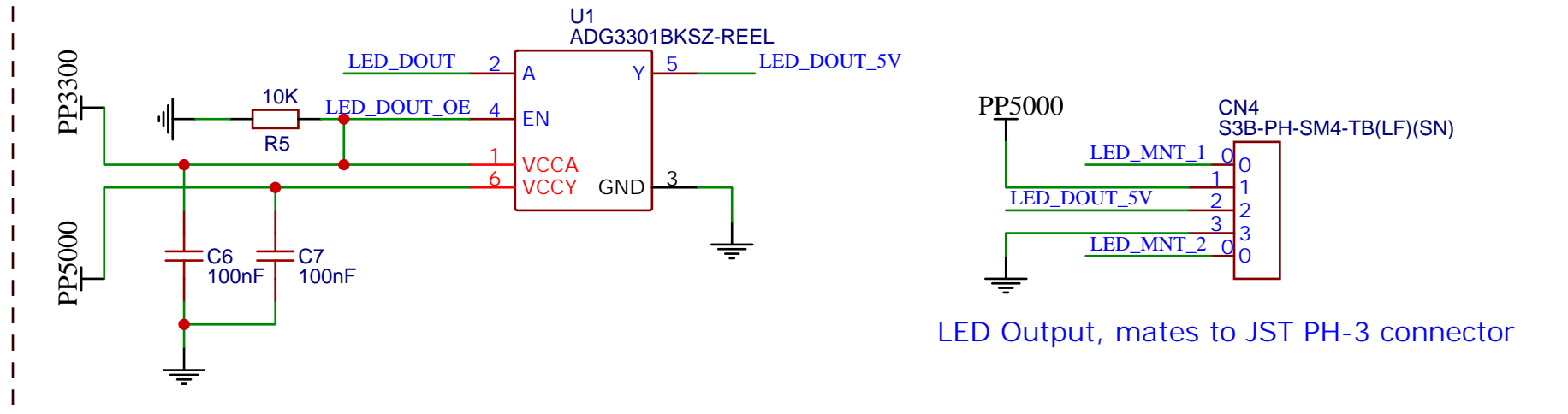


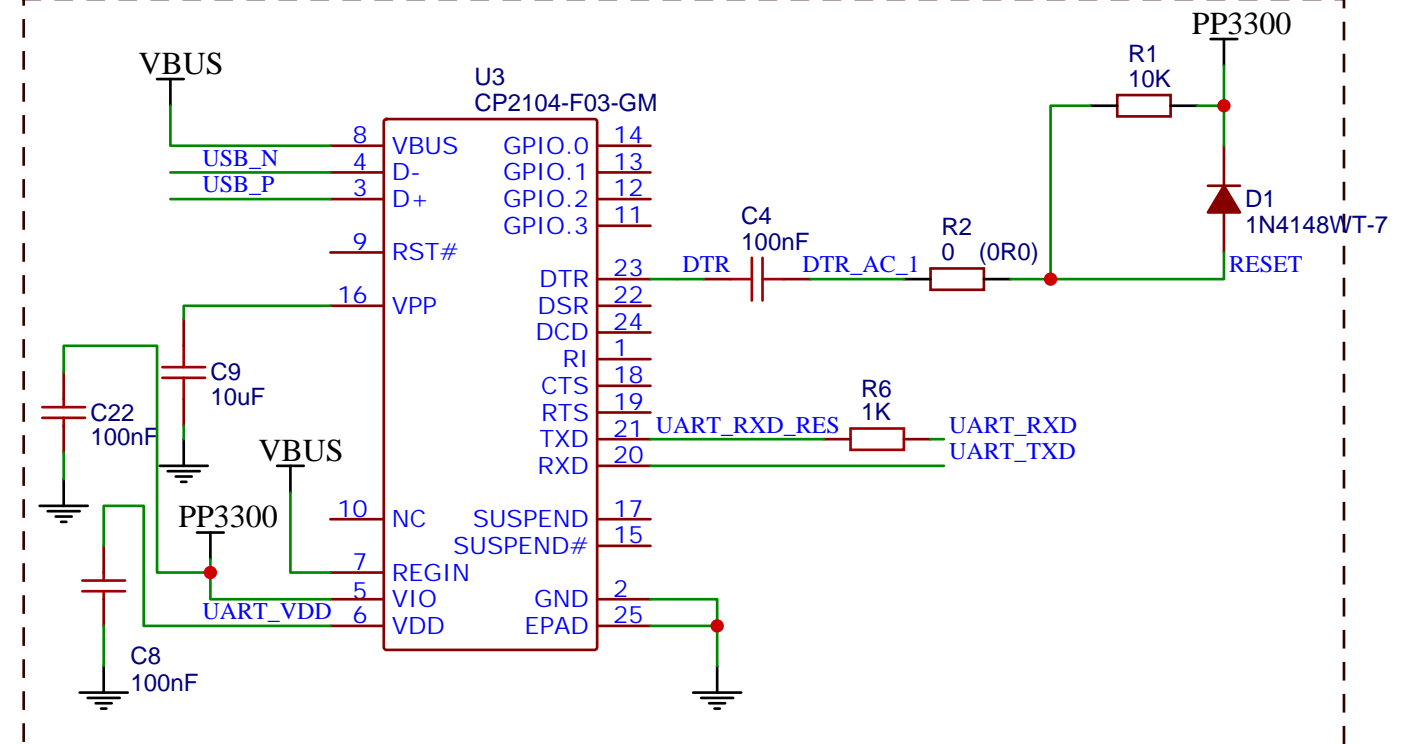
MCU & Debug



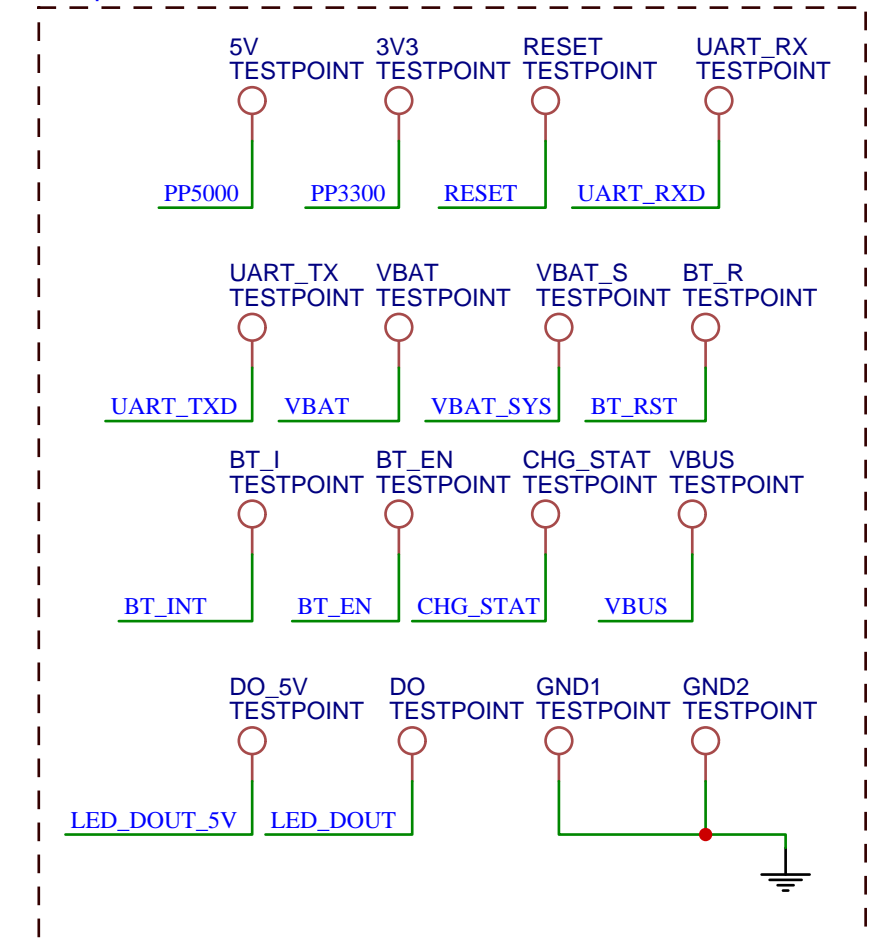
LED Conditioning



FTDI USB/UART



Testpoints



TITLE:

MCU & Logic

REV: 1.0

Date: 2018-12-09

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EasyEDA V5.8.22

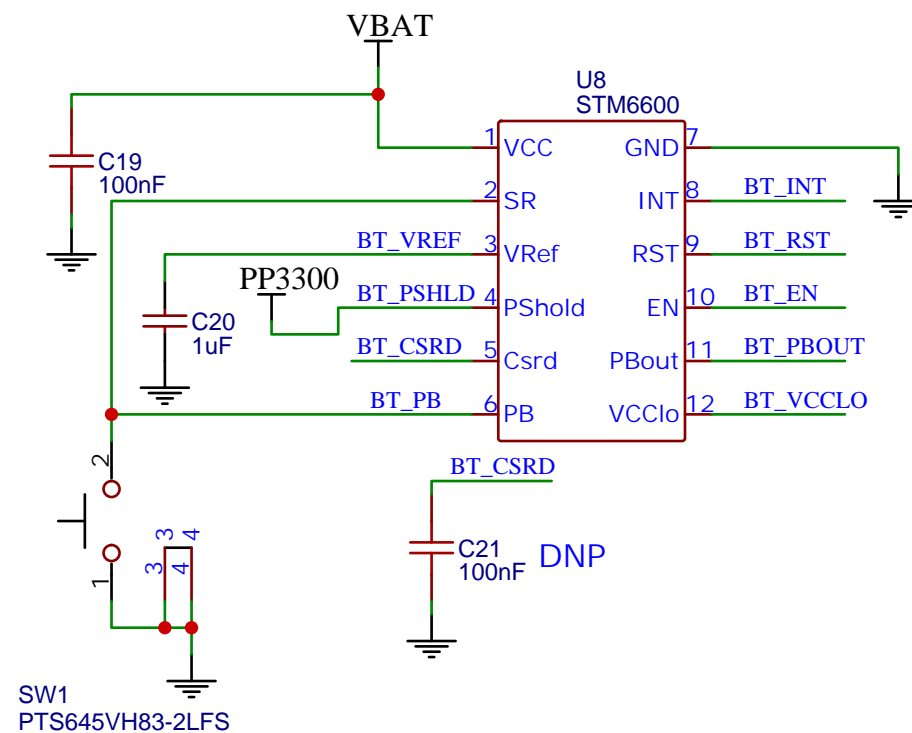
Drawn By: sam.zeckendorf

The schematic diagram shows the USB and VBAT sections of the circuit. The USB section includes a USB1 10118193-0001LF chip connected to VBUS, USB_N, USB_P, and ID pins. The VBAT section includes a JST_2PIN-SMT connector (CN1) connected to VBAT and VBAT_SYS, with a 100nF capacitor (C25) and resistors R18 (80.6K) and R19 (2M) connected to VBAT_SYS.

The schematic diagram shows the power supply and status signal circuit for the BT module. It includes the following components and connections:

- Power Supply:**
 - VBAT:** Connected to the VBAT pin (pin 3) of the MCP73831T-2AT1/OT (U9).
 - VBAT_SYS:** Connected to the VDD pin (pin 4) of the MCP73831T-2AT1/OT (U9).
 - Grounding:** The VBAT pin is also connected to ground through a 10uF capacitor (C10).
 - VBUS:** Connected to the VDD pin (pin 4) of the MCP73831T-2AT1/OT (U9).
 - Grounding:** The VBUS pin is also connected to ground through a 4.7uF capacitor (C24).
- Status Signal:**
 - CHG_STAT:** Connected to the STAT pin (pin 1) of the MCP73831T-2AT1/OT (U9).
 - Grounding:** The STAT pin is also connected to ground through a 10uF capacitor (C10).
- Other Components:**
 - R7:** A 2K resistor connected between VBUS and ground.
 - C10:** A 10uF capacitor connected between VBAT and ground.
 - C24:** A 4.7uF capacitor connected between VBUS and ground.

The diagram shows two charging paths. The first path, for VBAT, consists of a diode (FC-1608YOK-600H08) connected to VBAT_SYS, followed by a resistor (R16, 200R) connected to ground. The second path, for VBUS, consists of a diode (FC-1608YOK-600H08) connected to VBUS, followed by a resistor (R17, 402) connected to CHG_STAT.



5.0V Boost Regulator

TITLE: Power & IO		REV: 1.0
Date: 2018-12-10		Sheet: 1/1
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