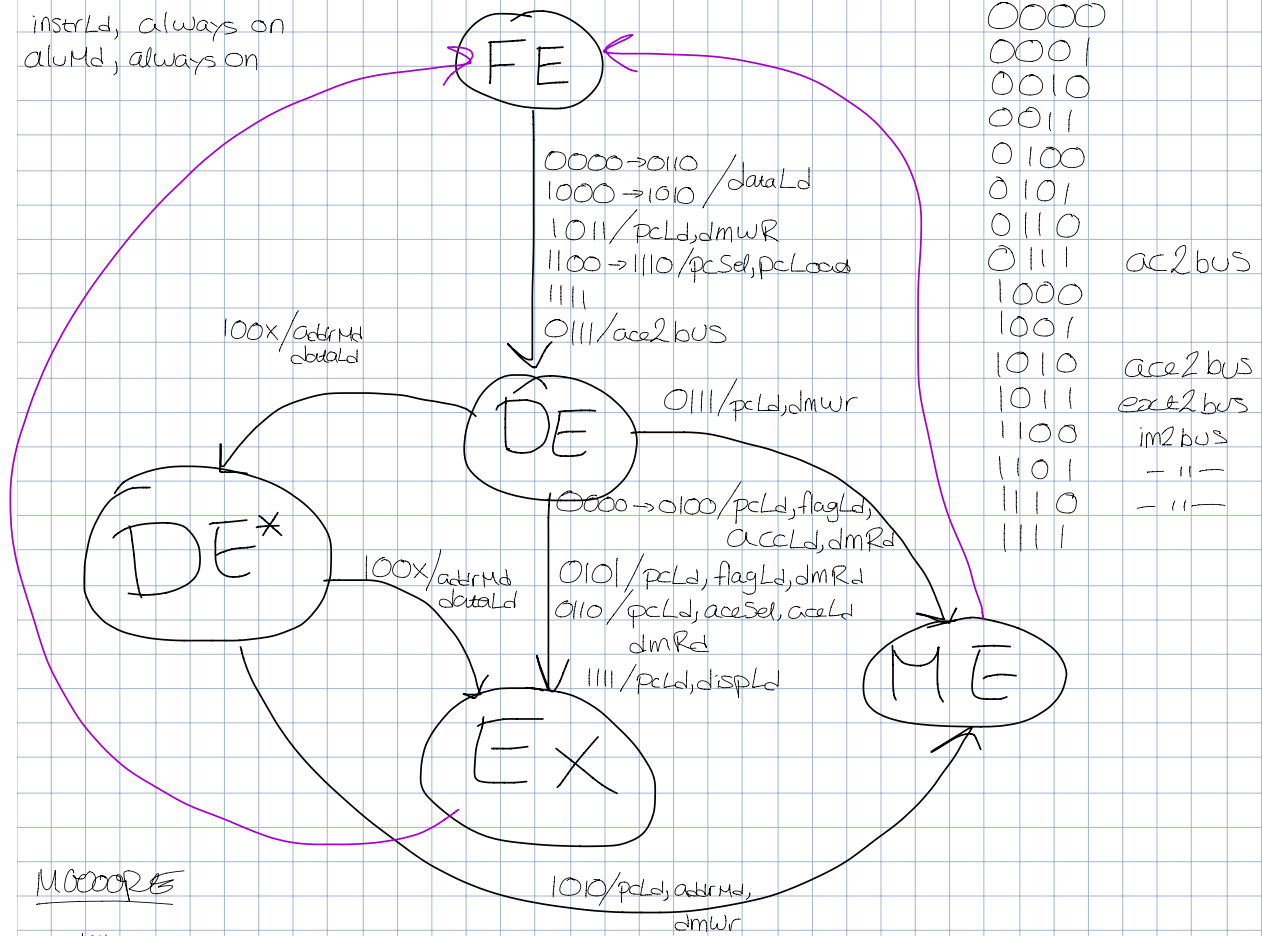
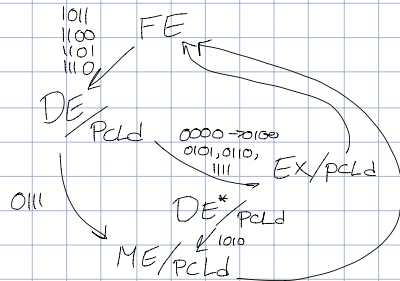


instrLd, always on
alUmd, always on



0000
0001
0010
0011
0100
0101
0110
0111 ace2 bus
1000
1001
1010 ace2 bus
1011 exe2 bus
1100 im2 bus
1101 - - -
1110 - - -
1111

Moore



Mealy

