

ERIC ZHOU

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Cadence • SPICE • Verilog • Java • Go • C • Python • Bash • Assembly

EDUCATION

August 2015 - May 2019 (Expected Graduation)

University of California, Berkeley

B.S. Electrical Engineering and Computer Sciences

GPA 3.89

Awards: Dean's List • Eta Kappa Nu

Relevant Courses: **EE140** (Linear Integrated Circuits) • **EECS151*** (Digital Design and Integrated Circuits) • **EE16AB** (Information Devices and Systems) • **EE105** (Microelectronic Devices and Circuits) • **CS61C** (Machine Structures) • **CS161*** (Computer Security) • **CS61B** (Data Structures) • **CS70** (Discrete Mathematics) • **CS186** (Databases) • **CS170*** (Efficient Algorithms and Intractable Problems)

* Currently enrolled

EXPERIENCE

Summer 2017

Software Development Engineer Intern at Amazon

I developed a web UI for Amazon Fresh internal usage that allows for safe and quick updates to merchant schedules. One of the impacts of this tool is that it increases the speed at which Fresh can launch in new regions. The application uses a Scala backend with an AngularJS frontend.

Summer 2016

Software Intern at [Rently](#)

I created support for controlling Rently Keyless smart home devices (mainly locks) on the Amazon Echo. I ended up created an Amazon Alexa Skill that forwards raw English text to a custom natural language parser, which processes the command and makes HTTP calls to Rently's servers, all done in Node.js.

PROJECTS

Course Projects

Mixed Signal Chip

The analog parts of a mixed-signal chip in 90nm process for embedded IOT applications, including an 8-bit successive-approximation analog to digital converter, a programmable gain amplifier, bandgap voltage reference and temperature sensor, and an analog multiplexer. Final design was simulated and tested in Cadence.

SIXT33N

A mobile robot on 3 wheels that moves around according to speech input. It uses a microprocessor along with some circuitry for driving the motor and filtering audio input. Voice recognition is implemented with PCA classification and straight driving with stable eigenvalue placement.

CPU

A 16-bit two-cycle processor based on RPIS (Reduced Processor Instruction Set), which is based on MIPS, created in Logisim.

Personal Projects

[grocery-split](#)

A simple web server written in Go that allows for uneven bill splitting. Parses generic HTML containing a list of items with prices and allows users to select which items they want to pay for.

[Anime Calendar](#)

A Japanese television animation calendar desktop application. Pulls shows from the AniList API, organizes it based on air time, and displays it to the user with a work-in-progress GUI.