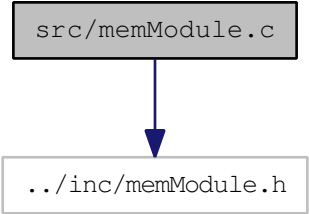


```
src/memModule.c
```



```
graph TD; A[src/memModule.c] --> B[../inc/memModule.h]
```

```
../inc/memModule.h
```