dsd running on OS X DMR, P25 and lots more

Graham Cottew github.com/zendata/SDR

@zenmountainman

supported formats

- P25 phase 1
- ProVoice
- X2-TDMA
- **■** DMR/MOTOTBRO
- NXDN
- **■** D-STAR

dsd requirements

- cmake
- mbelib
- sndfile (libsndfile)
- \Rightarrow itpp (TP++) >= 4.3
- portaudio

mac ports

- cmake 3.10.3_0
- libsndfile 1.0.28_0
- portaudio 19.20140130_2
- itpp 4.2.0

first issue

- mac ports itpp 4.2.0 does not allow dsd to compile
- uninstall itpp port

install IT++ (itpp)

```
wget -0 itpp-latest.tar.bz2 http://sourceforge.net/
projects/itpp/files/latest/download?source=files
tar xjf itpp-latest.tar.bz2
```

cd itpp-*

mkdir build && cd build cmake .. make -j`nproc`
sudo make install

install mbelib

https://github.com/szechyjs/mbelib.git

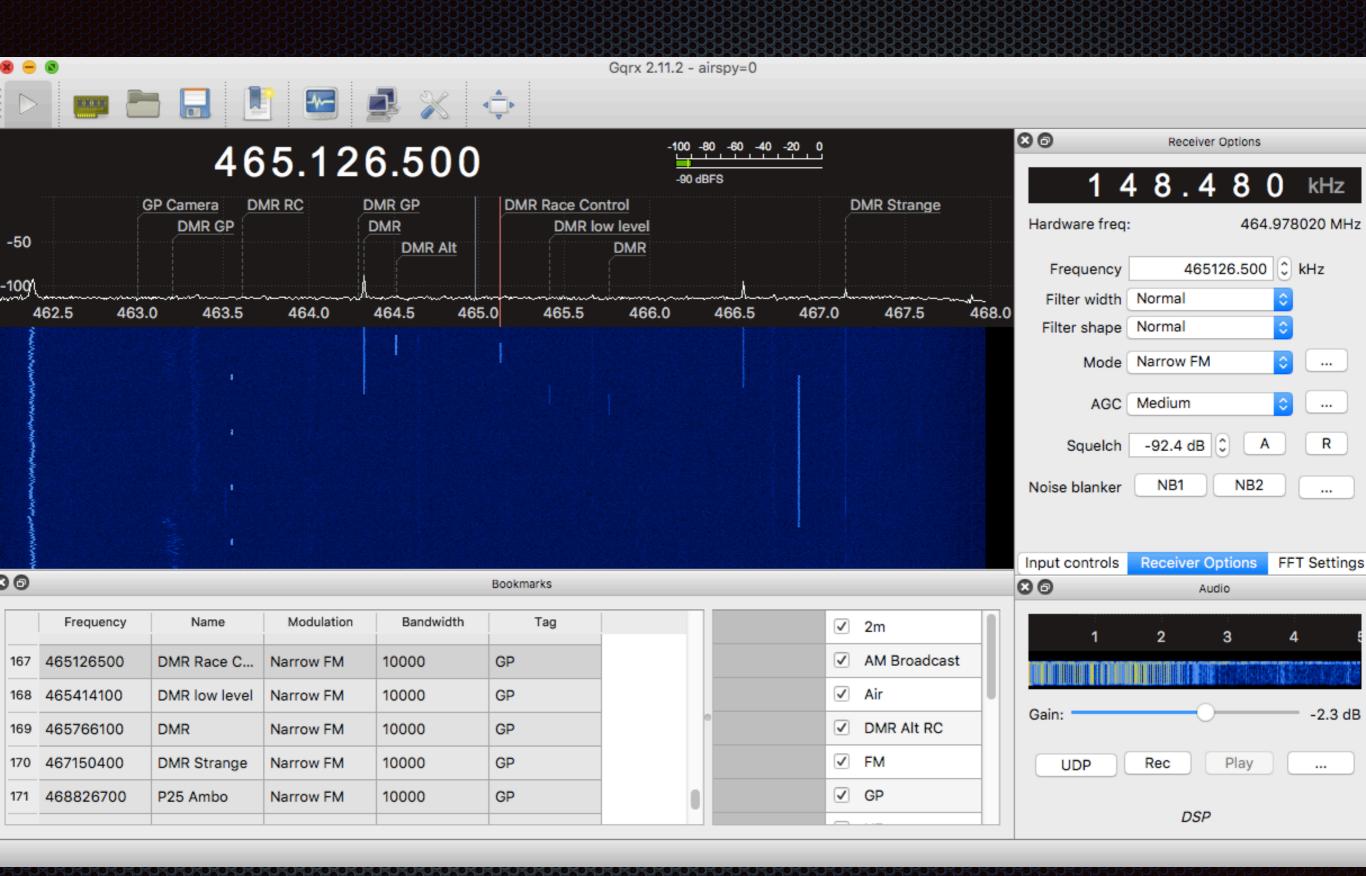
git clone https://github.com/szechyjs/mbelib.git

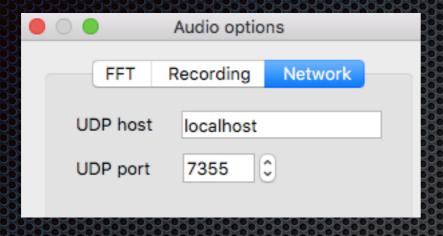
```
# Move into source folder # Create build directory # Move to build directory # Move to build directory # Create Makefile for current system # Compiles the library # Library is installed into computer
```

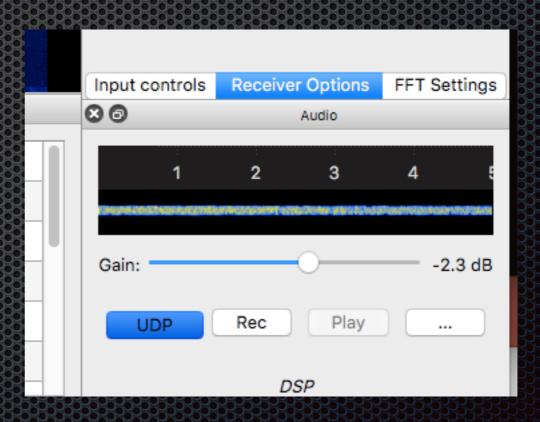
build dsd

git clone https://github.com/szechyjs/dsd.git

```
cd dsd
mkdir build
cd build
cmake ..
make
sudo make install
```







pipe audio over UDP into dsd

nc -l -u localhost 7355 | dsd -i - -o pa:1

```
• • •
                              newAir — dsd -i - -o pa:1 — 74×23
                                                               VOICE e:
          +DMR
                      mod: GFSK inlvl: 12% [SLOT0]
  Sync:
                                                      slot1
  Sync:
          +DMR
                      mod: GFSK inlvl: 12% [SLOT0]
                                                      slot1
                                                               VOICE e:
                           GFSK inlvl: 12% [SLOT0]
                                                               VOICE e:=
          +DMR
                                                      slot1
   Sync:
          +DMR
                           GFSK inlvl: 12% [slot0]
                                                      slot1
                                                               TLC
   Sync:
          +DMR
                      mod: GFSK inlvl: 12% slot0
                                                               Slot idle
   Sync:
                                                     [slot1]
                      mod: GFSK inlvl: 12% [slot0]
                                                      slot1
          +DMR
                                                               TLC
   Sync:
          +DMR
                           GFSK inlvl: 12% slot0
                                                               Slot idle
   Sync:
                                                     [slot1]
                      mod: GFSK inlvl: 12% [slot0]
          +DMR
                                                               TLC
                                                      slot1
   Sync:
          +DMR
                           GFSK inlvl: 12%
                                                               Slot idle
                                                     [slot1]
   Sync:
                                             slot0
                           GFSK inlvl: 12% [slot0]
                                                      slot1
                                                               TLC
          +DMR
   Sync:
                      mod: GFSK inlvl: 12% slot0
                                                     [slot1]
                                                               Slot idle
   Sync:
          +DMR
          +DMR
                      mod: GFSK inlvl: 12% [slot0]
                                                      slot1
                                                               TLC
   Sync:
   Sync:
          +DMR
                           GFSK inlvl: 12% slot0
                                                     [slot1]
                                                               Slot idle
          +DMR
                      mod: GFSK inlvl: 12% [slot0]
                                                               TLC
   Sync:
                                                      slot1
   Sync:
          +DMR
                      mod: GFSK inlvl: 12%
                                             slot0
                                                               Slot idle
                                                     [slot1]
          +DMR
                           GFSK inlvl: 12% [slot0]
                                                               TLC
                                                      slot1
   Sync:
          +DMR
                      mod: GFSK inlvl: 12% slot0
                                                               Slot idle
                                                     [slot1]
   Sync:
          +DMR
                      mod: GFSK inlvl: 12% [slot0]
                                                      slot1
                                                               TLC
   Sync:
          +DMR
                           GFSK inlvl: 12% slot0
                                                               Slot idle
   Sync:
                                                     [slot1]
                      mod: GFSK inlvl: 12% [slot0]
          +DMR
                                                      slot1
                                                               TLC
   Sync:
                      mod: GFSK inlvl: 12% slot0
  Sync:
          +DMR
                                                     [slot1]
                                                               Slot idle
                      mod: GFSK inlvl: 12% [slot0]
                                                               TLC
   Sync:
          +DMR
                                                      slot1
FML penny migningmes
                                   cd_anuradio
```

dsd running on OS X DMR, P25 and lots more

Graham Cottew github.com/zendata/SDR

@zenmountainman