

## Implementation of Hardware Stack TCP/IP using FPGA



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*(Dr. Teagan Beahan Sr.)*

## IMPLEMENTATION OF HARDWARE STACK TCP/IP USING FPGA



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LAP Lambert Academic Publishing Mai 2016, 2016. Taschenbuch. Book Condition: Neu. 221x151x7 mm. Neuware - There is a general consensus that in years to come more & more Internet devices will be embedded & not PC oriented. Just one such prediction is that by 2020, 95% of Internet-connected devices will not be computers. They will be Embedded Internet devices. A hardware TCP/IP stack has a couple of advantages. Firstly as they are hardware based, most run at close to line speeds encapsulating and striping streams of data on the fly. This makes it increasingly more difficult to cause a Denial of Service attack and almost impossible to run malicious code using principals of buffer overruns etc. However being hardware makes it difficult to upgrade should little quirks be found allowing say SYN attacks for example. The book focuses on the implementation of TCP/IP stack in hardware form using system generator and FPGA. 60 pp. Englisch.



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