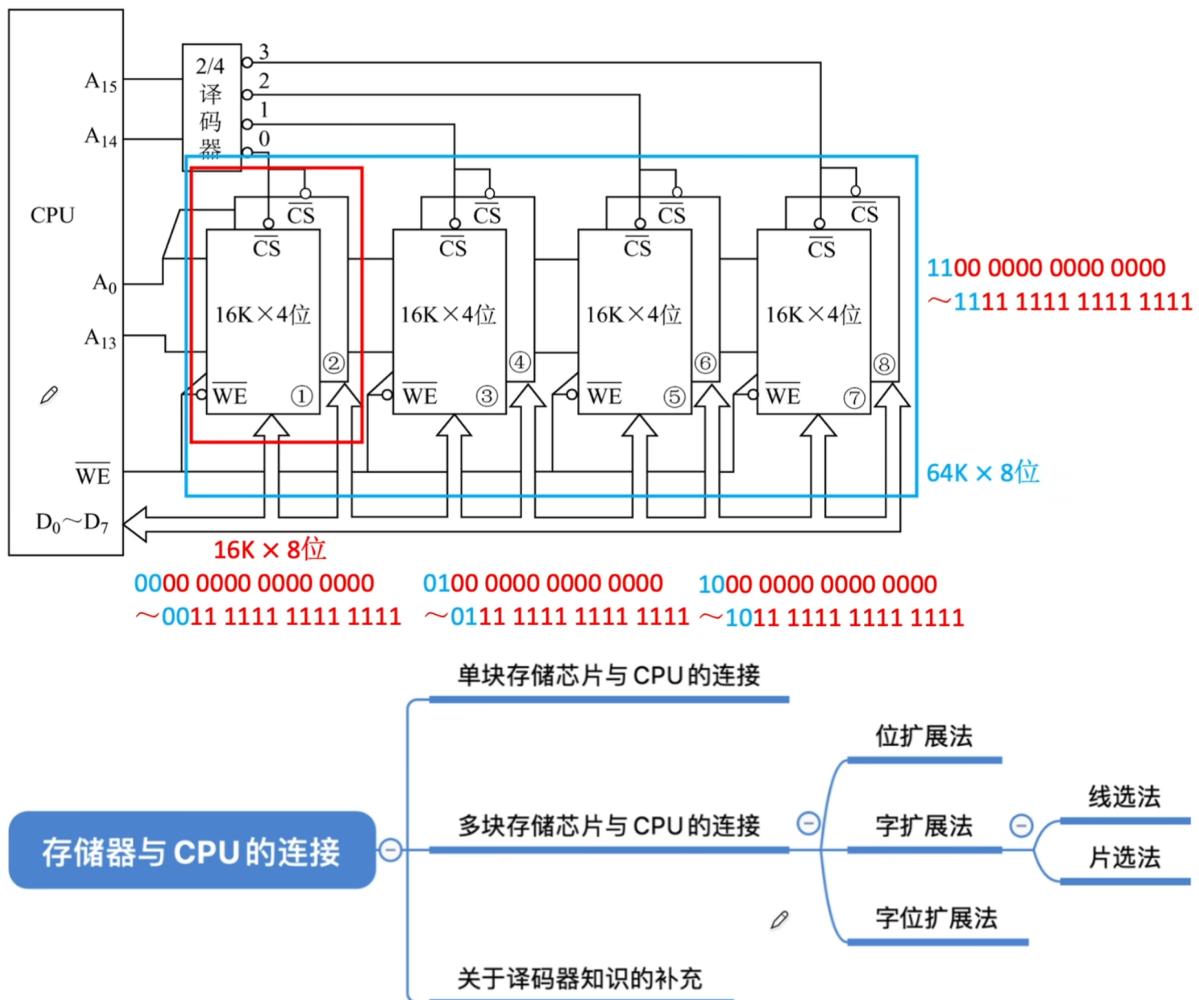
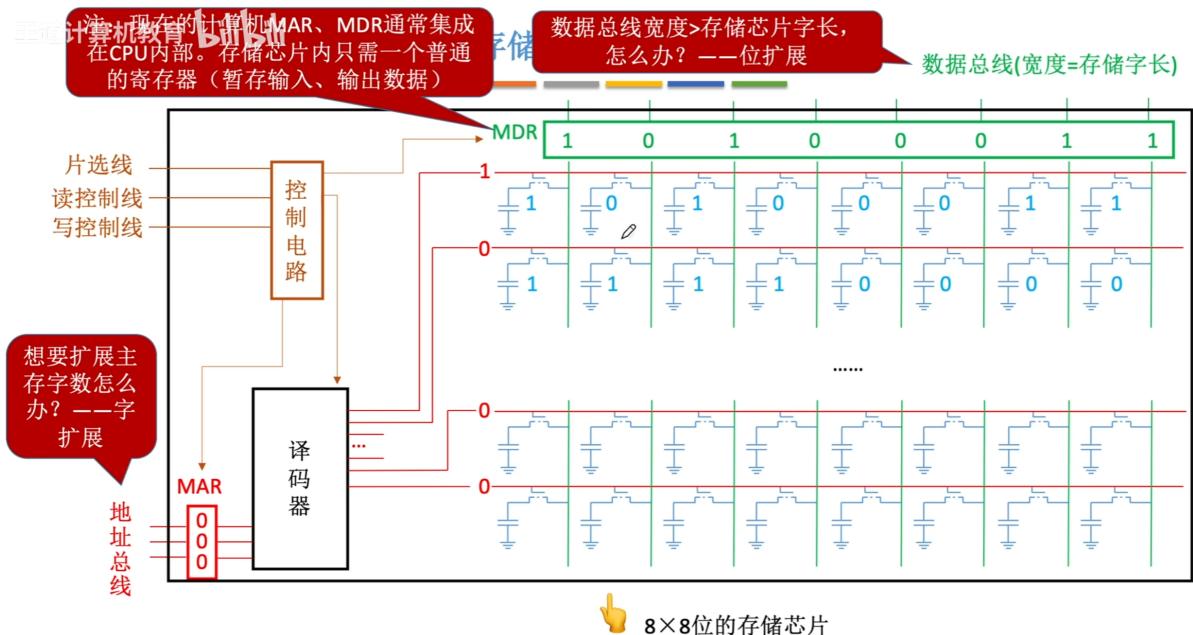


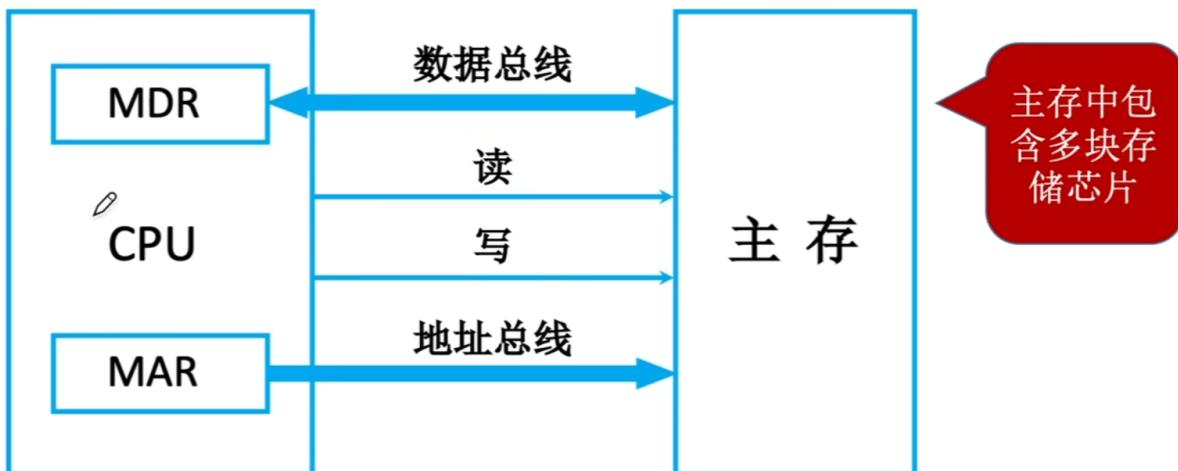
# 连接原理



## 单块存储芯片与CPU的连接

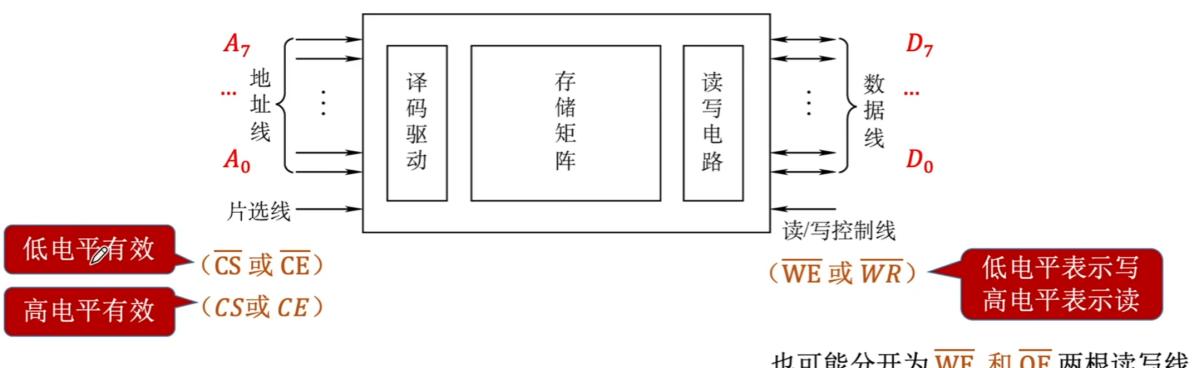


# 现在的计算机

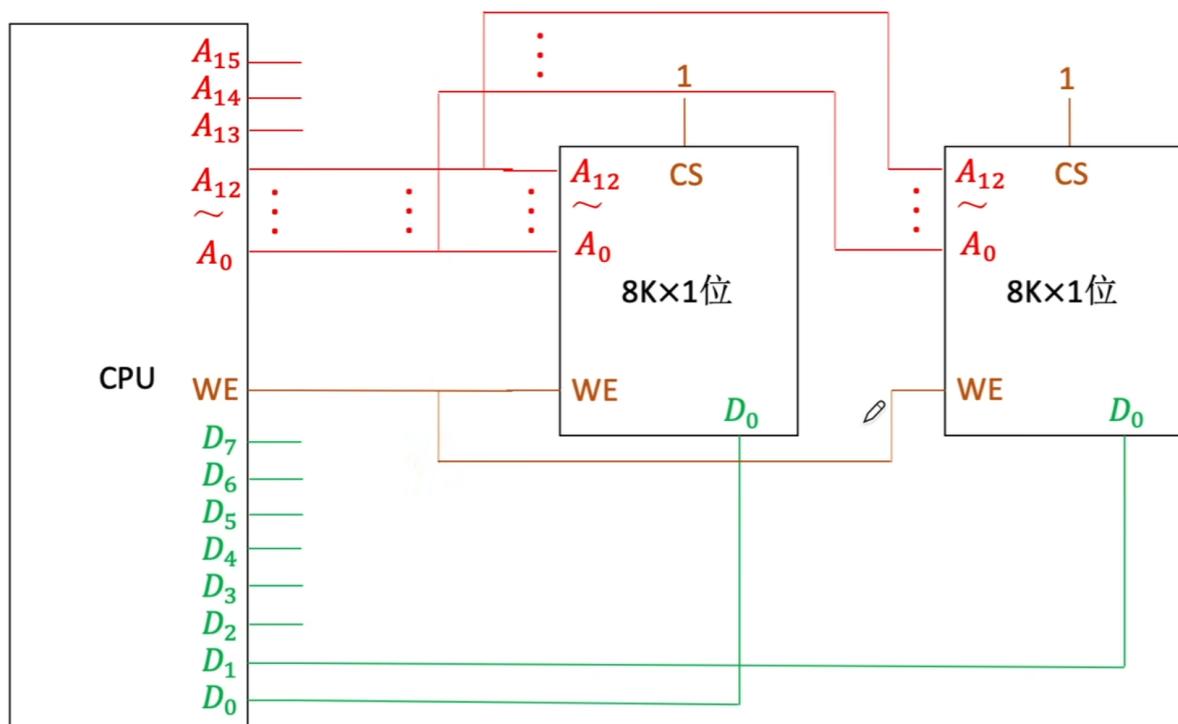


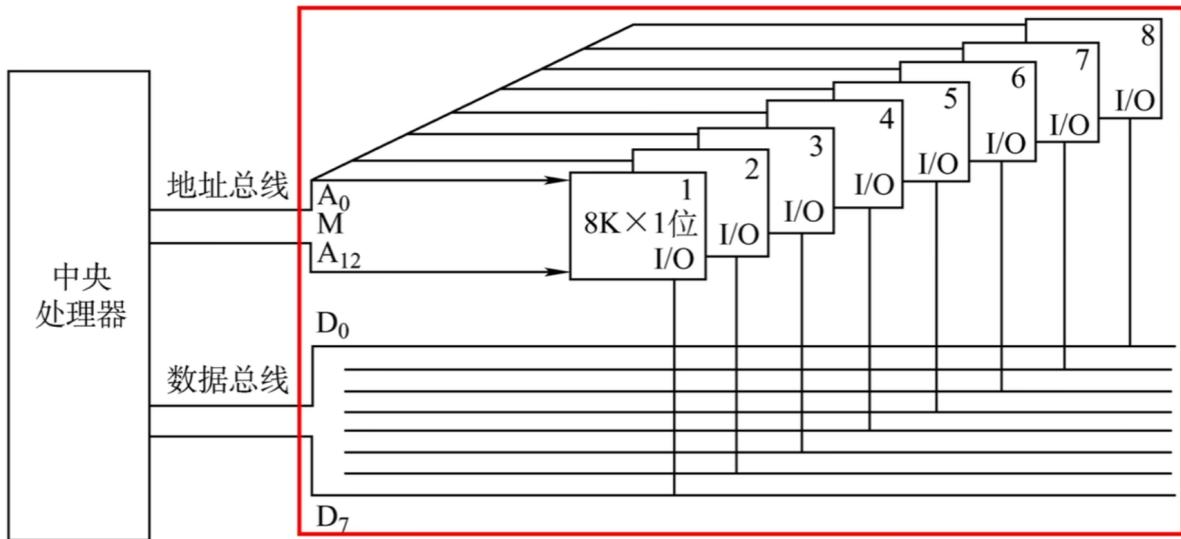
主存中包含多块存储芯片

## 存储芯片的输入输出信号



## 增加主存的存储字长-位扩展





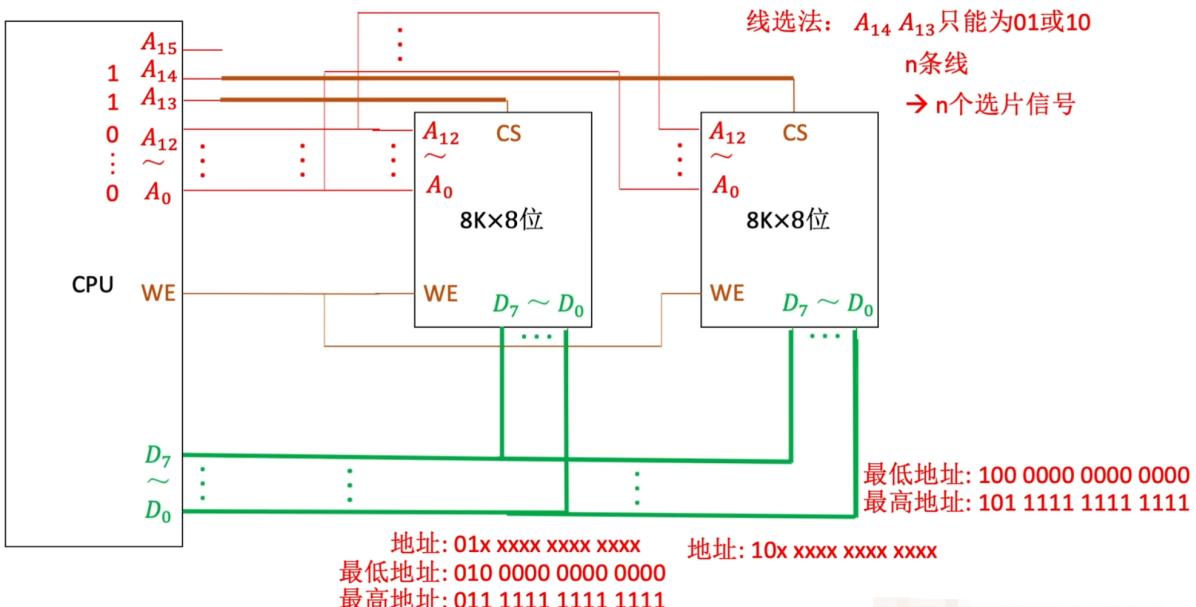
8片 $8K \times 1$ 位的存储芯片

→ 1个 $8K \times 8$ 位的存储器，容量 $8KB$

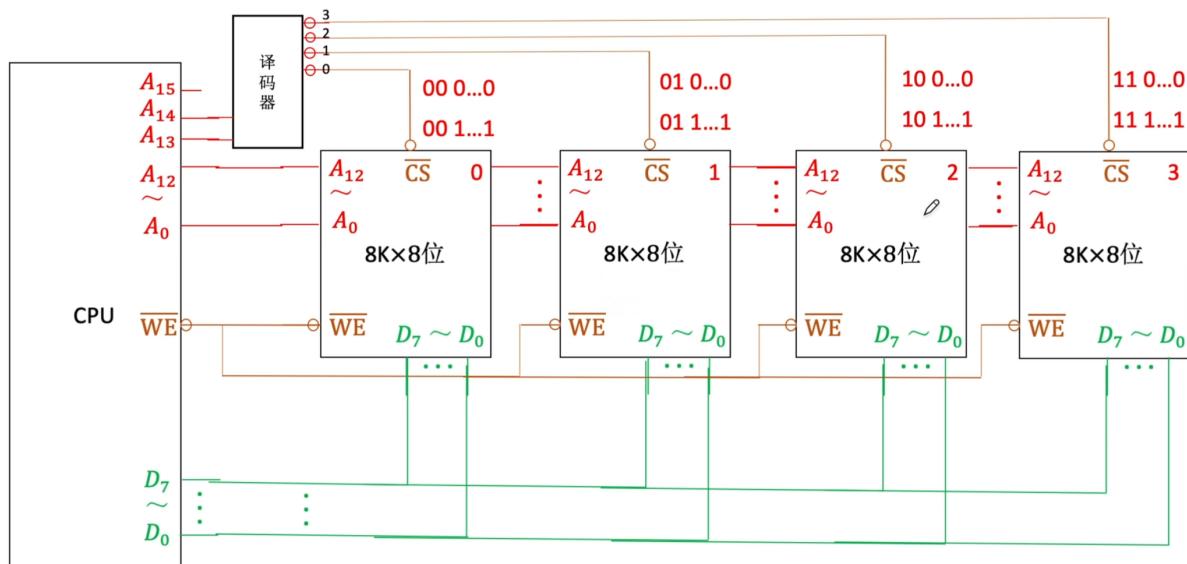
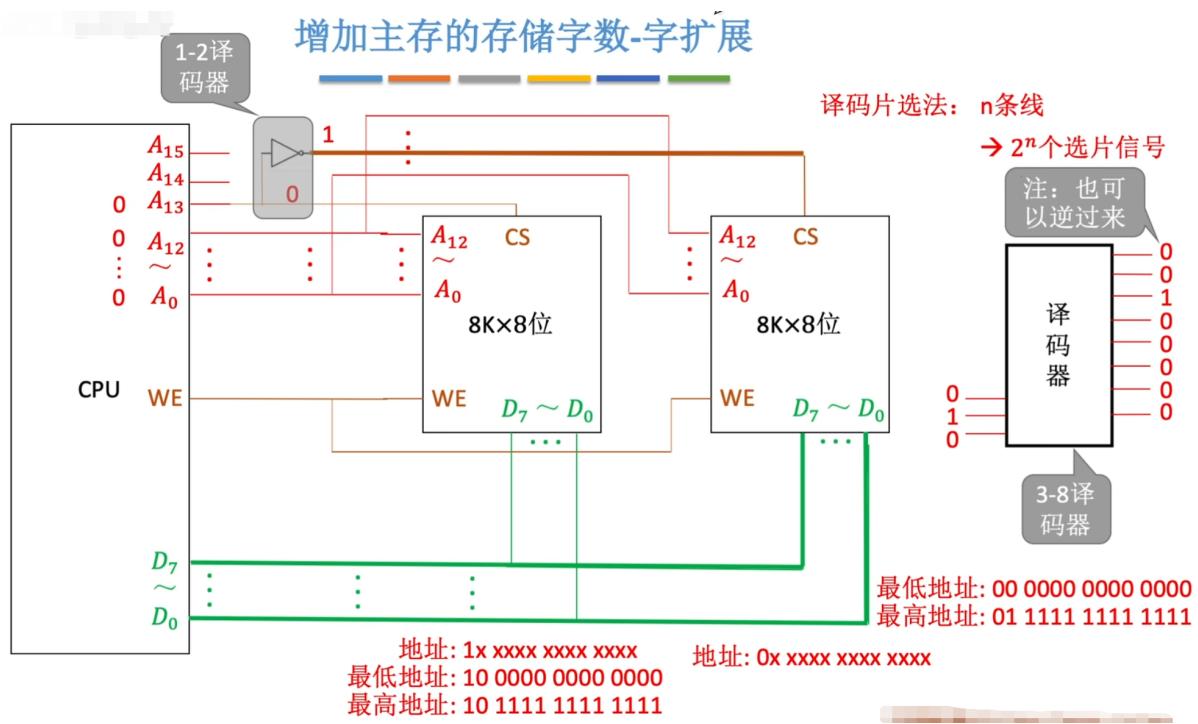
## 增加主存的存储字数-字扩展

线选法	译码片选法
$n$ 条线 → $n$ 个选片信号	$n$ 条线 → $2^n$ 个选片信号
电路简单	电路复杂
地址空间不连续	地址空间可连续

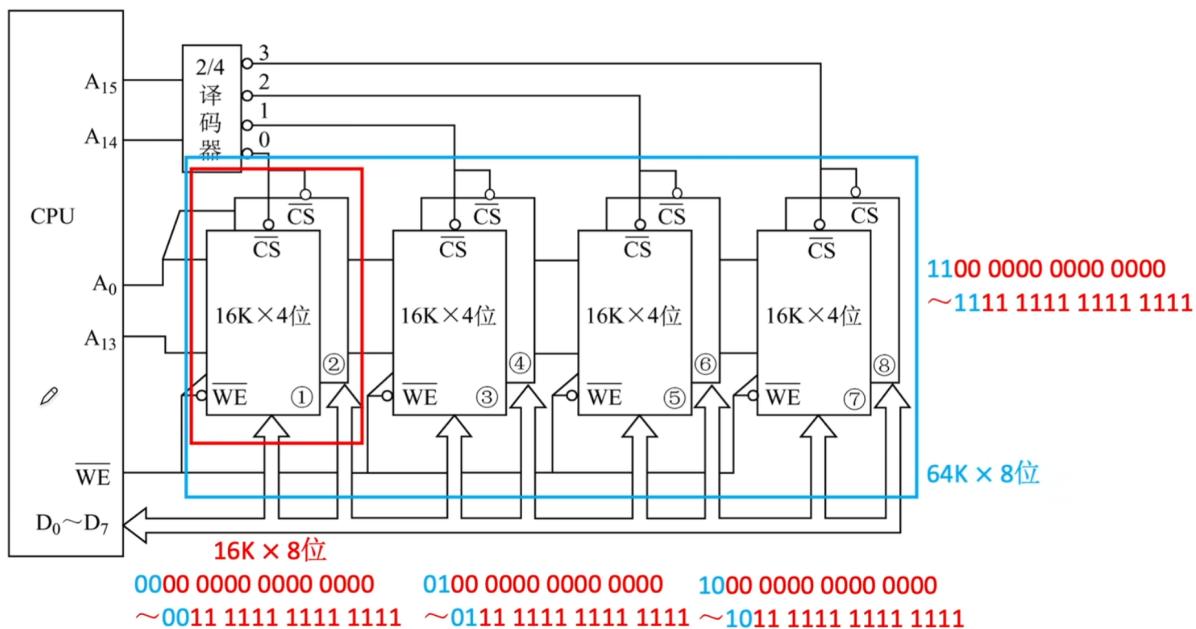
### 线选法



## 译码片选法



# 字位同时扩展



## 译码器

