## **Dewen Zeng**

**Address** Contact

Qiming college, 1037 Luoyu Road, Wuhan, China Huazhong University of Science&Technology

dewenzeng@hust.edu.cn

Research Interests

• Data miming and embedded computing

Education Huazhong university of science&technology, wuhan, China 2016 - present

• M.S. in microelectronic engineering

• GPA: 88.7/100

Huazhong university of science&technology, wuhan, China 2012 - 2016

• B.S. in microelectronic engineering

• GPA: 84.5/100

Research Experience

## Distance accelerator for Time Series Mining, HUST

2016 - 2017

- Develop a high throughput reconfigurable architecture for computing several distance function
- Develop a highly optimized memristor-based distance accelerator for real-time and energy-efficient data miming

**Publications** 

Xu X, **Zeng D**, Xu W, et al. An efficient memristor-based distance accelerator for time series data mining on data centers[C]//Design Automation Conference (DAC), 2017 54th ACM/EDAC/IEEE. IEEE, 2017: 1-6.

Xu, X, Lin, F, Xu, W, Yao, X, Shi, Y, **Zeng, D**, & Hu, Y (2018). MDA: A Reconfigurable Memristor-based Distance Accelerator for Time Series Mining on Data Centers. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems.

## Internship Wuhan easylinkin technology Co., Ltd.

2017 - 2018

- working as a developer on the real-time locating project
- develop a management system using Java for storing and sorting real-time data coming from different Internet of things(IoT) devices
- develop a demonstration system using javascript and jquery for viewing the real-time location of different nodes and IoT devices

Awards

- Second prize, National Undergraduate Electronics Design Contest(NUEDC), 2015
- Outstanding undergraduate, Qiming College, Huazhong University of Science and Technology, 2016

Computer Skills **Programming**: c/c++, java, Verilog, Matlab **Software**: Intellij, Myeclipse, Visual Studio