

Driver Magnetorquer

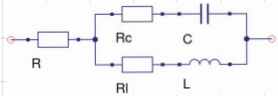
Como Funciona?

3 axis control system
3 magnetorquers x, y, z

Sensor:

H bridge and filtering components

Similar to inductors → Higher Resistance
// Parasitic Capacitance (more turns?)
Possible Lower Inductance



$\uparrow V \rightarrow \uparrow I \rightarrow \uparrow \mu \text{ (Am}^2\text{)}$
Controlled by its resistance

time constant = $\frac{\text{inductance}}{\text{resistance}} = \frac{V \cdot s}{A}$ magnetic flux
 $= \frac{\Phi(L)}{L}; R = \frac{\Phi(L)}{R \cdot L}$
Varies with size and temperature
Smaller is better
Resistance determines the voltage
Voltage controls the current on the magnetorquer

Ondulação Residual (Ripple)

↳ depends on the circuit → ripple voltage ↔ impedance frequency

Digital Control Stages

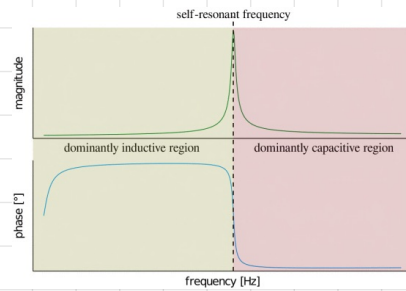
- receive commands
- store parameters
- responds w/ housekeeping data

output mode + time since reception
↓
Output signal value calculated

transformed into a PWM signal



Power Conversion Stage
+ Direction Selection Output



More formulas
Calculations
& strategies
for the control itself

Power conversion Stage

μ depends linearly on the I passing

Current Control

Operational Amplifiers with negative feedback loop

Output stability + dissipation of excess energy

Switched power converters } highly efficient

use energy storage capabilities of inductors → magnetorquers μ (if sufficient inductance)

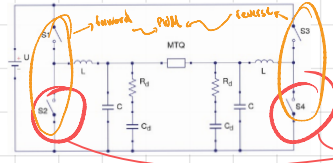
Convert pulsed voltage source with controllable duty cycles to continuous current flow

FORESAIL → Switching pwr converter + buck converter

Full H-bridge

↳ 4 N channelled Mosfets

Operate by digital control stage ≈ Synchronous continuous-mode buck converter operation



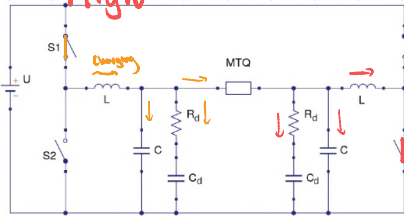
Current direction → changing into's ground terminal

two RLC circuits

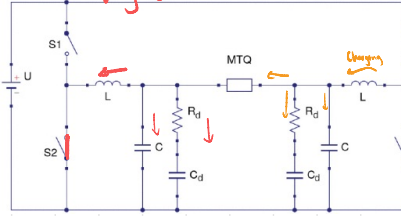
↳ third-order low pass filters on output terminals

3 power converters → symmetrical

Forward High



Reverse High

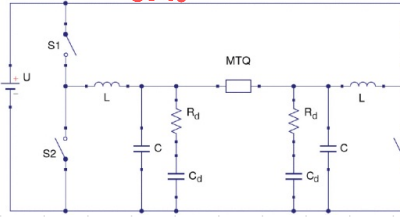
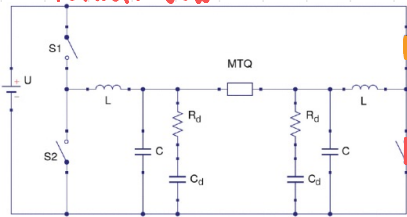


Mode	PWM	S1	S2	S3	S4
Forward	HIGH	ON	OFF	OFF	ON
	LOW	OFF	OFF	ON	ON
Reverse	HIGH	OFF	ON	ON	OFF
	LOW	OFF	OFF	ON	ON

Reverse Low

12,6 e 7,5V

Forward Low



Simulate on LT Spice

However, it is also possible to leave the lower MOSFET of the driving side switched off if they incorporate the body diode.

Not efficient enough for lower power consumption → use pulsed from the digital control stage

LT Spice Simulation

Mosfet → Voltage Controlled Switch

depletion: **NC** → only "N" type

enhancement: **NO** → "p" or "n" type
 ↓
 PMOS NMOS

TABLE IV Magnetometer Model Values

coils	long	short
R [Ω]	0.0	41.61
R_c [Ω]	106.51	689.56
C [pF]	115.34	68
R_l [Ω]	33.92	0.0
L [mH]	5.56	28.8
f_0 [kHz]	198.74	113.73
t_c [μ s]	164.01	692.14
Effective area [m^2]	2.145	2.045
Resistance @0 °C [Ω]	28.45	36.2
Resistance @20 °C [Ω]	30.7	39.08
Resistance @80 °C [Ω]	37.5	47.72
Power @20 °C, 0.15 A m^2 [mW]	149.64	208.75
Power @20 °C, 3.6 V [mW]	423.26	334.08
Dipole moment @20 °C, 3.6 V [$A m^2$]	0.304	0.188

Research past Drivers
Try - Copy & pasting

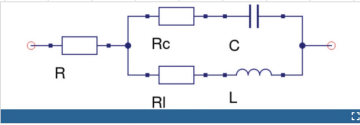
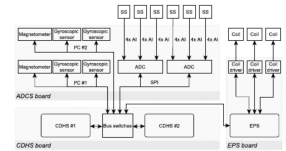


Fig. 2. Equivalent circuit model for magnetometer.



https://www.researchgate.net/publication/262822004_Attitude_determination_and_control_for_centrifugal_tether_deployment_on_the_ESTCube-1_nanosatellite

"Coil Driver" \rightarrow Allegro A3901 motor driver
 \rightarrow Maxim MAX3132SA Analogy Switch \rightarrow Clock 52 kHz
 Linear Technology LT6105 current sensor



<http://www.raumfahrt.fh-aachen.de/compass-1/download/Development%20of%20an%20Active%20Magnetic%20Attitude%20Determination%20and%20Control%20System%20for%20Picosatellites%20on%20highly%20inclined%20circular%20Low%20Earth%20Orbits.pdf>

"Coil Driver" \rightarrow

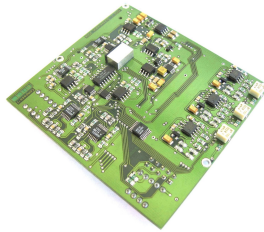


Figure 4-19: Bottom view of the ADCS flight spare model main board with complete component mount. The circuitry on the left is the analog section of the three-axis magnetometer; the three circuits on the right are the coil current drivers.

Controller_rev5.0_flat ~> Main?

Processor -> STM32 G473V

Blocks ->

1st -> ???

- 2nd -> Magnetorquer -> File: reactionwheel.kicad-sch
- 3rd -> LEDs -> File: LEDs.kicad-sch
- 4th -> Reaction Wheel -> ~~BLDC.kicad-sch -> USELESS for now~~
- 5th -> LED RGB + resistors -> Commutation Sequence motor driver
- 6th -> ADDR Connector Pin Header
- 7th -> ~~Encoder -> File: encoder.kicad-sch~~
- 8th -> Debug_Sig Connector Pin Header
- 9th -> Prog Connector Pin Header
- 10th -> UART Connector Pin Header
- 11th -> Mezzanine Plug ~ Mzz Breakout ~ Resistor Array ~ TCAN330
- 12th -> J-Samtec - TFM-120-x1-x18-D-RA
- 13th -> Crystal 16MHz
- 14th -> Power -> File: power.kicad-sch

Magnetorquer Block:

Inputs:

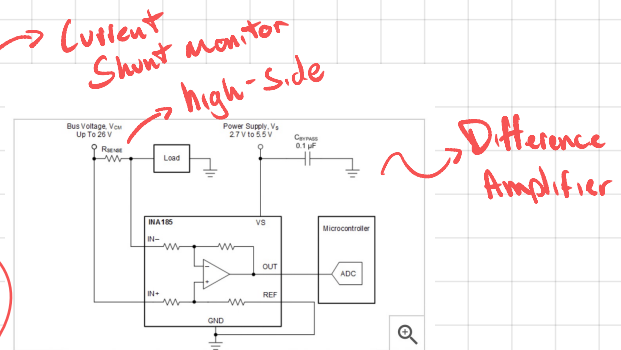
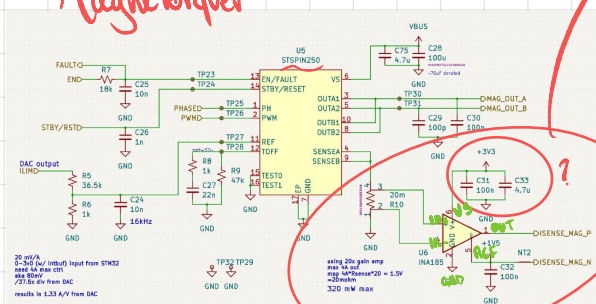
- EN (Mag-EN) ~> logic input 5V
- STBY/RST (Mag-STBY) ~> Logic input 5V ~> low = low consumption mode
- ILIM ~> Analog Input ~> Reference voltage & current limiter circuitry
- Phase ~> Logic input ~> phase input
- PWM ~> " ~> PWM input

Outputs:

- Fault (Mag-Fault) ~> open drain output
- ISENSE_MAG_P ~> Power output
- ISENSE_MAG_N ~> "
- Mag-out-A (Mag-1) ~> Power bridge output
- Mag-out-B (Mag-2) ~> "

Understanding the circuit:

Magnetorquer:



STSPIN250 -

Single low voltage DC motor driver integrating a $R_{ds(on)}$ power stage in a 3x3 mm package

Designed to be operated with batteries, thus can be forced into a zero consumption state.
gnd A

Protections:

- Over current
- Overtemperature
- Short Circuit

What is $R_{DS(on)}$?

Resistance → Drain-Source ON

- It's the total resistance between the drain and source in a Metal Oxide Field Effect Transistor, or Mosfet.
Basis for a maximum current rating

See more in microcontrollertips.com/mosfets-what-is-rdson-faq/



STSPIN250 Block Diagram

→ Rabbit Hole

- Representation of a control system
- Used to calculate the transfer function of the system block

What's a transfer function?

Math function that models the system output based on the input

- Summing point \leadsto add two or more signals 
- Take off point / Pick-off Point / Branch point \leadsto Distributes signals 

Reduction of Multiple Subsystems (controller systems are complex in nature)

- Get the overall transfer function
- Reduce Complexity = Reduce blocks

Block Diagram Algebra \rightarrow Bound by Block Diagram Reduction Rules

PWM current control

The device implements a current controller.

The voltage on the sense pins (VSENSE) is compared to the reference voltage applied on the REF pin (VREF).

When $VSENSE > VREF$, the current limiter is triggered, the OFF time counter is started, and the decay sequence is performed.

The decay sequence starts turning on all the low sides of the full-bridge. After the programmed OFF time the system returns to the ON state.

Current Shunt Monitor

- + Easy and cheap \leadsto no more than just an op-amp
- + Precise
- Add Resistance in gnd
- May require additional wire to the load that could be omitted

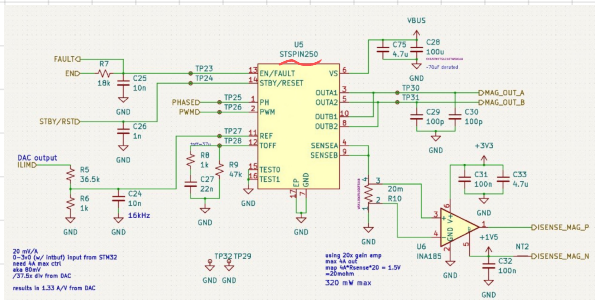
High Side Current Sensing

- between Supply and Load
- + Directly to the source \rightarrow detect downstream failure
- + No gnd disturbance
- Requires careful Resistor Matching to obtain common-mode rejection ratio
- Must withstand very high and dynamic common-mode voltages

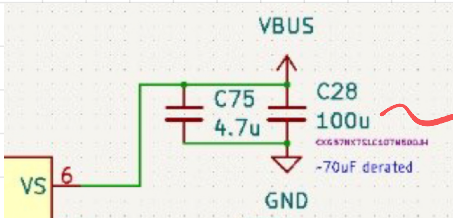
Current Shunt Monitor (family)

- Measure voltage across a Shunt Resistor \sim mV range \sim need amplification

STSPIN250 pin by pin



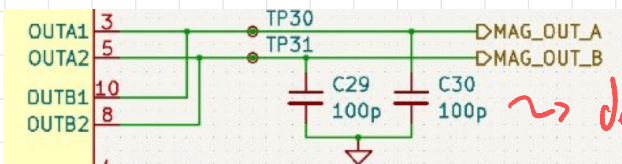
Pin 6 \rightarrow VS \rightarrow Voltage Supply



\rightarrow decoupling capacitors

typical values : 2.2μ / $16V$ & 22μ / $16V$

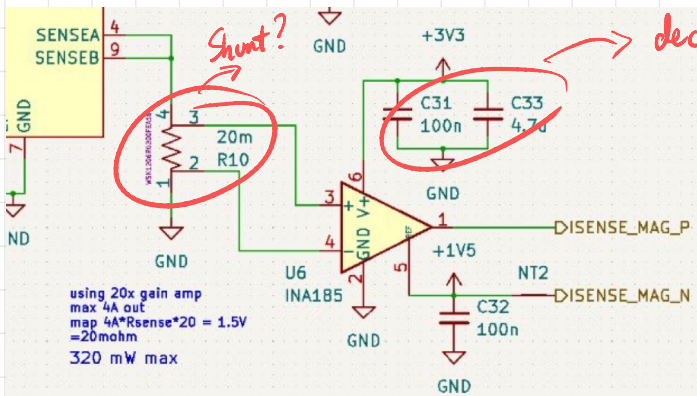
Pin 3, 5, 8, 10 \rightarrow Output



\rightarrow decoupling

} typical application

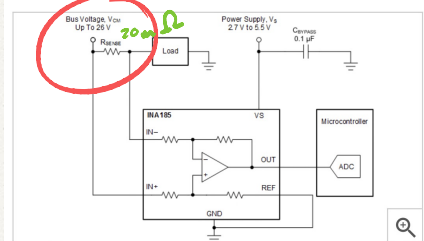
Pin 4, 9 \rightarrow Sense Pins



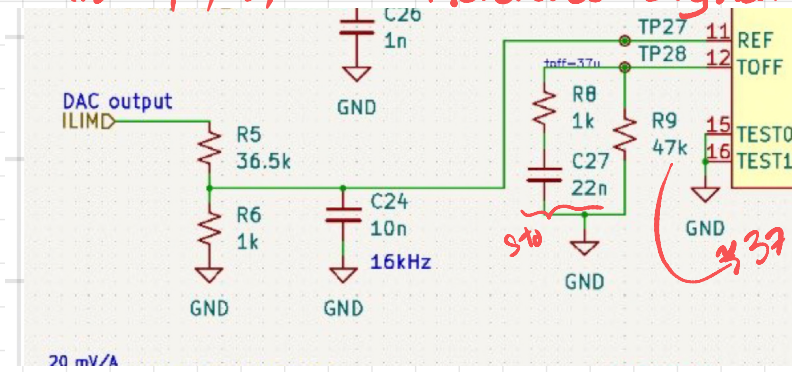
Shunt?

\rightarrow decoupling

HS \rightarrow between Supply and load



Pins 11, 12, 15, 16 → Reference Signal and Decay Time → compare to sense voltage



if (Sense Voltage > Reference Voltage) {
 Shutdown ();
 LSx1 && LSx2 = High ;
}