Assignment On

**Digital System Design**

**Submitted To:**

Md. Rashedul Islam

Lecturer

Dept. Of C.S.E

Hajee Mohammad Danesh Science & Technology University

**Submitted By**

Iqbal Mahmud

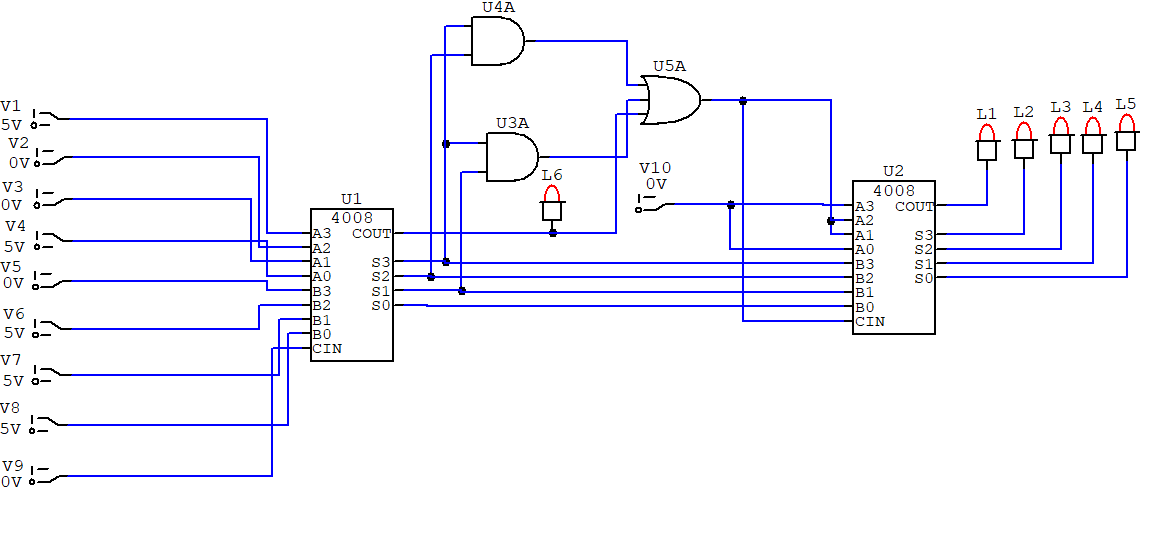
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Dept.: C.S.E

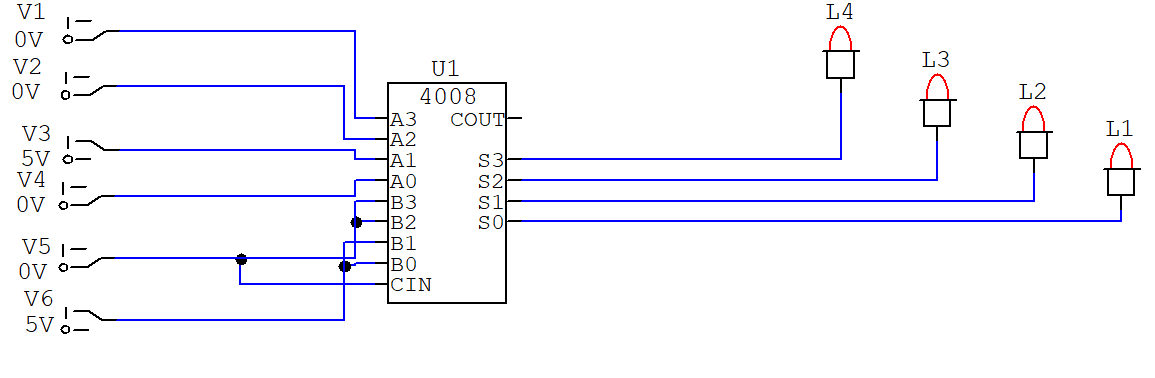
Level: 4 Sem.: I

Date Of Submission: 08.07.18

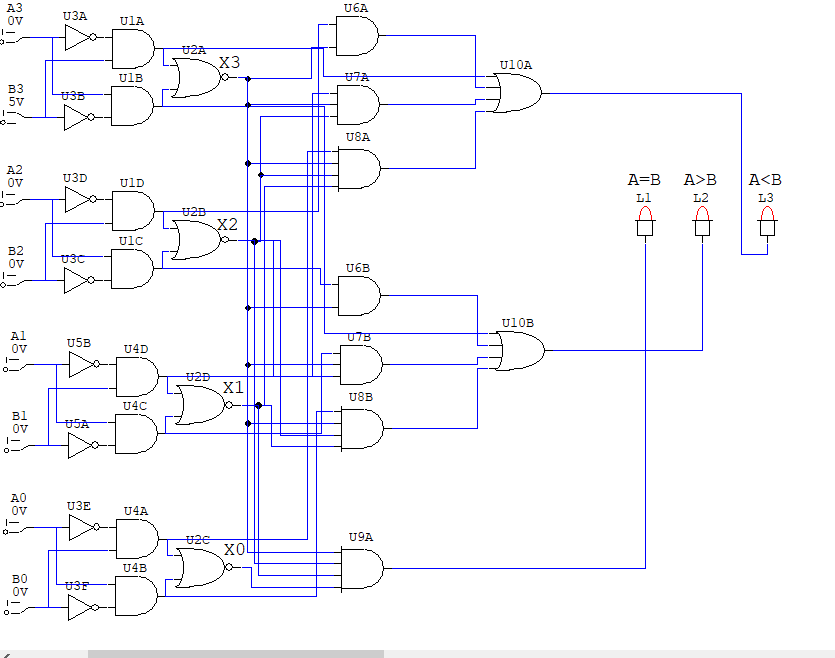
**BCD Adder:**

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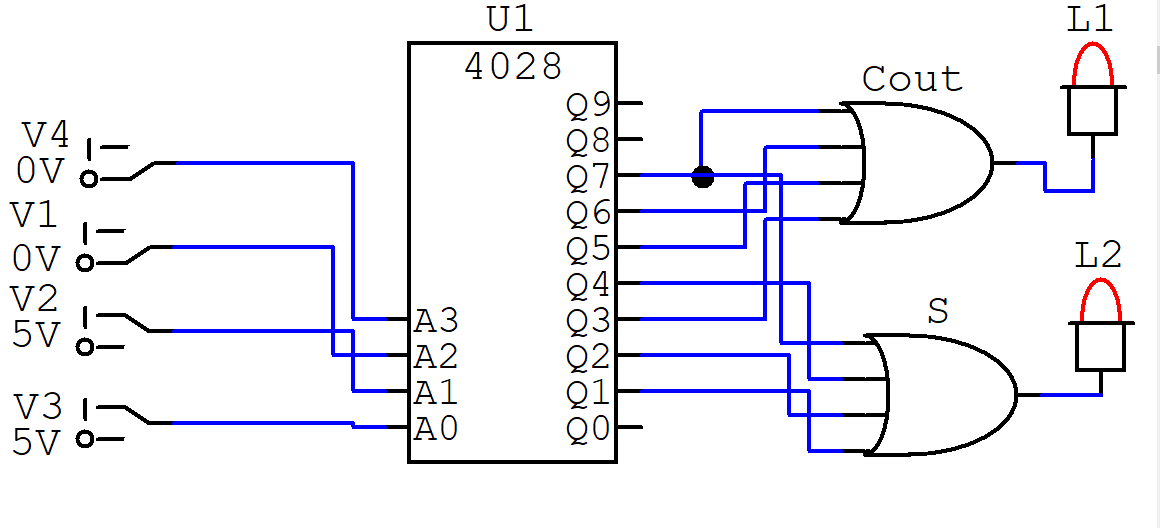
**BCD to Excess 3 Code Converter:**

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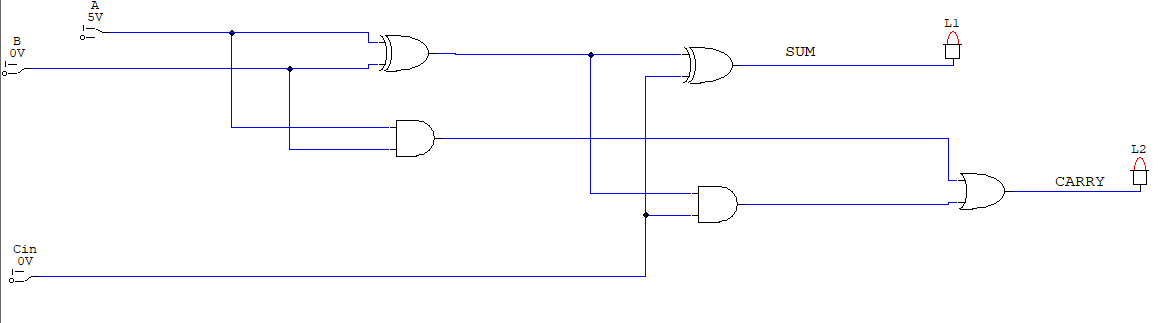
**Comparator:**

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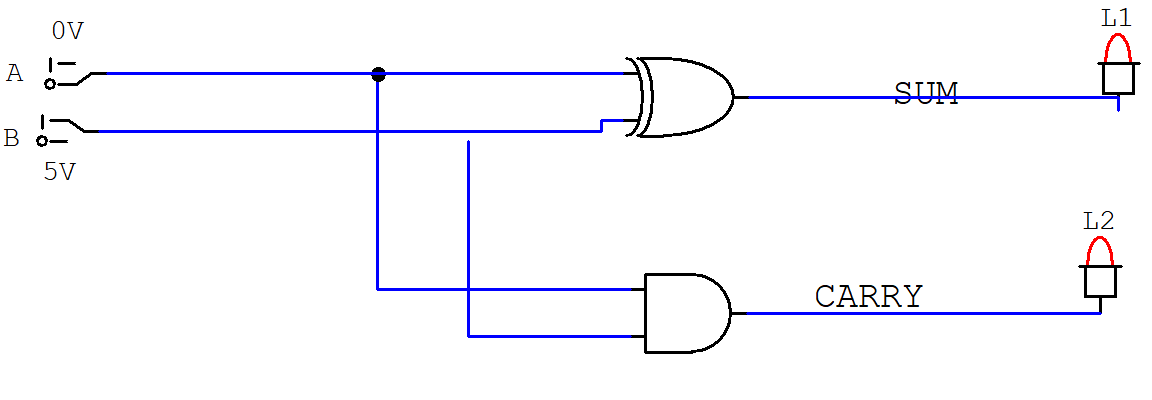
**Full Adder with Decoder:**

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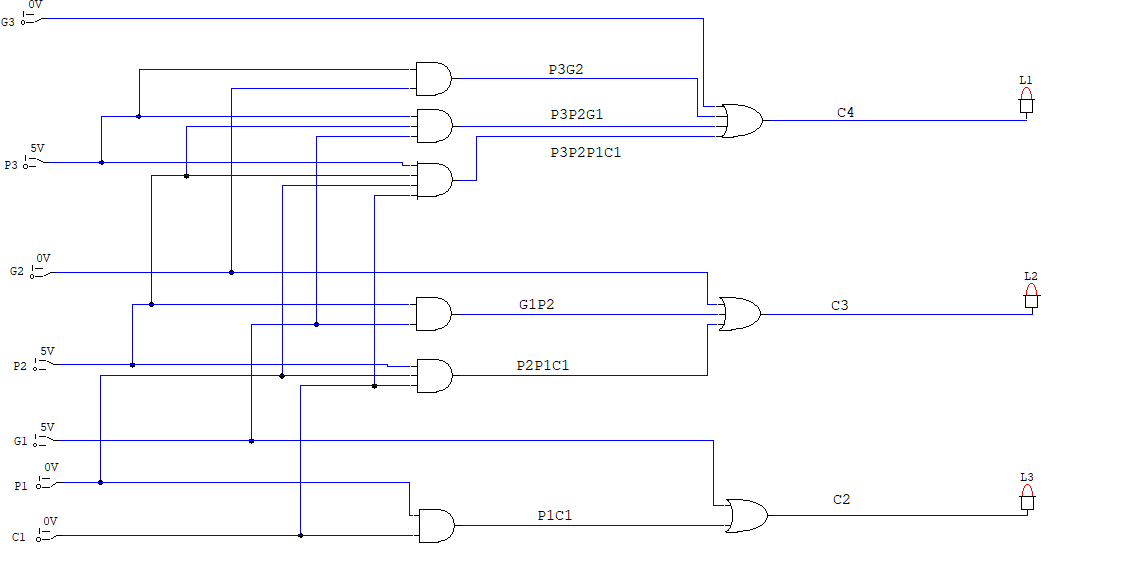
**Full Adder:**

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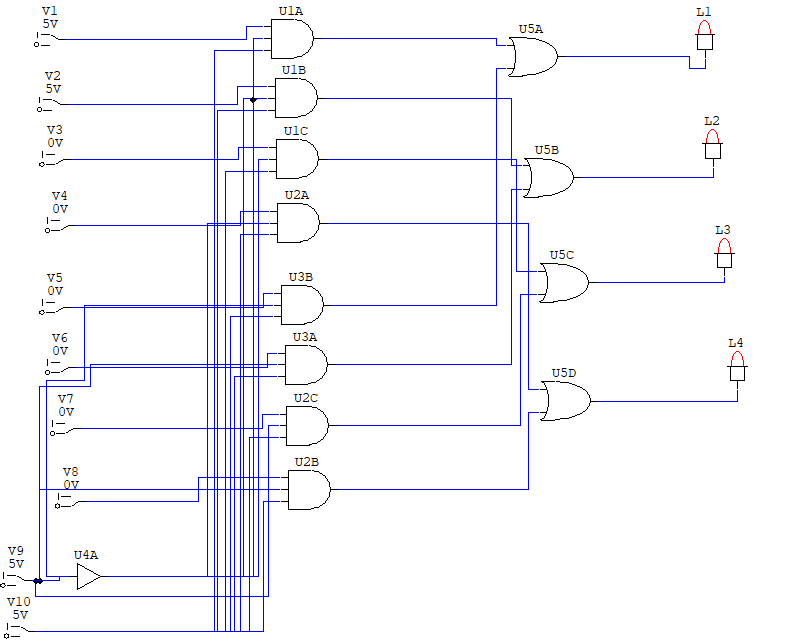
**Half Adder:**

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**Look Ahead Carry Genarator:**

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**Quadtruple 2 to 1 MUX:**

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