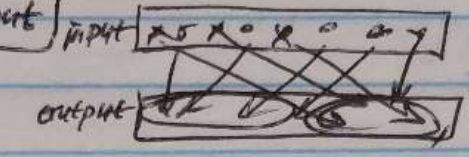
thrust

gather/scatter　　scan　　reduce

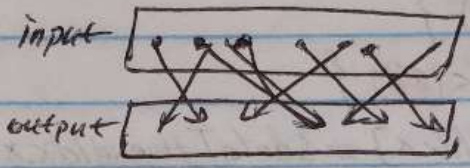
vectorize

gather: input → map → output



output[i] = input[map[i]]

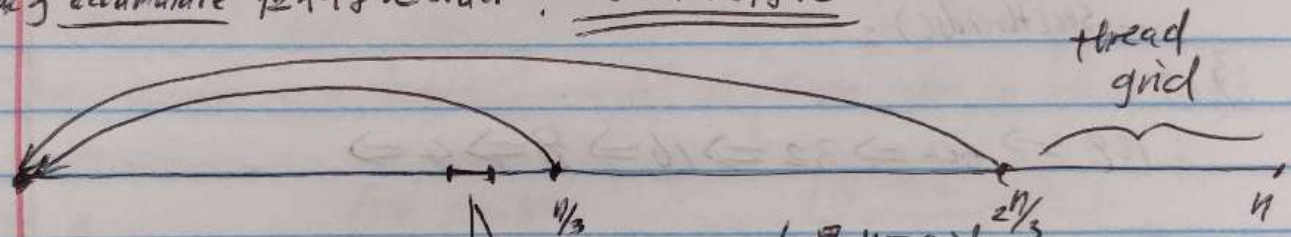
scatter: input



output[map[i]] = input[i]

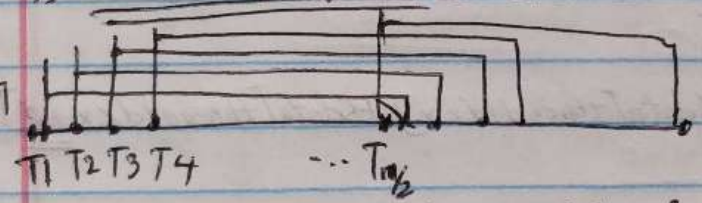
---

reduce 类似于 accumulate 但不保证 order. accumulate 保证



如果 n > grids

每个 block/thread. reduce 多个结果 → zoom in



① 一共 m Threads, 每个 thread reduce (i, i + m/2) 对结点

⇒ sync block

② 问题域缩小到 m/2.

再申请 threads. 每个 threads reduce

⇒ sync block

↳ 直到剩下一个 thread reduce 出最终结果.

⇒ 每 reduce 一次, 有一半的线程干活.

```
if (BLOCK_SIZE >= 512) {
    if (threadIdx.x < 256) {
        sdata[threadIdx.x] = sdata[threadIdx.x] + sdata[threadIdx.x+256];
    }
    __syncthreads();
}
if (BLOCK_SIZE >= 256) {
    if (threadIdx.x < 128) {
        sdata[threadIdx.x] = sdata[threadIdx.x] + sdata[threadIdx.x+128];
    }
    __syncthreads();
}
: 128 ⇒ 64 ⇒ 32 ⇒ 16 ⇒ 8 ⇒ 4 ⇒

if (BLOCK_SIZE >= 2) {
    if (threadIdx.x < 1) {
        sdata[threadIdx.x] = sdata[threadIdx.x] + sdata[threadIdx.x+1];
    }
    __syncthreads();
}
if (threadIdx.x == 0)
    blockResults[blockIdx.x] = sdata[threadIdx.x];
```

// 最后对于 reduce 结果写入全局 memory 了
每个 block 对应一个 reduce 结果.

在外围 kernel 返回时.
在 host 后面 reduce 最后结果.

inclusive_scan

1 2 3 4 5
1 3 6 10 15

exclusive_scan

1 2 3 4 5
0 1 3 6 10

warp size = 32   **8x warps** 1block

warp1 warp2 warp3 ... warp8

以warp为单位，拆分问题域n

← 32 threads

X

S_1 S_2 S_3 ... ...

X_1 X_1+X_2 $X_1+X_2+X_3$ ... $X_1+X_2+\dots+X_{32}$

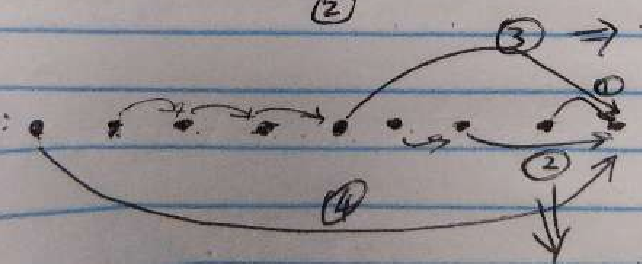
- 每个warp计算出一个carry = 最后一个加和结果 $\sum_{i=1}^{32} x_i$
- 如何计算32个数组的inclusive_scan.   sdata ∈ block shared memory
  - sdata[threadIdx.x] = input[i]; ← 每个线程 copy input to sdata (local)

R → C → W

- T0 (T0 不干活)
- T1 (T1 把 sdata[0]+sdata[1]) → Read 在T1的W之前
- T2 (T2在step1时 sdata[2]+sdata[1]; T2在step2时 sdata[0]+sdata[2])
- T3 (T3在step1时 sdata[3]+sdata[2] → Read 在T2的W之前; T3在step2时 sdata[3]+sdata[1]) → step2时 T1的step1已结束，故 sdata[1]已更新为 sdata[0]+sdata[1]
- T4 (... step1 ... ; step2, sdata[4]+sdata[2]) → sdata[2]已被T2已更新，故不需要再加sdata[0] sdata[1] ~~sdata[2]~~
- T5

③ ⇒ T4已在step2计算完.

T8:



② ⇒ T6已在step1计算完.

代码 ⇒

thread_lane, volatile T* sdata.

if (thread_lane >= 1) sdata[threadIdx.x] = sdata[threadIdx.x - 1] + sdata[threadIdx.x];

if (thread_lane >= 2) sdata[threadIdx.x] = sdata[threadIdx.x - 2] + sdata[threadIdx.x];

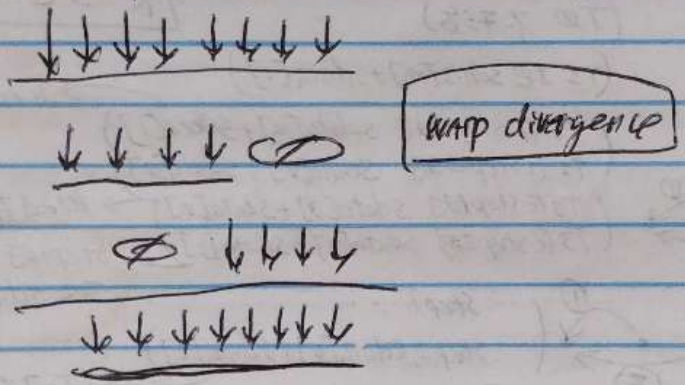
if (thread_lane >= 4) sdata[threadIdx.x] = sdata[threadIdx.x - 4] + sdata[threadIdx.x];

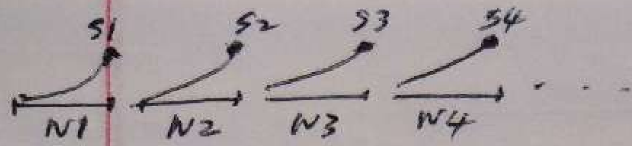
if (thread_lane >= 8) sdata[threadIdx.x] = sdata[threadIdx.x - 8] + sdata[threadIdx.x];

if (thread_lane >= 16) sdata[threadIdx.x] = sdata[threadIdx.x - 16] + sdata[threadIdx.x];

当 thread_lane：当前thread在32条threads (warp) 中的第n条 (warpSize-1)

= 3时，前3条if也会执行，其之后不会执行，但是会跟当thread同步到这条后





- 当每个warp计算出最后累加和之后则传给 carry.

(除了第1个warp之外)

- 对S1, S2, S3, S4 ⇒ 也计算 inclusive_scan

  因为 W2 需要加上S1 ⇒ S1+S2

  W3 需要加上 S1+S2 ⇒ ~~S1+~~. inplace_scan. carry.

  W4 需要加上 S1+S2+S3.

- 从W2开始每个元素都要加 S1.

  W3开始每个元素都要加 S1+S2.

  W4开始每个元素都要加 S1+S2+S3.

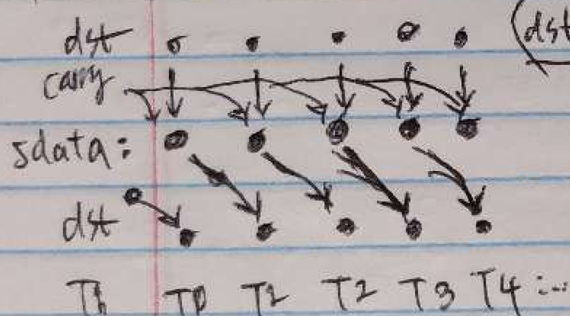
← 只会loop一次

```
for( i=begin; i<end; i+=32) {
    sdata[threadIdx.x] = carry + dst[i];
    dst[i] = (thread_lane==0) ? first[warp_lane] : sdata[threadIdx.x - 1];
}
```

前个线程已帮忙计算了.

sdata: block thread shared memory

(dst[i] 已累加过)



WARPS_PER_BLOCK = BLOCK_SIZE/WARP_SIZE = 256/32 = 8.

block 8

32 threads … warp

BLOCK_SIZE/warp_SIZE

一个block 有8个warp

每一个warp 一个carry

carry →

warp中第0个线程 threadlane=0 才更新

global thread index

8

一个warp 处理一个块的大小 n/warpsize

num_iters = units# / warps#

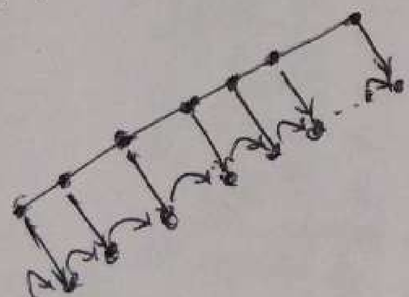
多个处理 的数据

per-warp size

假设为1

thread_size = WARP_SIZE*num_iters;

threadlane / warp中第n个thread = threadIdx.x & warp_size (%32)



warp_id = global thread id / warp_size

warp_lane = threadIdx.x / warp_size (block中第几个warp) (全局第n个warp local)

base = warp_id * thread_size = 32*warp_id

base

global thread index

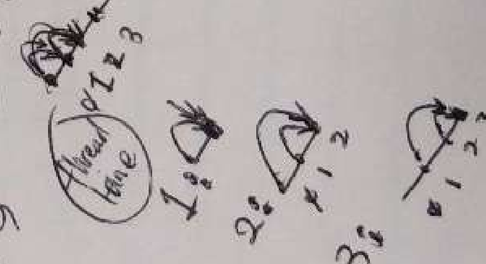
分治法

左右两边各自计算

carry

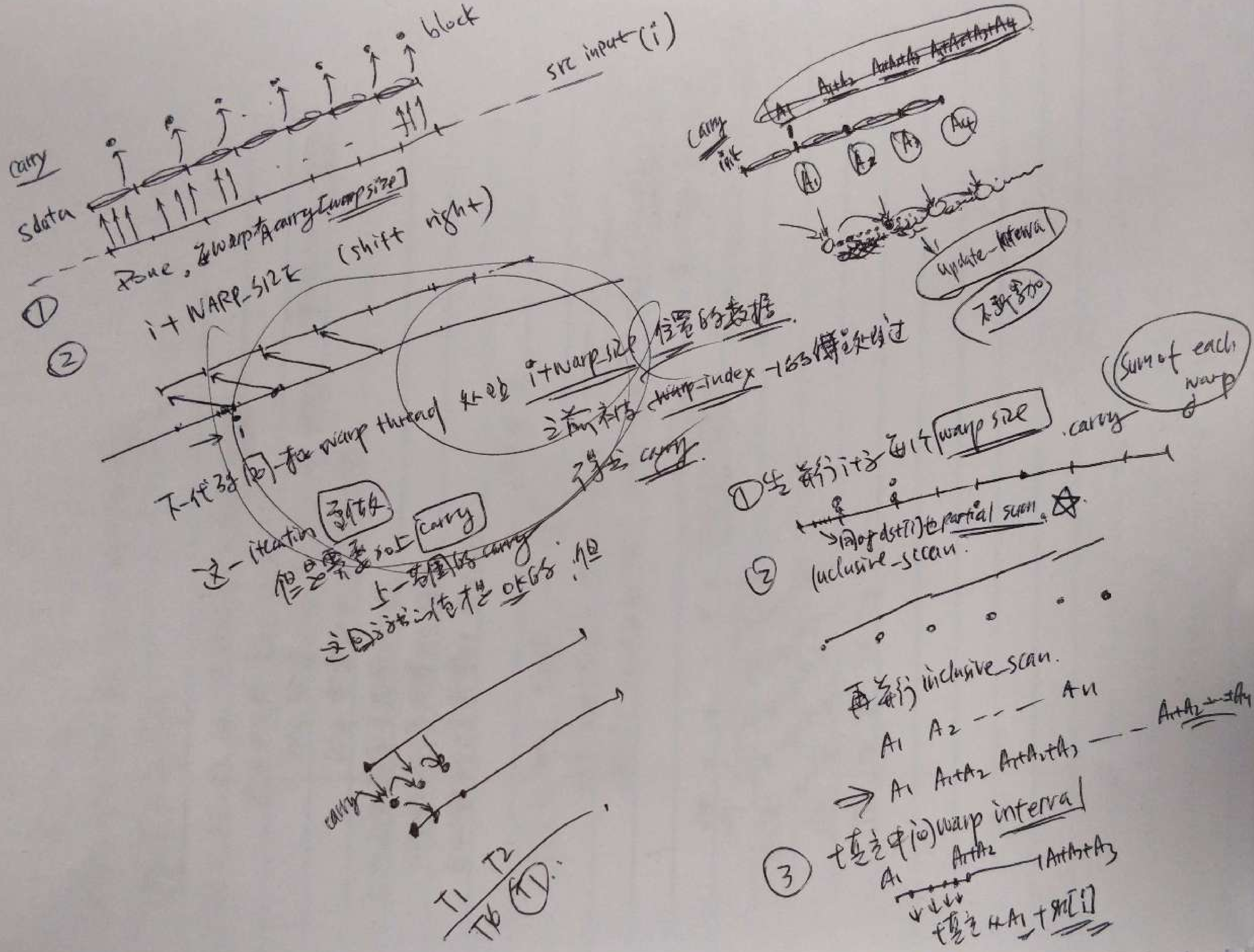
每个warp 各自计算

warp中的线程执行 data 决定 carry[warp_lane]

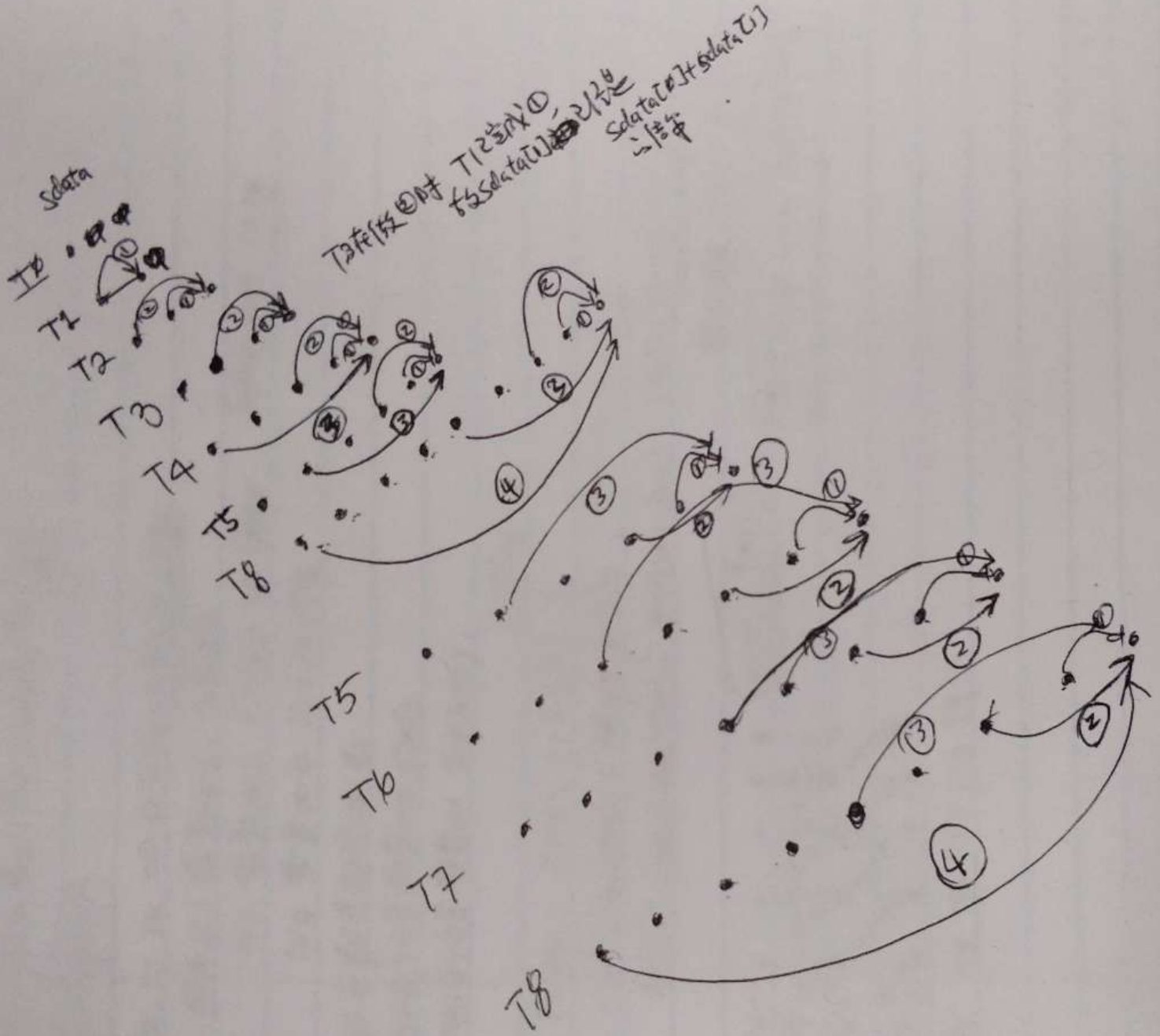


thread lane 0 1 2 3

1. 2. 3.



block
src input (i)
carry
sdata
done，每warp有carry[warp size]
(shift right)
①
i + WARP_SIZE
②
一个warp thread 处理 i+warp_size 位置的数据
得出 carry
这一iteration
但是需要加上 carry
上一轮的 carry
carry
carry
init
A1
A2
A3
A4
A1+A2
update_interval
不断累加
sum of each warp
① 先进行计算每个 warp size .carry
partial scan
② inclusive_scan
再进行 inclusive_scan
A1 A2 - - - An
A1 A1+A2 A1+A2+A3 - - - A1+A2+...+A4
③ 填充中间 warp interval
A1
A1+A2
A1+A2+A3
T1 T2
T6 T1


Sdata
T1
T2
T3
T4
T5
T8
T5
T6
T7
T8