

Introduction to Static Single Assignment (SSA)

(Slide content courtesy of Seth Goldstein.)

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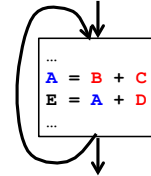
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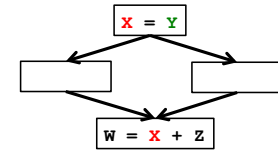
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Recurring Theme: Where Is a Variable Defined or Used?

- **Example: Loop-Invariant Code Motion**
 - Are **B**, **C**, and **D** only defined outside the loop?
 - Other definitions of **A** inside the loop?
 - Uses of **A** inside the loop?



- **Example: Copy Propagation**
 - For a given use of **X**:
 - Are all reaching definitions of **X**:
 - copies from same variable: e.g., **X** = **Y**
 - Where **Y** is not redefined since that copy?
 - If so, substitute use of **X** with use of **Y**



- It would be nice if we could *traverse directly* between related uses and def's
 - this would enable a form of *sparse* code analysis (skip over "don't care" cases)

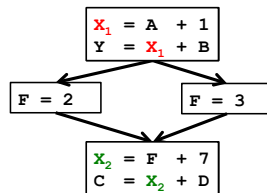
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Appearances of Same Variable Name May Be Unrelated



- The values in reused storage locations may be provably independent
 - in which case the compiler can optimize them as separate values
- Compiler could use renaming to make these different versions more explicit

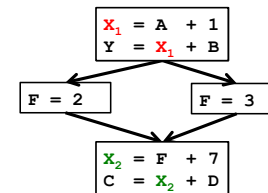
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Definition-Use and Use-Definition Chains



- **Use-Definition (UD) Chains:**
 - for a given definition of a variable X, what are all of its uses?
- **Definition-Use (DU) Chains:**
 - for a given use of a variable X, what are all of the reaching definitions of X?

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Unfortunately DU and UD Chains Can Be Expensive

```
foo(int i, int j) {
    ...
    switch (i) {
        case 0: x=3; break;
        case 1: x=1; break;
        case 2: x=6; break;
        case 3: x=7; break;
        default: x = 11;
    }
    switch (j) {
        case 0: y=x+7; break;
        case 1: y=x+4; break;
        case 2: y=x-2; break;
        case 3: y=x+1; break;
        default: y=x+9;
    }
    ...
}
```

In general,
 N defs
 M uses
 $\Rightarrow O(NM)$ space and time

One solution: limit each variable to ONE definition site

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Unfortunately DU and UD Chains Can Be Expensive

```
foo(int i, int j) {
    ...
    switch (i) {
        case 0: x=3; break;
        case 1: x=1; break;
        case 2: x=6;
        case 3: x=7;
        default: x = 11;
    }
    x1 is one of the above x's
    switch (j) {
        case 0: y=x1+7;
        case 1: y=x1+4;
        case 2: y=x1-2;
        case 3: y=x1+1;
        default: y=x1+9;
    }
    ...
}
```

One solution: limit each variable to ONE definition site

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Static Single Assignment (SSA)

- Static single assignment is an IR where every variable is assigned a value at most once in the program text
- Easy for a basic block (reminiscent of Value Numbering):
 - Visit each instruction in program order:
 - LHS: assign to a fresh version of the variable
 - RHS: use the most recent version of each variable

<pre>a ← x + y b ← a + x a ← b + 2 c ← y + 1 a ← c + a</pre>	→	<pre>a₁ ← x + y b₁ ← a₁ + x a₂ ← b₁ + 2 c₁ ← y + 1 a₃ ← c₁ + a₂</pre>
--	---	--

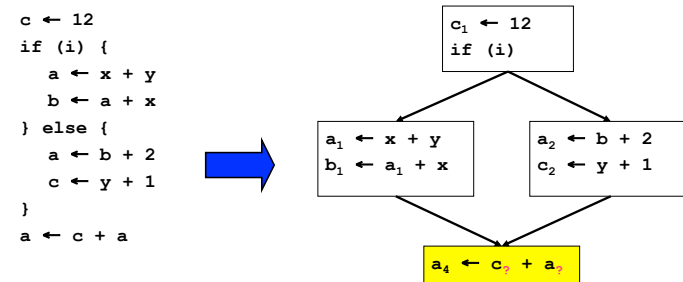
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What about Joins in the CFG?



→ Use a notational fiction: a Φ function

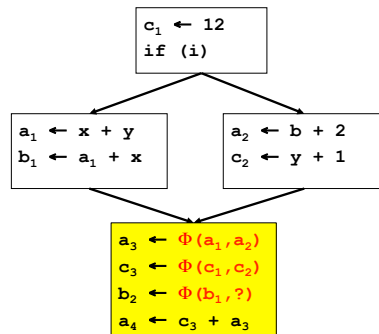
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Merging at Joins: the Φ function



The Φ function

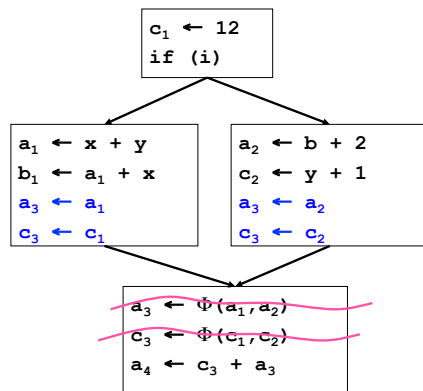
- Φ merges multiple definitions along multiple control paths into a single definition.

- At a basic block with p predecessors, there are p arguments to the Φ function.

$$x_{\text{new}} \leftarrow \Phi(x_1, x_1, x_1, \dots, x_p)$$

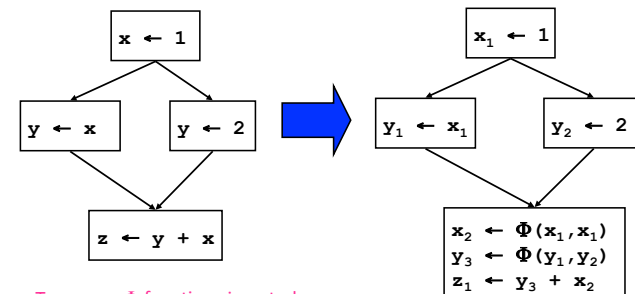
- How do we choose which x_i to use?
 - We don't really care!
 - If we care, use moves on each incoming edge

"Implementing" Φ



Trivial SSA

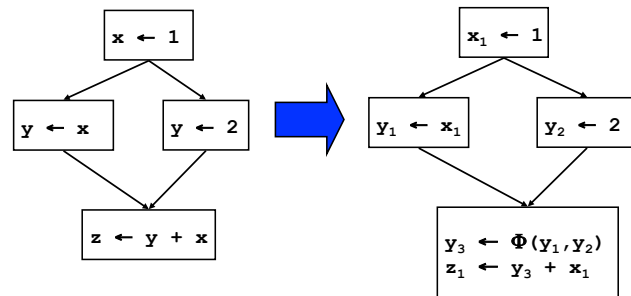
- Each assignment generates a fresh variable.
- At each join point insert Φ functions for all live variables.



Too many Φ functions inserted.

Minimal SSA

- Each assignment generates a fresh variable.
- At each join point insert Φ functions for **all live variables** with **multiple outstanding defs.**



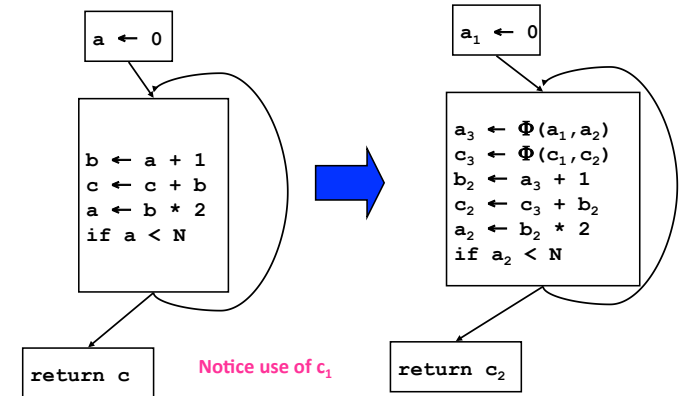
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Another Example



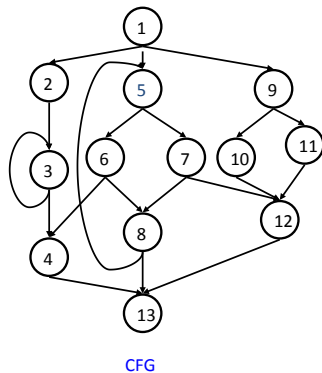
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When Do We Insert Φ ?



If there is a def of **a** in block **5**, which nodes need a $\Phi()$?

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When do we insert Φ ?

- We insert a Φ function for variable **A** in block **Z** iff:
 - A** was defined more than once before
 - (i.e., **A** defined in **X** and **Y** AND **X** \neq **Y**)
 - There exists a non-empty **path from x to z**, P_{xz} , and a non-empty **path from y to z**, P_{yz} , s.t.
 - $P_{xz} \cap P_{yz} = \{z\}$
 - $z \notin P_{xq}$ or $z \notin P_{yr}$ where $P_{xz} = P_{xq} \rightarrow z$ and $P_{yz} = P_{yr} \rightarrow z$
- Entry block contains an implicit def of all vars
- Note: $A = \Phi(\dots)$ is a def of **A**

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Dominance Property of SSA

- In SSA, **definitions dominate uses**.
 - If x_i is used in $x \leftarrow \Phi(\dots, x_i, \dots)$, then $BB(x_i)$ dominates i^{th} predecessor of $BB(\Phi)$
 - If x is used in $y \leftarrow \dots x \dots$, then $BB(x)$ dominates $BB(y)$
- We can use this for an **efficient algorithm to convert to SSA**

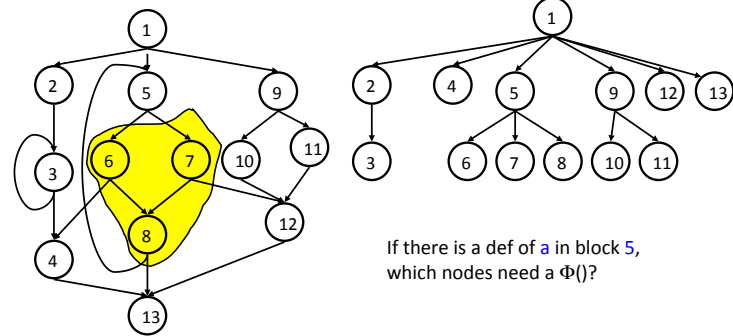
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Dominance



If there is a def of a in block 5,
which nodes need a $\Phi()$?

CFG

D-Tree

x strictly dominates w ($x \text{ sdom } w$) iff $x \text{ dom } w$ AND $x \neq w$

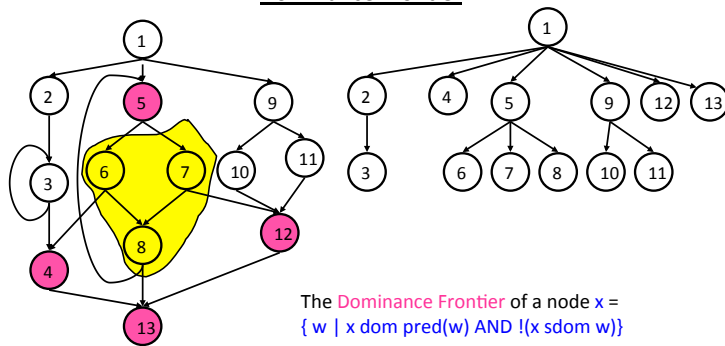
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Dominance Frontier



The **Dominance Frontier** of a node x =
 $\{ w \mid x \text{ dom pred}(w) \text{ AND } !(x \text{ sdom } w) \}$

CFG

D-Tree

x strictly dominates w ($x \text{ sdom } w$) iff $x \text{ dom } w$ AND $x \neq w$

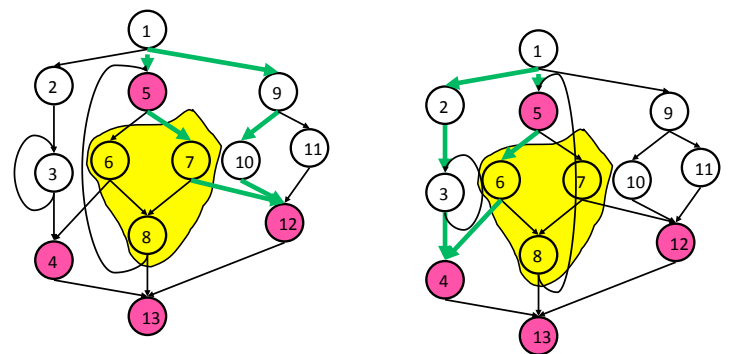
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Dominance Frontier and Path Convergence



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Using Dominance Frontier to Compute SSA

- place all $\Phi()$
- Rename all variables

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Using Dominance Frontier to Place $\Phi()$

- Gather all the defsites of every variable
- Then, for every variable
 - foreach defsits
 - foreach node in $\text{DominanceFrontier}(\text{defsits})$
 - if we haven't put $\Phi()$ in node, then put one in
 - if this node didn't define the variable before, then add this node to the defsits
- This essentially computes the **Iterated Dominance Frontier** on the fly, inserting the minimal number of $\Phi()$ necessary

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Using Dominance Frontier to Place $\Phi()$

```
foreach node n {
  foreach variable v defined in n {
    orig[n] U= {v}
    defsits[v] U= {n}
  }
}
foreach variable v {
  W = defsits[v]
  while W not empty {
    n = remove node from W
    foreach y in DF[n]
      if y  $\notin$  PHI[v] {
        insert " $v \leftarrow \Phi(v, v, \dots)$ " at top of y
        PHI[v] = PHI[v] U {y}
        if v  $\notin$  orig[y]: W = W U {y}
      }
  }
}
```

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Renaming Variables

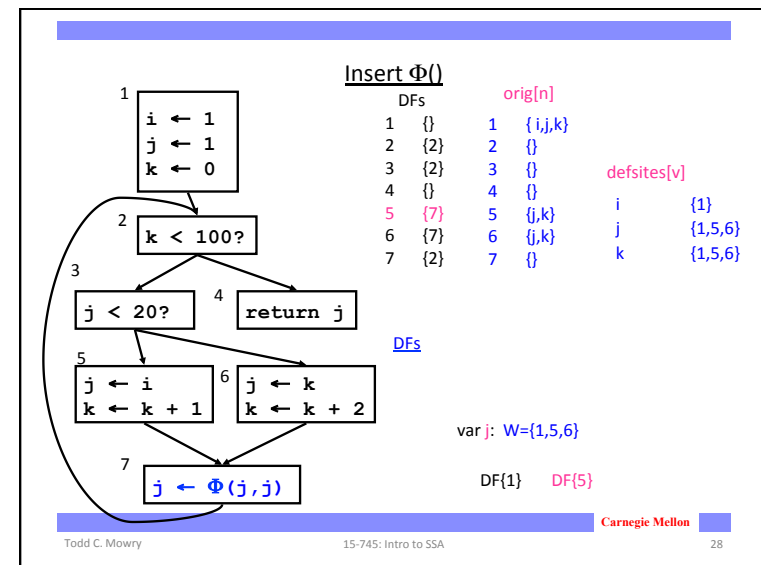
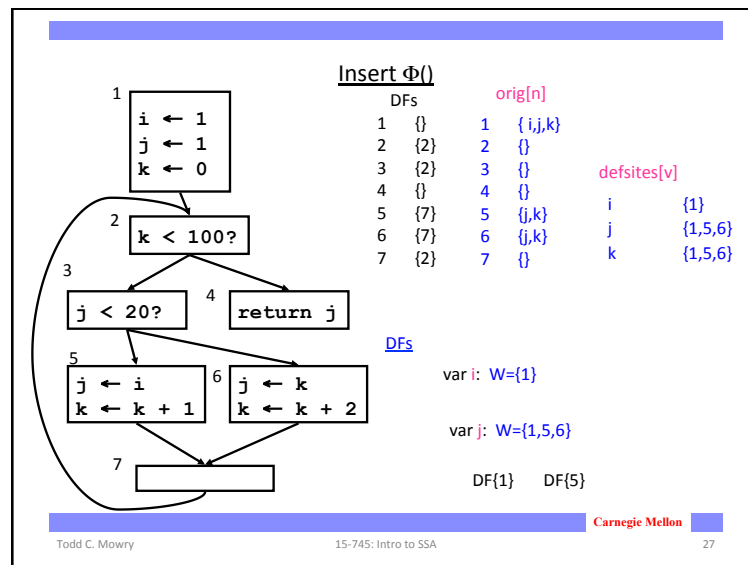
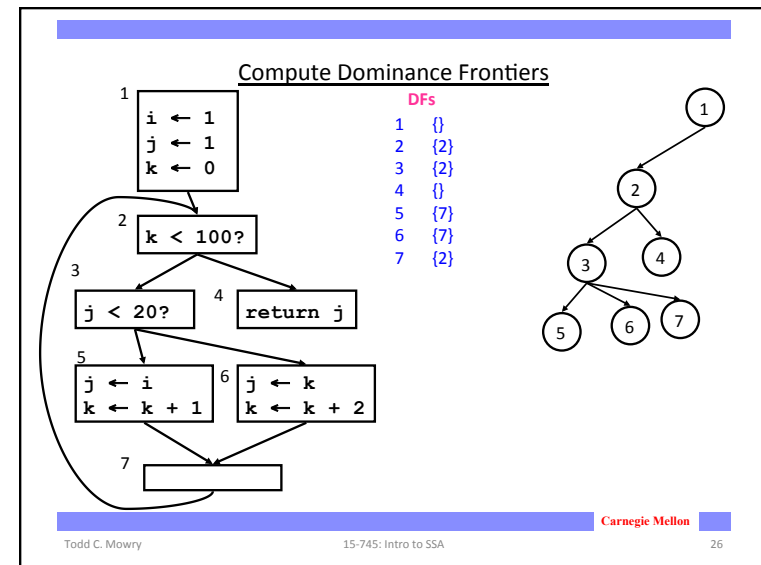
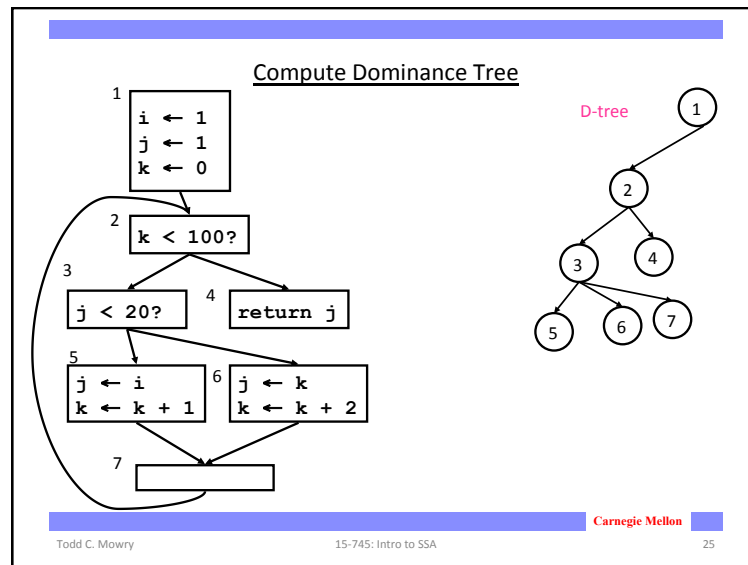
- Algorithm:
 - Walk the D-tree, renaming variables as you go
 - Replace uses with more recent renamed def
- For straight-line code this is easy
- What if there are branches and joins?
 - use the **closest def such that the def is above the use in the D-tree**
- Easy implementation:
 - for each var: **rename** (v)
 - **rename(v):** replace uses with top of stack
at def: push onto stack
call **rename(v)** on all children in D-tree
for each def in this block pop from stack

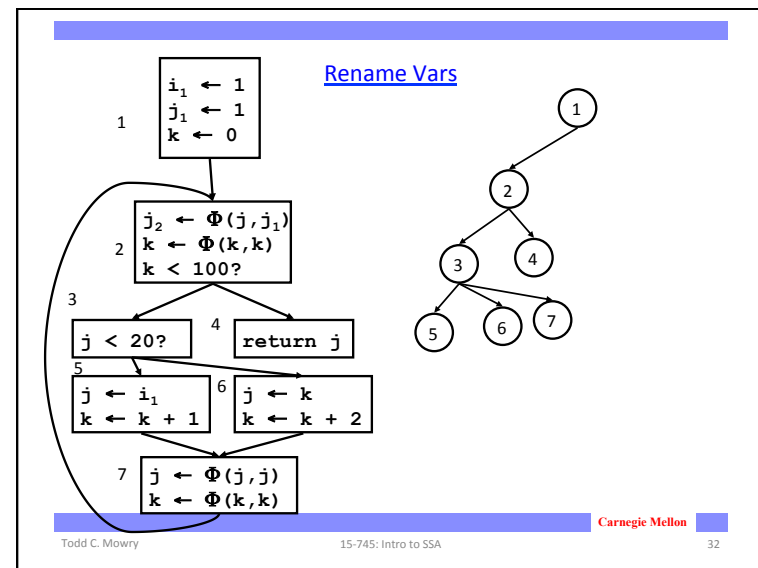
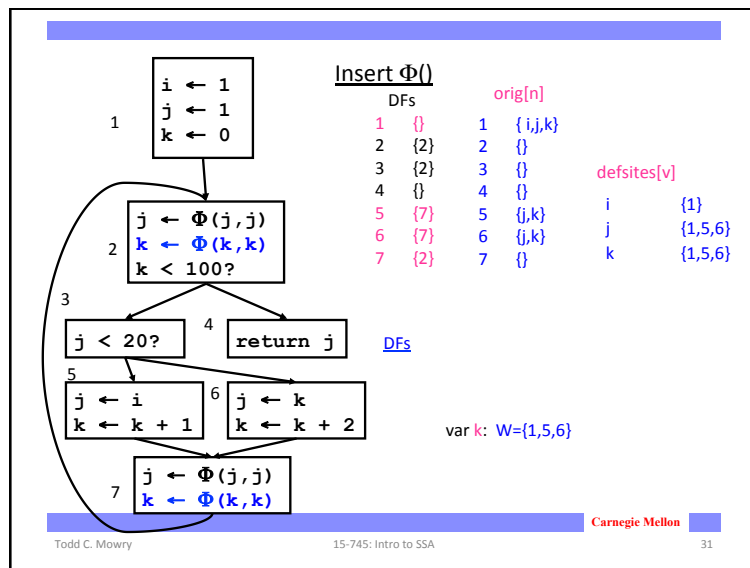
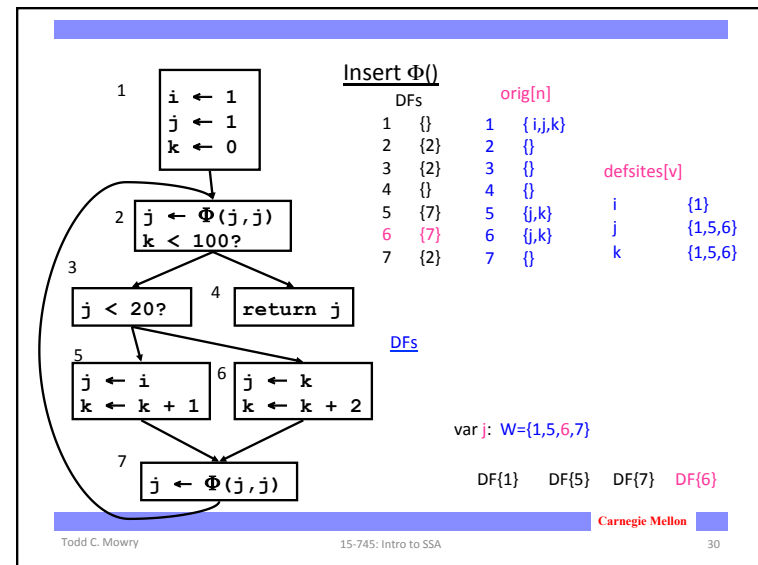
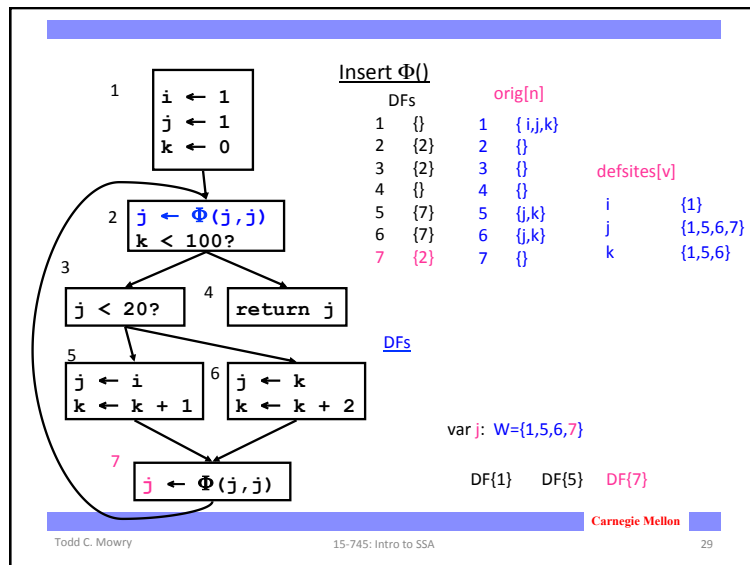
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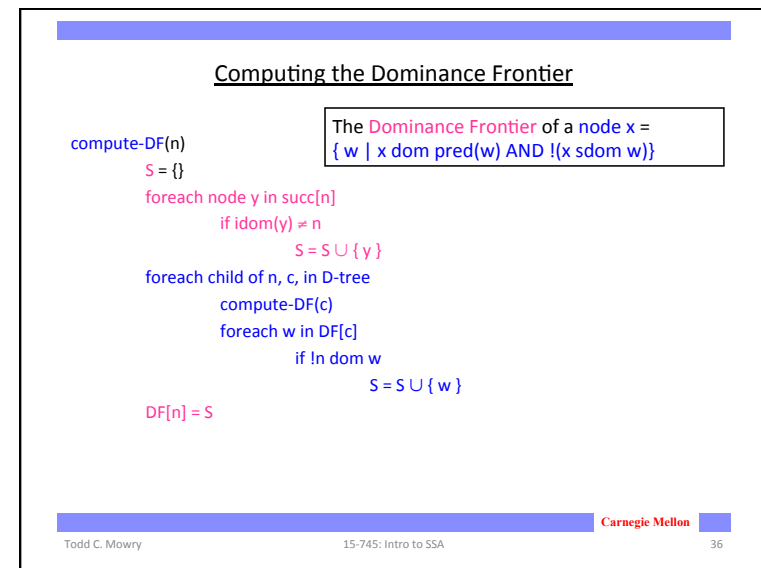
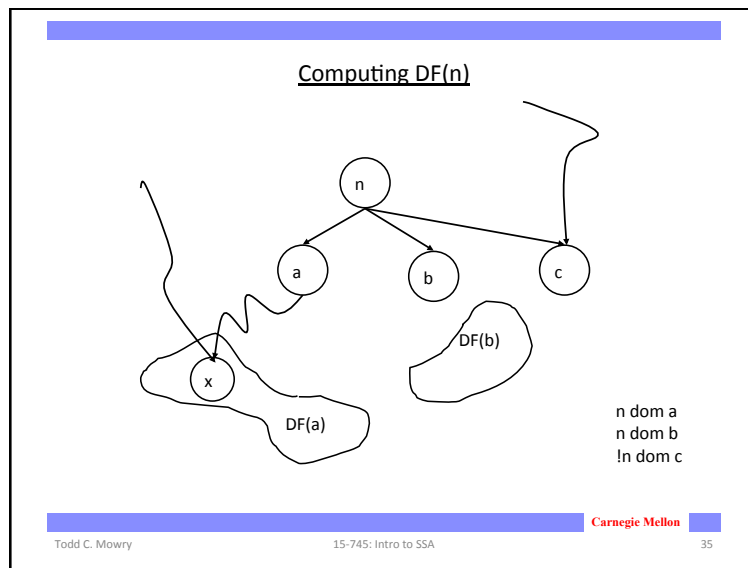
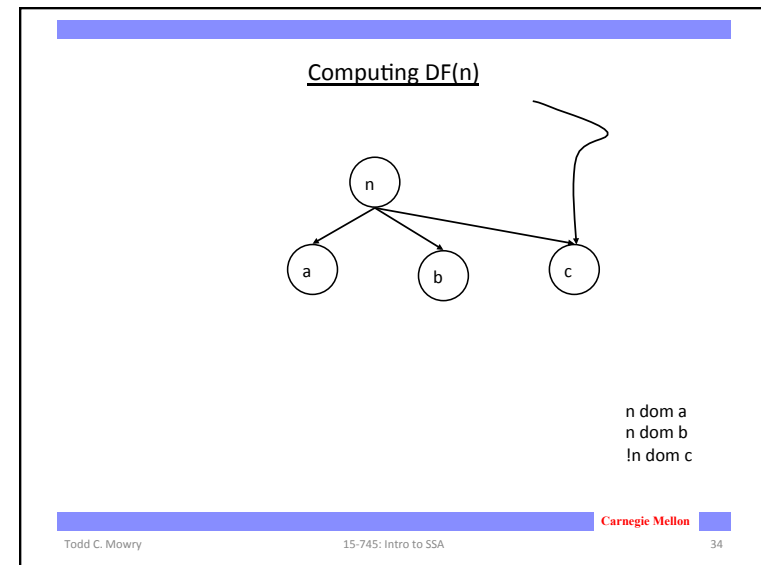
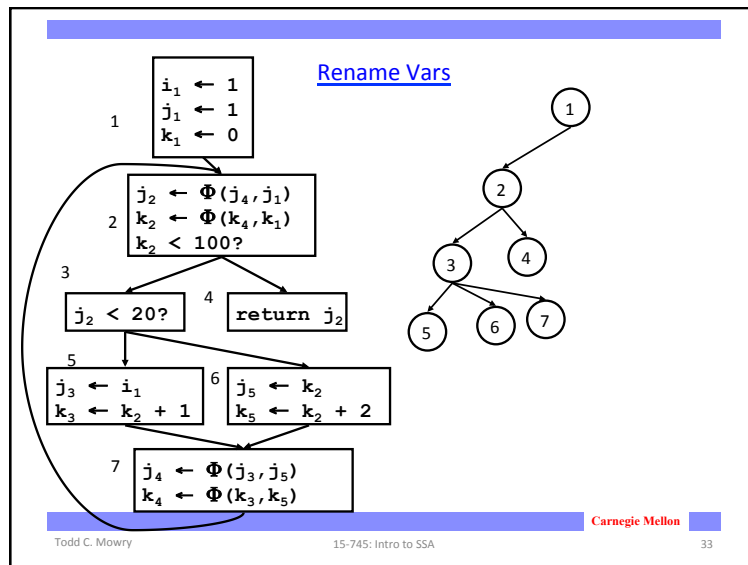
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SSA Properties

- Only 1 assignment per variable
- Definitions dominate uses