

**Title:** OpenTitan as an Integrated Chiplet RoT

**Speakers:** TBD (ZeroRISC) and Neeraj Upasani (Rivos)

**Abstract:**

OpenTitan® has striven to embody the principles of zero trust in the most straightforward way possible, by making the full logical implementation open source. The project's implementation principles of transparency, quality, and flexibility have enabled the creation of a dynamic ecosystem of compatible IP – one able to be rapidly remixed into different configurations (*i.e.* “top-levels” in project parlance) quickly.

This talk will describe how the OpenTitan project was able to move from a discrete silicon root of trust (RoT) design to an integrated top-level configuration suitable as an integrated chiplet RoT. This was made possible by the fertile collaborative soil of the lowRISC Community Interest Corporation's Silicon Commons, OpenTitan's host organization, which enabled seamless leverage of prior designs to an entirely new design space on a parallel track in under a year.

**Bio:**