

Looking Beyond Microarchitectural- Only Side Channels

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\$whoami



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"Meltdown & Spectre: 'Worst Ever' CPU bugs affect virtually all computers"

The Guardian

THE SEARCH ICEBERG



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CACHE MISSING FOR FUN AND PROFIT

Last-Level Cache Side-Channel Attacks are Practical

DRAMA: Exploiting DRAM Addressing for Cross-CPU Attacks

Peter Pessl, Daniel Gruss, Clémentine Maurice, Michael Schwarz, and Stefan Mangard,
Graz University of Technology

Port Contention for Fun and Profit

Don't Mesh Around: Side-Channel Attacks and Mitigations on Mesh Interconnects

Miles Dai, *MIT*; Riccardo Paccagnella, *University of Illinois at Urbana-Champaign*;
Miguel Gomez-Garcia, *MIT*; John McCalpin, *Texas Advanced Computing Center*;
Mengjia Yan, *MIT*

Hertzbleed: Turning Power Side-Channel Attacks Into Remote Timing Attacks on x86

Yingchen Wang*

UT Austin

Riccardo Paccagnella*

UIUC

Elizabeth Tang He

UIUC

Hovav Shacham

UT Austin

Christopher W. Fletcher

UIUC

David Kohlbrenner

UW

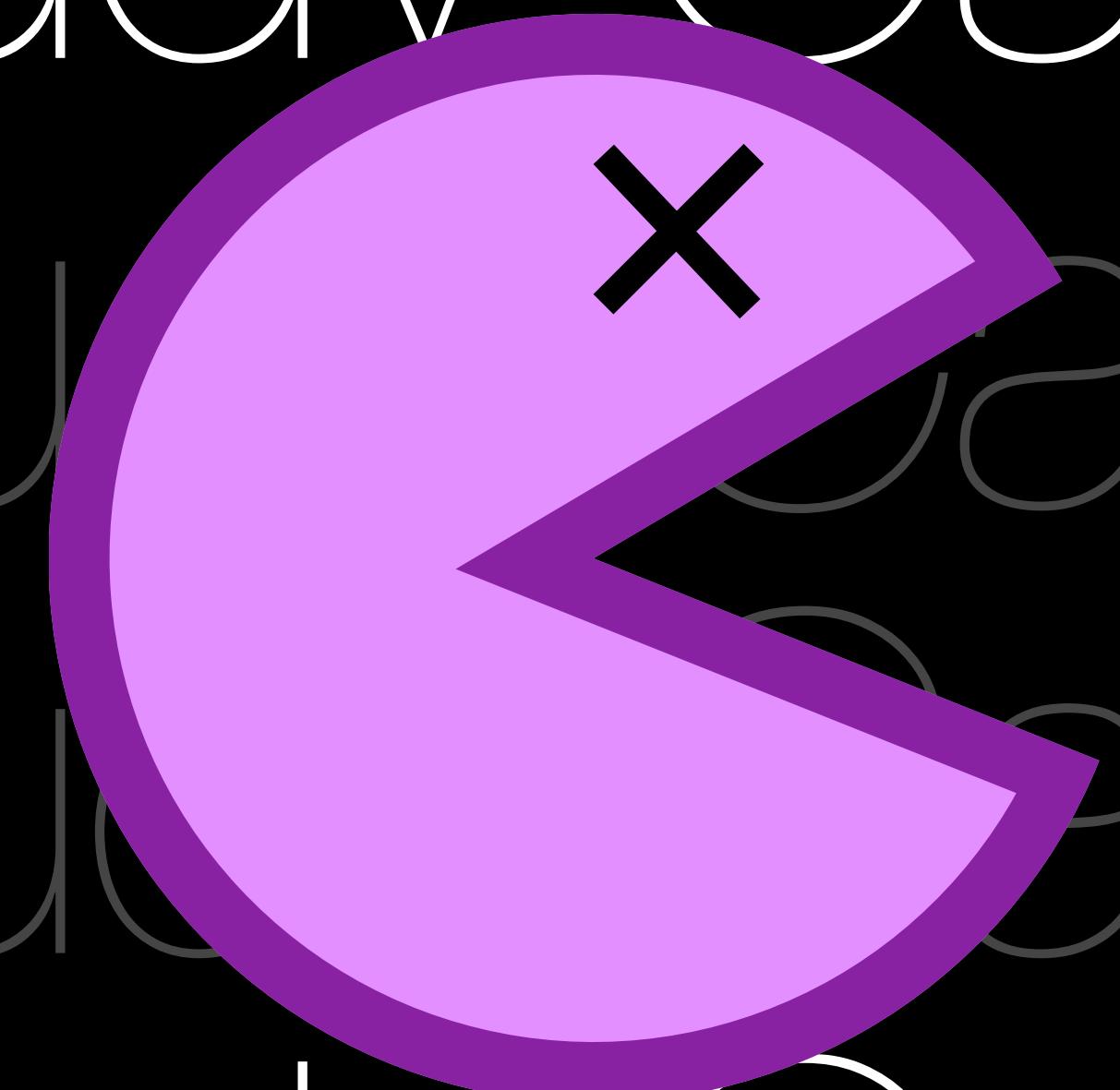
1 **Discrete, Stateful**

2 **Stateless, contention-based**

3 **Analog**

TODAY'S LANDSCAPE





PA^X MAN

ATTACKING ARM POINTER AUTHENTICATION
WITH SPECULATIVE EXECUTION

Joseph Ravichandran*, **Weon Taek Na***, **Jay Lang**, **Mengjia Yan**

*Both authors contributed equally to this work.

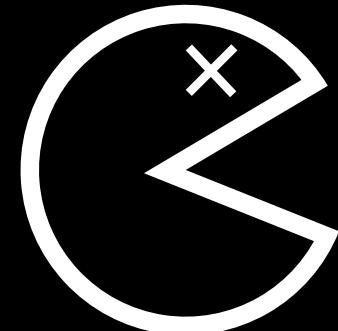


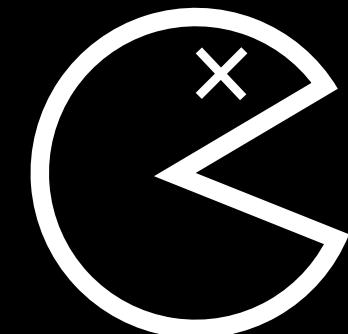
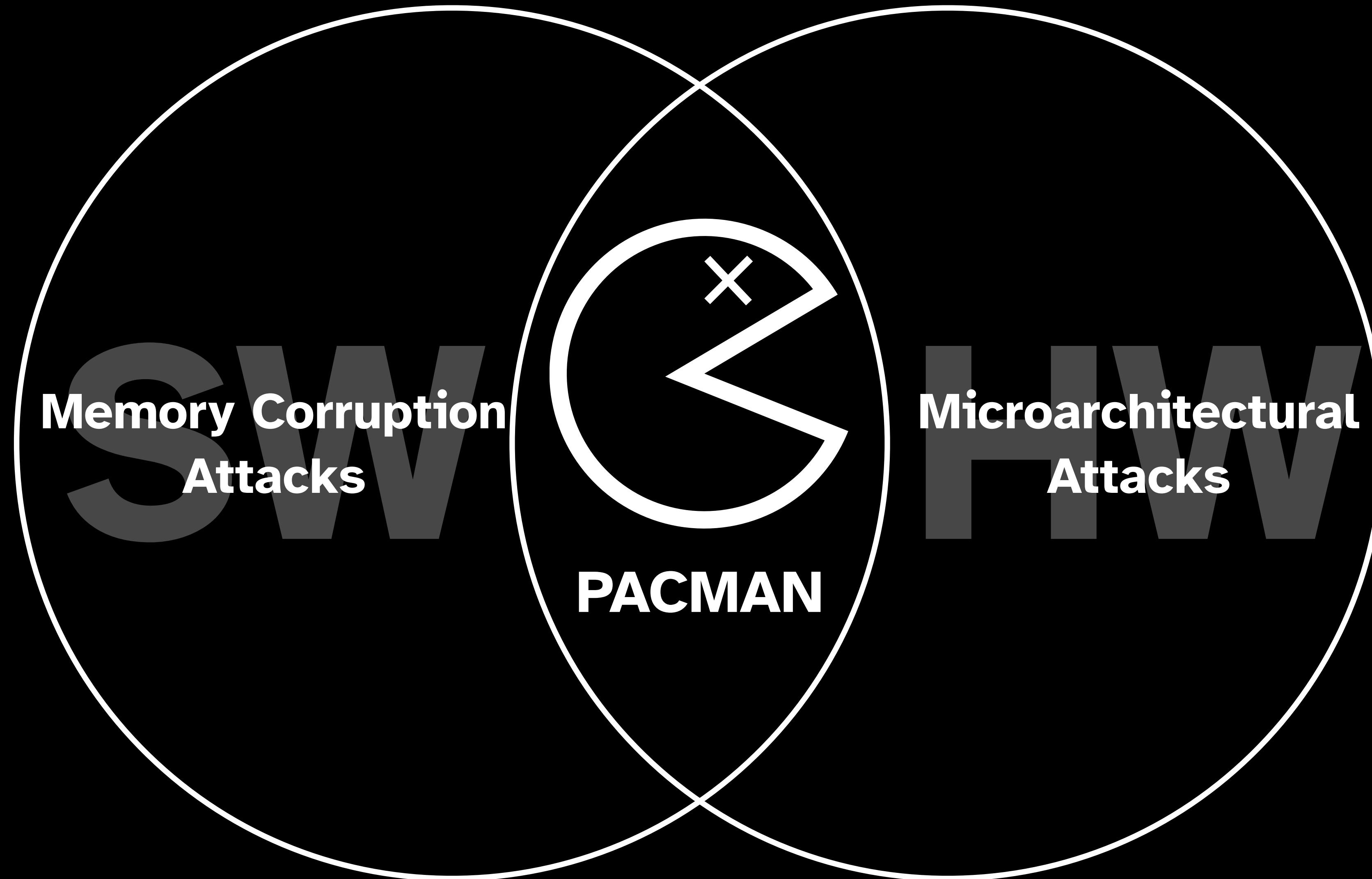
**Memory Corruption
Attacks**

A Venn diagram consisting of two overlapping circles. The left circle is labeled "SW" and contains the text "Memory Corruption Attacks". The right circle is labeled "HW" and contains the text "Microarchitectural Attacks". The intersection of the two circles is empty.

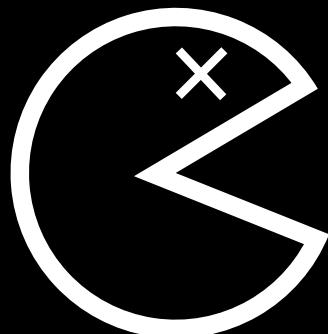
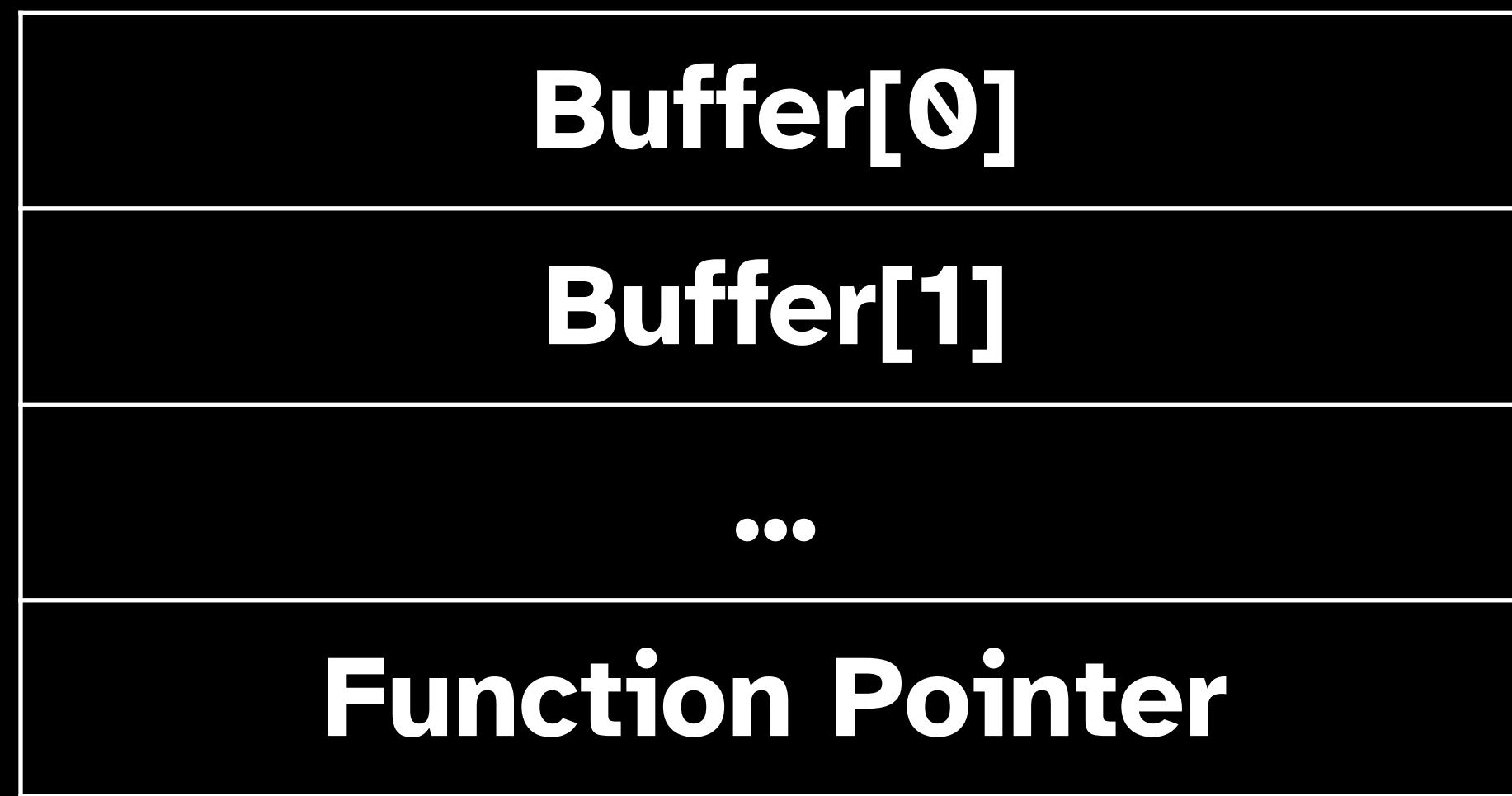
The diagram illustrates the relationship between software and hardware attacks, showing that memory corruption attacks are specific to software, while microarchitectural attacks are specific to hardware.

**Microarchitectural
Attacks**

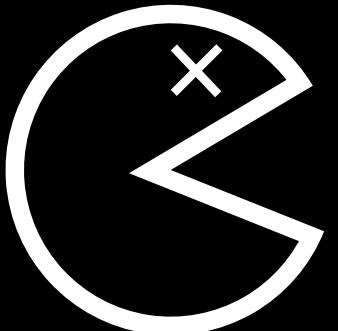
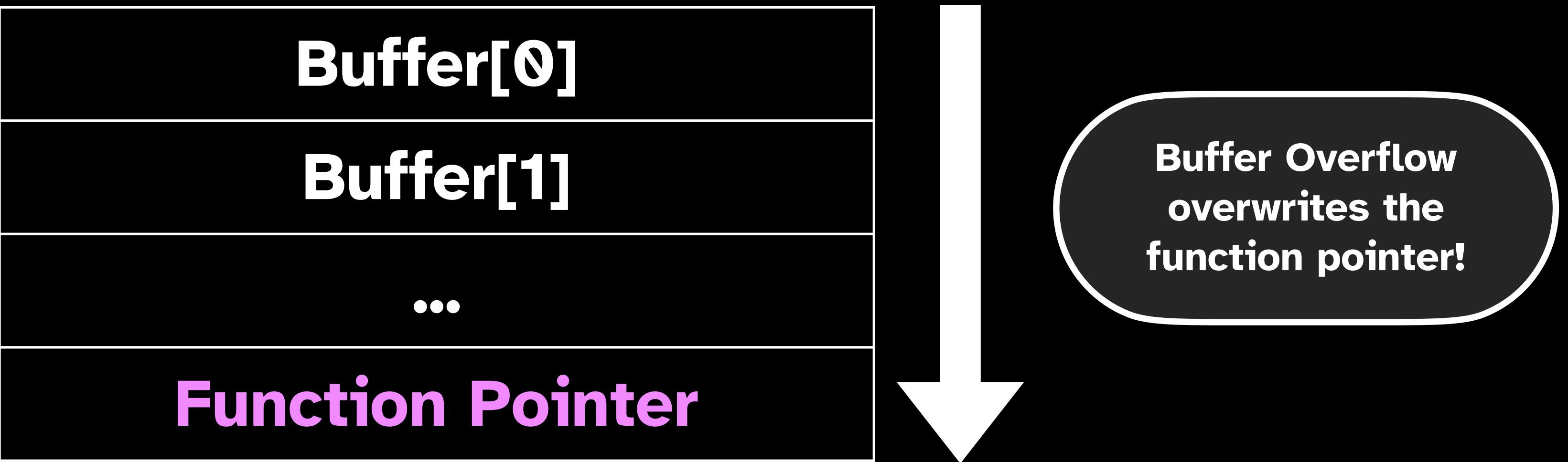




Buffer Overflow



Buffer Overflow

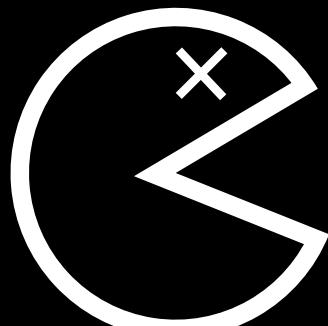
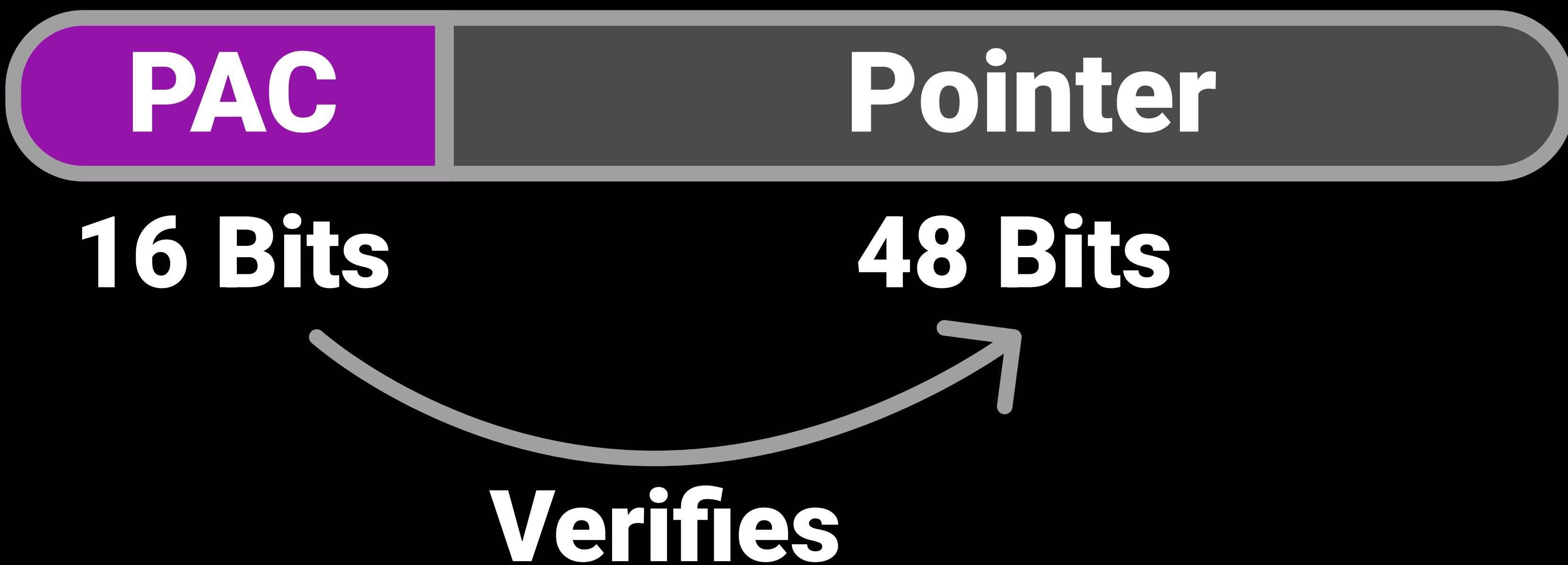




Let's fix this bug with Pointer Authentication.

ARM Pointer Authentication

PAC = crypto_fn(pointer, salt, key)



Two Operations

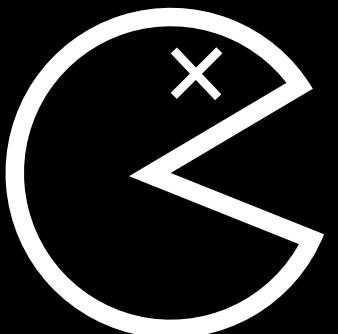
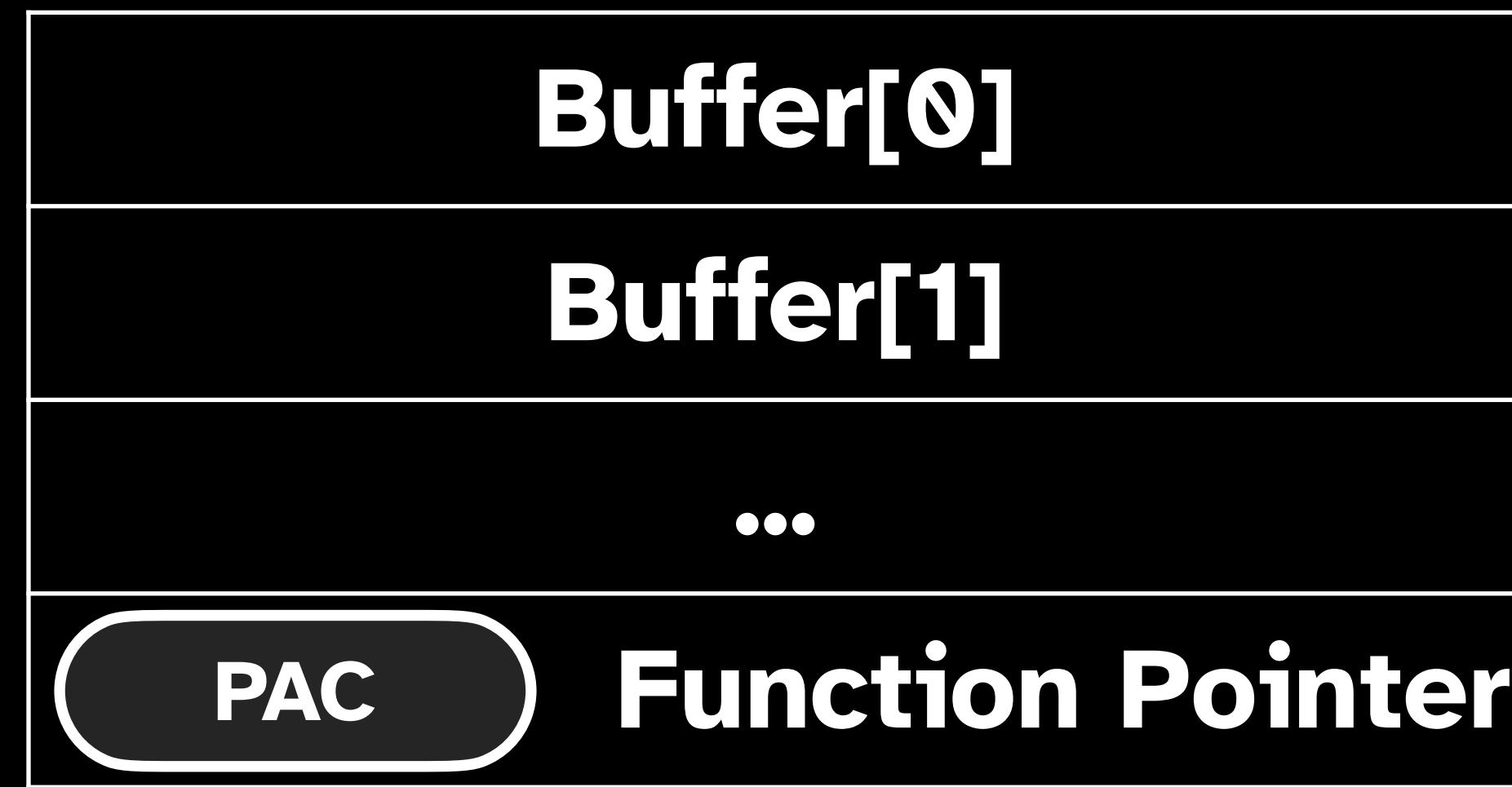
Sign

Before saving a pointer to memory, compute the PAC

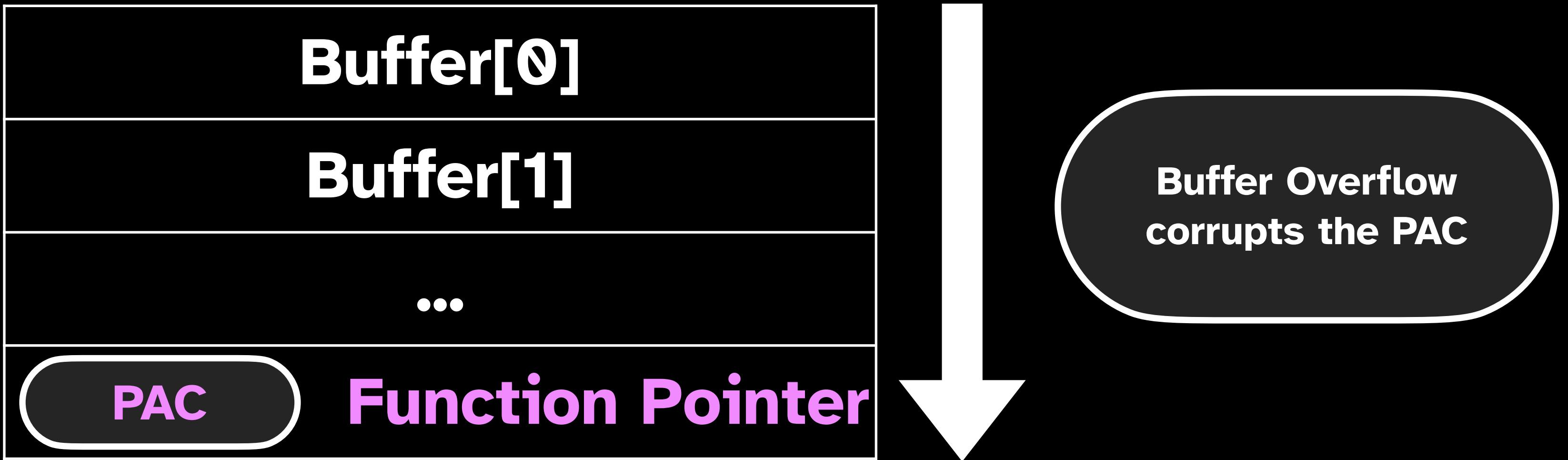
Verify

Before using a pointer, check the pointer's PAC

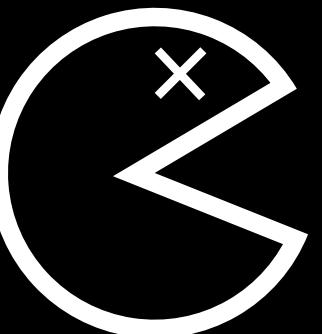
Buffer Overflow



Buffer Overflow



Invalid PAC means we **crash!**



Previous work **solely** considers memory safety threat model.



Previous work **solely** considers memory safety threat model.

Protecting Indirect Branches against Fault Attacks using ARM Pointer Authentication

PAC it up: Towards Pointer Integrity using ARM Pointer Authentication

Hans Liljestrand, Aalto University, Huawei Technologies Oy; Thomas Nyman, Aalto University; Kui Wang, Huawei Technologies Oy, Tampere University of Technology; Carlos Chinea, Tampere University

PTAuth: Temporal Memory Robust Points-to Authentication

Reza Mirzazade Farkhani, Mansour Ahmadi, and Long Lu, Northeastern University

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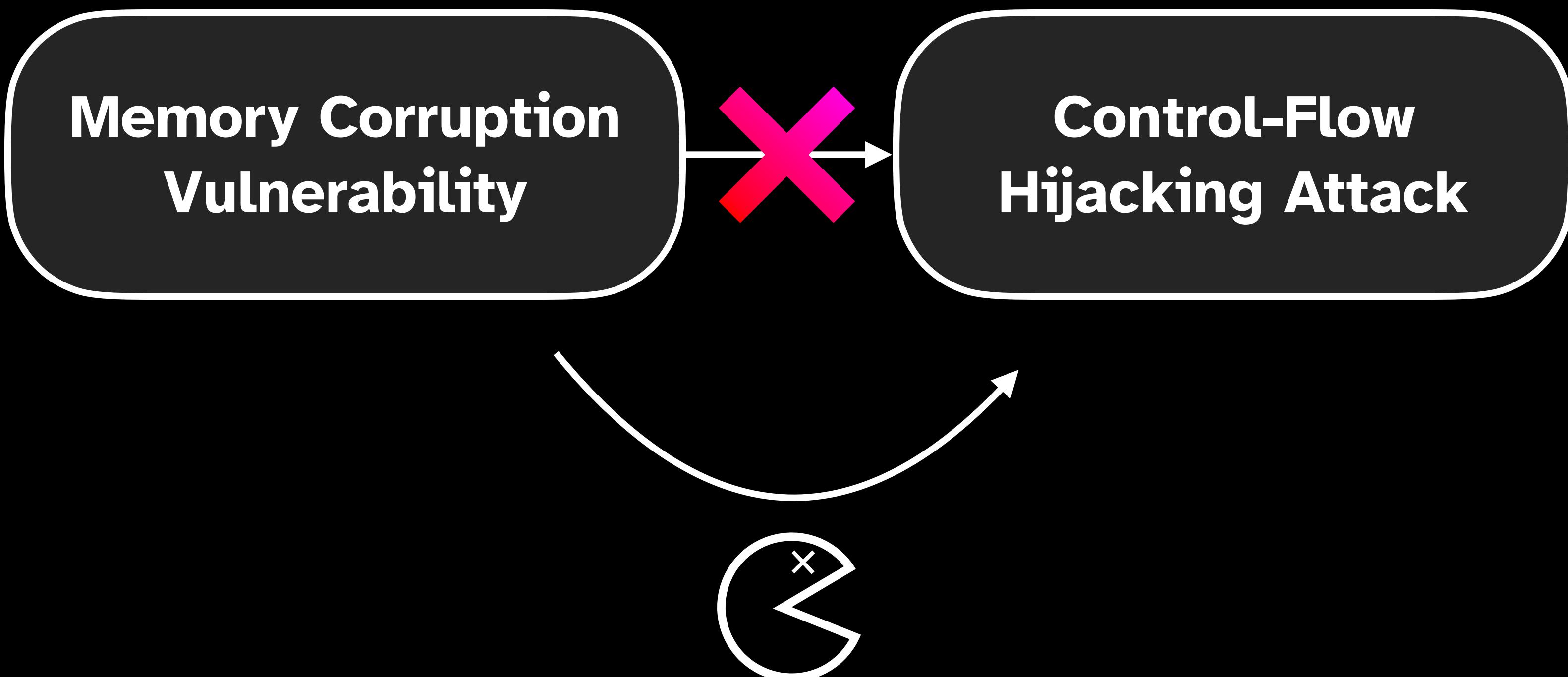
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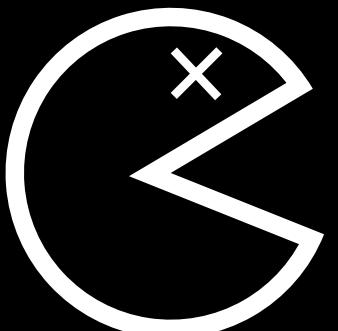
Memory Corruption

**Pointer Authentication
is the last line of defense**

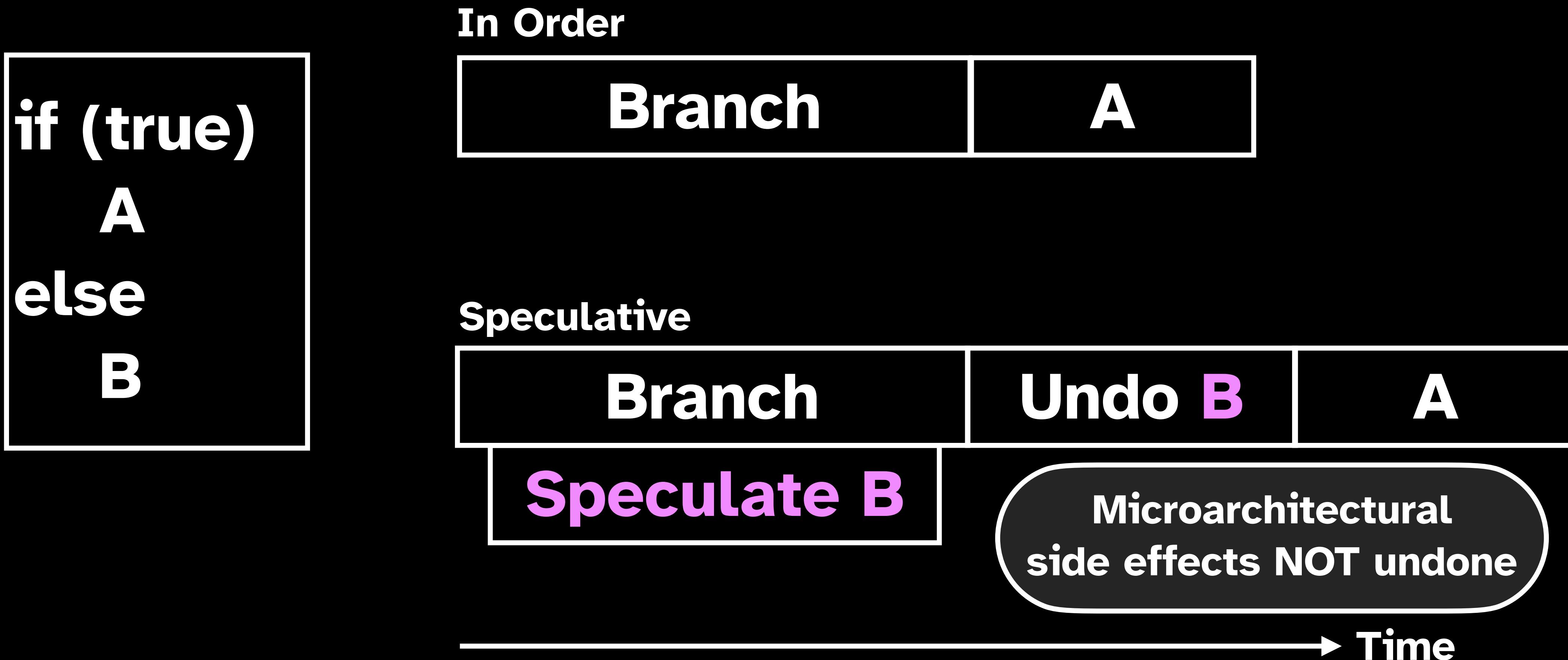


Break PAC with Hardware Attacks

- Guess a PAC **speculatively** to prevent crashes
- Leak verification results via side channel

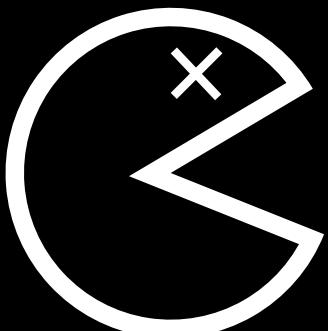


Speculative Execution



Data Gadget

```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```

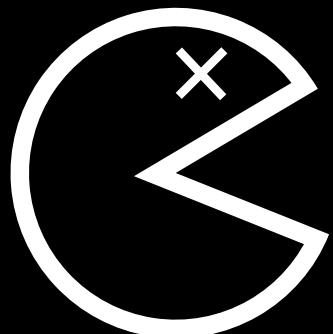


Data Attack

```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```

Correct PAC

Mispredict
Branch



Data Attack

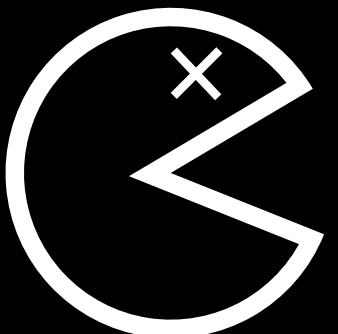
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Correct PAC

Mispredict
Branch

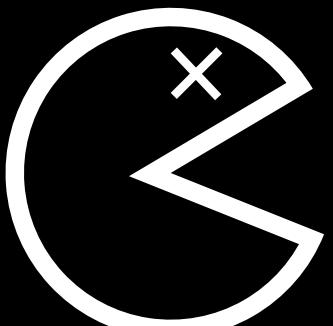


PAC Check
Succeeds



Data Attack

```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```

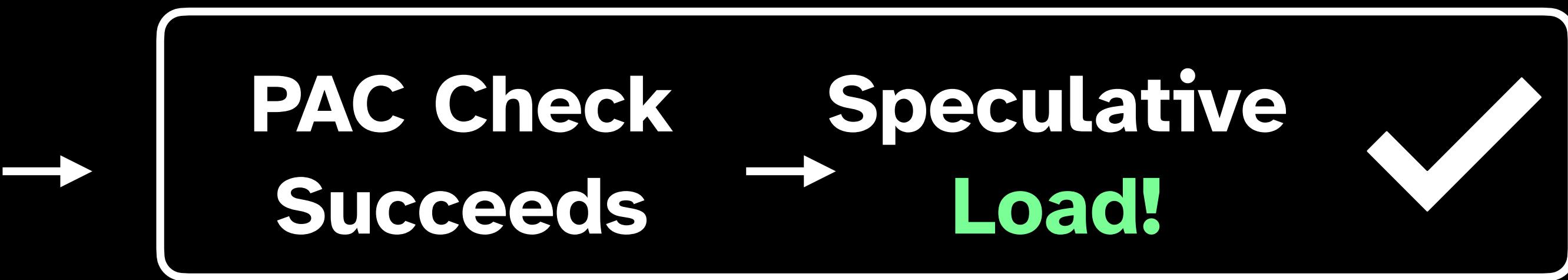


Data Attack

```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```

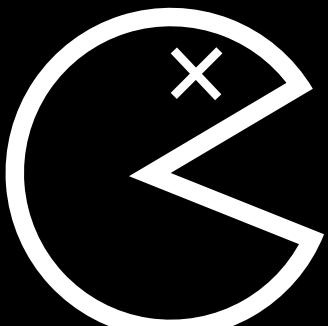
Correct PAC

Mispredict Branch



Incorrect PAC

Mispredict Branch



Data Attack

```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```

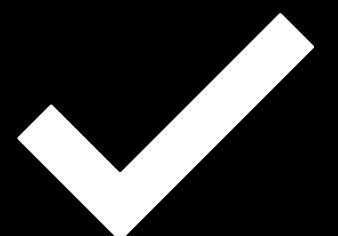
Correct PAC

Mispredict Branch



PAC Check
Succeeds

Speculative Load!

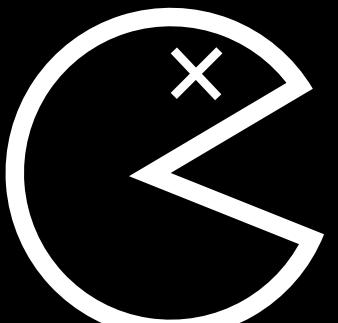


Incorrect PAC

Mispredict Branch

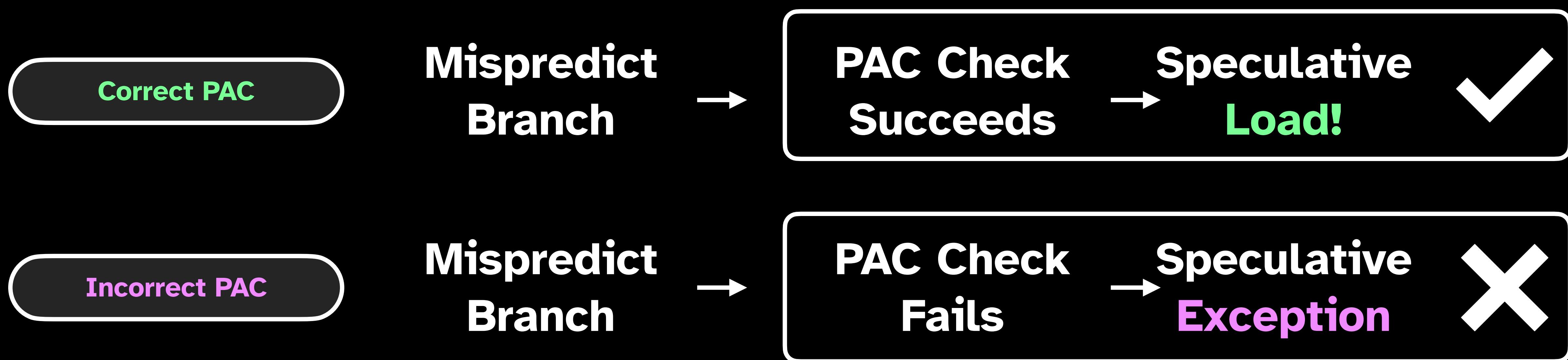


PAC Check
Fails



Data Attack

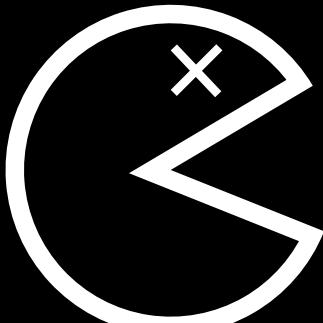
```
if (condition):  
    verified_ptr = check_pac(guess_ptr)  
    load(verified_ptr)
```



TARGET



The world's first desktop CPU
that supports Pointer Authentication.

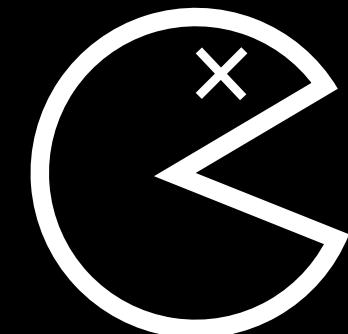
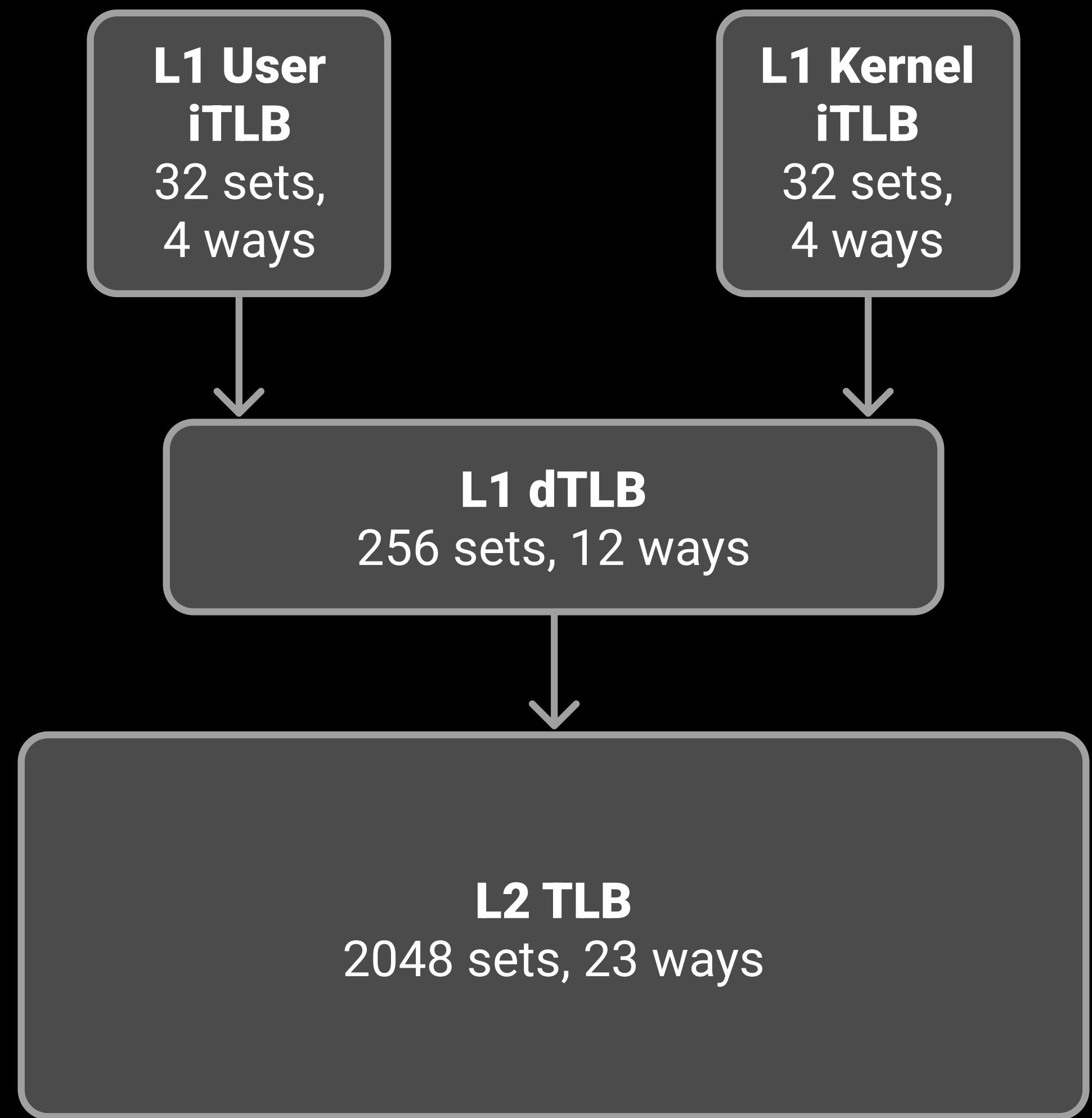


Challenges of Real World HW

- No documentation of microarchitectural details.
- No high resolution timer.
- macOS is a difficult system to integrate attacks on.

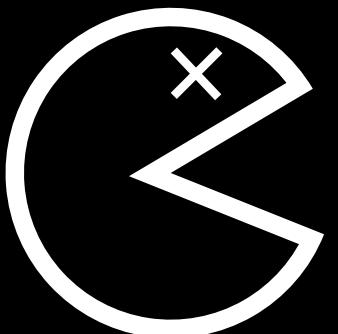
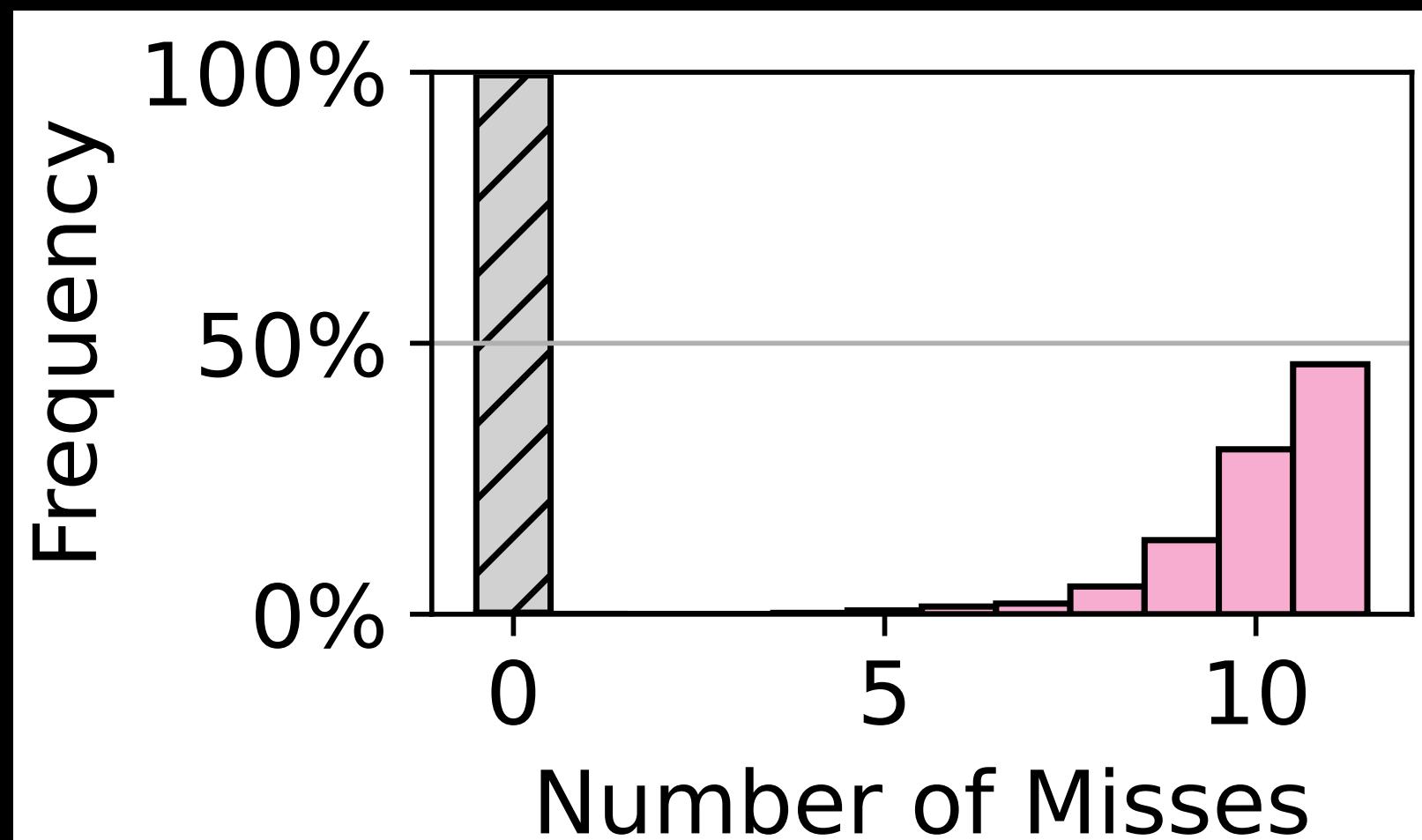
Essentially, we had to reinvent the wheel.

Conjectured TLB Hierarchy



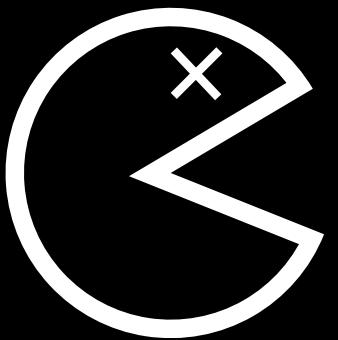
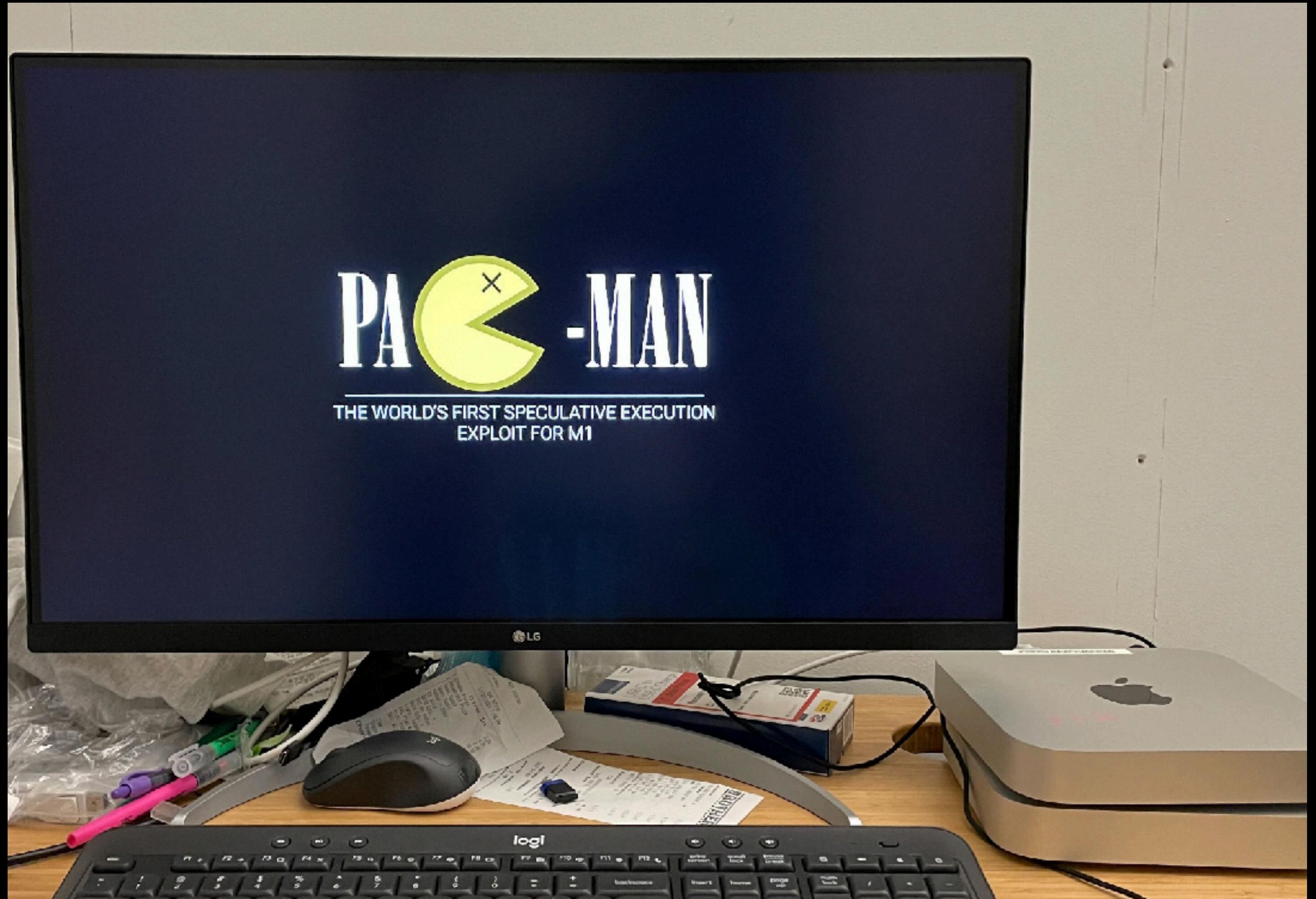
PAC Oracle Accuracy

 Incorrect PAC  Correct PAC



PacmanOS

A Rust-based bare metal environment for performing experiments.



Top news

 digitaltrends

The M1 has a big security loophole, and Apple can't patch it

4 hours ago



 TechCrunch

MIT researchers uncover 'unpatchable' flaw in Apple M1 chips

51 minutes ago



 HARDWARE

MIT Finds New Arm Vulnerability Present in Apple M1, Demos PACMAN Attack

4 hours ago



 9TO5Mac

PACMAN M1 chip attack defeats 'the last line of security' – but requires physical access

2 hours ago



All coverage

 Phoronix

Apple M1 Affected By "PACMAN" Hardware Vulnerability In Arm Pointer Authentication

4 hours ago



 VentureBeat

MIT researchers discover Apple M1 chip vulnerability

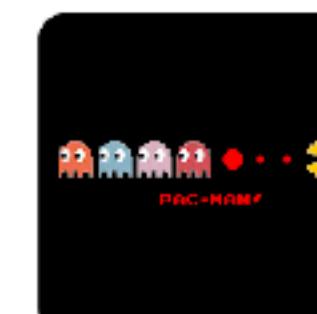
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 The Register

Apple M1 chip contains hardware vulnerability that bypasses memory defense

4 hours ago



 DARKReading

Design Weakness Discovered in Apple M1 Kernel Protections

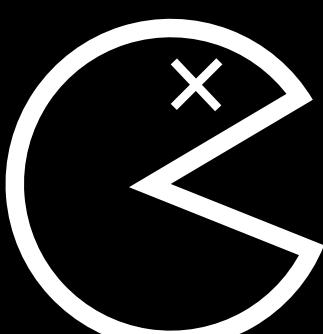
3 hours ago



 Macworld

Experts warn of 'PACMAN' flaw in M1 chip that can't be patched

1 hour ago



PACMAN @ DEF CON 30

PACMAN Attacking ARM Pointer Authentication with Speculative Execution

THE
PA MAN
ATTACK

09 Jun 2022

DEF CON 30 Talk

Our DEF CON 30 talk about PACMAN is now live. Check it out here!

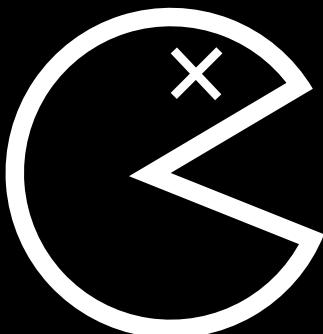
DEF CON 30 - Joseph Ravichandran - The PACMAN Attack: Break... Share

Think like an attacker.

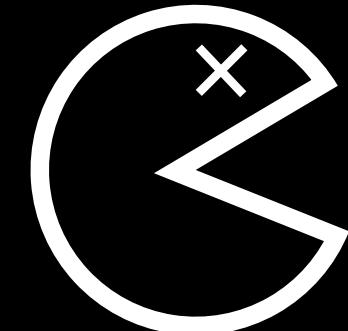
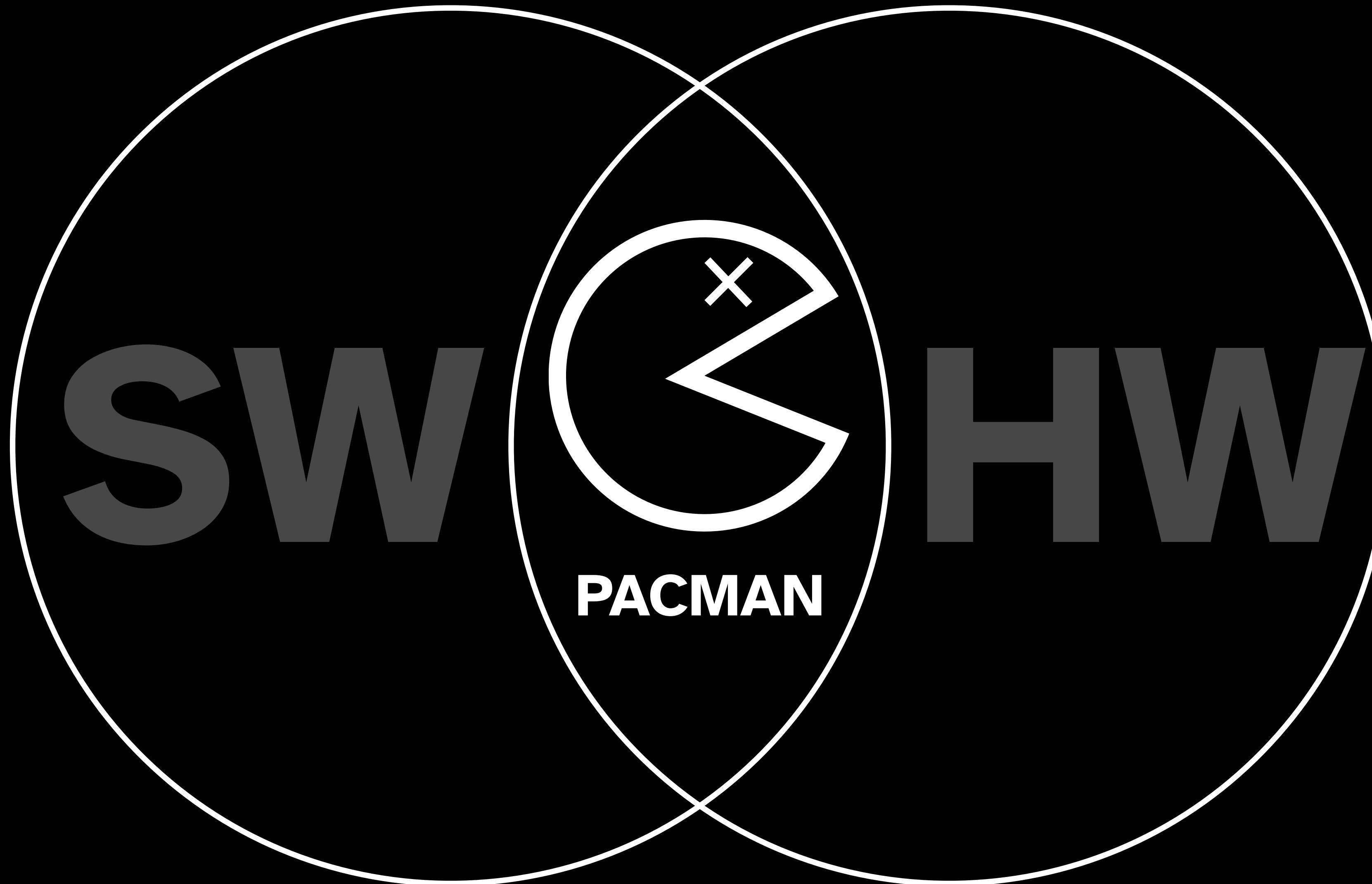
Think like a CPU designer.

Watch on YouTube

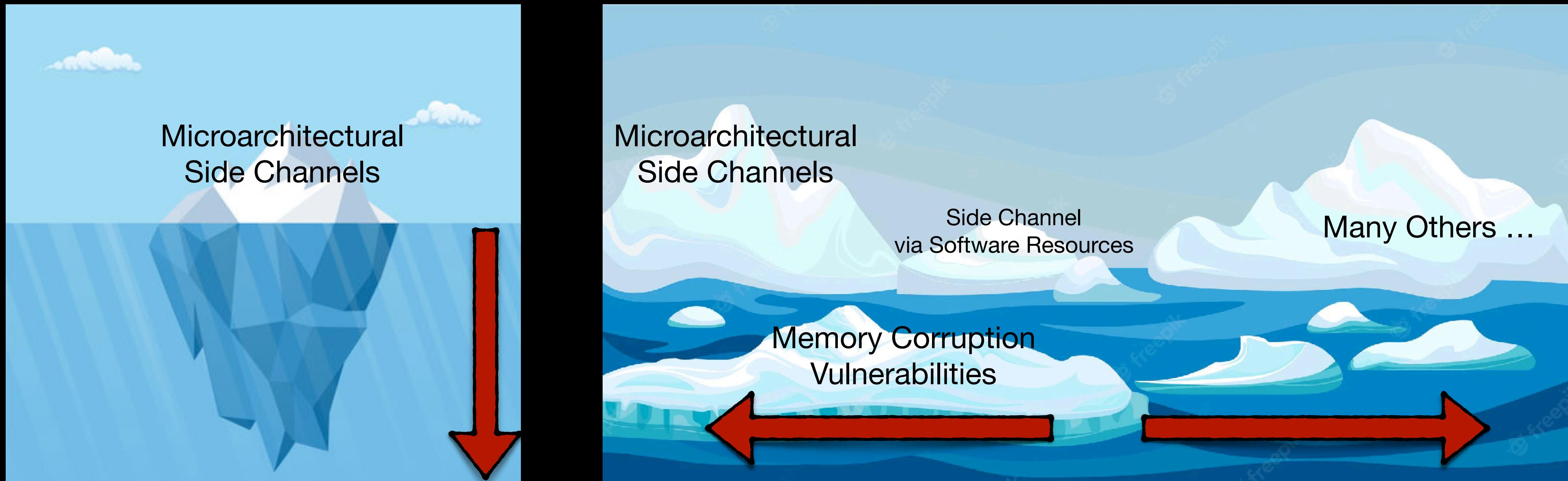
PacmanKit



PACMAN: Attacking ARM Pointer Authentication with Speculative Execution

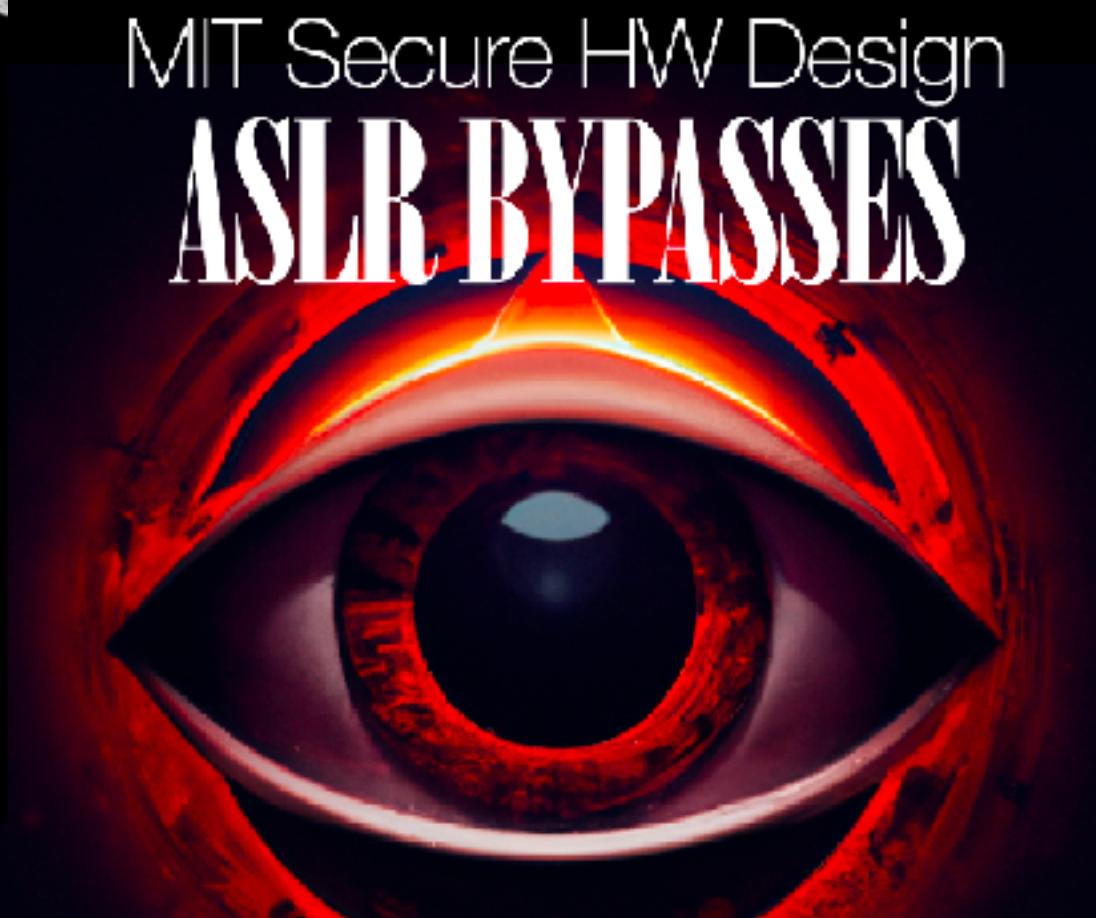
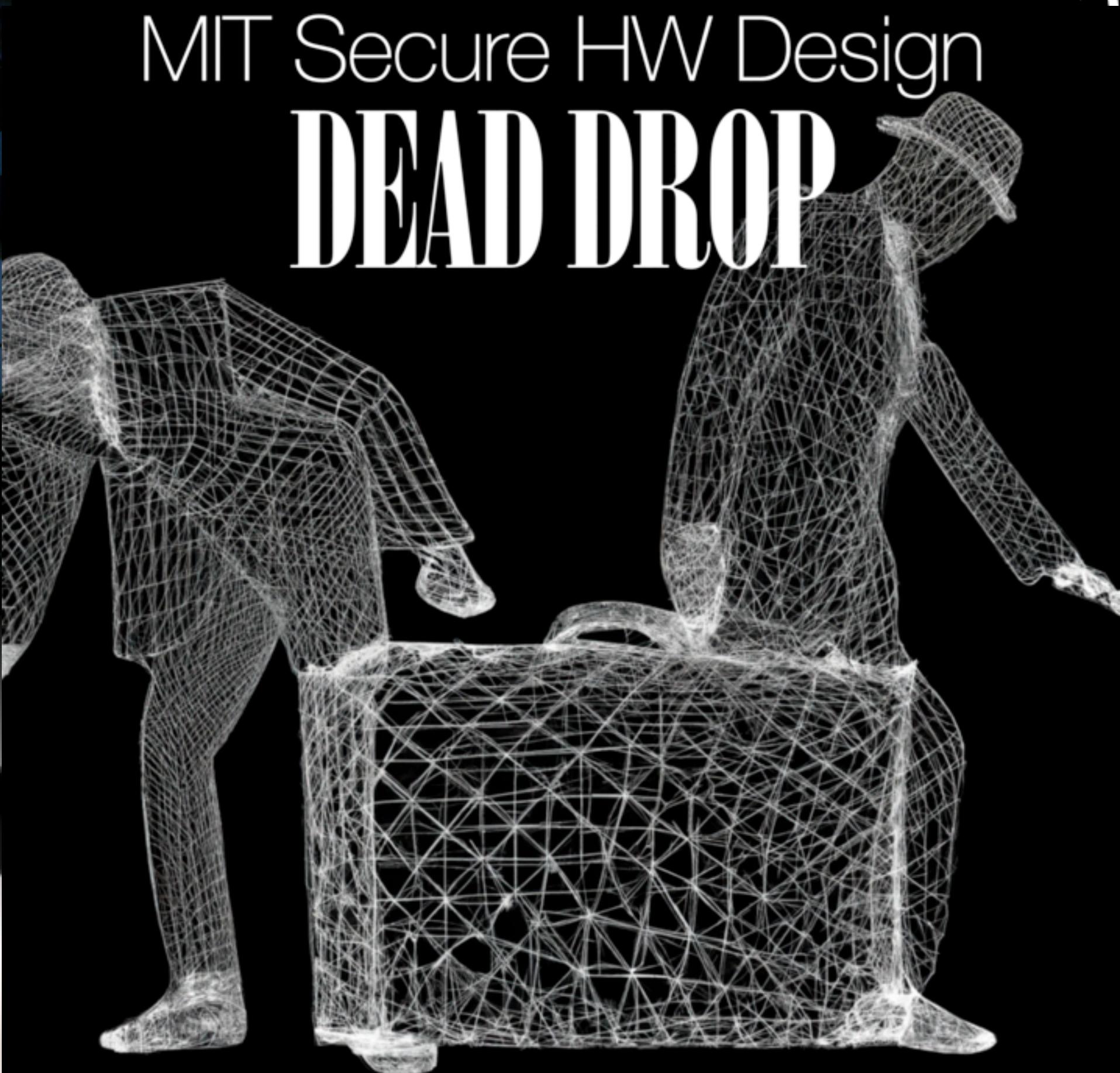
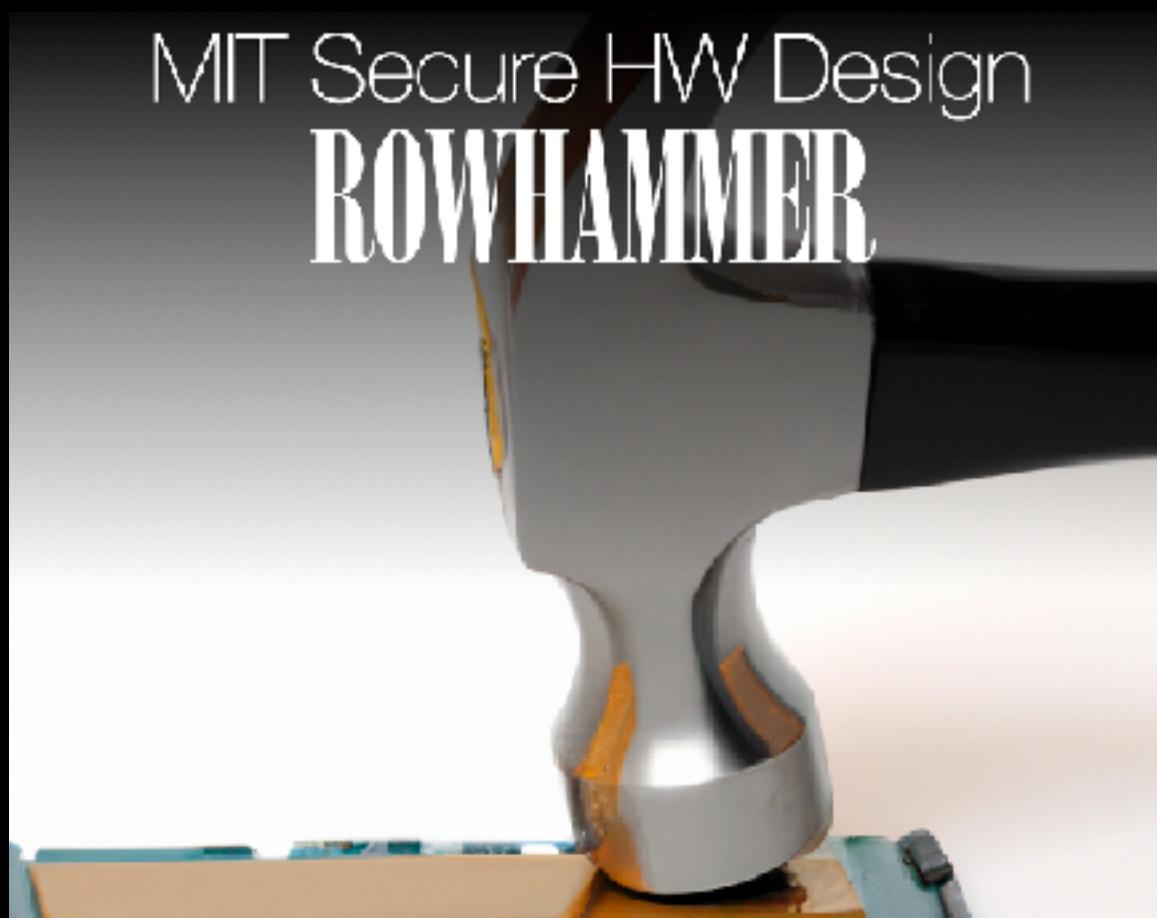


Takeaway: **New threats** arising from compound threat models



Learning CPU Architecture **for fun**

http://csg.csail.mit.edu/6.888Yan/for_instructors/



The Dream Team



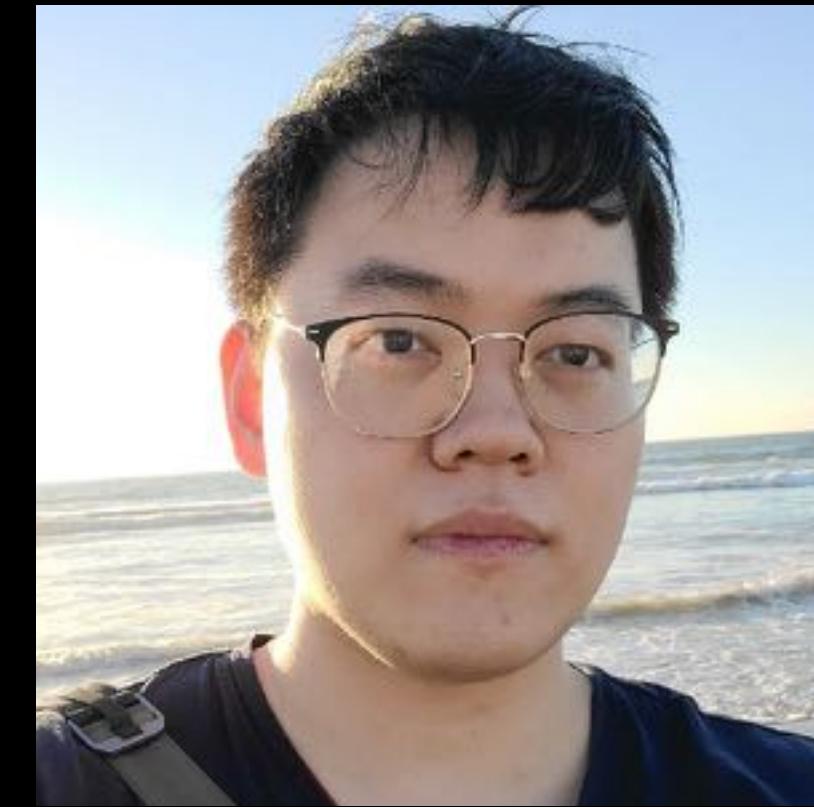
Peter Deutsch



Yuheng Yang



Joseph Ravichandran



Mengyuan Li



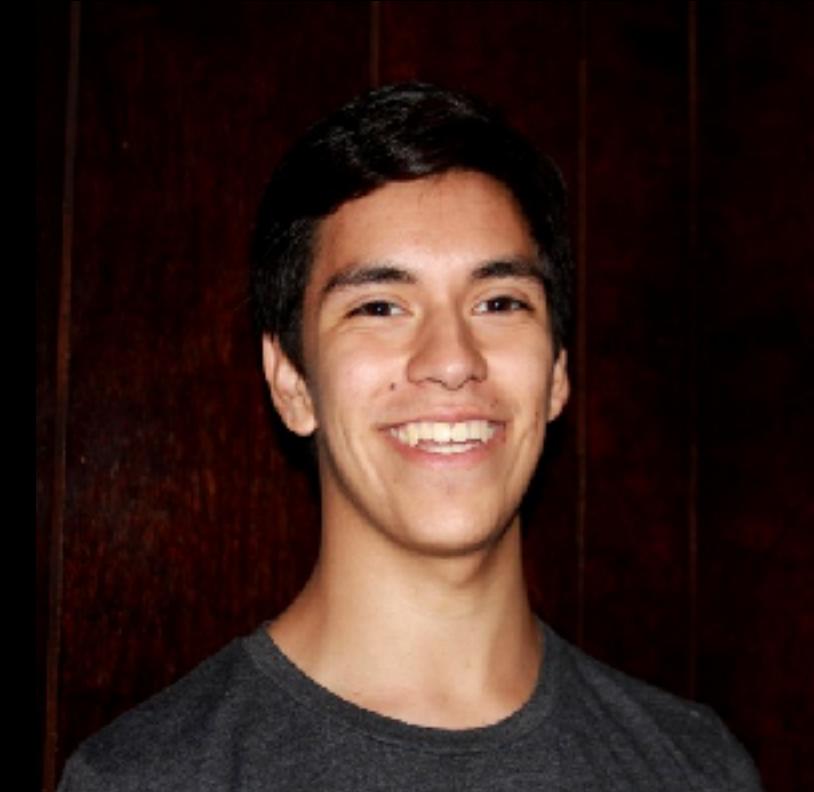
Jules Drean



Shixin Song

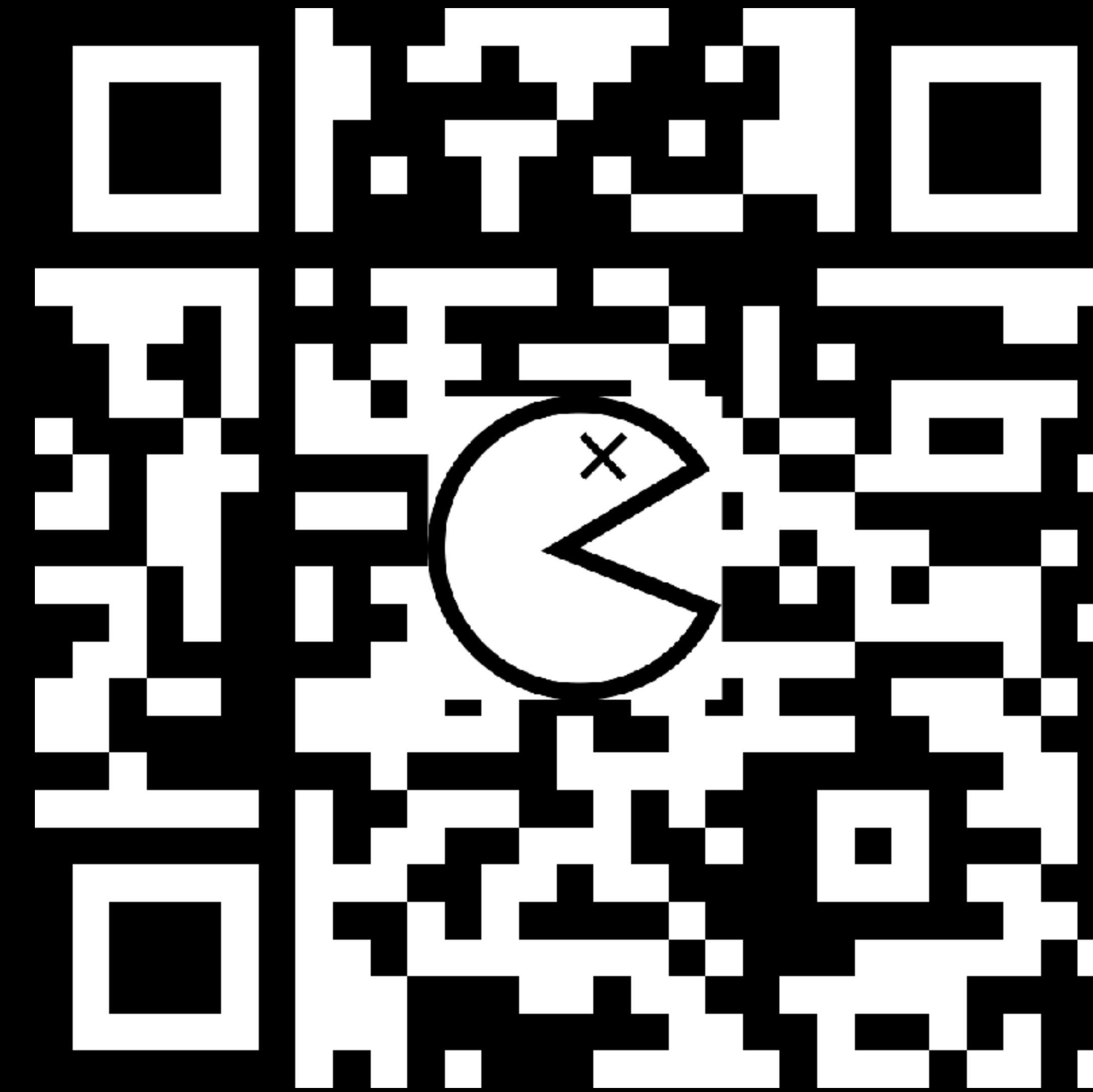


Jack Cook



Miguel Gomez-Garcia

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