

**Features**

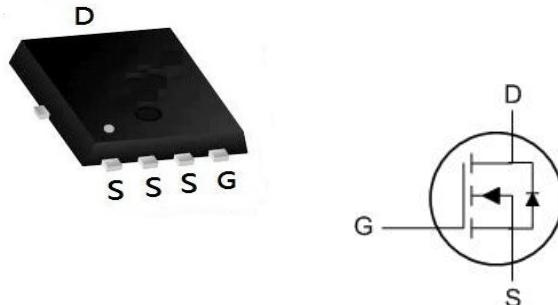
- Split Gate Trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low  $R_{DS(ON)}$

**Product Summary**

BVDSS	RDS(on)	ID
60V	4mΩ	100A

**Applications**

- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

**PDFN3333-8L Pin Configuration****Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current  $T_c=25^\circ\text{C}$	$I_D$	100	A
		64	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	385	A
Single Pulse Avalanche Energy <sup>2</sup>	$E_{AS}$	80	mJ
Total Power Dissipation  $T_c=25^\circ\text{C}$	$P_D$	73.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$	51	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	1.7	°C/W

**Electrical Characteristics ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	60	-	-	V
Gate-body Leakage Current	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current $T_J=25^\circ\text{C}$ $T_J=100^\circ\text{C}$	$I_{\text{DSS}}$	$V_{\text{DS}} = 60\text{V}, V_{\text{GS}} = 0\text{V}$	-	-	1	$\mu\text{A}$
			-	-	-	
Gate-Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.2	1.7	2.5	V
Drain-Source On-Resistance <sup>4</sup>	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 21\text{A}$	-	4.0	4.8	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$	-	5.2	6.6	
Forward Transconductance <sup>4</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = 10\text{V}, I_D = 21\text{A}$	-	89	-	S
<b>Dynamic Characteristics<sup>5</sup></b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$	-	2180	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	735	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	42	-	
Gate Resistance	$R_g$	$f = 1\text{MHz}$	-	1.8	-	$\Omega$
<b>Switching Characteristics<sup>5</sup></b>						
Total Gate Charge	$Q_g$	$V_{\text{GS}} = 10\text{V}, V_{\text{DS}} = 30\text{V}, I_D = 21\text{A}$	-	35	-	$\text{nC}$
Gate-Source Charge	$Q_{\text{gs}}$		-	6.6	-	
Gate-Drain Charge	$Q_{\text{gd}}$		-	8.4	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{GS}} = 10\text{V}, V_{\text{DD}} = 30\text{V}, R_G = 3\Omega, I_D = 21\text{A}$	-	9.4	-	$\text{ns}$
Rise Time	$t_r$		-	8.4	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	32.5	-	
Fall Time	$t_f$		-	12.5	-	
Body Diode Reverse Recovery Time	$t_{\text{rr}}$	$I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	50	-	$\text{ns}$
Body Diode Reverse Recovery Charge	$Q_{\text{rr}}$		-	20	-	$\text{nC}$
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	$V_{\text{SD}}$	$I_S = 21\text{A}, V_{\text{GS}} = 0\text{V}$	-	-	1.2	V
Continuous Source Current $T_C = 25^\circ\text{C}$	$I_S$	-	-	-	100	A

Notes:

- Repetitive rating, pulse width limited by junction temperature  $T_{J(\text{MAX})} = 150^\circ\text{C}$
- The EAS data shows Max. rating . The test condition is  $V_{\text{DD}} = 25\text{V}, V_{\text{GS}} = 10\text{V}, L = 0.1\text{mH}, I_{\text{AS}} = 40\text{A}$ .
- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
- The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
- This value is guaranteed by design hence it is not included in the production test.

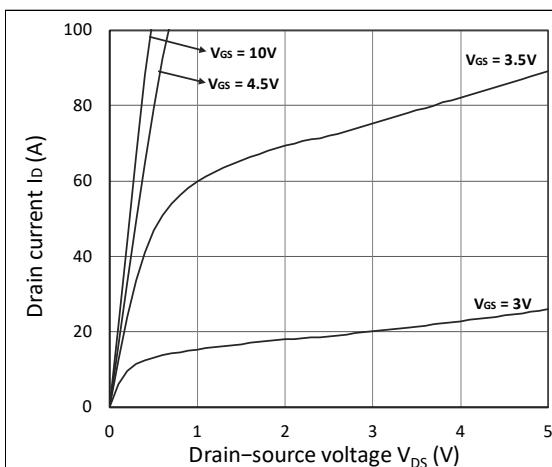
**Typical Characteristics**

Figure 1. Output Characteristics

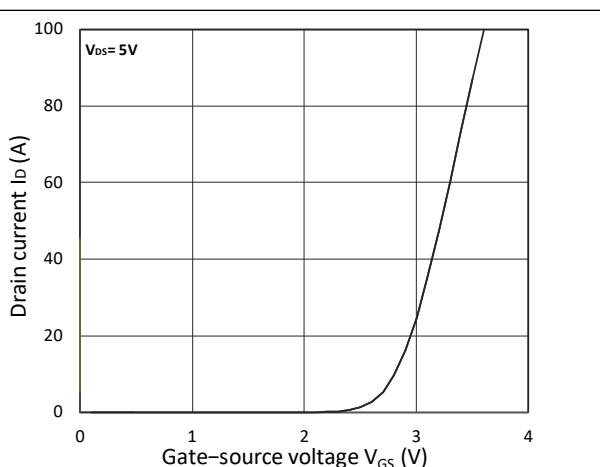


Figure 2. Transfer Characteristics

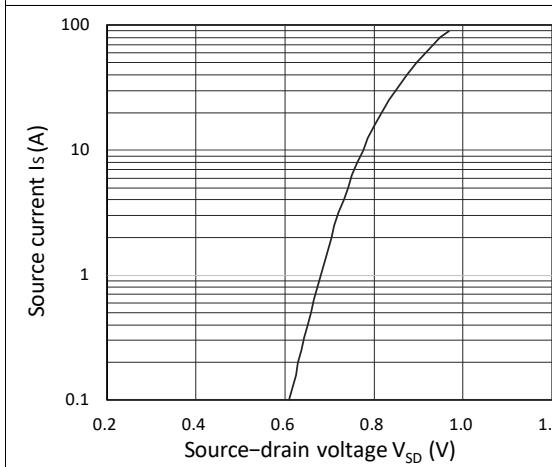
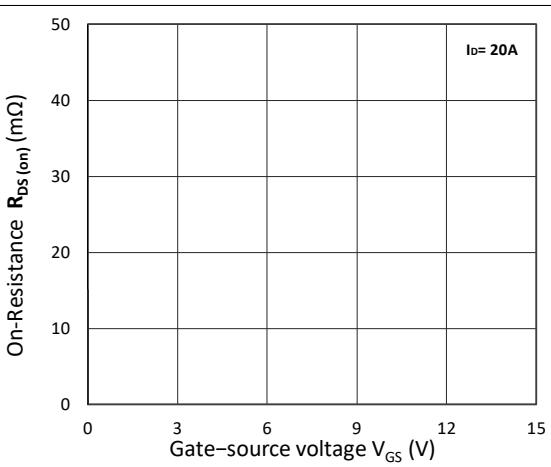
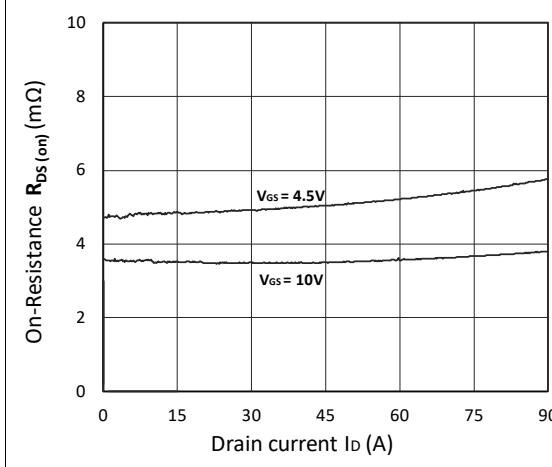
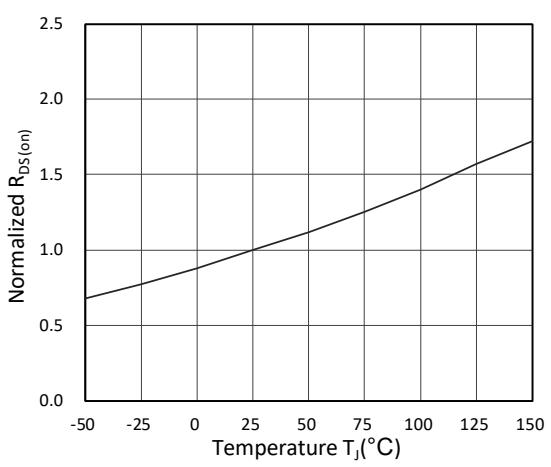


Figure 3. Forward Characteristics of Reverse

Figure 4.  $R_{DS(on)}$  vs.  $V_{GS}$ Figure 5.  $R_{DS(on)}$  vs.  $I_D$ Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature

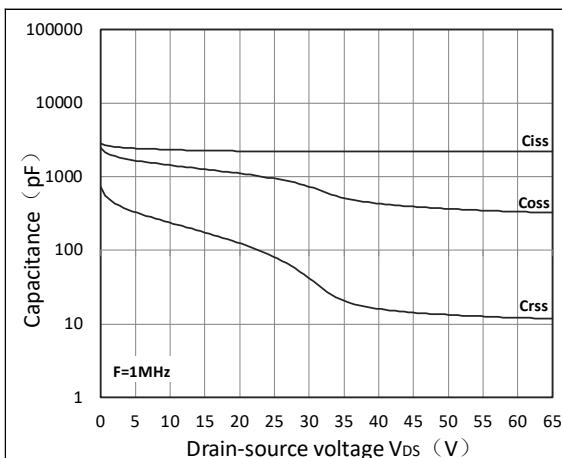


Figure 7. Capacitance Characteristics

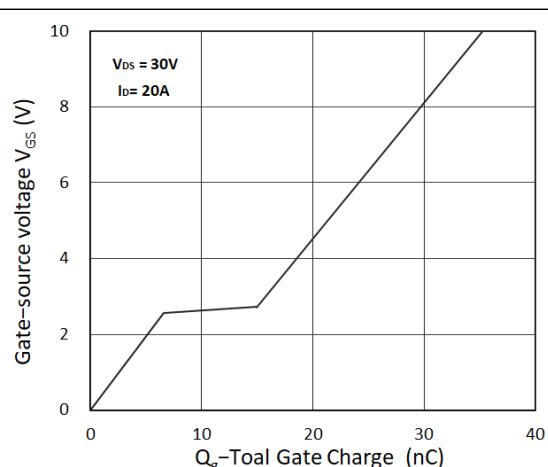


Figure 8. Gate Charge Characteristics

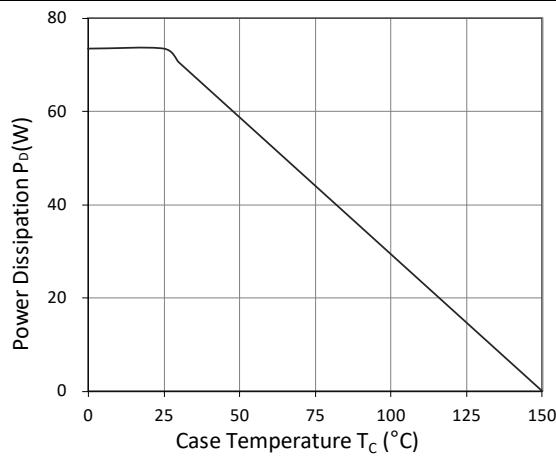


Figure 9. Power Dissipation

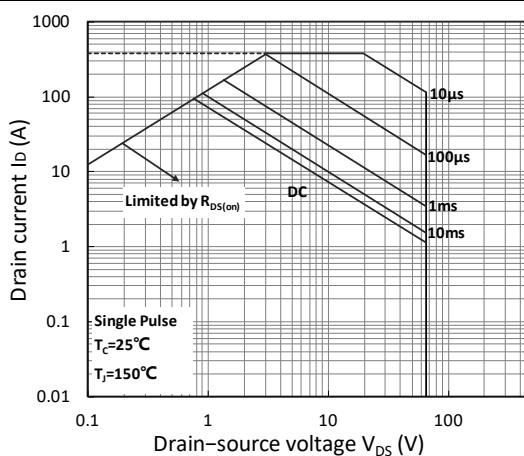


Figure 10. Safe Operating Area

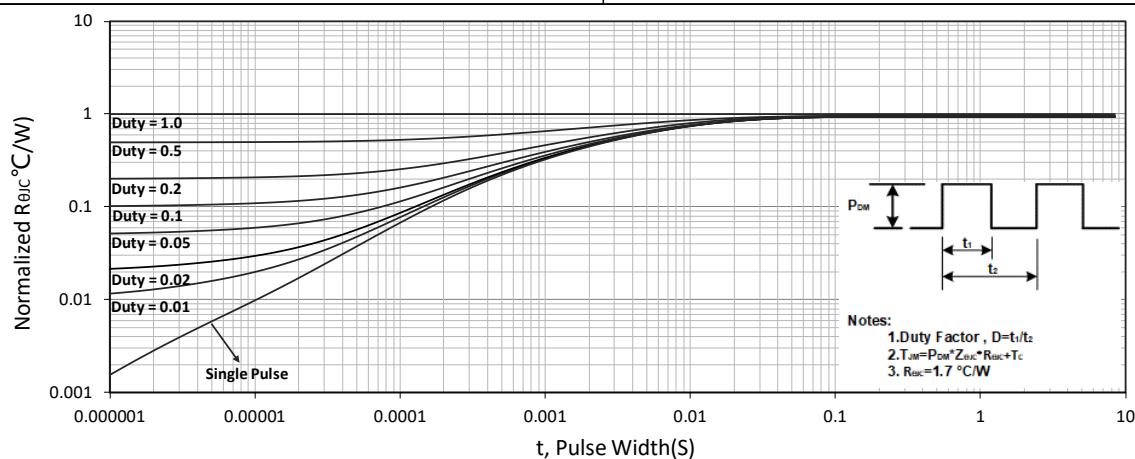
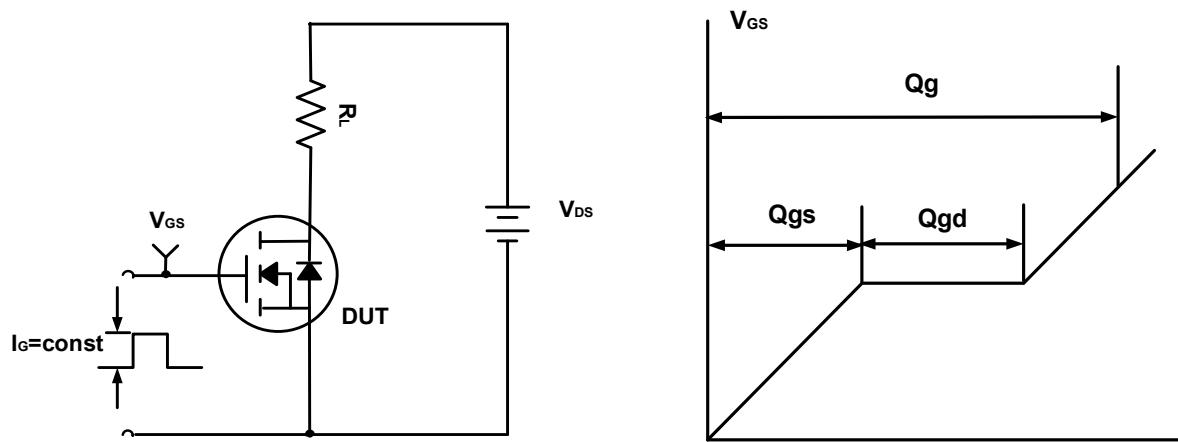
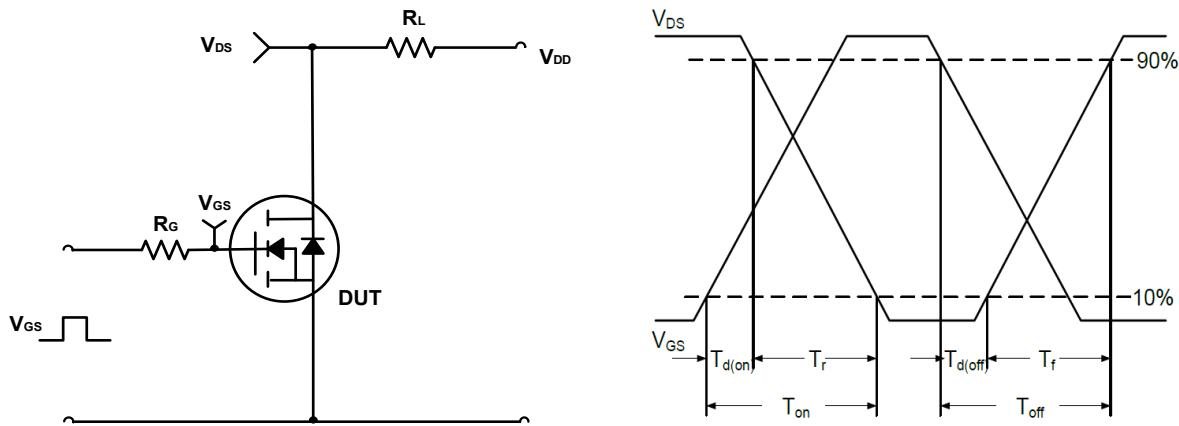
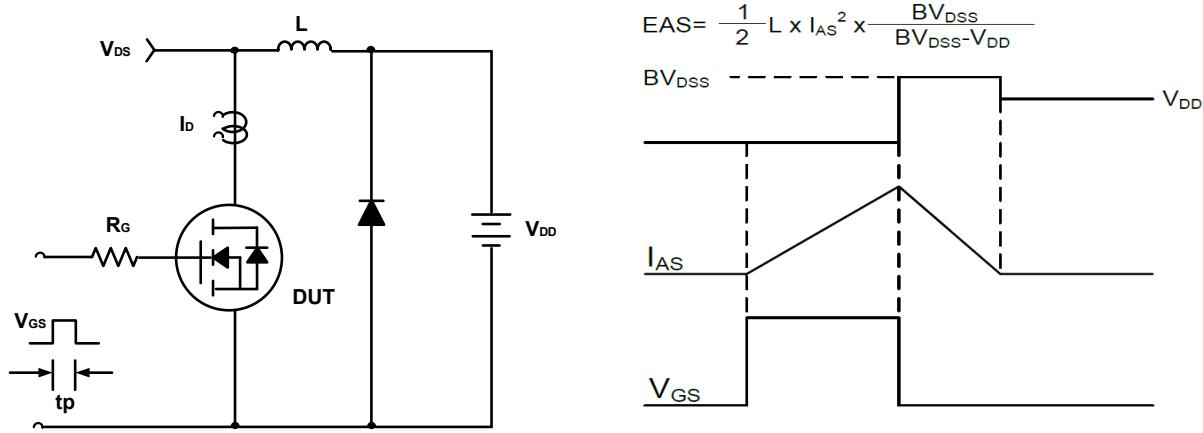
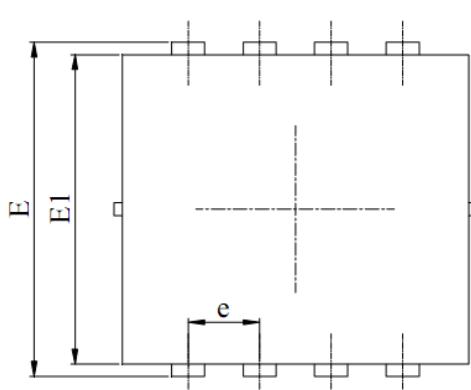


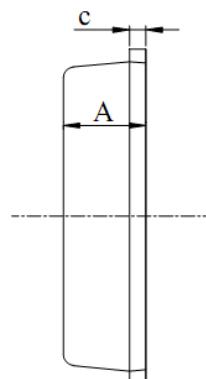
Figure 11. Normalized Maximum Transient Thermal Impedance

**Test Circuit****Figure A. Gate Charge Test Circuit & Waveforms****Figure B. Switching Test Circuit & Waveforms****Figure C. Unclamped Inductive Switching Circuit & Waveforms**

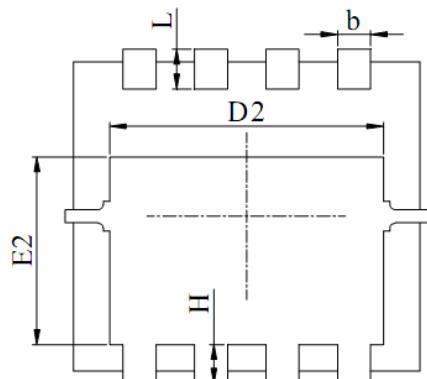
## Package Mechanical Data-PDFN3333-8L-Single



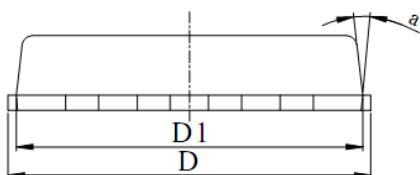
Top View



Side View



Bottom View

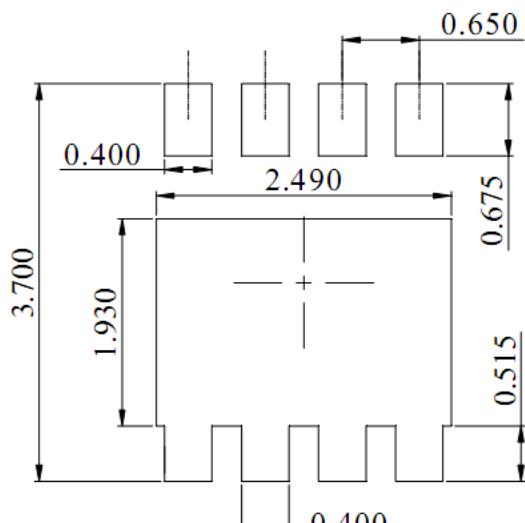


Front View

### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANGLE IN DEGREE).
3. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.20	0.25
D	3.00	3.15	3.25
D1	2.95	3.05	3.15
D2	2.39	2.49	2.59
E	3.20	3.30	3.40
E1	2.95	3.05	3.15
E2	1.70	1.80	1.90
e	0.65 BSC		
H	0.30	0.40	0.50
L	0.25	0.40	0.50
a	---	---	15°



DIMENSIONS:MILLIMETERS