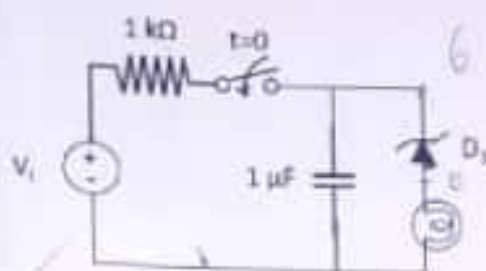


# EE 101: Introduction to Electrical and Electronic Circuits, 2019

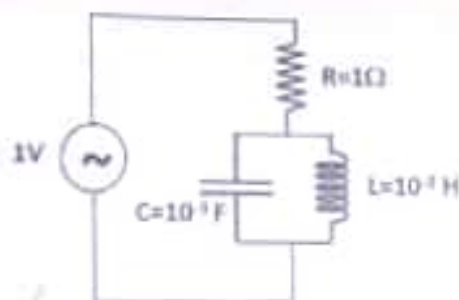
## Endsem

(Show all the steps in the solution properly. Weightage=50.5 %)

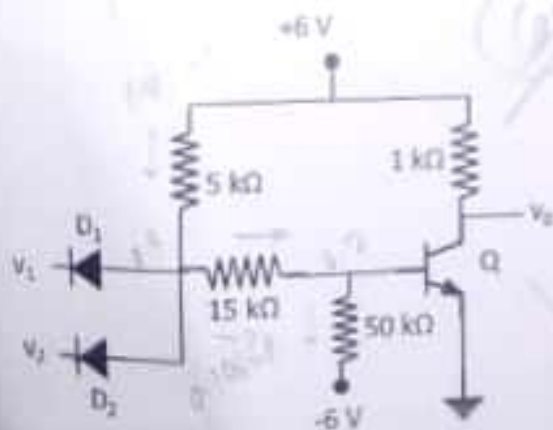
- 1) Consider the circuit below with a Zener diode (forward bias voltage drop=0.7 V and breakdown voltage of 6V). The bulb has a negligible resistance and does not glow when the current through it is zero. If the switch closes at  $t=0$ , find out the time at which the bulb may glow. Calculate the steady state capacitor voltage when a)  $V_i=8$  V b)  $V_i=4$  V and c)  $V_i=8$  V. [6 marks]



- 2) Consider the circuit shown below. Find out the transfer function  $h(\omega)$ . ( $h=V_R/V_m$ ). Sketch the real and imaginary parts of  $h$  as a function of  $\omega$ . [6 marks]

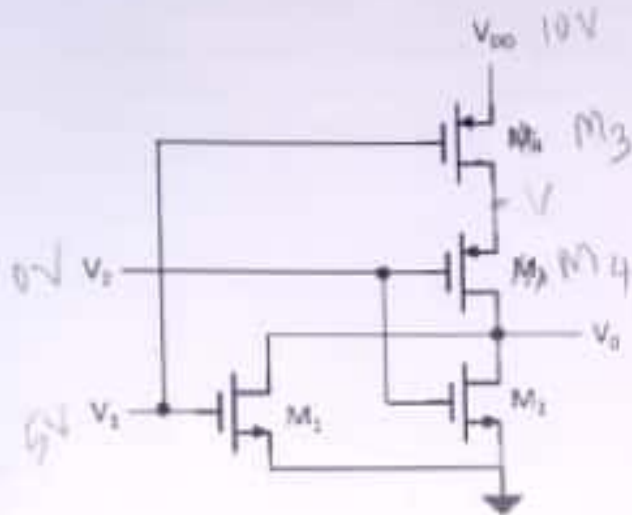


- 3) Consider logic circuit as shown below. Assume ON voltage of diode to be 0.7 V. Assume  $V_{be(on)}=0.7$  V and  $V_{be(off)}=0.2$  V. Show that the circuit works as a NAND gate. Find out the fan out. (Given  $\beta=100$ ). [8 marks]



$$4 = \frac{1}{A_1} \int 2C$$

4) Find out the output of the CMOS circuit when  $V_1 = 5\text{ V}$  and  $V_2 = 0\text{ V}$ . Given  $V_{DD} = 10\text{ V}$ ,  $K = 0.25\text{ mA/V}^2$  and the threshold voltages for  $M_1$  and  $M_2$  are  $1\text{ V}$ , whereas the threshold voltages for  $M_3$  and  $M_4$  are  $-1\text{ V}$ . Assume  $M_1, M_3$  in active region,  $M_4$  in ohmic region. Verify the assumptions. [8 marks]

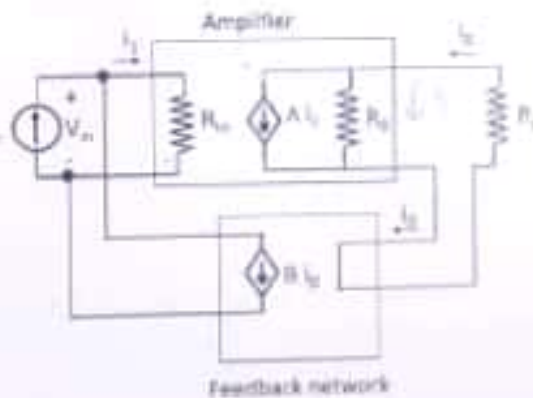


$$V_0 = 10 - V_{DS3} - V_{DS4}$$

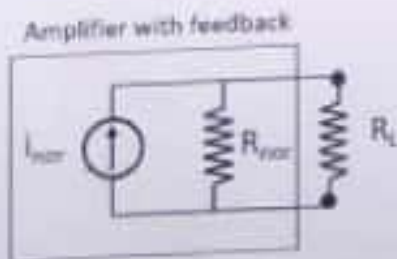
$$V_0 \geq 4$$

$$V_0 < 6$$

5) Consider a current amplifier with feedback network as shown below. Assuming  $R_L = 0$ , find out the input resistance ( $V_{in}/i_{in}$ ) and loop gain ( $i_o/i_{in}$ ) in terms of parameters A and B.

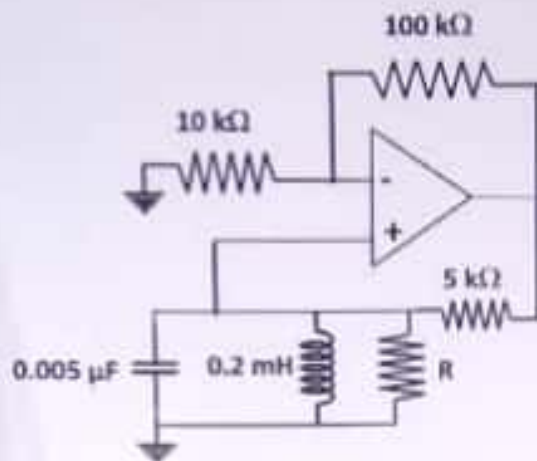


Now assume a general load resistance  $R_L$ . What is the input resistance now? Find out the  $i_{no}$  and  $R_{no}$  of the Norton equivalent circuit of the amplifier output as shown below.



[8 marks]

- 6) For the oscillator circuit shown below, assume that the op-amp is ideal. Find out the frequency of oscillation. Find out the minimum value of  $R$  for which oscillations occurs. [7 marks]



- 7) Consider common emitter amplifier as shown below. Assume that the capacitors can be replaced by short circuits for the ac part of the circuit. The small signal model of the BJT is shown on the right side. Using this small signal model, find out the expressions for ac voltage gain  $A_v = v_o/v_i$ ,  $A_{v_s} = v_o/v_s$  and the ac input resistance,  $R_{in}$ .

Find out the numerical values of  $A_v$  and  $R_{in}$  assuming,  $R_1 = R_2 = 20 \text{ k}\Omega$ ,  $R_C = R_L = 2 \text{ k}\Omega$ ,  $g_m = 0.08 \text{ S}$ ,  $r_{\pi} = 450 \text{ k}\Omega$ ,  $r_e = 1.3 \text{ k}\Omega$  and  $r_o = 13 \text{ M}\Omega$ .

[8 marks]

