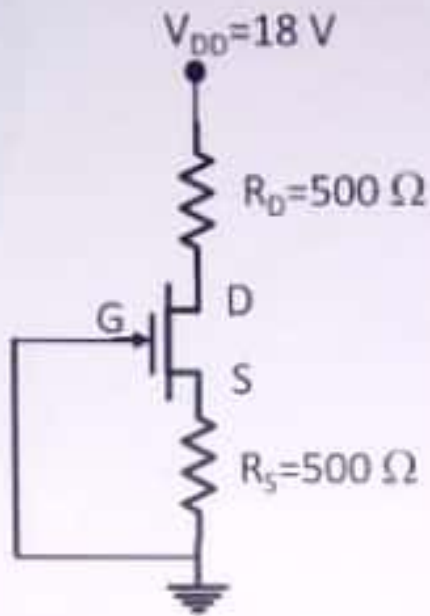


EE 101: Introduction to Electrical and Electronic Circuits, 2019

Quiz 4

(Show all the steps in the solution properly. Weightage=6 %)

1) Consider a JFET circuit shown below. Given $I_{DSS}=16 \text{ mA}$ and $V_P=-4 \text{ V}$. Find out V_{GS} , V_{DS} and drain current. [3 marks]



2) An NMOS circuit as shown below. Both the enhancement MOSFETs have threshold voltage $V_T=2 \text{ V}$ and $K=0.25 \text{ mA/V}^2$. Find out the drain current and V_{DS1} .

[3 marks]

