


CYFeather_CYSUB_RP01_Rev3

	CONTENTS
PAGE	DESCRIPTION
01	Title Table, Table of Contents, Drawing Numbers
02	Block Diagram
03	Power Supply
04	Processor
05	Interface
06	Kit Prog3
07	Revision History

Drawing Numbers

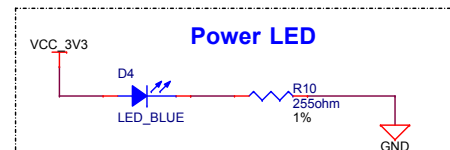
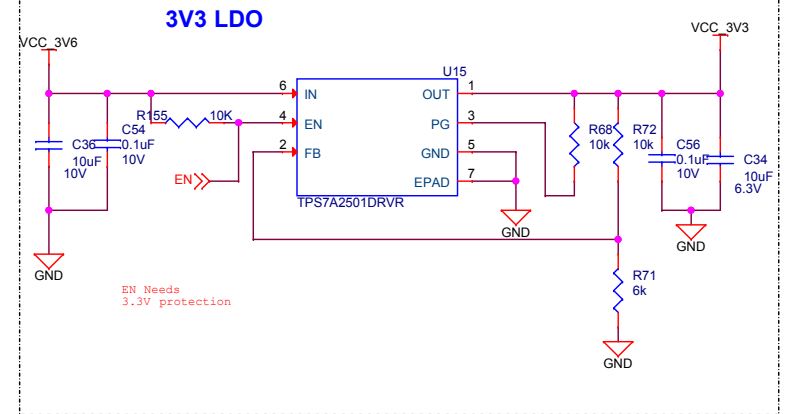
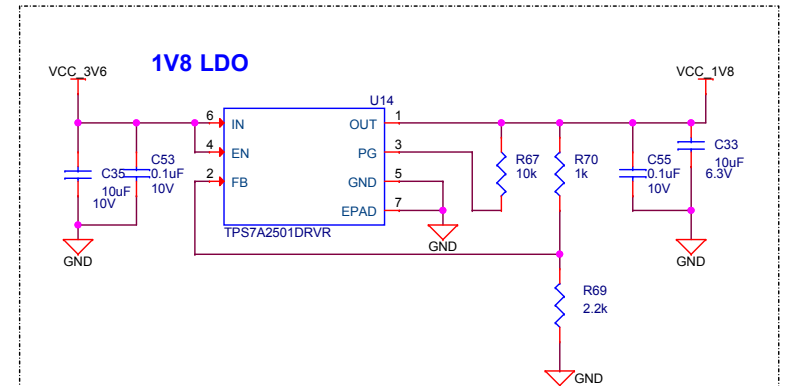
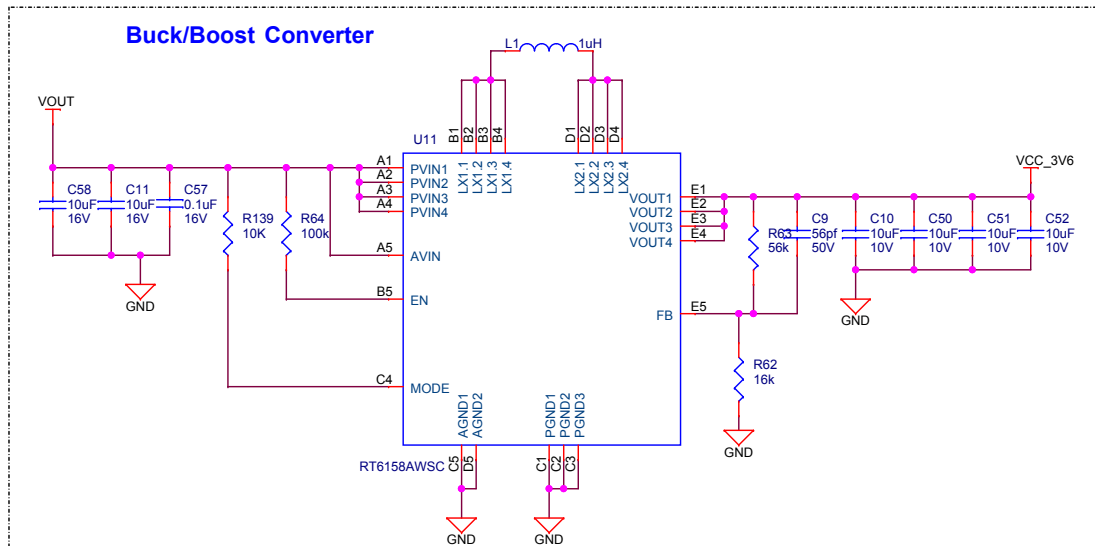
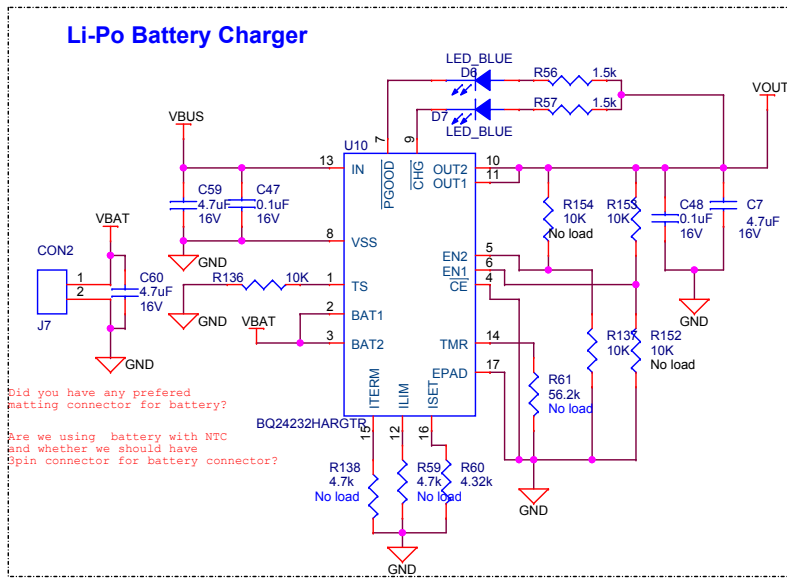
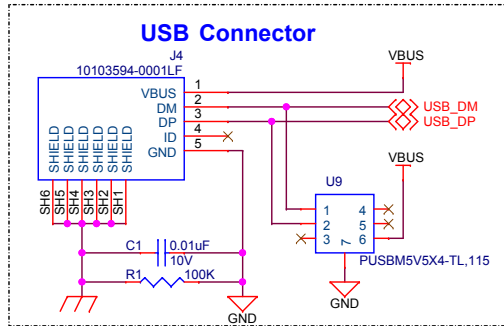
PCBA	121-ZET0006-01
PCB	600-ZET0006-01
FAB DRW	610-ZET0006-01
ASSY DRW	620-ZET0006-01
SCH DRW	630-ZET0006-01

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Page Title :Title Block				
Size B	Document Number 630-ZET0006-01	Drawn By ABID	Approved By	Rev 0.1
Date: Monday, July 29, 2019		Sheet 1 of 7		

Power



1



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Page Title :Power supply

Size B	Document Number 630-ZET0006-01	Drawn By ABID	Approved By	Rev 0.1
Date: Monday, July 29, 2019	Sheet 3 of 7			

U16

CYSUB_RP01


Please check whether NC pins needs to connected to any spare connector?

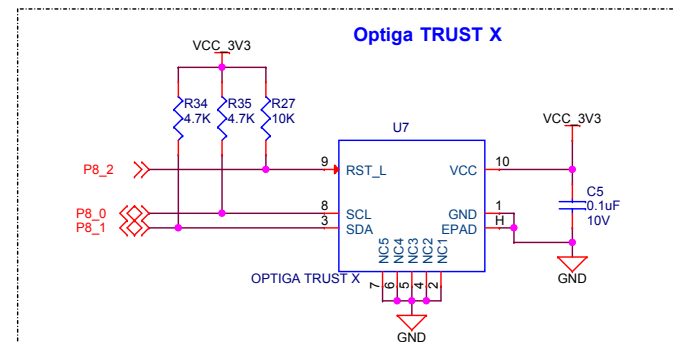
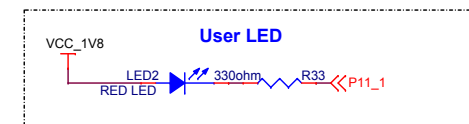
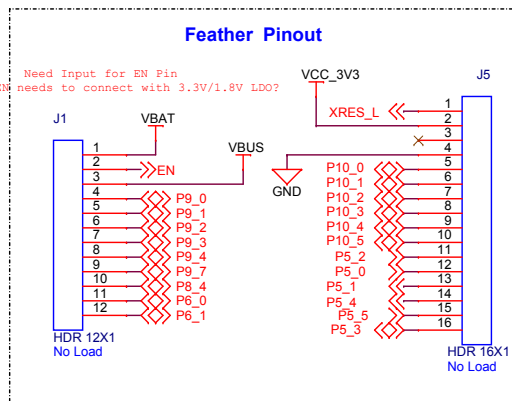
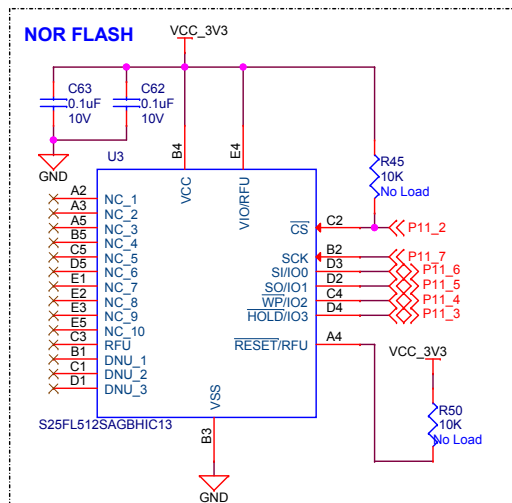
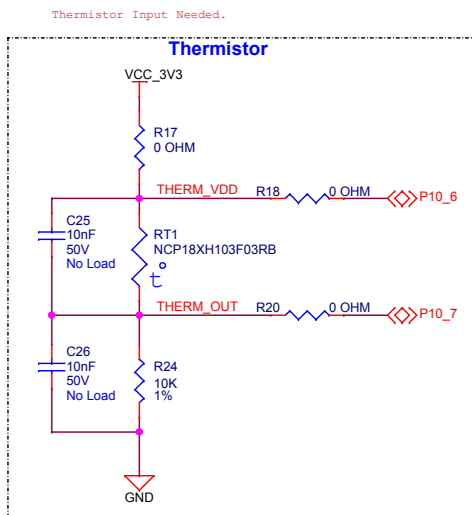
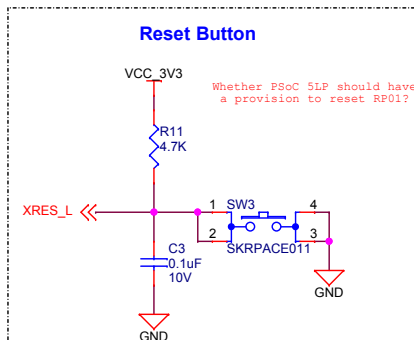
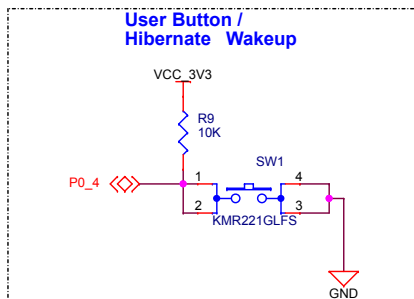


The first diagram shows a voltage divider for VDDA. It consists of a 10k resistor (R146) connected between VCC_1V8 and a 0 OHM jumper connected to VDDA. Another 10k resistor (R147) is connected between VCC_3V3 and the same 0 OHM jumper.

The second diagram shows a voltage divider for VDDIO1. It consists of a 10k resistor (R148) connected between VCC_1V8 and a 0 OHM jumper connected to VDDIO1. Another 10k resistor (R149) is connected between VCC_3V3 and the same 0 OHM jumper.

The third diagram shows a voltage divider for VBACKUP. It consists of a 10k resistor (R150) connected between SUPER_CAP and a 0 OHM jumper connected to VBACKUP. Another 10k resistor (R151) is connected between VCC_3V3 and the same 0 OHM jumper. A note "Super Cap input Needed" is present next to the SUPER_CAP pin. A capacitor C61 is connected between SUPER_CAP and GND.

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Page Title :Processor			
Size B	Document Number 630-ZET0006-01	Drawn By ABID	Approved By
Date: Friday, July 26, 2019		Sheet 4 of 7	
Rev 0.1			



REVISION HISTORY		
REV	DESCRIPTION OF CHANGE	DATE
0.1	Initial Draft	26/7/19



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Size B	Document Number 630-ZET0006-01	Drawn By ABID	Approved By	Rev 0.1
Date:	Friday, July 26, 2019		Sheet	7 of 7