


PSoC 6A 512K CapSense Validation Board

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Drawing Numbers

PCBA	121-ZET0001-01
PCB	600-ZET0001-01
FAB DRW	610-ZET0001-01
ASSY DRW	620-ZET0001-01
SCH DRW	630-ZET0001-01

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SCH Title : PSoC 6A 512K CapSense Validation Board					
Page Title :Title Page					
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Place Block Diagram



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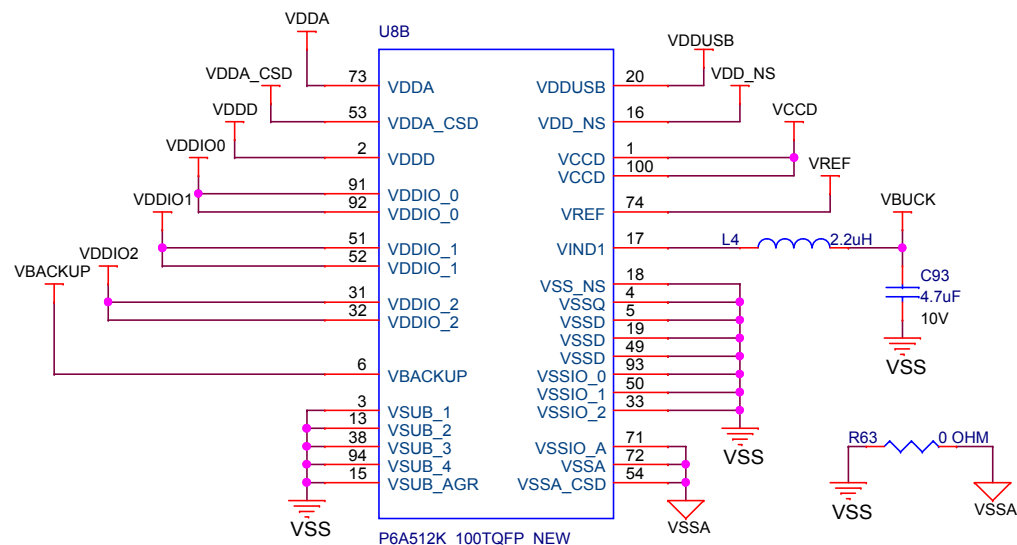
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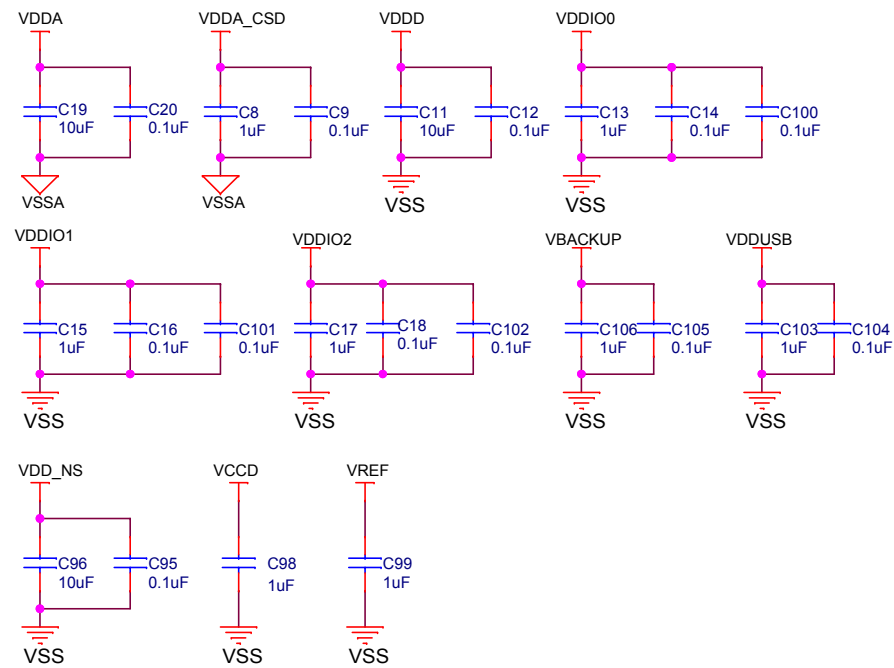
Page Title :Block Diagram

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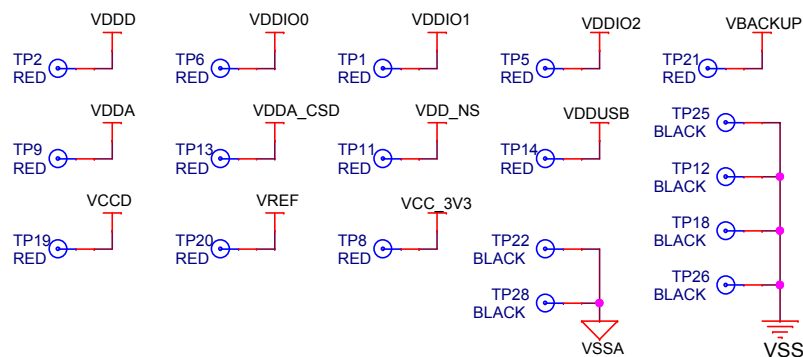
PSoC 6A 512K Power



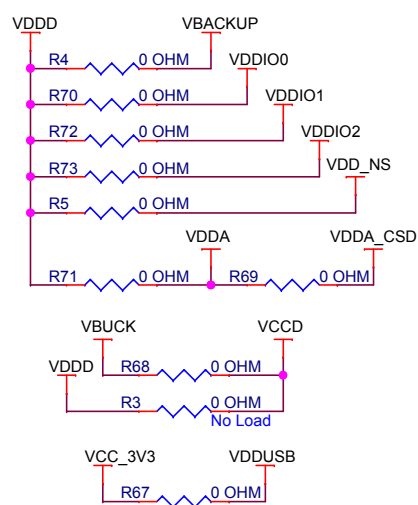
Decoupling Capacitors



Test Points



Power Supply Domain



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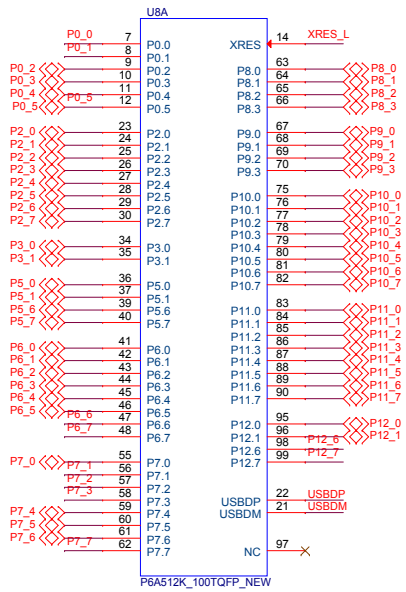
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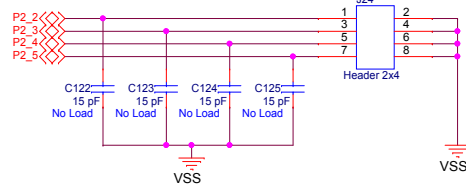
Page Title :PSoC 6A 512K Power Supply

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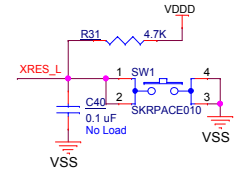
PSoC 6 GPIOs



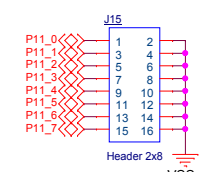
GPIO Termination



Reset



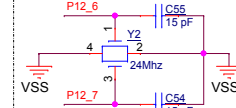
Extended Header



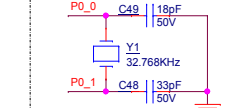
User LEDs



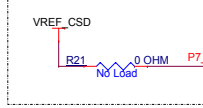
SRSS



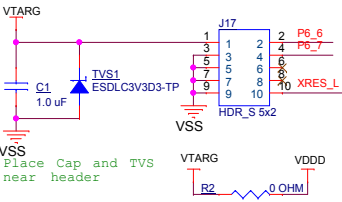
ECO



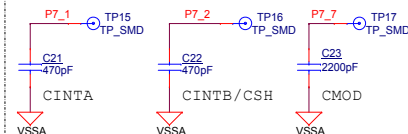
VREF for CSD



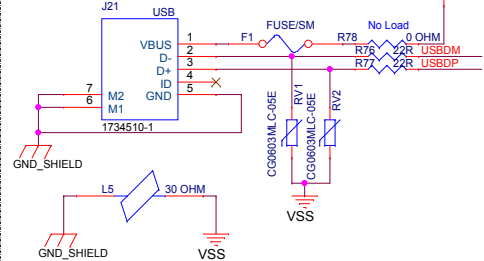
MiniProg3 Header



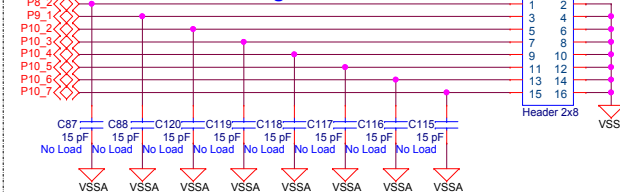
CapSense Capacitors



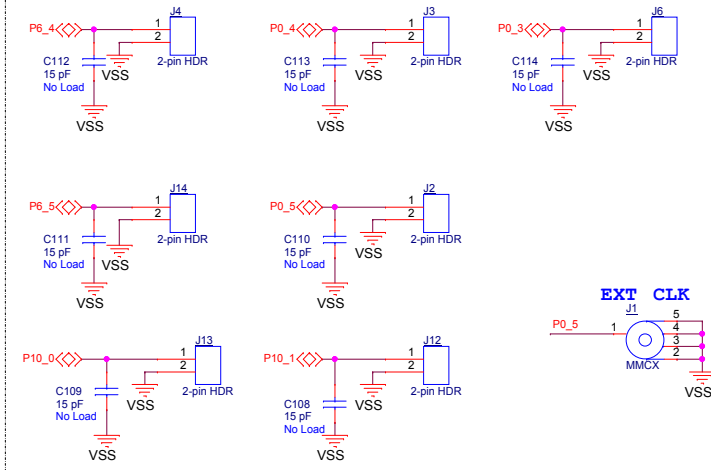
USB Mini B Header



Analog IO Header



Spare & DFT pins



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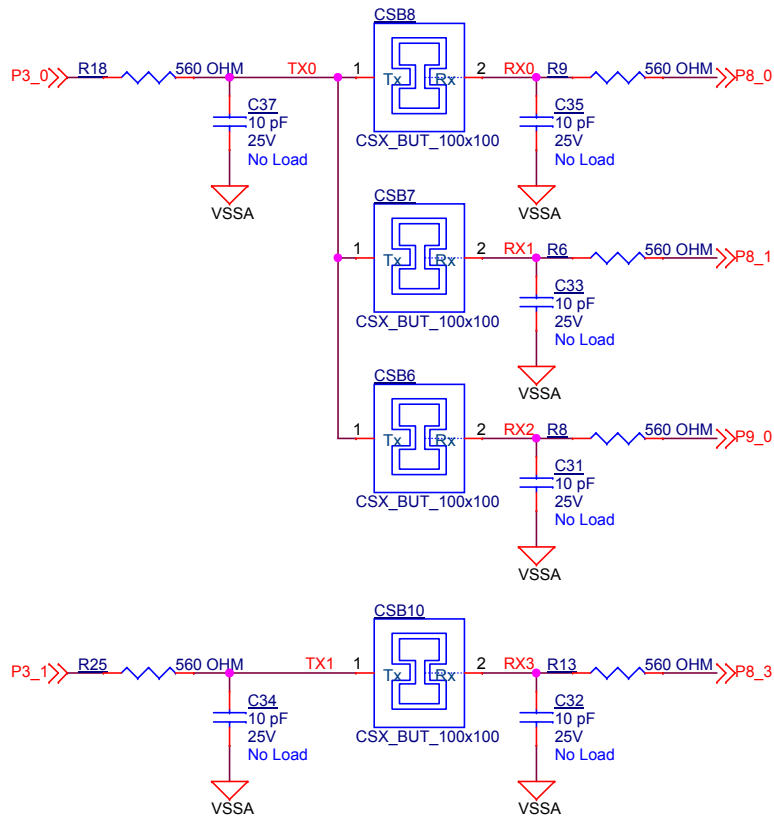
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Page Title :PSoC 6A 512K GPIOs

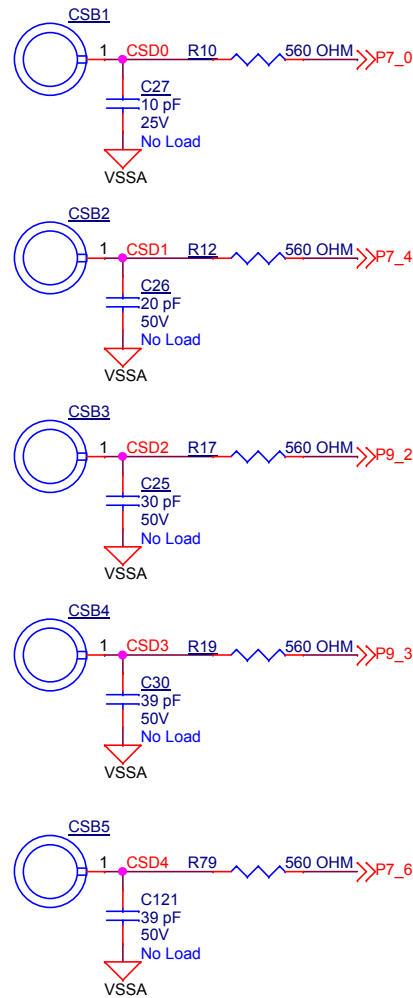
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CSX Buttons

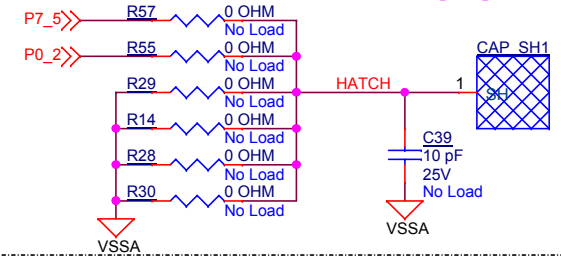


CSD Buttons

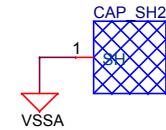


CSD Shield

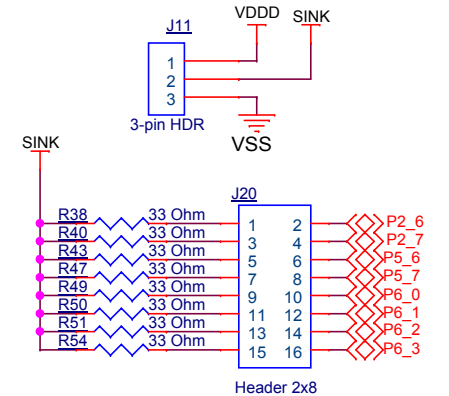
Please review and comment if changed connections are proper.



CSX Shield



GPIO Load Transient Circuit



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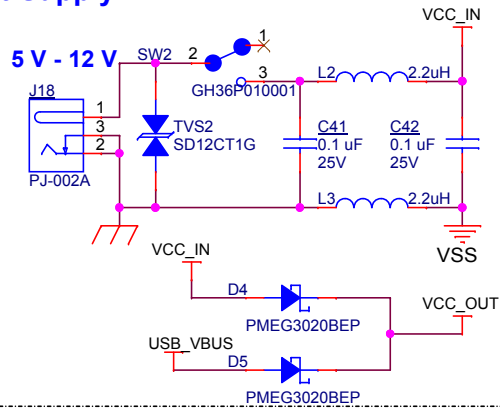
SCH Title : PSoC 6A 512K CapSense Validation Board

Page Title :CapSense, Load Transient, SMIF

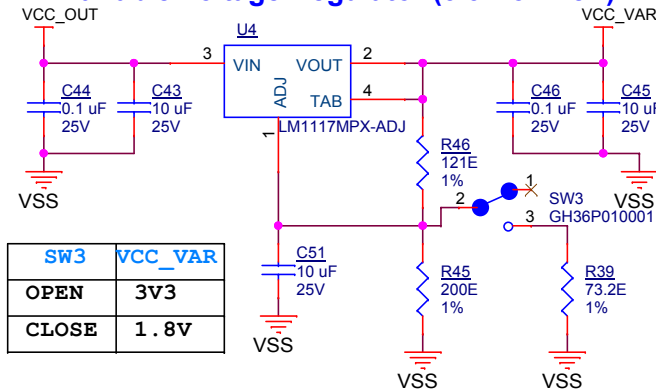
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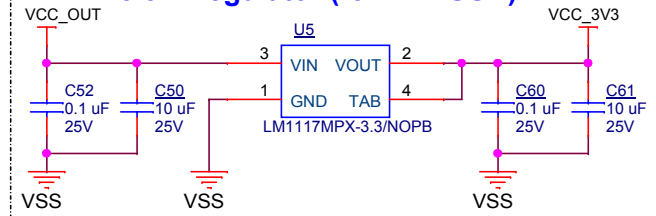
Input Supply



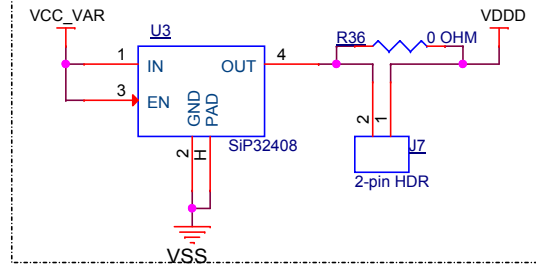
Variable Voltage Regulator (3.3V or 1.8V)



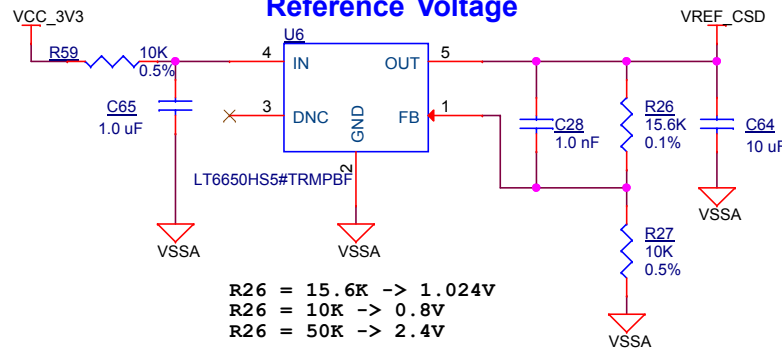
3.3V Regulator (for VDDUSB)



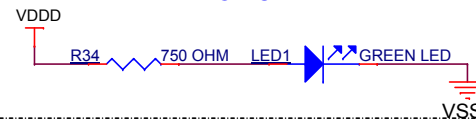
Reverse Voltage Protection



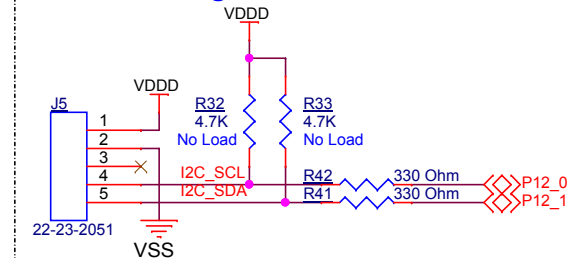
Reference Voltage



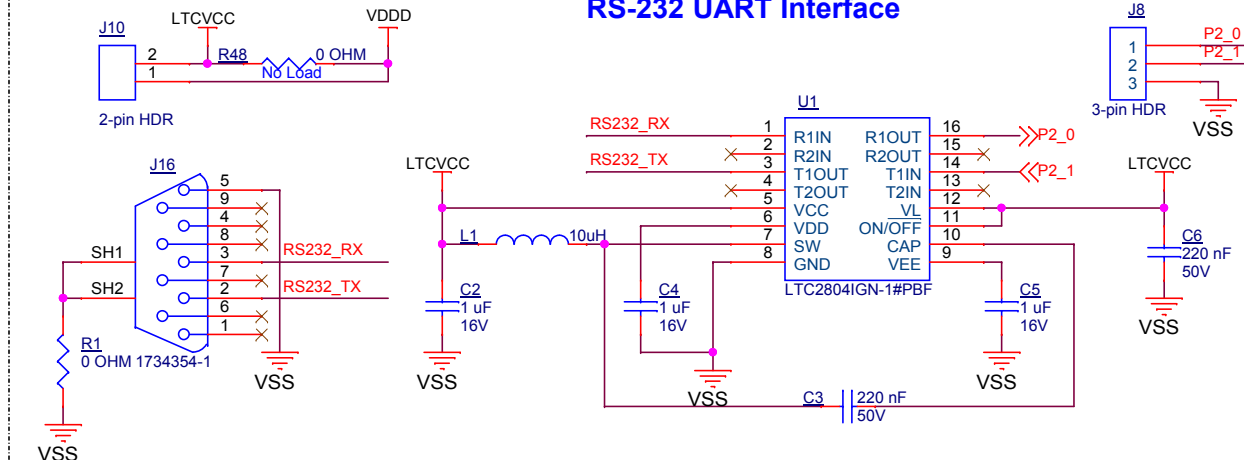
Power LED




MiniProg3 Header for I2C



RS-232 UART Interface





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Page Title :Power Input, RS232, I2C Header, Spare

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REVISION HISTORY		
REV	DESCRIPTION OF CHANGE	DATE
0.1	Initial Draft of PSoC 6A 512K CapSense Validation Board - Merged from PSoC 6A 2M Eval board and 512K Breakout Board	2019/05/16
0.2	Updated for cypress review comments dated May20,2019	2019/05/23
0.3	Updated for cypress review comments dated May27,2019	2019/05/28

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