Pin Out For iCE5UP5K (iCE40 UltraPlus - 5K)
Revision 0.4 - Remove FWG49
Revision 1.1 - Fix RGB pins, swapped C5 & A5 pins as well as 39 & 41
Updated Jun 19, 2017

FNC	Pin Type	BANK	Differential Pair	UWG30
IOB_0a	PIO	2		E5
IOB_2a	DPIO	2	TRUE_of_IOB_3b	D5
IOB_3b_G6	DPIO/GBIN	2	COMP_of_IOB_2a	F5
IOB_4a	DPIO	2	TRUE_of_IOB_5b	-
IOB_5b	DPIO	2	COMP_of_IOB_4a	-
IOB_6a	PIO	2		-
IOB_8a	DPIO	2	TRUE_of_IOB_9b	-
IOB_9b	DPIO	2	COMP_of_IOB_8a	E4
IOB_10a	DPIO	1	TRUE_of_IOB_11b	C3
IOB_11b_G5	DPIO/GBIN	1	COMP_of_IOB_10a	F4
creset_b	CONFIG	1		F3
IOB_12a_G4_CDONE	CONFIG/DPIO/GBIN	1	TRUE_of_IOB_13b	D3
CDONE	CONFIG	1		-
IOB_13b	DPIO	1	COMP_of_IOB_12a	E3
IOB_16a	PIO	1		-
IOB_18a	PIO	1		-
IOB_20a	PIO	1		-
IOB_22a	DPIO	1	TRUE_of_IOB_23b	-
IOB_23b	DPIO	1	COMP_of_IOB_22a	-
IOB_24a	DPIO	1	TRUE_of_IOB_25b	B1
IOB_25b_G3	DPIO/GBIN	1	COMP_of_IOB_24a	F2
IOB_29b	PIO	1		-
IOB_31b	PIO	1		-
IOB_32a_SPI_SO	DPIO/CONFIG_SPI	1		F1
IOB_33b_SPI_SI	DPIO/CONFIG_SPI	1		E1
IOB_34a_SPI_SCK	DPIO/CONFIG_SPI	1		D1
IOB_35b_SPI_SS	DPIO/CONFIG_SPI	1		C1
VCCPLL	VCCPLL			B2
IOT_36b	DPIO/I3C	0	COMP_of_IOT_37a	A1
IOT_37a	DPIO/I3C	0	TRUE_of_IOT_36b	A2
IOT_38b	DPIO	0	COMP_of_IOT_39a	-
IOT_39a	DPIO	0	TRUE_of_IOT_38b	-
IOT_41a	PIO	0		-
IOT_42b	DPIO	0	COMP_of_IOT_43a	-
IOT_43a	DPIO	0	TRUE_of_IOT_42b	-
IOT_44b	DPIO	0	COMP_of_IOT_45a	-
IOT_45a_G1	DPIO/GBIN	0	TRUE_of_IOT_44b	-
IOT_46b_G0	DPIO/GBIN	0		B3
IOT_47a	PIO	0		A4
IOT_48b	DPIO	0	COMP_of_IOT_49a	-
IOT_49a	DPIO	0	TRUE_of_IOT_48b	-
IOT_50b	DPIO	0	COMP_of_IOT_51a	-

IOT_51a	DPIO	0	TRUE_of_IOT_50b	-
RGB2	**LED**	0		C5
RGB1	**LED**	0		B5
RGB0	**LED**	0		A5
GND	GND	GND		B4
GND	GND	GND		E2
GND	GND	GND		-
VCC	VCC	VCC		C2
VCC	VCC	VCC		-
VCCIO_0	VCCIO	0		A3
SPI_Vccio1	VCCIO	1		D2
VCCIO_2	VCCIO	2		C4
VPP_2V5	VPP	VPP		D4

*SG48 GND is connected to package paddle

SG48
46
47
44
48
45
2
4
3
-
-
8
-
7
6
9
10
11
12
21
13
20
19
18
14
17
15
16
29
25
23
27
26
28
31
32
34
37
35
-
36
43
38

42
41
40
39
Paddle
Paddle
Paddle
5
30
33
22
1
24