

Assignment 7

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Q1)

a) RegWrite and ALUSrc

b)

b)

	Inputs I[31-26]						Outputs								
Instruction Type	op5	op4	op3	op2	op1	op0	RegDst	ALUSrc	MemToReg	RegWrite	MemLoad	MemWrite	Branch	ALUOp1	ALUOp0
R-Format	0	0	0	0	0	0	1	0	0	1	0	0	0	1	0
lw	1	0	0	0	1	1	0	1	1	1	1	0	0	0	0
sw	1	0	1	0	1	1	x	1	x	0	0	1	0	0	0
beq	0	0	0	1	0	0	x	0	x	0	0	0	1	0	1
addi	0	0	1	0	0	0	0	1	0	1	0	0	0	0	0

Q1)

c)

Inputs

