

22)

$$a) \text{ I.P.S} = \frac{\text{Clock rate}}{\text{CPI}}$$

$$\text{IPS for } P_1 = \frac{3 \text{ GHz}}{1.5} = 2 \times 10^9$$

$$\text{IPS for } P_2 = \frac{2.5 \text{ GHz}}{1} = 2.5 \times 10^9$$

$$\text{IPS for } P_3 = \frac{4 \text{ GHz}}{2.2} = 1.82 \times 10^9$$

Therefore, P_2 is the one with highest performance in IPS.

$$b) \begin{aligned} \text{no. of instructions} &= \text{IPS} \times \text{CPU Time (Always 10 in this case)} \\ \text{no. of cycles} &= \text{CPU Time (10)} \times \text{clock rate} \end{aligned}$$

P_1 :

$$\begin{aligned} \text{instructions} &= 2 \times 10^9 \times 10 = 2 \times 10^{10} \\ \text{cycles} &= 10 \times 3 \times 10^9 = 3 \times 10^{10} \end{aligned}$$

P_2 :

$$\begin{aligned} \text{instructions} &= 2.5 \times 10^9 \times 10 = 2.5 \times 10^{10} \\ \text{cycles} &= 10 \times 2.5 \times 10^9 = 2.5 \times 10^{10} \end{aligned}$$

P_3 :

$$\begin{aligned} \text{instructions} &= 1.82 \times 10^9 \times 10 = 1.82 \times 10^{10} \\ \text{cycles} &= 10 \times 4 \times 10^9 = 4 \times 10^{10} \end{aligned}$$

$$c) \text{ Execution time} = \frac{\text{cycles}}{\text{clock rate}} \quad \text{or} \quad \frac{\text{instruction} \times \text{CPI}}{\text{clock rate}}$$

$$0.7 (\text{old execution time}) = \text{new execution time}$$

Q2)

c) continued:

$$0.7 \left(\frac{\text{instruction}_1 \times \text{CPI}_1}{\text{clock rate}_1} \right) = \frac{\text{instruction}_2 \times \text{CPI}_2}{\text{clock rate}_2}$$

The number of instructions stays the same, so we can cancel it out:

$$0.7 \left(\frac{\text{CPI}_1}{\text{clock rate}_1} \right) = \frac{\text{CPI}_2}{\text{clock rate}_2} \quad \text{or} \quad \text{CPI}_2 = 1.2 \text{ CPI}_1$$

$$\frac{0.7}{\text{clock rate}_1} \Rightarrow \frac{1.2}{\text{clock rate}_2} \Rightarrow 0.7 \text{ clock rate}_2 = 1.2 \text{ clock rate}_1$$

$$\Rightarrow \text{clock rate}_2 = \frac{1.2}{0.7} \text{ clock rate}_1 = 1.71 \text{ clock rate}_1$$

The clock rate must increase by 71%.