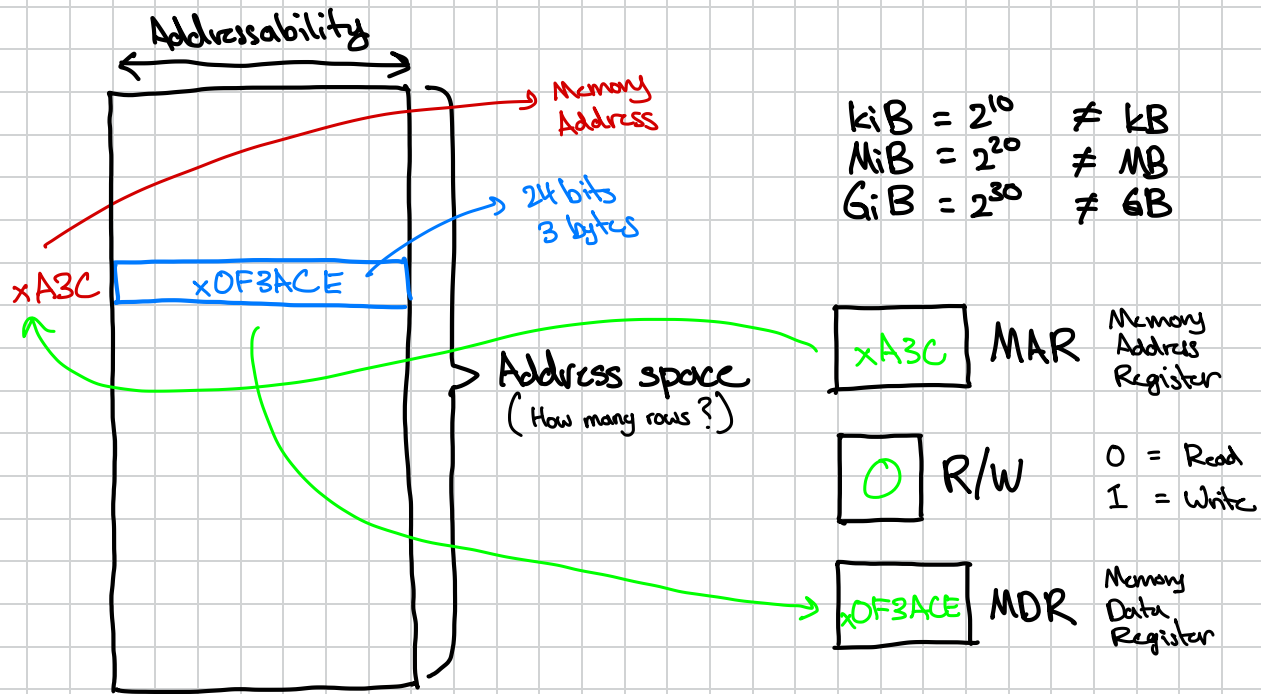
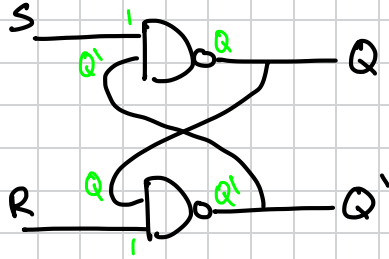


# MEMORY



# LATCHES

SR latch



S	R	Q
0	0	Invalid
0	1	1
1	0	0
1	1	Holds

D latch