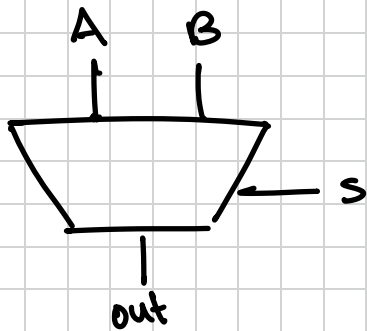
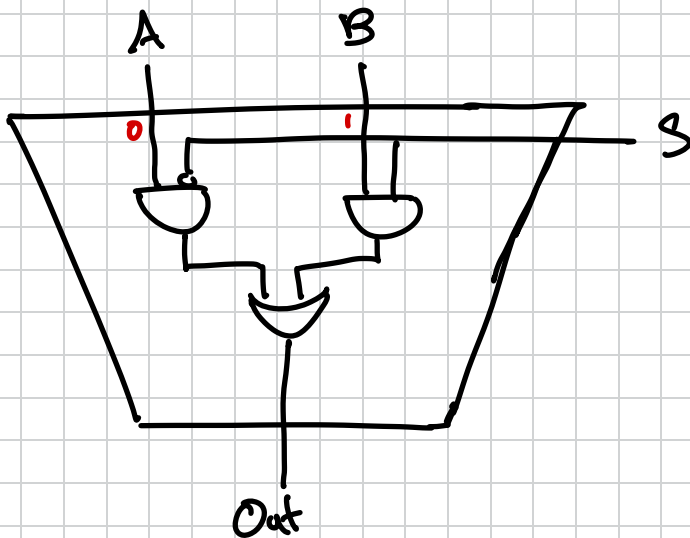


# Mux



if  $S=0$  then  $out = A$   
if  $S=1$  then  $out = B$

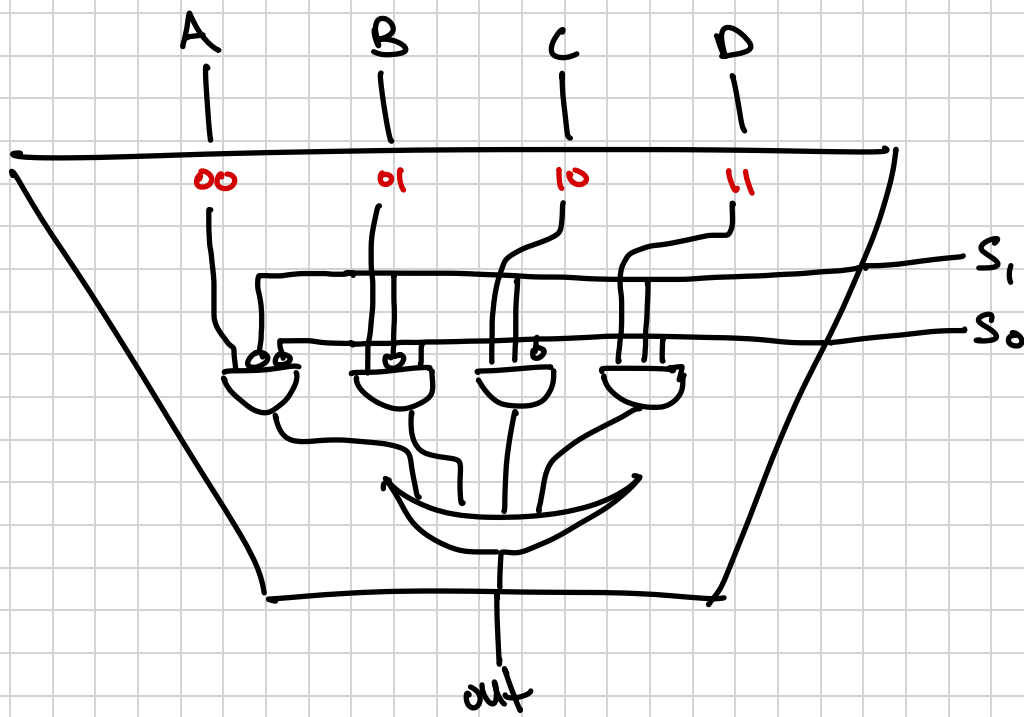


A	B	S	out
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

$$A'BS + AB'S' + ABS' + ABS$$

$$BS(A' + A) + AS'(B' + B)$$

$$BS + AS'$$



"Vending Machine" - Retrieves value based on key

# Design Process

System Description



Truth Table



Boolean Expressions

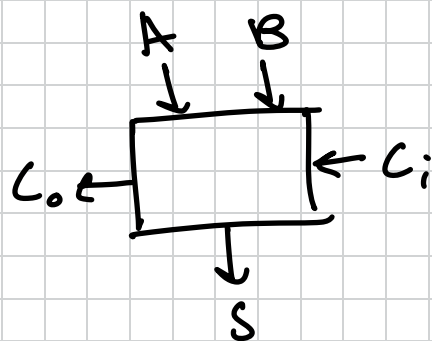


Logic Circuit

Programmable  
Logic  
Array



## Adder 1-bit



SOP S:

$$A'B'C_i + A'BC_i + AB'C_i + ABC_i$$

SOP C<sub>o</sub>:

$$A'BC_i + AB'C_i + ABC_i + ABC_i$$

A	B	C <sub>i</sub>	S	C <sub>o</sub>
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

# Adder 4-bit

