

LC3 VS ARM

	LC3	ARM
Arithmetic	NOT AND ADD	logic: ANDS ORRS EORS BICS arith: ADDS SUBS MULS shift: LSLS LSRS
Move Data	LD LDR LOI ST STR STI	LDR STR LDRB STRB PUSH {register} POP {register}
Control	BR ... \approx NZP JSR JMP TRAP	B ... \approx NZVC $\{ f(A > B) \}$ R1 $\leftarrow A$ R2 $\leftarrow B$ CMP A, B BLE next next NE, EQ, GT, LT, LE, GE, MI, PL
	JMP R7	BL Sub BX BX LR SVC

TOOLS

Assembler
Linker
Loader
Maker
Debugger
Flasher
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