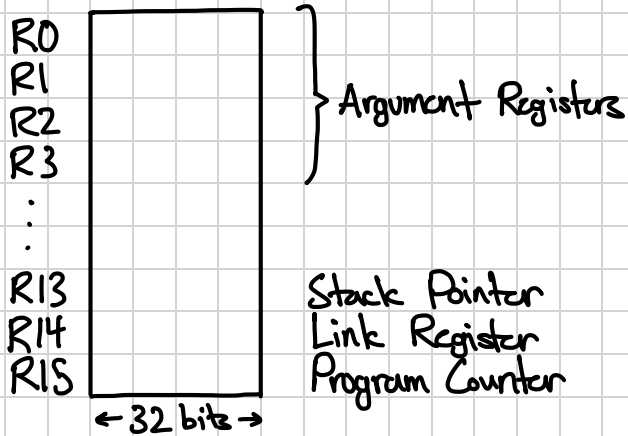


# REGISTERS



# INSTRUCTIONS

## Data movement

- ①  $\text{Reg} \leftarrow \text{Constant (in MEM or immediate)}$
- ②  $\text{Reg} \leftarrow \text{RAM (load LDR)}$
- ③  $\text{Reg} \rightarrow \text{RAM (store STR)}$

$\text{R0} \leftarrow 42$

`MOVS R0, #42`  $\xrightarrow{\text{imm 8-bit unsigned (0-255)}}$

$\text{R1} \leftarrow 100,000$

`LDR R1, =100000`

`LDR R2, =0xff00`

`LDR R3, =-5500`

0x Hex  
0c Octal  
0b Binary

# MEMORY

VI: .long 0x12345678

.long 32 bit  
.short 16 bit  
.byte 8 bit

LDR R0, =VI — LEA-like

LDR R1, [R0]