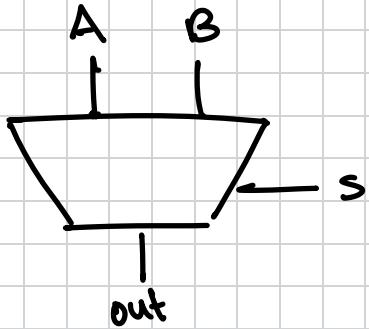
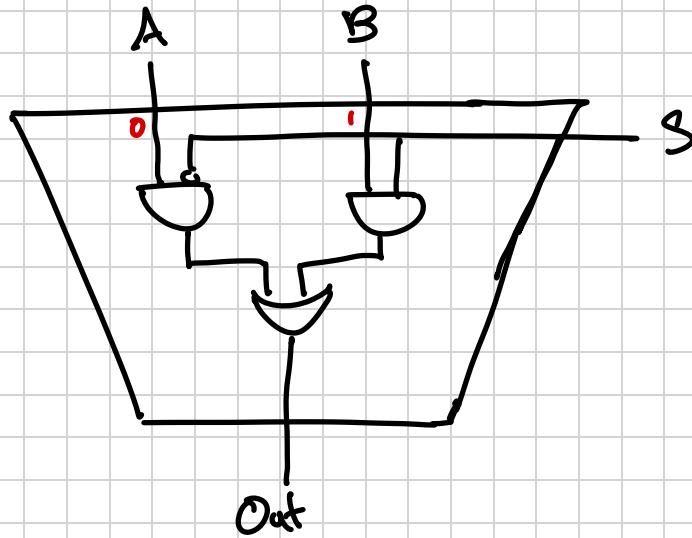


Mux



if $s=0$ then $out = A$
 if $s=1$ then $out = B$

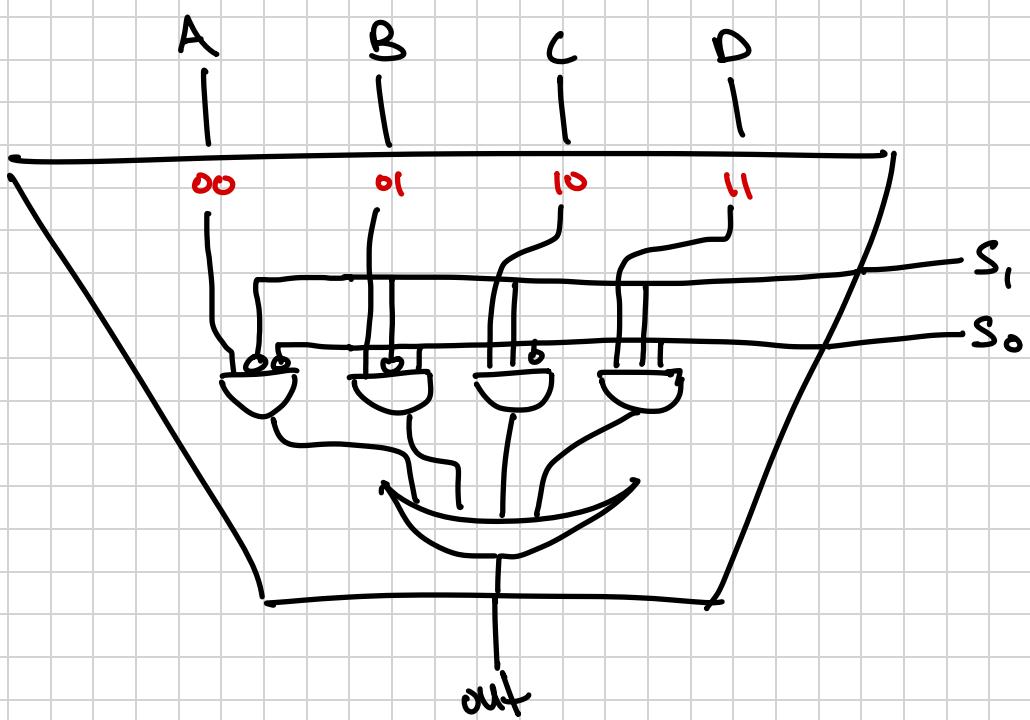


A	B	s	out
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

$$A'B'S + AB'S' + ABS' + ABS$$

$$BS(A' + A) + AS'(B' + B)$$

$$BS + AS'$$



"Vending Machine" - Retrieves value based on key

Design Process

System Description



Truth Table



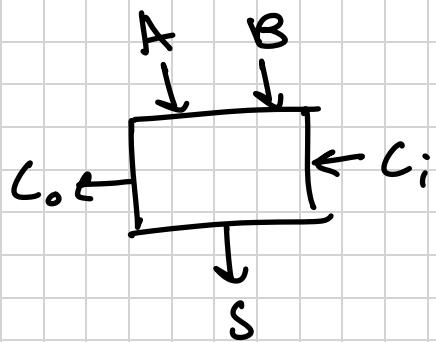
Boolean Expressions



Logic Circuit

Programmable
Logic
Array

Adder 1-bit



SOP S:

$$A'B'C_i + A'BC'i + ABC'i + ABCi$$

SOP Co:

$$A'B'C_i + AB'C_i + ABC'i + ABCi$$

A	B	C _i	S	C _o
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Adder 4-bit

