
Software Transactional Memory

C++14 STM

Research Manual

Zoltan Fuzesi

November 5, 2017

Student ID: C00197361

Supervisor: Joseph Kehoe

Institute of Technology Carlow

Software Engineering

Institiúid Teicneolaíochta Cheatharlach



INSTITUTE *of*
TECHNOLOGY
CARLOW

At the Heart of South Leinster

Contents

1	Abstract	1
2	Introduction	1
3	Concurrency	2
3.1	What is concurrency	2
3.2	Why need concurrency	3
3.3	Single-core, Multi-core	3
3.4	Parallel vs. Concurrent	4
3.5	Process vs. Thread	5
3.6	Issues	6
3.6.1	Mutual Exclusion and Semaphores	7
3.6.2	Dedlock	7
3.6.3	Live-lock	7
3.6.4	Starvation	8
3.7	Concurrency Conclusion	9
3.7.1	Advantages	9
3.7.2	Disadvantages	9
4	Software Transactional Memory (STM)	10
4.1	Transaction.	10
4.2	Transactional Memory(TM).	11
4.2.1	How does it work?	12
4.3	STM algorithms	15
4.3.1	Lock based concurrency	15
4.3.2	Actor based concurrency	16
4.3.3	STM implementation	16
4.4	Why use STM?	16
4.5	STM Code semantics.	17
4.5.1	Four ways to demarcate a transaction	17
4.5.2	Transactional syntaxes	20
4.5.3	Atomic variables member functions:	22
4.5.4	Atomic variables specialized member functions:	23
4.6	Compiler support.	24
4.7	STM Conclusion.	24
4.7.1	Advantage of STM.	25

4.7.2	Disadvantage of STM.	25
5	STM Libraries	26
5.1	Bloom Filter	26
5.2	Tiny STM	26
5.3	TL2 STM	26
5.4	fastSTM	26
5.5	Haskell and Clojure	27
5.6	Scala STM	27
5.7	Conclusion of STM libraries	27
6	C++14/17	28
6.1	C++.	28
6.2	New features in C++14	28
6.3	New features in C++17	29
6.4	C++ Libraries	38
7	Platforms	42
7.1	Integrated Development Environment(IDE)	43
7.2	Compilers	46
7.2.1	GCC and G++	48
7.2.2	MVSC	48
7.2.3	CLANG	48
7.3	Conclusion of platform	49
8	Conclusion	50
8.1	Concurrency as a base problem	50
8.2	Software Transactional Memory	50
8.3	Platform and Compilers	50
9	Bibliography	52

1 Abstract

The purpose of this research document is to find all the answers associated with the Software Transactional Memory (STM) implementation in C++ programming language. Discover the language syntaxes for transactional statements, how other STM libraries are implemented and what kind of algorithms or logic should be used in the Software Transactional Memory library implementation. This paper examines the way of replacing the locks with software based transactions using C++14 programming language features.

2 Introduction

The Software Transactional memory is a promising alternative to the concurrency control in shared memory systems. Why do we need the STM solution if we can control the concurrent programming with mutex and semaphore. If the Transaction memory has been solved in the Hardware level why it is important to implement at software level as well. What is the difference between the diverse versions of C++ languages. Which is the best IDE and compiler to implement the STM library in C++ language.

The aim of this project to create a C++ Software Transactional memory library to control concurrent programming features without locks. Software Transactional memory is a software solution for concurrency control in shared memory access. It provides lock free solution for threaded programming which reduces the challenges in concurrent programming. STM use Transactions to realize automatic conflicts resolution through atomic transactions and avoid the classical lock based associated problems like deadlock and livelock.

The research paper contains several sections to describe the main questions around the STM implementation. The first section begins with the concurrent and parallel concepts, such as why we need it and what is the problem with it. What is the difference between concurrent and parallel programming. What is the problem with concurrent programming and how to control the threads in concurrent programming environment.

The next section discusses the STM concepts and how it works. Discover the benefits and the problems associated with the software level transactional mem-

ory, give sample code for atomic transaction and describe the logical solution, what I need to implement in the project.

The following section describes some of the previous STM library implementations in different programming languages with very short explanation.

The C++14/17 section will mostly list the differences between the two versions of C++ release and the C++17 version libraries.

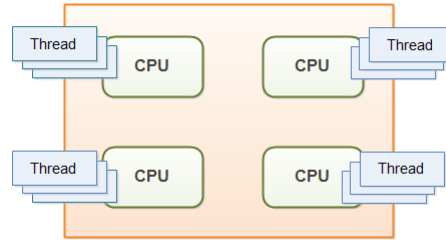
The last section discusses the difference between the most popular Integrated Development Environment(IDE) functionalities and the available compilers to the different platforms. This section helps me to choose the best IDE and the compiler version to create the STM library.

3 Concurrency

3.1 What is concurrency

Concurrency in the real-world example could be when many things are happening at the same time simultaneously with someone, example eating the breakfast, reading the newspaper, listening to music and typing text messages. In computer science concurrency is a stage of system/process where multiple processes are executing at the same time and may or may not sharing resources and communicating with each other. The concurrency in a system program required parallel processing, but the challenge with concurrent programming, that the running processes on the processor is running sequentially. To achieve concurrent processing, the process need to split into multiple smaller processes, and running them concurrently. On the modern computers where is more core available inside the CPU (Central Processing Unit) and the programming module not using concurrent programming features the processes can run parallel at same time on the different cores, but those processes are completely independent and not sharing resources.

To execute a process on the CPU cores concurrently/parallel at the same time, that requires threaded programming. On a single core processor never able to get true concurrency, but true concurrency with threaded programming can happen on multi-core processors.*Figure 1.*

Figure 1: *Concurrency multicore-multithread* [19]

3.2 Why need concurrency

Moore's law, is the number of transistors in an integrated circuit doubles approximately every 18 to 24 months. Today's processors have hundreds of millions of transistors embedded into the silicon chip. Moore's law held true until around 2005, since the silicon chips are nearly reaching the maximum number of transistors of the physical nature of processor material. Actually, the development of silicon chips is running into a problem with the brick wall. This brick wall is when the size of the transistors are shrinking, but couldn't scale down the power consumption and the heat any more. So, instead of adding more transistors, the better solution was to add more execution units (cores) into the CPU (central processing unit). The more cores can take more instructions at the same time in parallel manner, but the sequential programming or single-threaded application performs the same no matter how many cores the CPU has. The parallel, concurrent/multi-threaded programming design is a way to split the process into multiple smaller processes and run them in parallel on the CPU cores, that offers much better throughput to the CPU.[40]

3.3 Single-core, Multi-core

Back in days the computers had only one core in the CPU, and all the processes, running programs were executed sequentially on the single-core CPU one by one with various execution times, depending on the operating system queue logic. In other words, when more than one process/program were executed on the single-core system, those processes needed to wait in the queue while executed sequentially on the same core on the CPU. The process can run concurrently on a single-core processor, but the threads are accessing the process resources at different times.

due the core limitation. True concurrency cannot happen without multi-core system. *Figure 2-3.*

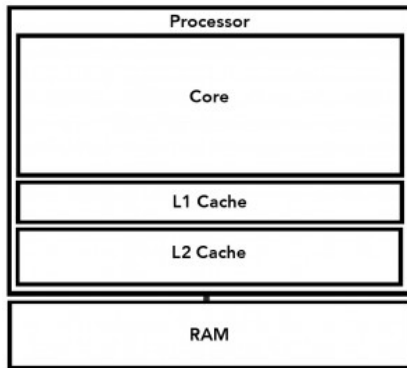


Figure 2: *Single-core CPU.*[24]

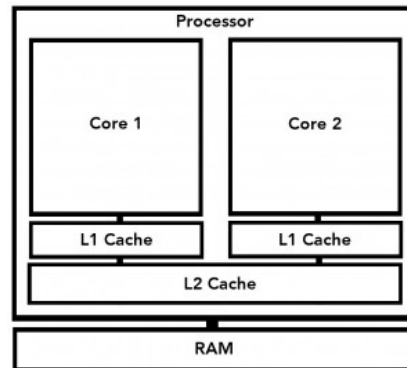


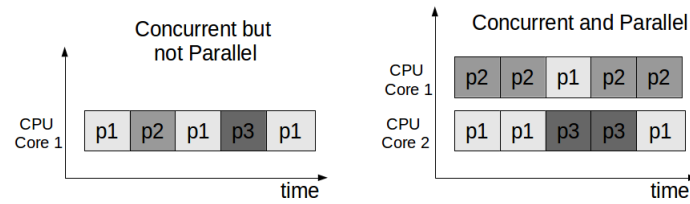
Figure 3: *Multi-core CPU.*[24]

On the modern computers the CPU has more than one core, the processes can run parallel on the different cores at the same time. So, if multiple processes running on the operating system, those processes can be executing same time on the same processor on different cores. With these changes of the CPU architecture the parallel computing has been achieved but the single processes still running sequentially on the separated cores inside the processor without threaded programming. Therefore, the true concurrency is the parallel execution on the different cores at the same time, with diverse threads form the same process.

3.4 Parallel vs. Concurrent

The parallel computing is executing multiple processes simultaneously on separated cores, while concurrent computing is composing independent processes working together.

Concurrent computing can happen on single core processor, however if programming design apply threaded programming, that cant help to run process faster on single core. The process with threaded programming design can run faster on multi-core, since the different processes and the multiple threads can run parallel on multiple cores. *Figure 4.*

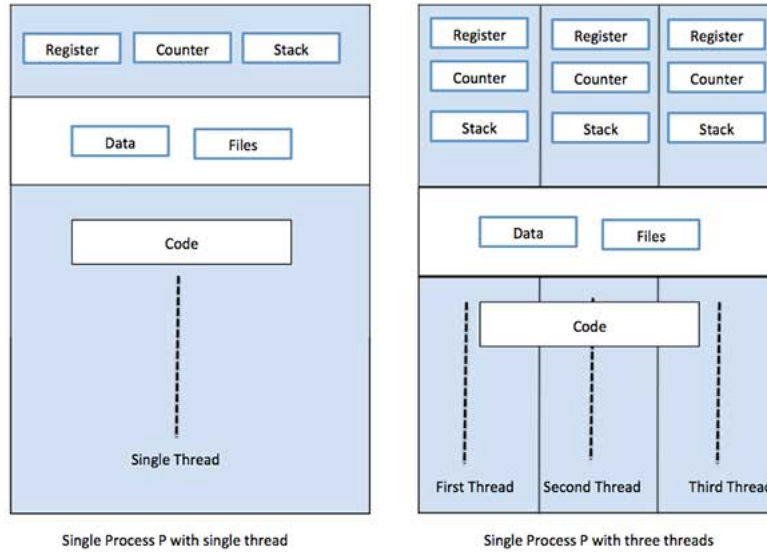
Figure 4: *Concurrent vs. Concurrent and Parallel* [26]

3.5 Process vs. Thread

Process (heavy-weight) is a running instance single thread of program in execution. Each process has its own address space in the memory, that is not shared between the running processes. If two processes need to communicate, they must use IPC (Inter Process Communication) which allows processes to communicate and synchronize their actions. This Inter Process Communication happen through the OS (operating system). There are two types of communications such as message passing and shared memory. The OS controlling how to use the shared memory and use lock to close out the other process while the another read or write the memory space. The other way for communication between processes is the message passing, where pipe created between the processes and use consumer-producer synchronization. To create processes can be expensive, as each process has its own address space, program code, file handles.

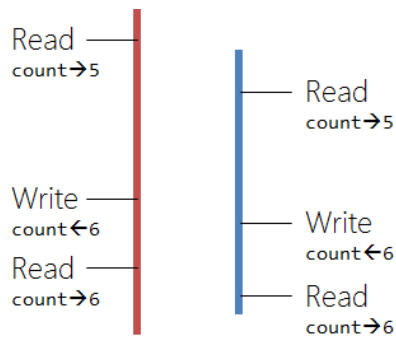
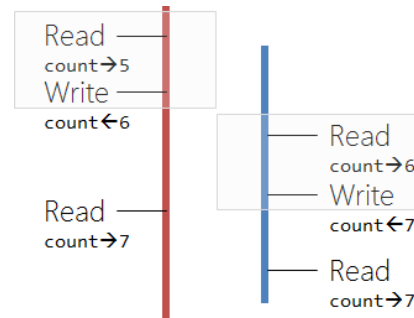
Thread is a Light-weight process(LWP). Using threads is a way to improve the performance of the process (running application) through parallelism and concurrency. When the program executed and the instruction-set loaded into the memory, then the process has created. Using concurrent programming design, to create any number of threads associated with the process to run concurrently, parallel on the CPU cores. Those threads are belonging to the same process and sharing the same memory space. Because the threads are sharing the same memory space, therefore use of shared resources leading to new way of resource control. This resource control derives to new issues that requires reliable techniques to synchronize their actions, execution, memory allocation and scheduling time.[39]

Figure 5.

Figure 5: *Process vs. Multiple Threads* [26]

3.6 Issues

The multi-threaded programming use parallel and concurrent programming design, that derives to new issues and require creating locks on shared variables/memory spaces. The unsynchronized shared memory access introduces to race conditions. Race condition is when more than one thread try to access the shared memory space and try to read and write the memory at the same time. If the race condition is not controlled and the threads accessing and modifying the memory at the same time, that cause unexpected behaviour of the process. *Figure 6.*

Figure 6: *Unsynchronized*[39].Figure 7: *Synchronized*[13].

To solve race condition, in concurrent programming design must use mutex and semaphores to control the shared memory space/shared variables access by the multiple threads.

3.6.1 Mutual Exclusion and Semaphores

Mutex is a mutual exclusive flag. Mutual exclusion is a type of gate that is ensuring to reduce accessibility to a shared resource to only one thread at the same time. The other solution is to use Semaphores. Semaphores is somewhat like an integer value and can do only two operations on it, increment and decrement. Those two operations can happen through methods like Signal and Wait. If the value of the Semaphore is zero, then other threads cannot be pass the line where the method call happened.[25] So, mutual exclusion and Semaphores can solve the race conditions problem, but may lead to other problems like Deadlock - Live lock and Starvation.

3.6.2 Deadlock

Deadlock is a condition, when two threads try to access the same shared resource and waiting for each other to do some action. Deadlock often arises because the control of semaphore or mutex applied logically wrong way to protect the critical section to access by more than one thread at the same time.*Figure 8.*

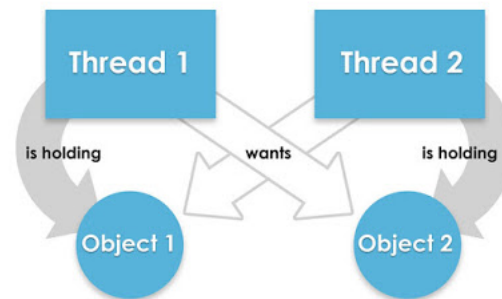


Figure 8: *Deadlock* [31].

3.6.3 Live-lock

Live-lock is a situation when two threads are repeatedly access the critical section and the program detect the deadlock situation and release the lock to the

other thread. After, both threads are trying to access the critical section and release again, but no progress being made, hence the name is Livelock. *Figure 9.*

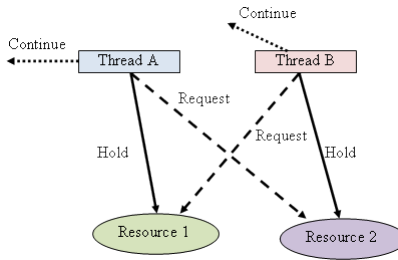


Figure 9: *Livelock situation* [17].

3.6.4 Starvation

Starvation is the state when some threads are making progress, but other threads are waiting in the queue for execution. The waiting thread priority can be too low and never will get out of the queue to make progress. *Figure 10.*

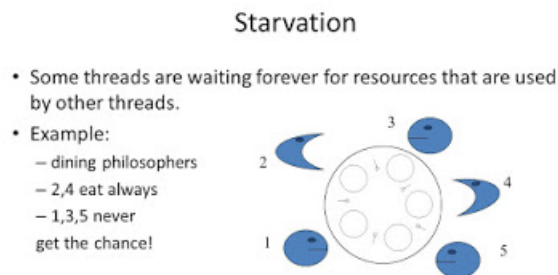


Figure 10: *Starvation* [34].

3.7 Concurrency Conclusion

There are advantages and disadvantages of concurrent and parallel programming pattern. With concurrent and parallel programming the applications are able to handling multitasking within the application. While the user interacting with the user interface, the program able to perform other tasks in the background. For instance, the web browser able to render high number of web pages and the browser tabs at the same time perform number of downloads in the background.

3.7.1 Advantages

The main advantages of the concurrent/multi-threaded application are:

Performance:

The processes in which executed with multi-threaded technique can reduce the computation time and resolve problem in faster manner.

Reactive:

The single program able to run multitasking in the background while user keep interacting with the application(multitasking).

Maximum CPU throughput:

Parallel concurrent programming design only way to exploit the multi-core CPU in efficient manner.

High performance:

While single threaded application performs the same no matter how many cores in the CPU, the concurrent application uses all the cores in the CPU.

3.7.2 Disadvantages

The main disadvantages of the concurrent/multi-threaded application are:

Difficult to write code:

Multi-threaded applications are not easy to write.

Debugging:

Much harder to regenerate the same error in the multi-threaded application and harder to debug to.

4 Software Transactional Memory (STM)

"Software Transactional Memory (STM) is a mechanism that allows transactions on memory similar to database transactions."[42] STM is a concurrency control for threaded programming, that does not use locks and semaphores as its primary locking mechanism. However, concurrent programming with locks and semaphores solving the issues to access shared memory space by more than one thread at the same time, but it is still very complicate to write efficient programs by the programmers specially if the software is working in big scale.

Software Transactional memory use transaction instead of locks and semaphores, that is similar to the database systems. The main purpose of STM to avoid concurrent programming issues and guaranties safety operations on shared memory space.[21]

The hardware support for STM is the Hardware Transactional memory(HTM). A Hardware Transactional Memory (HTM) system uses multi-word(multi-fixed size piece of data) synchronization operations of the CPU for threads to manipulating shared data.

Transactions are very important part of the STM solution. There are four properties of the transaction operations, it know as the ACID properties.

1. **Atomic** operations can guaranties that all of the operations happens, or none of them.
2. **Consistency** all data will be consistent and never get data violated.
3. **Isolation** all the transactions within the system completely separated and they can not access each other data until not finished.
4. **Durability** guaranteed that all the data changes will be recorded after the transaction has finished and available to the other transactions. **But it is not applies to the Memory Transaction.**

4.1 Transaction.

In real life example for transaction is a money transfer between two account in the bank. When someone wants to transfer some amount of money from his/her

bank account to an another bank account, the biggest problem is the state of the transferred amount of money. How the system should implement the transaction? First decrement the source bank account and the increment the destination bank account with the amount was sent. The other solution could be if the target account in incremented and then the source decremented? What's happen with the transferred money if the system goes down during the transaction before the amount arrived to the destination, but the source account decremented already?

The solution for those questions is the Transaction. The transaction is a way of processing, where all the separated processes need to succeed, otherwise the transaction will fail. In the bank transfer case, if the destination bank account is not available, then all the processes involved in the transaction will rolling back, and the amount of money goes back to the source bank account.*Figure 9.*

```
void transfer(A, B, amount)      void transfer(B, A, amount)
  atomic {                        atomic {
    withdraw(A, amount);          withdraw(B, amount);
    deposit(B, amount);           deposit(A, amount);
  }                               }
```

Figure 11: *Transaction memory* [5].

4.2 Transactional Memory(TM).

Transaction Memory is an approach of concurrency management and it is called optimistic, because assume multiple transactions can complete frequently without interfering with each other. Transactional Memory is a synchronization for the threaded/multi-core programming to simplify concurrent programming to avoid of the concurrent programming issues like Deadlock, Race Condition, Corruption, Lost wakeup. The Transactional Memory control provides atomic and isolated execution for region of codes. The abstraction of the atomic transaction required a hardware mechanism, that detect conflicts and revert back all the changes made on the shared memory space.

When any thread enters the block of code that is use atomic execution, the other threads can't access or see any modification until the thread is not finished

with the code execution and leave the block. This software design can avoid of unnecessary concurrent programming conflicts and the shared variables overwriting issues. [4]

The concurrency control mechanism analogous to the database transactions that controls the access to the shared memory in concurrent programming. The STM functions are alternative to the lock-based synchronization mechanism, but it is implemented in a lock-free way. The transaction is when a block of code executes series of actions on the shared memory space. [1] While database transaction as a result are written to the disk, until Software Transactional Memory has a life-time while the code executing and only change the state of the shared variable by the threads in the memory. *Figure 12.*

Programming with transactional memory



```

void deposit(account, amount){
    lock(account);
    int t = bank.get(account);
    t = t + amount;
    bank.put(account, t);
    unlock(account);
}

void deposit(account, amount){
    atomic {
        int t = bank.get(account);
        t = t + amount;
        bank.put(account, t);
    }
}

```

Figure 12: *Transaction memory* [5].

4.2.1 How does it work?

The Transactional Memory software solution allowing the transactions within the process to be executed in parallel and monitoring the access to the shared/transaction variables. With this transaction synchronization monitoring solution the software is able to detect a conflict between transactions and allowing only one transaction at the same time to access the shared variable and rolling back to the other transactions. So, because the Transaction Memory is lock free, the threads are not waiting to the lock, all of them can access the atomic block in parallel and make changes on the shared memory space. Increasing the associated version number with the memory block and assuming there is no conflict happens. When the threads are finish with the atomic block execution, they are checking the version number associated with the memory block in which they are working on and if the version number are higher then the version number use by the thread, that is indicates, other threads are made changes on the same memory space. In this case

the transaction can not happen and must to restart/roll-back caused by the version number conflict. In this case the thread must roll-back all the changes happened on the variable and restart the transaction. This process will goes on until all the transaction finish they execution and the threads are joining back to the main process. [3]Figure 13.

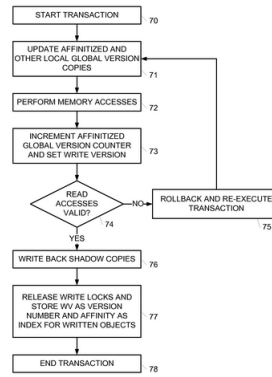


Figure 13: *Transaction rollback* [30].

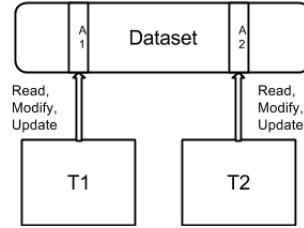
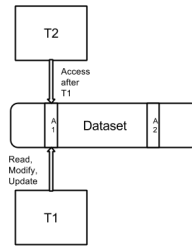
The proper STM implementation at least must has two properties:

1. **Atomic.** Grouped set of generic operation executed on the shared memory space or fail them all.
2. **Serializable.** The current state of the data structure can be reconstructed by simply replaying the transaction by sequential order from start to end.[11]

The diagram below shows the single shared data-set space use by two threads. The data can be anything from array, binary tree, hash map etc. However, the data-set shared, but the two threads are accessing different variables in the data-set. Those two variable has a unique memory address, thus the threads can update the variables concurrently.Figure 14.

The simple illustration of Software Transactional Memory, need at least two thread and those threads must to access the same memory space at the same time to try update the shared variable. Figure 15.

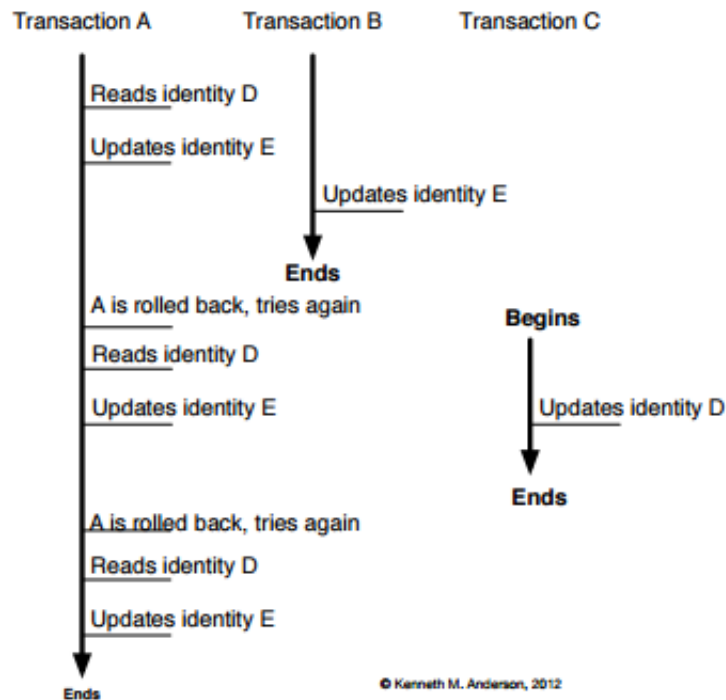
When the first thread(T1) get ownership of the memory address and the second thread(T2) try to access the same memory space, then the second thread will

Figure 14: *Shared data-set by two threads. [11].*Figure 15: *STM implementation with two threads. [11].*

fail. Because, the software use STM implementation, the method must be use atomic and the second thread keep trying get access to the memory space. When the first thread(T1) finish it is action with the shared variable, then releasing the memory space and the second thread(T2) can execute the it's transaction on the shared variable.[11]

All updates occur via transactions:[22]Figure 16.

1. If only transaction A is updating identity B, no locks are encountered.
2. If transactions A and B are updating identity C at the same time
3. The first thread get access to memory space and the others are rolled back and retried.

Figure 16: *STM rolling back atomic function.* [22].

4.3 STM algorithms

4.3.1 Lock based concurrency

Is a mechanism to control threads to access shared memory space. It uses locks to take control or ownership of the mutex lock before any other thread can access then critical section to make changes on the shared variable. Whit this way of programming design the object is associated with the lock.[41]

Benefits : in very small system easier to implement lock based concurrency.

Issues: needs explicit control of lock, that can be very difficult in big system. Easy to produce deadlock, livelock.

4.3.2 Actor based concurrency

The actor model is another way to control concurrent programming. In this way the actors are separate light-weight threads. Instead of taking ownership of the lock, the actors are communicating with each other via message passing. *cite*Volkmann

Benefit : memory not shared.

Issues : Difficult to coordinate actors communication, big messages.

4.3.3 STM implementation

One way to avoid corrupting data in shared memory is to provide immutable data structures or collection classes. If data cannot be changed, then it doesn't need to be protected. This way the program creates the same data structure in memory. It can be slow and use a lot of memory.[41]

Another option to implement the STM algorithm is to use a time stamp or version number on the transactions. The time stamp or version number is a global progress condition variable that is used to compare values at the beginning and at the end of the transaction. The progress condition will be used to determine the commit time or the number of commits by the threads in the transactional processes.

When a thread accesses the transactional function, it will check the global version number and store it. At the end of the transaction, it will compare the stored version number with the global version number. If the stored version number is lower than the global one, the transaction aborts and restarts. If the version number is the same as the global one, it will commit the changes in the shared memory and leave the transactional block and increase the global version number. This process will continue until all the threads finish with their function in the transactional process.[32]

4.4 Why use STM?

Why should programmers use Software Transactional Memory implementation if semaphores and mutexes are solving the accessibility problem with

the shared variables. Because, managing concurrency with locks by the programmers are very difficult. For small, trivial system, managing the locking solution are possible, but scaling that system into more complex that is very difficult and unnecessary.

In other worlds the STM try to simplifying the concurrent programming. In threaded programming must use locks and condition variables. With STM easier to write clear code, since don't need to use locks and condition variables and programmer can specify code regions are executing atomically.

Locking provides poor support for code compositions and reuse. The software that use transactional programming features, every shared memory access divided into transactions, which are indented to be executed atomically. All the transactions operations are atomically in which all changes takes place or all rolling back if any part of the transactions has failed.[1]

4.5 STM Code semantics.

"C++ supports transactional memory in two flavours: *synchronized blocks* and *atomic blocks*.[15] The following are sample codes for atomic and synchronized transactional declaration.

In transaction there is no default action, explicit need to tell what should be happen:

4.5.1 Four ways to demarcate a transaction

Atomic and synchronized -like code region :[37]

```
synchronized
{
    //synchronized block behave as a global block ,
    //not rolling back
}
atomic_noexcept
{
    //no exeption inside the block
    //if exception throws std::abort will be called
    //and the program aborts , and roll-back
```

```

}
atomic_cancel
{
// default std::abort called , but if transaction_safe
// exception throws the cause transaction to rollback
}
atomic_commit
{
// if exception occurs cause the transaction to commit
}

transaction_safe_exceptions : std::bad_alloc ,
std::bad_array_length , std::bad_array_new_length ,
std::bad_cast , std::bad_typeid , std::bad_exception ,
std::exception ,

```

The synchronized - Allows non-transactional synchronization. It can not cause deadlock because it is not protects the memory block but protects the total program. However, it is not transaction safe.

The atomic * - Allows transactional-safe code, and synchronization.

The atomic commit - transaction commit everything done so far.

The atomic noexception - Terminates the program with undefined behaviour.

The atomic cancel - Will roll back the transaction. [37]

Hello world example .

```

void foo()
{
    cout << "Hello World!" << endl;
}

```

If more then one thread executing the same code block the output can be printed in any order from half printed sentence mixed with the another printed sentence :

Hello Hello world!
world!

OR

Hello world!
Hello world!

But if the foo function use transaction the output could be like the follow:

```
void foo()
{
    cout << "Hello World!" << endl;
}
// Thread 1                                // Thread2
atomic_noexcept                            atomic_noexcept
{
    foo();
}
{
    foo();
}
```

The output can be like the follow:

Hello Hello ... Hello

It could be print more Hello because one of the transaction rolls back and restart the transaction!

The solution for the threaded function to print out the two sentence in the correct way should use the synchronized transaction:

```
void foo()
{
    cout << "Hello World!" << endl;
}
// Thread 1                                // Thread2
synchronized                            synchronized
{
    foo();
}
{
    foo();
}
```

The output will be in the following order:

Hello world!

Hello world!

The synchronized function solve the problem but it is not able to roll back the transaction.

In transactional function should not use any printing statement, because if the

transaction rolls back then the function restart and prints again and again until there is no conflict happens in the function. So, only computations should be in the transactional functions!

The non-virtual functions can be declared implicitly as a safe function.

```
void foo() { x++; }
```

```
atomic_noexcept
{
    foo();
}
```

Call the foo function is safe after the atomic definition. It can help with the template functions. In this way the template function can be reuse as a safe function.

```
template <class T>
void func(int& x, T t)
{
    f(x++);
}
void(*p1)(int) transaction_safe;
atomic_noexcept
{
    func(v, p1);
}
```

4.5.2 Transactional syntaxes

[16]

```
--transaction_relaxed
{
    // statement
}
--transaction_atomic
{
    // statement
}
--transaction_safe
```

```
__transaction_safe_dynamic  
__transaction_unsafe  
__transaction_cancel
```

```
__transaction_callable  
__transaction_may_cancel_outer  
__transaction_pure
```

```
__transaction_safe_dynamic
```

```
__transaction_wrap
```

The transaction atomic - Other execution code can not see intermediate atomic transaction, can be cancelled.

The transaction relaxed - Other execution code can not see intermediate atomic transaction, can not be cancelled.

The transaction safe - Code block is use safe transaction.

The transaction unsafe - Code block is not transaction safe.

The transaction safe dynamic - The qualifier may only appear in a function declarator that declares a virtual function in a class definition, but it should be declared as transaction safe. If any function that declared synchronized or atomic call the transaction safe dynamic function can cause undefined behaviour.

Some of the declaration has no correct documentation. [37]

```
__transaction  
__transaction_atomic  
__transaction_cancel
```

The transaction atomic and transaction relaxed - Similar to atomic commit and synchronized.[37]

The following sample codes are demonstrating the atomic, transaction safe and synchronized functions.


```

int function(){
    int x = 0;
    atomic{
        x++;
        return x;
    }
}

int function() transaction_safe{
    x++;
}

void doSomething(){
    _Synchronized {
        value = doSomethingElse();
    }
}

```

Explicit transaction safe declaration for template class or template class function:
[18]

```

template<class T>
void f(T) transaction_safe;

template<>
void f(bool); // not transaction-safe

```

To initialize the variables in c++ as an atomic variable need use the following variable declarations with the variable type and the assignment:

```

std::atomic<variable_type>variable_name;
std::atomic_store(&variable_name, value);

```

Or can be done in one line assignment:

```

std::atomic<variable_type>variable_name(value);

```

4.5.3 Atomic variables member functions:

`fetch_add`

Atomically adds the argument to the value stored in the atomic object and obtains the value held previously

`fetch_sub`

Atomically subtracts the argument from the value stored in the atomic object and obtains the value held previously

`fetch_and`

Atomically performs bitwise AND between the argument and the value of the atomic object and obtains the value held previously

`fetch_or`

Atomically performs bitwise OR between the argument and the value of the atomic object and obtains the value held previously.

`fetch_xor`

Atomically performs bitwise XOR between the argument and the value of the atomic object and obtains the value held previously.

4.5.4 Atomic variables specialized member functions:

```
operator++ // increment
operator-- // decrement
operator+= // add
operator-= // subtracts
operator&= // and
operator|= // or
operator^= // xor
variable->store(value) //Modify contained value
variable->load() //Read contained value
CAS – compare and swap
T z = x->exchange(y) //z=x; x=y
bool success = x->compare_exchange_strong(y,z);
    //if x==y make x=z return True
    //Otherwise set y=x and return False
```

Atomic store memory order :[?] The atomic variables can store the value in diverse memory order:

```
syntax : x.store(value, std::memory_order_*);
memory_order_relaxed
//No synchronization in order on other reads and writes
```

```

memory_order_consume
//consume operation on affected memory location
//no reads or write can be reorder before
memory_order_acquire
//acquire on memory space no reads or writes can be
// reorder after all writes visible
memory_order_release
//acquire on memory space no reads or writes can be
// reorder after all writes visible
memory_order_acq_rel
//read-modify-write , acquire and released ,
//no memory reads or writes before and after store
memory_order_seq_cst
//acquire - release plus single total order

```

4.6 Compiler support.

To use the Transactional Memory coding features, might need to update the (Linux) gcc and the g++ compiler on the host computer to the newest version or at least to version 6.1, because the old versions are not supporting these TM syntaxes. However, the compilers are updated and supporting the TM features, need to tell the compiler to use these features. Without the **-fgnu-tm** switch the program with the TM features will not work and the compilation process will fail. As an other option we can add the **-std=c++1y -pthread -fgnu-tm** switch to the compilation code in the Makefile to support threads and the newest gcc and g++ support..

4.7 STM Conclusion.

STM make programmers life easier since don't need to use semaphores and mutex locks to controlling the threads in concurrent programming. In other worlds it simplifies the multi-threaded programming design, easier to read, maintain, extend and working with existing high level objects and modules. STM offers transactional environment without any extra hardware cost.

There are advantages and disadvantages with the Software Transactional Memory.

4.7.1 Advantage of STM.

1. Crossing the the memory barrier.
2. Easier to read, write, extend.
3. Transactional methods and variables.
4. Deadlock and live-lock are prevented.
5. Intended to replace locks and conditions.
6. The changes or memory space happen or rolling back, no middle state.
7. Each transaction can be view as a single-threaded function.
8. Increase of efficiency of parallel systems.
9. Better way to to write lock-free data structure.

4.7.2 Disadvantage of STM.

1. Performance. Because the functions are transactional, the threads may keep trying to access the same memory space good few time and that can cause performance issues.
2. Transaction Memory does not make software application parallel..

5 STM Libraries

Software Transactional Memory are implemented in diverse programming and scripting languages.

5.1 Bloom Filter

"A Bloom filter is a data structure designed to tell you, rapidly and memory-efficiently, whether an element is present in a set."[23] It is a probabilistic data structure, to test whether an object is a member of a set. This data structure use by some STM library to determine the thread is a member of a transaction or not.

5.2 Tiny STM

"The tinySTM system was developed at the University de Neuchtel by Pascal Felber."[27] The tinySTM is word-based and light weighted word-based STM implementation. TinySTM algorithm is derived from lazy snapshot algorithm, that guarantees that the transactions always read from the actual memory state. It is compiles and run on booth 32 and 64-bit operating systems and cross platform since was tested on Unix, Mac OS, and on Windows using cygwin. *"No bloom filter is used for fast membership test in the read- or write-set."*[27]

5.3 TL2 STM

"The Transactional Locking 2 (TL2) is a state-of-the-art word-based software transactional system which is developed and maintained at sun micro-systems."[27] TL2 is a time and lock-based STM system, that used global shared counter and time stamping when data was accessed by object/thread during transaction. This way the memory location temporary locked by the commit time and conflict can be resolved if at least one of the transactions must be cancelled. TL2 store the read and write sets in a simple linked list. *"For fast membership test in the read and write-set a bloom filter is used."*[27]

5.4 fastSTM

"We present fastSTM, a modular word-based software transactional memory library, which supports commit-time and encounter-time locking. Validation of the read- and write-set is done using a global version-clock. To reduce cache

misses we store read- and write-entries together in small blocks of multiple cache line size. For fast membership test in the read and write-set a bloom filter is used.”[27]

5.5 Haskell and Clojure

Clojure is the only language, that the transactional memory has built in. Clojure used Multi Version Concurrency Control(MVCC), multiple logic version of data.”*During transaction a thread watches the snapshot of data for the moment of its start.”[20]*

Haskell is a purely functional language. *”In Haskell the STM library implements transactional memory. It is part of the Haskell Platform. Types ensure that transactions are used correctly and violations cause errors at compile time. In order to execute any action atomically in Haskell it is preceded by the word atomically.”[20]*

5.6 Scala STM

Haskell and Clojure STM implementation have been influence of Scala STM development. It use the Akka framework for parallel programming and it is provide Ref cell that can be changed inside transaction only.[20]

5.7 Conclusion of STM libraries

There are many diverse Software Transactional Memory(STM) implementation built from various of programming languages. Haskell the only programming language that has STM library built in to the platform. However, these libraries use different logic to tracking the threads and the shared variables or memory spaces inside the functions, all has the same purpose. These STM libraries allows to simplify the threaded programming issues, speed up the applications, increase concurrency and with atomic functions and variables to make it thread safe.

6 C++14/17

6.1 C++.

C++ is a general-purpose object-oriented programming (OOP) language, developed by Bjarne Stroustrup, and is an extension of the C language. It is therefore possible to code C++ in a "C style" or "object-oriented style." In certain scenarios, it can be coded in either way and is thus an effective example of a hybrid language.[36] It is one of the most popular language to write driver software, system/applications and embedded firmware.

C++11 is the ISO standard programming language, was approved on August 2011 and it was the major upgrade over C++98/03. C++14 is intended to be a small update over C++11 that include small improvements and bug fixes. It was released on December 2014. The next generation of C++ has arrived on 2017 with lots of major changes.

6.2 New features in C++14

The changes between C++11 and C++14 are very small and not highly significant.

1. auto Enhancement : auto for return type deduction for normal functions.
2. decltype Enhancement : decltype(auto) for type deduction.
3. Template Feature Extension : added support of template variables.
- 4._INITIALIZER Enhancement : Relaxing few restriction of aggregate member initialization.
5. Numeric Literal Enhancement : Support of binary literals.
6. Lambda Enhancement 1 : Generic Lambdas
7. Lambda Enhancement 2 : Lambda captures expressions
8. deprecated Attribute : Deprecated attribute to mark to discourage use of entities.[35]

6.3 New features in C++17

The next generation of C++ has a lots of new features in the language. The most significant updates in C++17 are the follows:[2]

1. auto rules for direct-list-initialization

```
auto x = foo(); // copy-initialization
auto x{foo}; // direct-initialization, initializes an
               initializer_list
int x = foo(); // copy-initialization
int x{foo}; // direct-initialization
```

2. static assert with no message : allows to have conditions without passing the message

```
static_assert-declaration:
static_assert ( constant-expression ) ;
static_assert ( constant-expression , string-literal ) ;
```

3. type name in a template template parameter

```
template <template <typename...> typename Container>
//          used to be invalid ^^^^^^^^
struct foo;

foo<std::vector> my_foo;\
```

4. Removing trigraphs

Removes ??=, ??(, ??> ,

5. Nested namespace definition

```
namespace A {
    namespace B {
        namespace C {
            //
        }
    }
}
```


6. Attributes for namespaces and enumerators

```
enum E {
    foobar = 0,
    foobat [[deprecated]] = foobar
};

E e = foobat; // Emits warning

namespace [[deprecated]] old_stuff{
    void legacy();
}

old_stuff::legacy(); // Emits warning
```

7. u8 character literals

```
char x_ascii = { u'x' };
```

8. Allow constant evaluation for all non-type template arguments

```
template<int *p> struct A {};
int n;
A<&n> a; // ok

constexpr int *p() { return &n; }
A<p(>> b; // error before C++17
```

9. Fold Expressions

```
template<typename... Args>
auto SumWithOne(Args... args){
    return (1 + ... + args);
}
```

10. Unary fold expressions and empty parameter packs

11. Remove Deprecated Use of the register Keyword

```
storage-class-specifier:
    register //removed
```

Operator	Value when parameter pack is empty
<code>+</code>	<code>+</code>
<code>++</code>	<code>int()</code>
<code>&</code>	<code>~!</code>
<code>+</code>	<code>int()</code>
<code>&&</code>	<code>true</code>
<code> </code>	<code>false</code>
<code>,</code>	<code>void()</code>

Figure 17: *Unary fold expressions.* [2].

```

static
thread_local
extern
mutable

```

12. Remove Deprecated operator++(bool)

```

VQ T& operator++(VQ T&);
T operator++(VQ T&, int);

```

13. Removing Deprecated Exception Specifications from C++17

```

void legacy() throw(something)
// remove ->throw(something)
try
{
    // function body as before
}
catch(const something&) {
    throw;
}
catch(...) {
    terminate();
}

```

14. Make exception specifications part of the type system

```

void (*p)();
void (**pp)() noexcept = &p;
// error: cannot convert to pointer
//to noexcept function

struct S { typedef void (*p)(); operator p(); };

```

```
void (*q)() noexcept = S(); // error: cannot convert
                             to pointer to noexcept function
```

15. Aggregate initialization of classes with base classes

```
struct base { int a1, a2; };
struct derived : base { int b1; };

derived d1{{1, 2}, 3};
// full explicit initialization
derived d1{{}}, 1};
// the base is value initialized
```

16. Lambda capture of *this

```
struct S {
int x ;
void f() {
// The following lambda captures are currently identical
auto a = [&]() { x = 42 ; }
// OK: transformed to (*this).x
auto b = [=]() { x = 43 ; }
// OK: transformed to (*this).x
a();
assert( x == 42 );
b();
assert( x == 43 );
}
};
```

17. Using attribute namespaces without repetition

```
FROM
void f() {
    [[rpr::kernel, rpr::target(cpu, gpu)]]
    // repetition
    do-task();
}
TO
```

```
void f() {  
    [[using rpr: kernel, target(cpu, gpu)]]  
    do-task();  
}
```

18. Dynamic memory allocation for over-aligned data

```
class alignas(16) float4 {  
    float f[4];  
};  
float4 *p = new float4[1000];
```

19. has include in preprocessor conditionals

```
#if __has_include(<optional>)  
# include <optional>  
# define have_optional 1  
#elif __has_include(<experimental/optional>)  
# include <experimental/optional>  
# define have_optional 1  
# define experimental_optional 1  
#else  
# define have_optional 0  
#endif
```

20. Template argument deduction for class templates

```
void f(std::pair<int, char>);  
  
f(std::pair<int, char>(42, 'z'));
```

21. Non-type template parameters with auto type

```
template <auto value> void f() { }  
f<10>(); // deduces int
```

22. Guaranteed copy elision

```
Foo f() {  
    return {};
```

```

    }

    int main() {
        auto &&foo = f();
    }

```

23. New specification for inheriting constructors

```

// Inheriting constructor parameters are no longer
// copied
struct A { A(const A&) = delete; A(int); };
struct B { B(A); void f(A); };
struct C : B { using B::B; using B::f; };
C c({0}); // was ill-formed, now ok (no copy made)
c.f({0}); // ok (unchanged)

```

24. Direct-list-initialization of enumerations

```

enum class Handle : uint32_t { Invalid = 0 };
Handle h { 42 }; // OK

```

25. Stricter expression evaluation order

```

// unspecified behaviour below!
f(i++, i);

v[i] = i++;

std::map<int, int> m;
m[0] = m.size(); // {{0, 0}} or {{0, 1}} ?

```

26. constexpr lambda expressions

```

constexpr auto ID = [] (int n) { return n; };
constexpr int I = ID(3);
static_assert(I == 3);

constexpr int AddEleven(int n) {
    // Initialization of the 'data member' for n can
    occur within a constant expression since 'n' is

```

```

    of literal type.
    return [n] { return n + 11; }();
}
static_assert(AddEleven(5) == 16);

```

27. Different begin and end types in range-based for

From

```

{
    auto && __range = for-range-initializer;
    for ( auto __begin = begin-expr,
          __end = end-expr;
          __begin != __end;
          ++__begin ) {
        for-range-declaration = *__begin;
        statement
    }
}

```

To

```

{
    auto && __range = for-range-initializer;
    auto __begin = begin-expr;
    auto __end = end-expr;
    for ( ; __begin != __end; ++__begin ) {
        for-range-declaration = *__begin;
        statement
    }
}

```

28. fallthrough attribute

```

switch (c) {
case 'a':
    f()    // Warning emitted, fallthrough
           is perhaps a programmer error
case 'b':
    g()
[[fallthrough]]    // Warning suppressed,
fallthrough is intentional

```

```

    case 'c':
        h()
    }

```

29. nodiscard attribute

```

[[nodiscard]] int foo();
void bar() {
    foo(); // Warning emitted, return value of a
           // nodiscard function is discarded
}

```

30. maybe_unused attribute

```

static void impl1() { ... }
// Compilers may warn about this
[[maybe_unused]] static void impl2() { ... }
// Warning suppressed

```

```

void foo() {
    int x = 42;
    // Compilers may warn about this
    [[maybe_unused]] int y = 42;
    // Warning suppressed
}

```

31. Ignore unknown attributes

```

// compilers which don't support
MyCompilerSpecificNamespace will ignore this attribute
[[MyCompilerSpecificNamespace::do_special_thing]]
void foo();

```

32. Pack expansions in using-declarations

```

template <typename... Ts>
struct Overloader : Ts... {
    using Ts::operator()...;
    // [ ]
};

```

33. Structured Binding Declarations

```
From
int a = 0;
double b = 0.0;
long c = 0;
std::tie(a, b, c) = tuple;
// a, b, c need to be declared first
To
auto [ a, b, c ] = tuple;
```

34. init-statements for if and switch

```
if (auto val = GetValue(); condition(val))
    // on success
else
    // on false ...
```

35. Inline variables

```
struct MyClass
{
    inline static const int sValue = 777;
};
```

36. DR: Matching of template template-arguments excludes compatible templates

```
template <template <typename> typename Container>
struct A
{
    Container<int>      m_ints;
    Container<double>  m_doubles;
};
```

37. std uncaught exceptions()

```
int uncaught_exceptions() noexcept;
```

38. constexpr if-statements


```
template <typename T>
auto get_value(T t) {
    if constexpr (std::is_arithmetic_v<T>) {
        //...
    }
    else {
        //...
    }
}
```

6.4 C++ Libraries

The C++ Object Oriented Programming language including a Standard Library what is a collection of classes and functions and the Standard Function Library that are inherited of C and part of the C++ ISO standard.[38]

The Standard Libraries provide several interface to manipulate data, function objects, string, data structures through specific syntax and semantics of generics algorithms to the C++ programming language for daily task like find the fraction of a number, printing to the screen, threaded programming, iterations and much more.

[39]

1. Standard Function Library : General purpose, stand-alone functions and not part of nay class.
 - I/O
 - String and character handling,
 - Mathematical,
 - Time, date, and localization,
 - Dynamic allocation,
 - Miscellaneous,
 - Wide-character functions,
2. Object Oriented Class Libraries : Collection of Classes and inherited functions.

- The Standard C++ I/O Classes,
 - iosfwd
 - ios
 - istream
 - istream
 - ostream
 - fstream
 - sstream
 - stringstream
 - iomanip
 - streambuf
 - cstdio
- Thread Support libraries,
 - thread
 - mutex
 - shared mutex
 - future
 - condition variable
- Atomic operation library,
 - atomic
- Filesystem library,
 - filesystem
- Regular Expression library,
 - regex
- Localization library,
 - locale
 - locale
 - codecvt
- The String Class,
 - ctype
 - ctype

- `cstring`
 - `cwchar`
 - `cucchar`
 - `string`
 - `string view`
 - `charconv`
- The Numeric Classes,
 - `cmath`
 - `complex`
 - `valarray`
 - `random`
 - `numeric`
 - `ratio`
 - `cfenv`
- The STL Container Classes,
 - `array`
 - `vector`
 - `deque`
 - `list`
 - `forward list`
 - `set`
 - `map`
 - `unordered list`
 - `unordered map`
 - `stack`
 - `queue`
- The STL Algorithms,
 - `algorithm`
 - `execution`
- The STL Iterators,
 - `iterator`

- The Localization library,
 - locale
 - clocale
 - codecvt
- Exception Handling Classes,
 - exception
 - stdexcept
 - cassert
 - system error
 - cerno
- Dynamic memory management,
 - new
 - memory
 - scoped allocator
 - memory resource
- Utilities library
 - cstdlib
 - csignal
 - csetjmp
 - cstdarg
 - typeinfo
 - typedindex
 - typed traits
 - bitset
 - functional
 - utility
 - ctime
 - chrono
 - cstdint
 - initializer list
 - tuple
 - any

- optional
- variant

7 Platforms

C++ is a compiled language while some other programming language is interpreted. It means the C++ code can not run directly from the Integrated Development Environment(IDE) it need to invoke the compiler to to generate an executable that can run on the operating system. This executable can run only the operating system platform in which compiler created the executable file. However, the C++ programming language is cross-platform, it means able to run on all operating system but the executable can run only the type operating system platform where it was created. It is also possible to create a semi-compiled C++ code use the LLVM compiler to produce LLVM byte code, and then the target operating system platform can completing the translation from byte-code to the final executable file. So. to run the C++ code on different platform the code must be compiled with the target platform to run on it. [8]

The main platforms for desktop computer are:

1. Mac OSX //derived from Unix
2. Windows
3. Linux // Kernel
 - (a) Ubuntu
 - (b) Debian
 - (c) Red hat
 - (d) Fedora
 - (e) Suse
4. Solaris //derived form Unix
5. Unix systems
 - (a) FreeBSD
 - (b) NetBSD

(c) OpenBSD

6. Amiga

7. OpenVMS

8. Mobile phone platforms //iOS, Android, Blackberry etc..

7.1 Integrated Development Environment(IDE)

IDE is a software product that provide comprehensive set of tools to programmers to create software products in different programming languages. To create an application in any programming language the best way to use an IDE. The IDE give a suggestions for programming language syntax, highlights errors, matching brackets and can run a source code with the built in runtime environment. The another very useful function is a built in debugger in the IDE. To find logical errors in the code more difficult then find syntax errors. Some of the IDE has no default debugger built in, then need to attach a remote debugger to the IDE.

Some of the IDE are supporting many programming languages, some of them specialized only for one given language. Most of the IDE are cross-platform but some are written for single-platform. There are many IDE available but here will be describe only few of the top IDE developer tools.

1. **QT creator** : is available for free with free core libraries or payable professional libraries as well. *"Qt Creator provides a cross-platform, complete integrated development environment (IDE) for application developers to create applications for multiple desktop, embedded, and mobile device platforms, such as Android and iOS."*[12] It is supported on variety of 64 and 32-bit systems with the built in GCC vendor supplied compiler, or can attach any third party compiler. Qt provide code editor with C++, QML and ECMAScript, syntax highlighting and code completion, static code checking, source code refactoring and parenthesis matching. The most powerful feature is the Qt GUI application development framework that includes integrated GUI designer. QT use has it is own GUI libraries to place C++ Qt widgets to create visual applications.

QT creator has integrated set of tool like version control to connect with:

- (a) Git
- (b) Subversion
- (c) Perforce
- (d) CVS
- (e) Mercurial

It does not include a debugger for native code, but it provide debugger plug-in that acts interface between the IDE and the external debugger to debug C++ code. Supported debuggers are :

- (a) GNU debugger (GDB)
- (b) LLVM debugger (LLDB)
- (c) Microsoft Console Debugger (CDB)
- (d) Internal Javascript Debugger

Supported platforms :

- (a) Linux
- (b) Windows
- (c) MacOS

2. **Microsoft Visual Studio(MSVS) :** is Microsoft IDE that available in free community version and payable professional version. MVSV is a very powerful software developer kit to create desktop, web, mobile, console and nearly any kind of application. It's code editor support IntelliSense(code completion) and code refactoring. MSVS has built integrated debugger that works on source-code and on machine-code level as well. As a built in feature MSVS able to create C++ GUI application without any external library. However, it is works on Windows platform and just released to Mac OsX, Microsoft Visual Studio is a very powerful Integrated Development Environment.[6]

Microsoft C and C++ compiler and linker is the cl.exe that produce the Common Object File Format(COFF), objetc file (.obj) and produce the executable (.exe)file or dynamic-link(Dlls) libraries.

MSVS has built in version control and can connect to different version control systems:

- (a) Git
- (b) GitHub
- (c) AnkhSVN
- (d) Team Foundation Version Control(TFVC)
- (e) other extensions

Supported platforms :

- (a) Windows
- (b) MacOS from 2017

3. **Netbeans** : is a free IDE provide developer interface for many different languages. Netbeans provide project types and templates for C and C++ projects. It is well integrated with GNU gdb debugger, code refactoring, code completiton. Supports variety of leading compiler such Oracle Solaris Studio, GNU, CLang, LLVM, Cygwin, MinGW, and has option to pre-process definition, compile-time, language standards etc. As an interesting factor in Netbeans support QT application development framework to create GUI application.[29]

Netbeans provide version control in default for some remote system:

- (a) Git
- (b) Mercurial
- (c) Subversion
- (d) other extensions/plugins are available to connect to different version control systems.

Supported platforms :

- (a) Linux
- (b) Windows
- (c) MacOS

4. **EMACS** : is a free extensible, customizable, self-documenting, real-time display free text editor. It is a highly customizable developer kit since it

has more than 10,000 built-in commands, that the user can combine and automatize to do automated work. Use Lisp programming language in the background to operate the IDE. Since Emacs use its own key combination, thus the users who never used before Emacs, it could be very difficult at the begging. Supporting lots of different programming languages, other text and unicode format as well. Since Emacs use command line to control the IDE and compile the C++ code, thus any type of compiler can be use with it.[14]

Emacs version control interface is called VC. Commands works with several different version control systems:

- (a) Bazaar
- (b) CVS
- (c) Git
- (d) Mercurial
- (e) Monotone
- (f) RCS
- (g) SCCS/CSSC
- (h) Subversion

Supported platforms :

- (a) Linux
- (b) Windows
- (c) MacOS

7.2 Compilers

While the IDE helps to create a runnable code the compiler takes this program code or source code/high-level language and converts it to machine code or low-level language. Then that code will be translated to an executable file that the computer able to run on the operating system. In other word, the compiler converts the instructions set to machine code that computer can run. The different platforms has many different free and payable compilers available to use. There

are major different between the compilers like code optimization, minor syntactical difference, runtime libraries used, header, implementation and more.[9]

To create a cross-platform project in C++ has no real solution. The best way to run a C++ application on the target platform is to use the default platform supported or a platform specific compiler.

"CMake is an extensible, open-source system that manages the build process in an operating system and in a compiler-independent manner."[28] Every platform needs to have some sort of file that build the required program from the source code in a manner to linking the files in the project and creating the output files. On Unix/Linux system the Make file is responsible for this task. If we want to build the same source code on Microsoft Windows platform, then need a different make file called nmake.

Make is a build-system that drives the compiler and the build tools to build the application from the source code. CMake is a generator for build-system. it can produce platform specific build files using the CMakeList.txt file.

Many compilers available in the market for different purpose and platforms. In the following list can find the main compilers with short description:[7]

1. GCC
Part of the GNU compiler Collection and the standard compiler for Unix-like operating systems and the C programming language.
2. G++
Part of the GNU compiler Collection is same as the GCC but extended to compile the C++ programming language.
3. MVSC
Microsoft Visual C++ IDE with built in compiler for Microsoft operating system to create Windows API, .NET framework and much more.
4. Clang
Is the default compiler for Mac OS development it replaced the GCC compiler in 2005.
5. MinGW-w64

Provide Libraries and header to the GNU compiler to run on Windows system.

6. AMD x86 Open64

Is a version of Open64 compiler that has been used for AMD processors.

7. OrangeC

Supports C standards like C99 and C11, and it runs on Windows and DOS.

8. Apple Xcode

Is an Apple IDE with built in GNU C compiler.

9. TinyC

Is a small Linux and Windows compiler that generates x86 native binaries.

7.2.1 GCC and G++

The GCC and G++ programs are not compilers, but drivers that can invoke other programs depending on the arguments that passing with it in the terminal. The programs that GCC and G++ invokes with the argument can be such as cp compilers and cc1 linkers, ld assembler which are part of the GNU Compiler Collection. So, GCC use the C compiler and the G++ use the C++ compiler[33]

7.2.2 MVSC

MSVC in an IDE for Microsoft system, that include compiler, debugger, editor and profiler. MVSC compiler supports most of ISO C++11 most of C++14 and C++17, while the C compiler is outdated. Provide support for diverse languages to create applications for Windows desktop and mobile, Android platform to compile and run.[7]

7.2.3 CLANG

Clang is a compiler developed by Apple and support C, C++, Objective C and Objective C++ languages. It is part of the Low Level Virtual Machine(LLVM) project and intermediate compiler language for LLVM IR what is provide instruction that similar output like the byte code. The back end of LLVM generates the machine code for the targeted system and required processor. The idea of CLang

to mix the compiler front end with the targeted back-end. The front end compiler support many languages.[?]

7.3 Conclusion of platform

All of the computing platforms providing compiler solution not only for C and C++ programming languages but all the programming languages that are able to run on the operating system. However, the compilers are around since decades but is still no real cross-platform compiler solution for the C programming language family. If the source code written in the plain C99 or C++11 standard without any external library that should able to compile on any Unix-based standard operation system platform but not on Windows. To get working on Windows platform need to use conditional compilation with `#if` preprocessor directive to wrap all the operating specific services at compilation time.

Qt development is a cross platform GUI builder to create C++ application that can run on any targeted platform if the required libraries are installed in the target operation system. Qt uses the vendor system GCC compiler or third party attached compiler.

8 Conclusion

8.1 Concurrency as a base problem

The parallel programming pattern has many advantages, but as any other programming design it has disadvantages as well. The concurrent and parallel programming design help the application to handle multitasking and are able to speed up the the software execution time. While a user executes multiple instructions within the application in the background, the software breaks down the instructions to parallel to the CPU cores and creates threads to handle computations in a faster manner than the sequential programming design.

As advantages in concurrent programming design is to help increase the performance, the reaction time of the applications and parallel programming fashion maximizes the CPU throughput in efficient manner.

However, it has lots of advantages that must mention the difficulties with the code writing design and the complicated debugging process as the disadvantages.

8.2 Software Transactional Memory

The Software Transactional Memory is a lock free solution of concurrent programming, since it doesn't need to use mutex and semaphores to control threads. It is easier to read , maintain, extend and work with the high level objects and modules. It offers transactional environment without extra hardware cost.

As the main advantages the STM prevents deadlock, livelock, uses transactional methods, atomic variables and controlling the threads in easier manner than the semaphores and mutex can. As a disadvantage, performance issues can happen because the transactions may roll back too many times before the threads finish their execution in the transactional function.

8.3 Platform and Compilers

C and C++ programming languages are required compiler to translate the source code to object code or machine code to run on the operating system. Every programming language has many different versions of compiler on the different

operating platforms. However, there are many compiler forms in diverse languages, no real cross-platform compiler solution to the C programming language family. If the source code is written in the plain C99 or C++11 standard without any external library, that should be able to compile on any Unix standard operation system platform but not on Windows. To get working on Windows platform, it needs to use conditional compilation with `#if` preprocessor directive to wrap all the operating specific services at compilation time.

QT development is including the QT libraries and the GUI designer to create cross platform C++ applications that can run on any targeted platform, if the build process doesn't include any other libraries that comes outside of the QT framework. QT uses the vendor system GCC compiler or third party attached compiler to create the machine code.

9 Bibliography

References

- [1] M. Amgad, E. H. Hassan, S. Khlid, G. Mina, and G. Mohamed. Software Transactional Memory (STM). <https://www.cs.purdue.edu/homes/amadkour/presentations/SoftwareTransactionalMemory.pdf>. 2017, (Accessed : 24/10/2017).
- [2] Bartek. Bartek's coding blog. <http://www.bfilipek.com/2017/01/cpp17features.html>. 2017, (Accessed : 29/10/2017).
- [3] Ben. What is transactional memory? <https://stackoverflow.com/questions/11255640/what-is-transactional-memory>. 2012, (Accessed : 22/10/2017).
- [4] O. Bryen, S. Don, and G. John. Real World Haskell. <http://book.realworldhaskell.org/read/software-transactional-memory.html>. 2008, (Accessed : 22/10/2017).
- [5] K. Christos. Parallel Computer Architecture and Programming . http://www.cs.cmu.edu/afs/cs/academic/class/15418-s12/www/lectures/20_transactionalmem.pdf. 2012, (Accessed : 22/10/2017).
- [6] M. com. Visual Studio. <https://www.visualstudio.com/>. 2017, (Accessed : 29/10/2017).
- [7] H. Cristopher. Free C/C++ Compilers. <https://www.thefreecountry.com/compilers/cpp.shtml>. 2017, (Accessed : 30/10/2017).
- [8] G. Crowd. Best Integrated Development Environment (IDE) Software. <https://www.g2crowd.com/categories/integrated-development-environment-ide>. 2017, (Accessed : 29/10/2017).
- [9] B. David. List of Free C and C++ Compilers. <https://www.thoughtco.com/list-of-free-c-compilers-958190>. 2017, (Accessed : 30/10/2017).

- [10] B. David. What is Clang. <https://uk.dice.com/technews/what-is-clang/>. 2015, (Accessed : 30/10/2017).
- [11] J. Dennis. DeathByTape . <http://deathbytape.com/articles/2015/04/12/software-transactional-memory.html>. 2015, (Accessed : 22/10/2017).
- [12] Q. Documentation. Qt Documentation. <http://doc.qt.io/qtcreator/> . 2017, (Accessed : 29/10/2017).
- [13] R. Florian. Asynchronous models and patterns. <https://www.codeproject.com/Articles/562021/Asynchronous-models-and-patterns>. 2013, (Accessed : 16/10/2017).
- [14] F. S. Foundation. GNU Emacs. <https://www.gnu.org/software/emacs/index.html>. 2017, (Accessed : 29/10/2017).
- [15] R. Grimm. Transactional Memory in C++20. <https://www.linkedin.com/pulse/transactional-memory-c20-rainer-grimm>. 2015, (Accessed : 28/10/2017).
- [16] B. Hall. Transactional Memory in Practice. <https://www.youtube.com/watch?v=k20nWb9fHj0>. 2015, (Accessed : 25/10/2017).
- [17] installsetupconfig. Windows Thread Synchronization 11. <http://java91.blogspot.ie/2017/01/what-is-starvation-in-thread.html>. N/A, (Accessed : 24/10/2017).
- [18] ISOIEC. Technical Specification for C++ Extensions for Transactional Memory. <http://www.open-std.org/Jtc1/sc22/wg21/docs/papers/2015/n4514.pdf>. 2015, (Accessed : 29/10/2017).
- [19] J. Jakob. Java Concurrency / Multithreading Tutorial. <http://tutorials.jenkov.com/java-concurrency/index.html>. 2014, (Accessed : 16/10/2017).
- [20] JIghtuse. Transactional Memory: History and Development. <https://kukuruku.co/post/transactional-memory-history-and-development/>. 2014, (Accessed : 25/10/2017).

- [21] E. Kasper. A Software Transactional Memory Library for C++. <http://www.diku.dk/forskning/performance-engineering/Kasper/>. N/A, (Accessed : 22/10/2017).
- [22] M. A. Kenneth. Software Transactional Memory. <https://www.cs.colorado.edu/~kena/classes/5828/s12/lectures/19-stm.pdf>. 2012, (Accessed : 22/10/2017).
- [23] llimllib. Bloom Filters by Example. <https://llimllib.github.io/bloomfilter-tutorial/>. N/A, (Accessed : 22/10/2017).
- [24] C. Looper. All about multiprocessors. <https://www.pcmach.com/article/all-about-multi-core-processors-what-they-are-how-they-work-and-where-they-came-from/>. 2015, (Accessed : 16/10/2017).
- [25] P. Nick and Z. Julie. Thread and Semaphores Examples. <https://see.stanford.edu/materials/icsppcs107/23-Concurrency-Examples.pdf>. 2008, (Accessed : 16/10/2017).
- [26] G. Nikolay. Overview of Modern Concurrency and Parallelism Concepts. <http://nikgrozev.com/2015/07/14/overview-of-modern-concurrency-and-parallelism-concepts/>. 2015, (Accessed : 16/10/2017).
- [27] S. Olivier. fastSTM. <https://nebelwelt.net/publications/students/08hs-saurer-libstm.pdf>. 2008, (Accessed : 22/10/2017).
- [28] C. org. About CMake. <https://cmake.org/overview/>. N/A, (Accessed : 30/10/2017).
- [29] N. org. Netbeans IDE features. <https://netbeans.org/features/cpp/index.html>. 2017, (Accessed : 29/10/2017).
- [30] G. Patents. STM with multiple global version counters . <http://www.google.sr/patents/US8688921>. 2014, (Accessed : 22/10/2017).
- [31] J. Paul. Javarevisited. <http://javarevisited.blogspot.ie/2010/10/what-is-deadlock-in-java-how-to-fix-it.html>. 2010, (Accessed : 16/10/2017).

- [32] K. Priyanka, P. Sanhys, and V. K. A TimeStamp based Multi-version STM Algorithm. https://www.iith.ac.in/~sathya_p/index_files/MVTO-ICDCN2014.pdf. 2009, (Accessed : 30/10/2017).
- [33] P. Rohan. I'm a Compiler Compiler Compiler. <https://www.quora.com/What-is-the-difference-between-gcc-cc-and-g++>. 2013, (Accessed : 30/10/2017).
- [34] S. Sitansu. What is starvation in Thread ?. http://www.installsetupconfig.com/win32programming/threadprocesssynchronizationapis11_10.html. 2017, (Accessed : 24/10/2017).
- [35] S. Swarup. What are the key new features in the C++ 14 standard? <https://www.quora.com/What-are-the-key-new-features-in-the-C++-14-standard>. 2014, (Accessed : 29/10/2017).
- [36] Techopedia. C++ Programming Language. <https://www.techopedia.com/definition/26184/c-programming-language>. N/A, (Accessed : 29/10/2017).
- [37] R. Torvald. Transactional Memory for C++. <http://www.gsd.inesc-id.pt/~mcouceiro/eurotm/finalevent2015/riegel.pdf>. 2015, (Accessed : 22/10/2017).
- [38] Tutorialspoint. C++ Standard Library. https://www.tutorialspoint.com/cplusplus/cpp_standard_library.htm. 2017, (Accessed : 29/10/2017).
- [39] Tutorialspoint. Operating System Multi-Threading. https://www.tutorialspoint.com/operating_systems_multi_threading.htm. 2013, (Accessed : 16/10/2017).
- [40] T. Vasudevan. Multicore CPUs and the concurrency changes they bring. <https://www.ibm.com/developerworks/library/j-nothreads/index.html>. 2012 (Accessed : 16/10/2017).
- [41] R. M. Volkmann. Software Transactional Memory. <http://java.ociwab.com/mark/stm/article.html>. 2009, (Accessed : 30/10/2017).

- [42] Wikibooks. Haskell/Concurrency. <https://en.wikibooks.org/wiki/Haskell/Concurrency>. 2015, (Accessed : 22/10/2017).