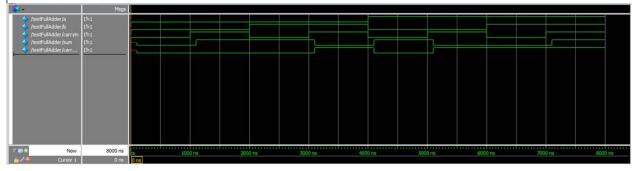
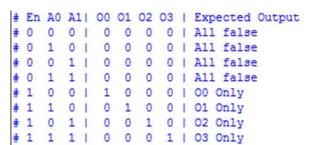
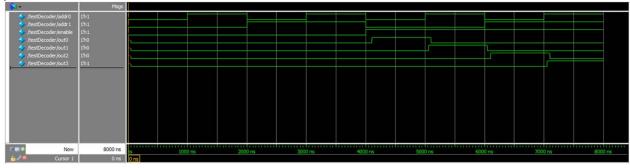
Adder

#	A	В	Cin	1	Cout	S	1	Cout	S	Expected
#	0	0	0	1	0		I	0	0	
#	0	0	1	1	0	1	ı	0	1	
#	0	1	0	1	0	1	ı	0	1	
#	0	1	1	1	1	0	ı	1	0	
#	1	0	0	1	0	1	I	0	1	
#	1	0	1	Ī	1	0	1	1	0	
#	1	1	0	1		0	1	1	0	
#	1	1	1	1	1	1	1	1	1	



Decoder





MUX

#	A0	A1	ı	IO	I1	12	I3	1	OUT	1	Expected	Output
#	0	0	ı	0	0	0	0	ı	0	1	0	
#	0	0	ı	1	0	0	0	1	1	1	1	
#	1	0	ı	0	0	0	0	1	0	1	0	
#	1	0	ı	0	1	0	0	ı	1	1	1	
#	0	1	I	0	0	0	0	1	0	1	0	
#	0	1	ı	0	0	1	0	ı	1	1	1	
#	1	1	ı	0	0	0	0	ı	0	1	0	
#	1	1	i	0	0	0	1	i	1	ī	1	

