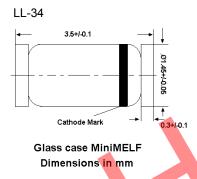
Silicon Epitaxial Planar Switching Diode

Fast switching diode in MiniMELF case especially suited for automatic surface mounting



Absolute Maximum Ratings (T_a = 25°C)

Parameter		Symbol	Value	Unit		
Peak Reverse Voltage		V_{RM}	100	V		
Reverse Voltage		V_R	75	V		
Average Rectified Forward Current		I _{F(AV)}	200	mA		
Non-repetitive Peak Forward Surge Current	at t = 1 s at t = 1 ms at t = 1 µs	I _{FSM}	0.5 1 4	Α		
Power Dissipation	3.7 ps	P _{tot}	500 ¹⁾	mW		
Junction Temperature		Tj	175	°C		
Storage Temperature Range		T_{stg}	- 65 to + 175	°C		
1) Velid was ideal that a last selection are broaden as a problem to manage true						

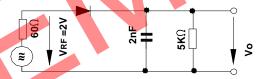
¹⁾ Valid provided that electrodes are kept at ambient temperature.





Characteristics at T_a = 25℃

Parameter	Symbol	Min.	Max.	Unit
Reverse Breakdown Voltage tested with 100 µA Pulses	V _{(BR)R}	100	-	V
Forward Voltage at I _F = 10 mA	V _F	-	1	V
Leakage Current at $V_R = 20 \text{ V}$ at $V_R = 75 \text{ V}$ at $V_R = 20 \text{ V}$, $T_i = 150 ^{\circ}\text{C}$	I _R I _R	- - -	25 5 50	nA µA µA
Capacitance at $V_R = 0$, $f = 1$ MHz	C _{tot}	-	4	pF
Voltage Rise when Switching ON tested with 50 mA Forward Pulses tp = 0.1 s, Rise Time < 30 ns, fp = 5 to 100 KHz	V _{fr}		2.5	V
Reverse Recovery Time at I_F = 10 mA to I_R = 1 mA, Irr = 0.1 x I_R , V_R = 6 V, R_L = 100 Ω	t _{rr}	-	4	ns
Thermal Resistance Junction to Ambient Air	R _{thA}	-	0.35 ¹⁾	K/mW
Rectification Efficiency at f = 100 MHz, V _{RF} = 2 V	ην	0.45	-	-
1) Valid provided that electrodes are kept at ambient temperature.				



Rectification Efficiency Measurement Circuit





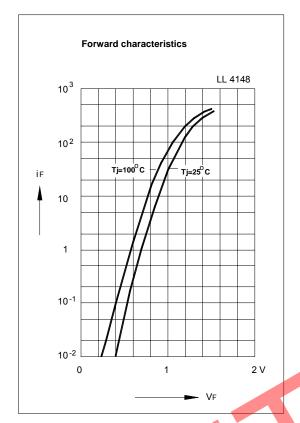


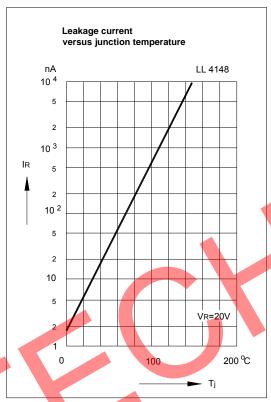


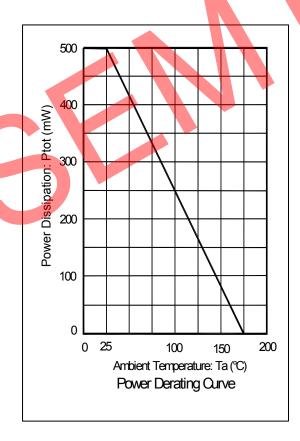


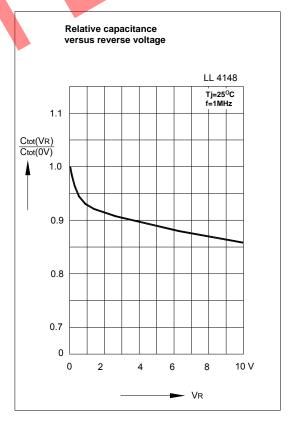


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