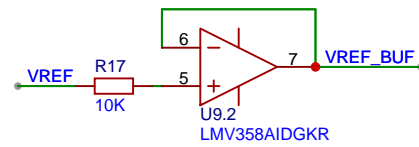
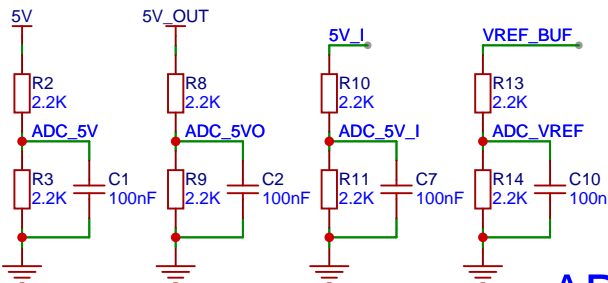


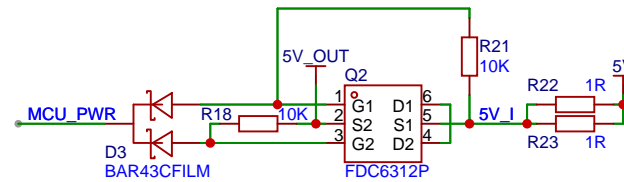
IO电压缓冲



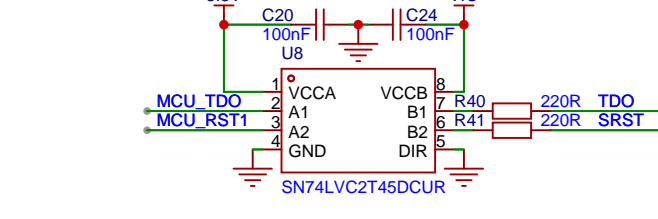
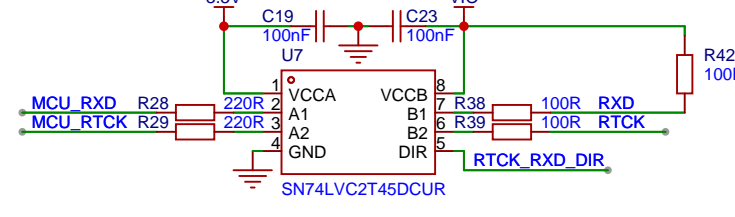
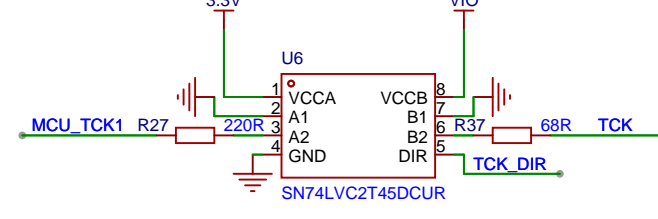
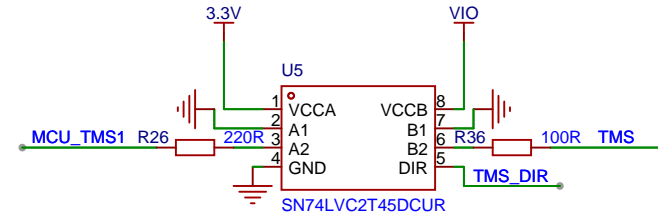
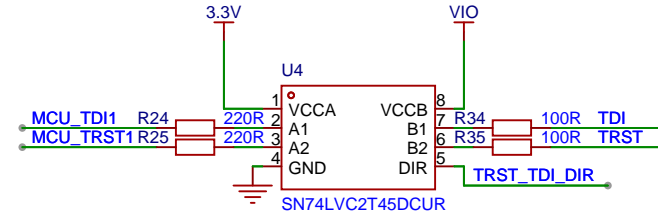
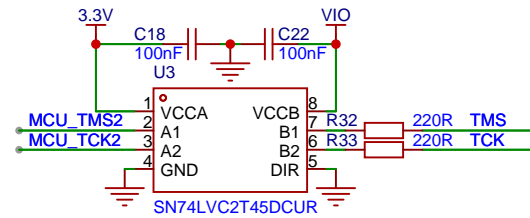
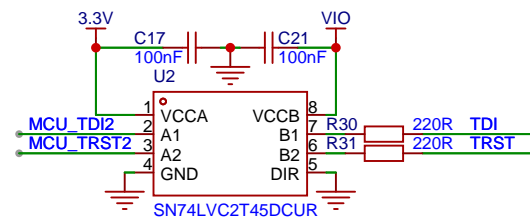
Vref缓冲



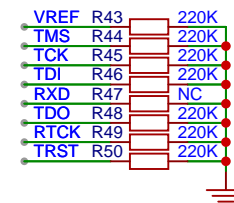
ADC分压



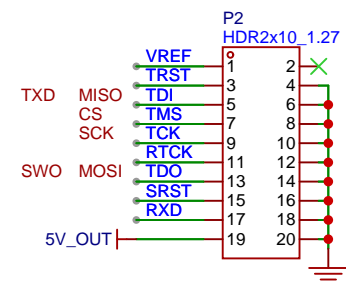
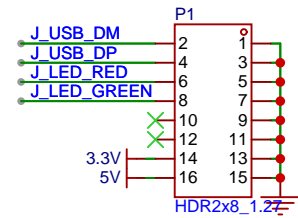
5V输出开关



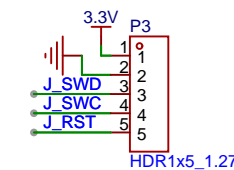
电平转换



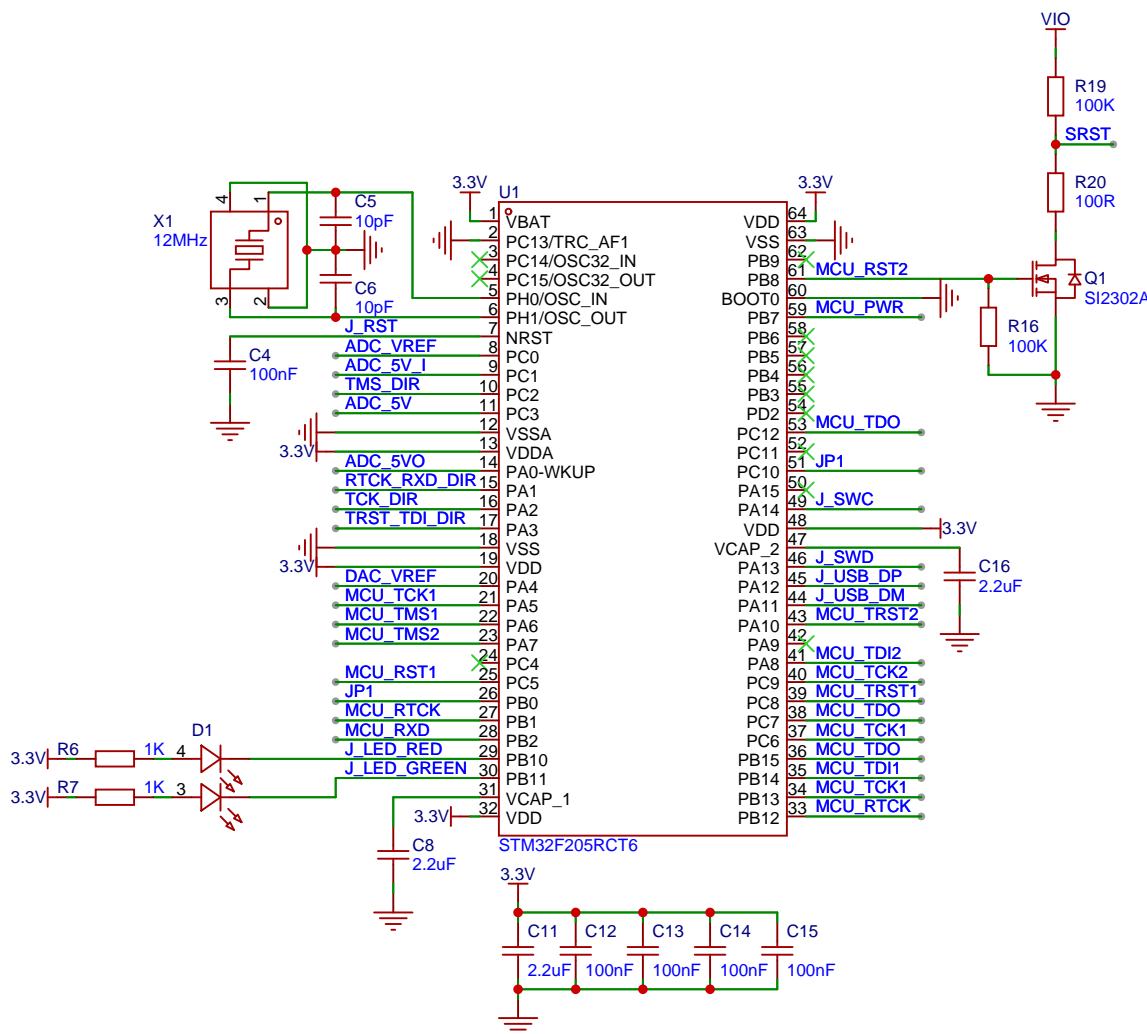
接口下拉电阻



连接排针



SWD接口



单片机

原理图	X-Link_V9.3_M	更新日期	2024-10-04	
		创建日期	2024-10-04	
图页	X-Link_V9.3_M		物料编码	
绘制	X-Link_V9.3_M			
审阅				
		版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4	嘉立创EDA