

Version	Date		Change Note	Approved
V1.0	20170302		First edictor	
v1.0	20190102		<p>NOTE: 1. 拆SATA,HD接口 2. 去掉DVI接口</p>	
v1.0	20190308		<p>NOTE: 1. Page21, 新增 LO接口, 原设计定义更使之兼容并新增M及CAM启动电源路。 2. Page25, 原24PIN PCI接口改为此侧插(ZN1ZP)封装); 删除了USB连接;</p>	
v1.0	20190311		<p>NOTE: 1. Page25, PCIE首次复位连接;</p>	
v1.0	20190312		<p>NOTE: 1. Page25, PCIE删除接口连接; 2. I₂C1 上拉脚改为跳线容跳选电阻; 3. PCIE座为立式4层;</p>	
v1.0	20190313		<p>NOTE: 1. Page25, PCIE删除接口连接; 2. I₂C1 上拉脚改为跳线容跳选电阻; 3. Page21, I₂C2电源改由PMU输出的VCC3 V3_SVCC_MDP电源; 4. Page21, I₂C2电源改由PMU输出的VCC_VO_TOUCH及VCC_MPIL提供; 5. Page24, Type-C端口能控制脚; 6. Page13, 新温度检测DC; 7. Page24, FUSE302的2QI_N脚去拉低; 8. Page21, 剔除NS开关, 使VCC_SYS与VCC_Q96连通; 同一脚;</p>	
v1.1	20190708		<p>1. Page23, AP6256 41#引脚CTS功能调; 2. Page9, LPDDR4颗粒网络VCC1V8#DR接到VCC 8#脚; 3. Page8, CPU锁容部分: C6, C9删除, 改为0Ω; C16, C17, C24, C25, C26, C40, C41, C42, C43, C44, C32, C33改为100nF; C8, C10, C11, C12, C16, C17, C18, C19, C23, C24, C25, C26, C27, C28, C31, C32, C33, C34, C35, C39, C40, C41, C42, C43, C44, C45, C46参数为0Ω;</p>	
	20190717		<p>4. Page13增加功能键电路: R8D2、SW107、Q5 5、ES#; 5. Page15删除BOR ADD电路R74 R56、底氏9#; 6. Page15, TOUCH_RST_L改用GPIO4#; 7. Page21删除LCD&显示识别电路Q65033\Q65034\Q3\R90380\R90390 384, 改用IC FSA457P6X(U919);</p>	
	20190719		<p>8. Page21, 删掉地平切换电路U9 19, R06; 9. Page15, I₂C电压改为VCC1V8#; 10. Page23, 37.4MH晶振配电容901# & C1# 改5pF; 11. Page15多余网络WL-MKE-AP与GND的连接同时删除436, GND_D#及CTP1_INT网络; 12. Page15, VCC5V0_TYPEC_E连接由GPIO_D2改为@IO2_B4, @IO4_D2改接CSIEN_CTP_RST;</p>	
	20190725		<p>13. Page23, 删除电阻R134、且优化晶振线;</p>	
	20190726		<p>14. Page25, GPIO座PIN脚改GPIO4_C6#; 15. Page21, CAN桥子J4#复位信号CSI_RST_H#共用, 改为独立RST_R#_C#; 16. Page21, CAN桥子J4#改为与J701#共用; 17. Page21删除R9030、R905 1#; 18. Page21, J4701 EDP_CABC_E#接GPIO0#</p>	
	20190822		<p>19. Page23, 漂电容至; 21. Page23, C9016# C1#改为Q02, 1# 22. Page05, U9001#参数为焊盘P50284、R0# 23. Page25, 调接口串接R90406、R90K阻; 24. Page23, 漂匹配C0171、Q016# C41#为0#连接;</p>	
	20190822		<p>25. Page25, 增加PE接口@IO2、C44# C44, C45, C90#, Q5#</p>	
	20191030		<p>26. Page25, 漂电阻R0408、B0409#IE座子新增#0 DM#连接</p>	



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RK3399 I2C MAP

Port	Pin name	Domain	Bus name	Pull-up voltage	Slave Device	Slave Addr (MS 7Bits)	Note	Slave Bus Capability
I2C0	GPIO1_B7/SPI3_RXD/I2C0_SDA GPIO1_C0/SPI3_TXD/I2C0_SCL	PMUIO2 VCC1V8_PMU	I2C_SDA_PMIC I2C_SCL_PMIC	VCC1V8_PMU	TI LP8752-Q1	0x60	DC regulator	100kHz, 400kHz, 1MHz fast mode+ 3.4MHz high-speed mode
					Rockchip RK808	0x1b	PMIC	
					Fairchild FUSB302B	0x44, 0x46	USB-TypeC Mux	100kHz, 400kHz, 1MHz fast mode+
I2C1	GPIO4_A1/I2C1_SDA GPIO4_A2/I2C1_SCL	API05 VCC1V8_IO	I2C_SDA_AUDIO I2C_SCL_AUDIO	VCCA1V8_CODEC	Everest ES8316	0x10	Audio codec	
			I2C_SDA_CAM I2C_SCL_CAM	VCCA1V8_CODEC	Omnivision OV8858	0x6c, 0x20	MIPi Camera	
I2C2	GPIO2_A0/VOP_D0/CIF_D0/I2C2_SDA GPIO2_A1/VOP_D1/CIF_D1/I2C2_SCL	API02 VCC1V8_IO	Other pin function					
I2C3	GPIO4_C0/I2C3_SDA/UART2B_RX GPIO4_C1/I2C3_SCL/UART2B_TX	API04 VCC3V0_IO	I2C_SDA_HDMI I2C_SCL_HDMI	VCC3V0_IO				
I2C4	GPIO1_B3/I2C4_SDA GPIO1_B4/I2C4_SCL	PMUIO2 VCC1V8_IO	I2C_SDA_MEMS I2C_SCL_MEMS	VCC1V8_PMU	Capellamicro CM3218	0x10, 0x0c	ALS Sensor	
					AsahiKasei AK8963C		Compass	
			I2C_SDA_TOUCH I2C_SCL_TOUCH	VCC1V8_TOUCH	InverSense MPU6500	0x34	Accer+Gyro	
					Silead GSL3680	0x40	Touch panel	
I2C5	GPIO3_B2/MAC_RXER/I2C5_SDA GPIO3_B3/MAC_CLK/I2C5_SCL	API01 VCC3V3_LAN	I2C_SDA_PCIE I2C_SCL_PCIE	VCC3V3_IO				
I2C6	GPIO2_B1/SPI2_RXD/CIF_HREF/I2C6_SDA GPIO2_B2/SPI2_TXD/CIF_CLKIN/I2C6_SCL	API02 VCC1V8_IO	I2C_SDA_BAT I2C_SCL_BAT	VCC1V8_DVP	Chrome Battery	0x19	Battery	100kHz
					TI BQ25700	0x6a (I2C) 0x09 (SMBus)	Charger	
					Fairchild FUSB302B	0x44, 0x46	USB-TypeC Mux	100kHz, 400kHz, 1MHz fast mode+
I2C7	GPIO2_A7/VOP_D7/CIF_D7/I2C7_SDA GPIO2_B0/VOP_CLK/CIF_VSYNC/I2C7_SCL	API02 VCC1V8_IO	Other pin function					



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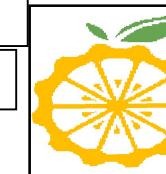
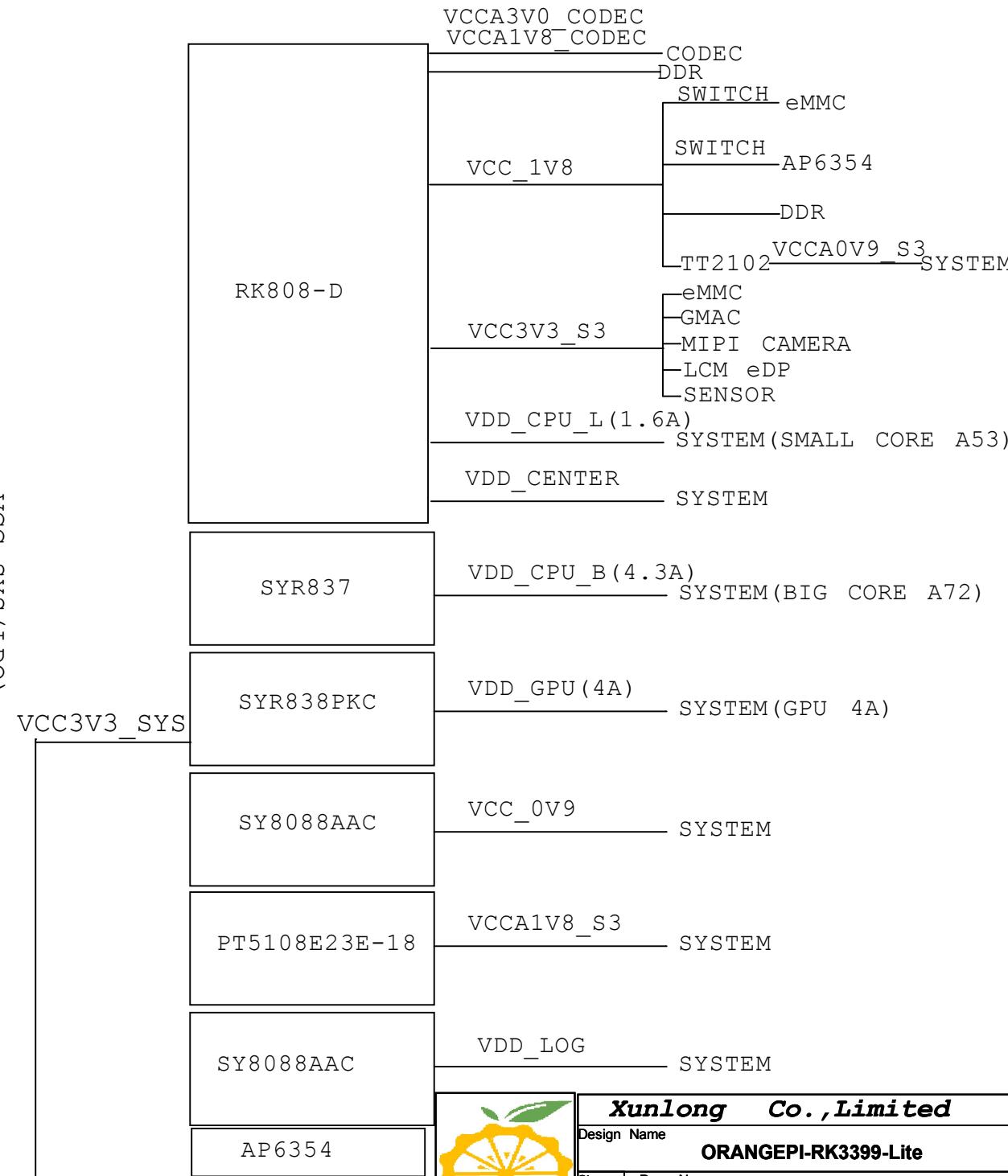
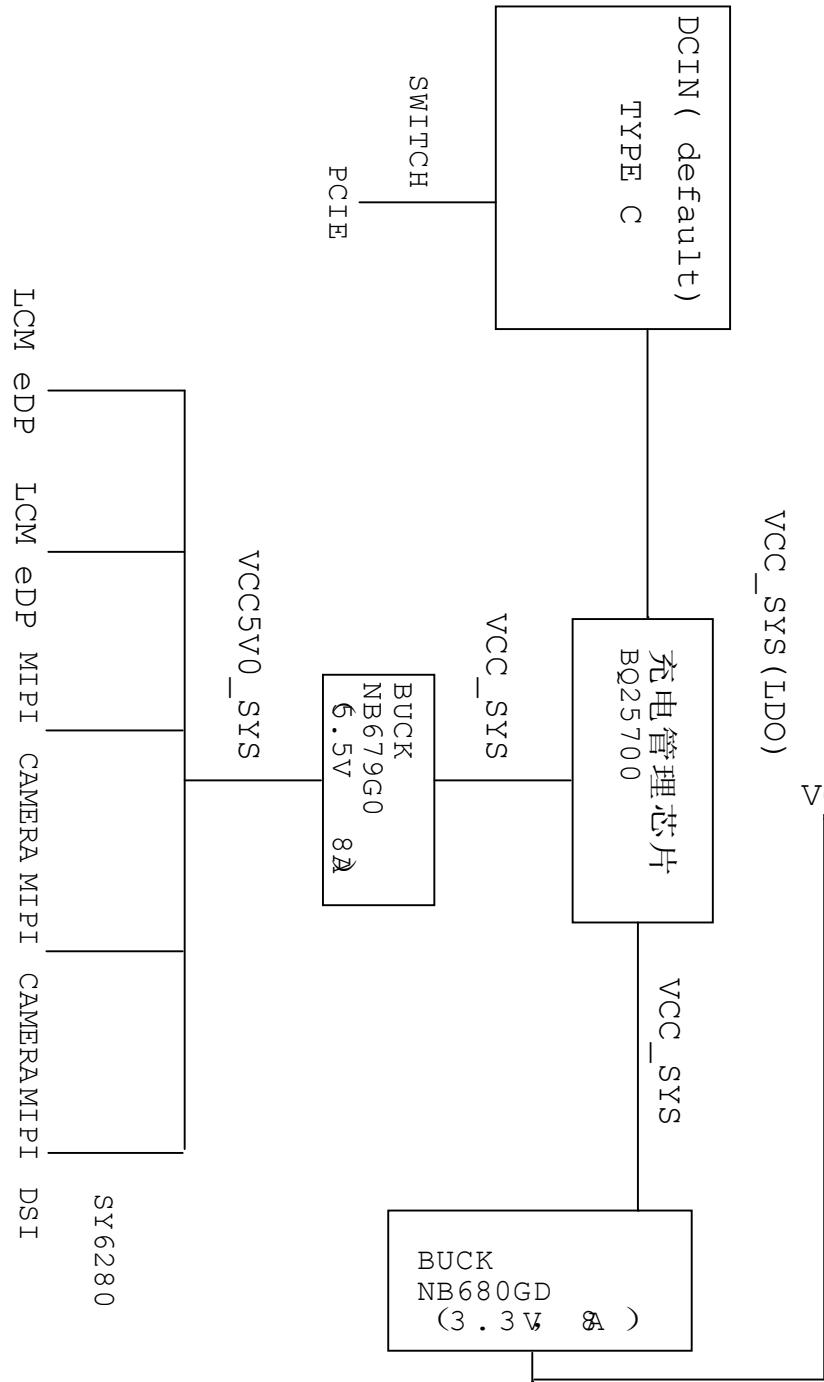
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RK3399 POWER DIAGRAM

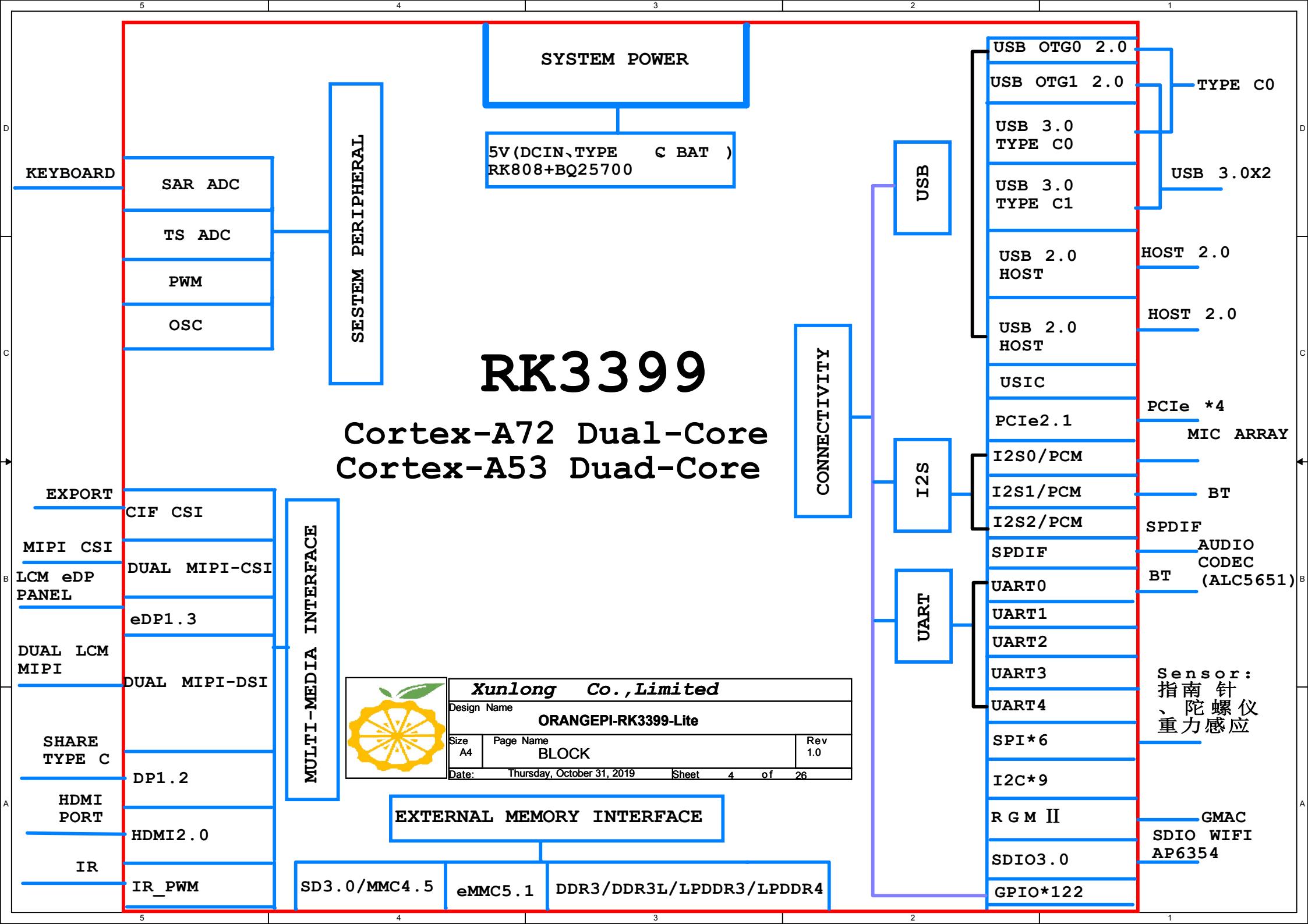


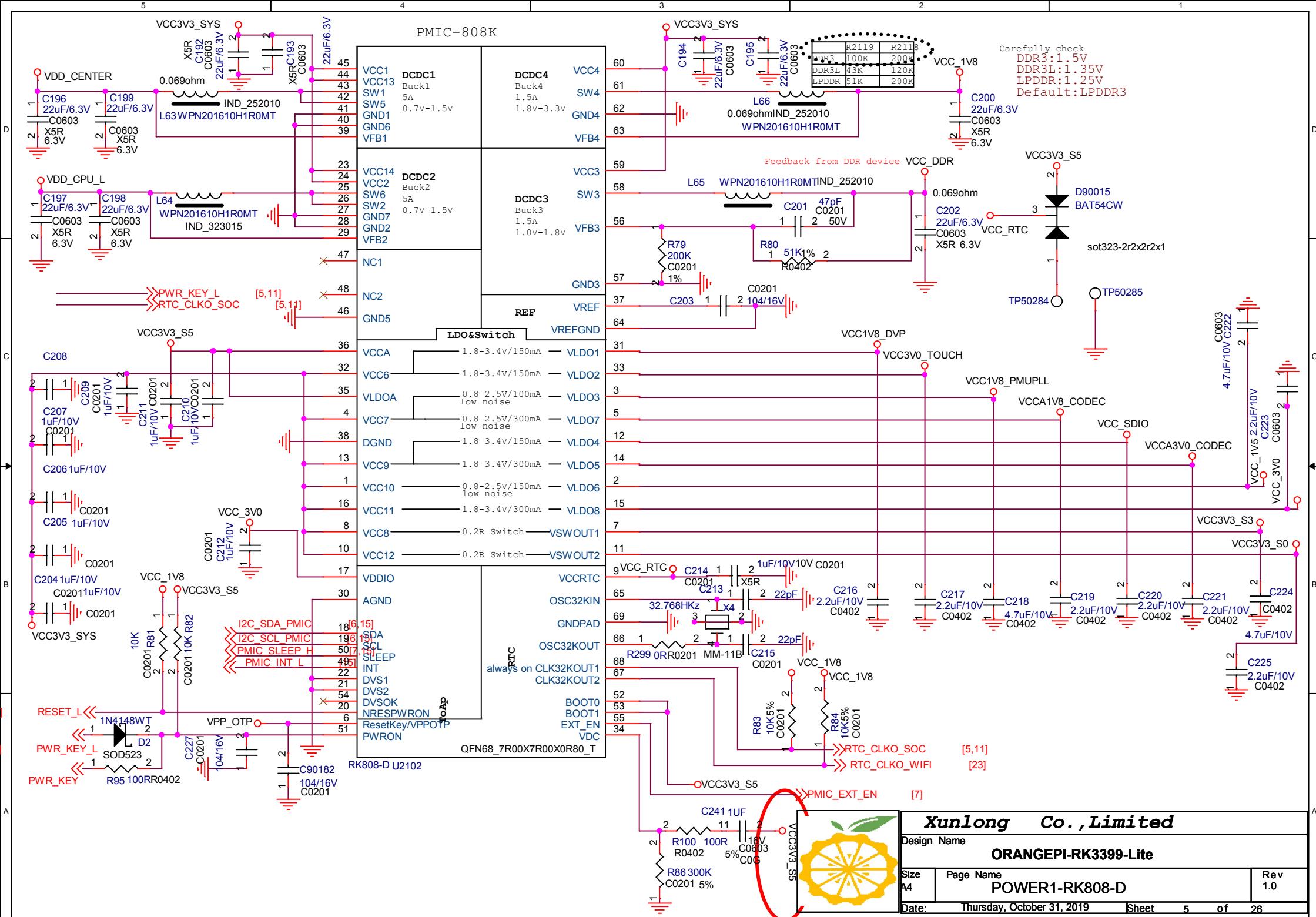
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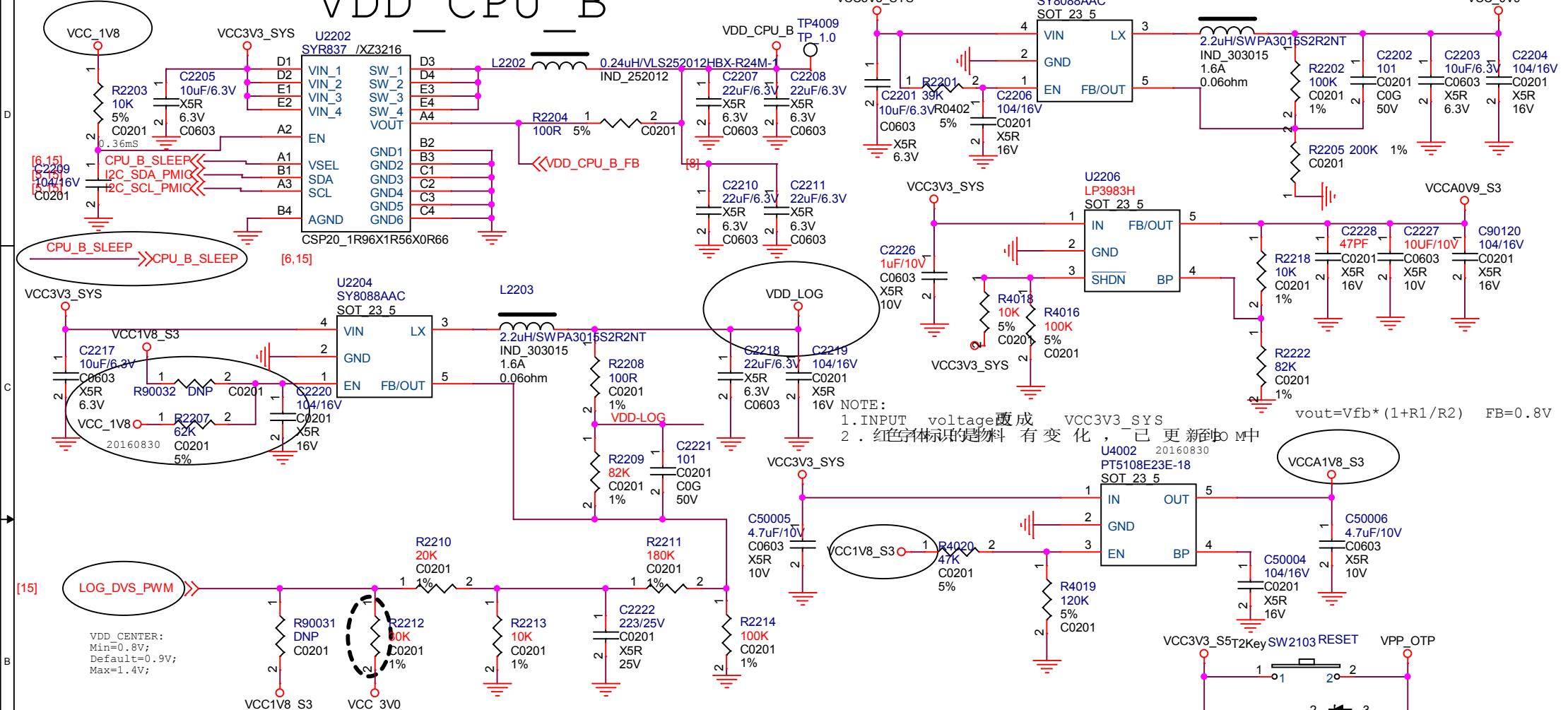
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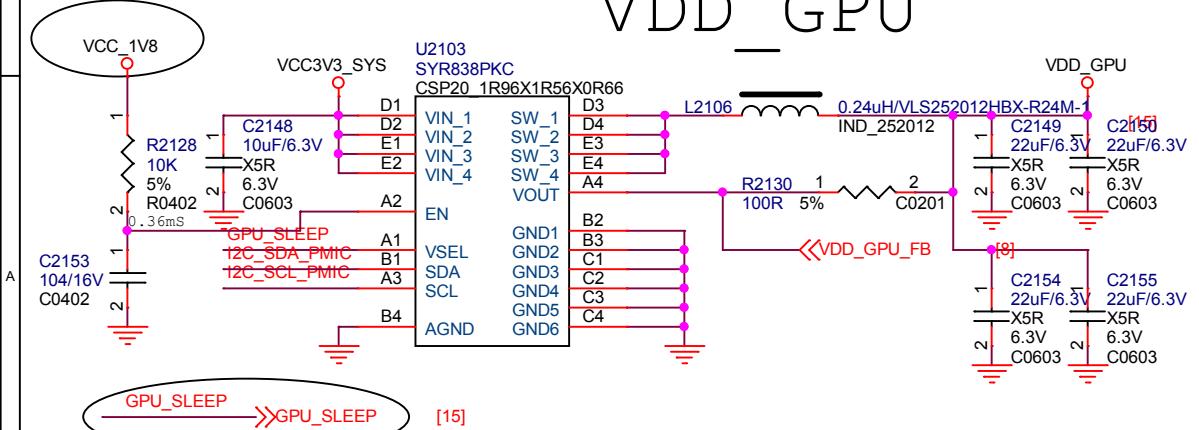




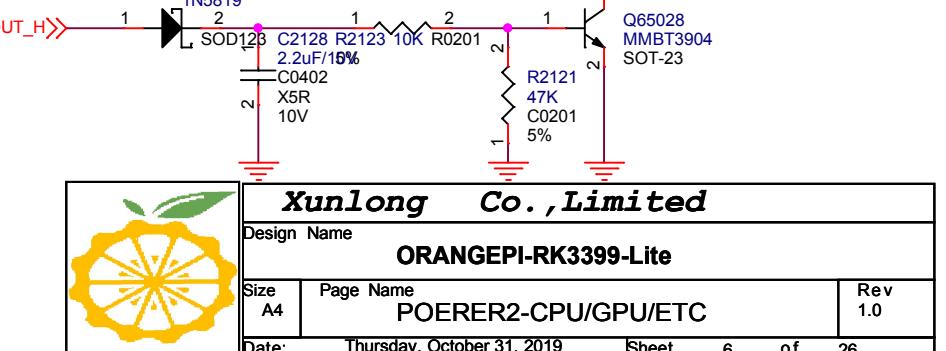
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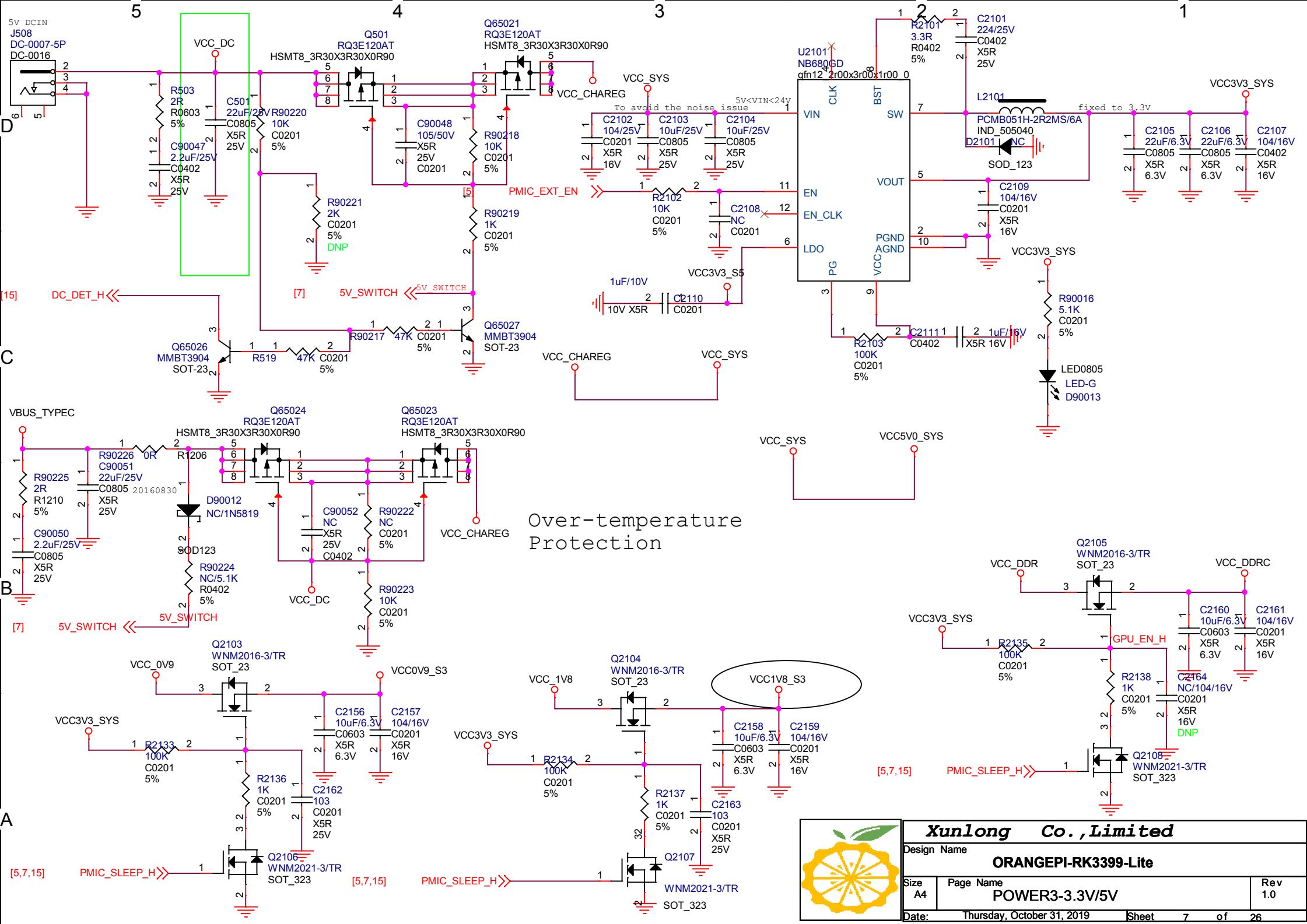
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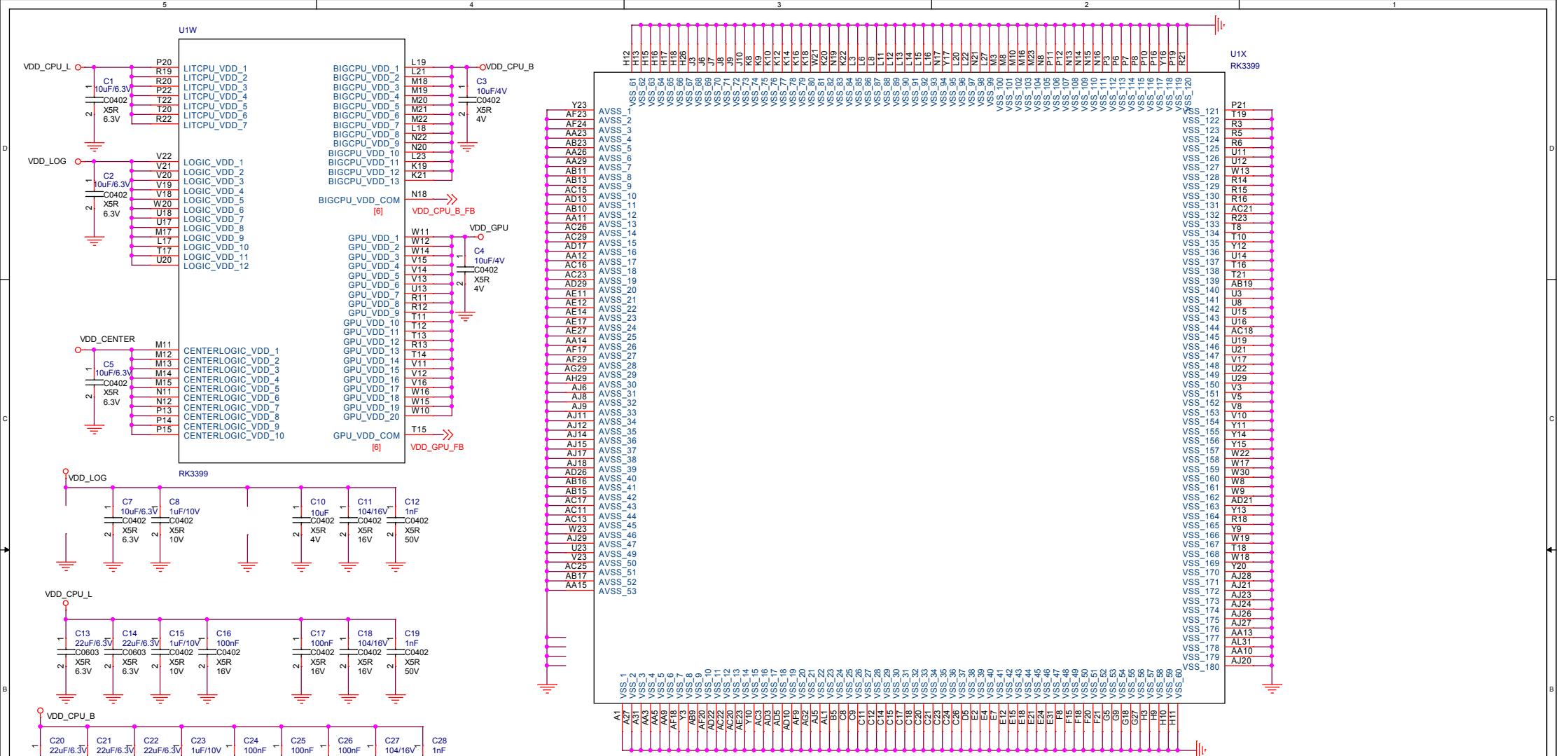
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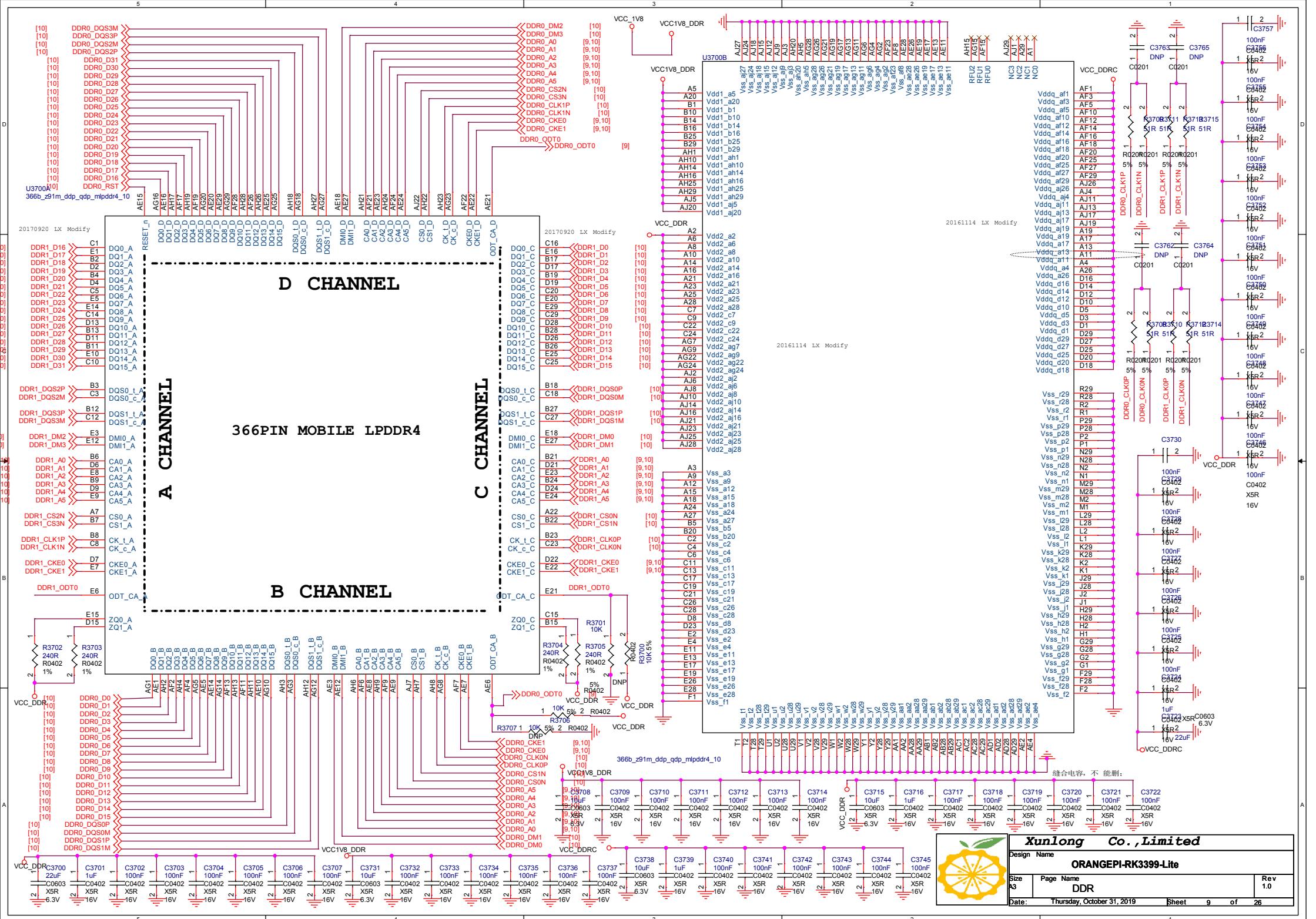


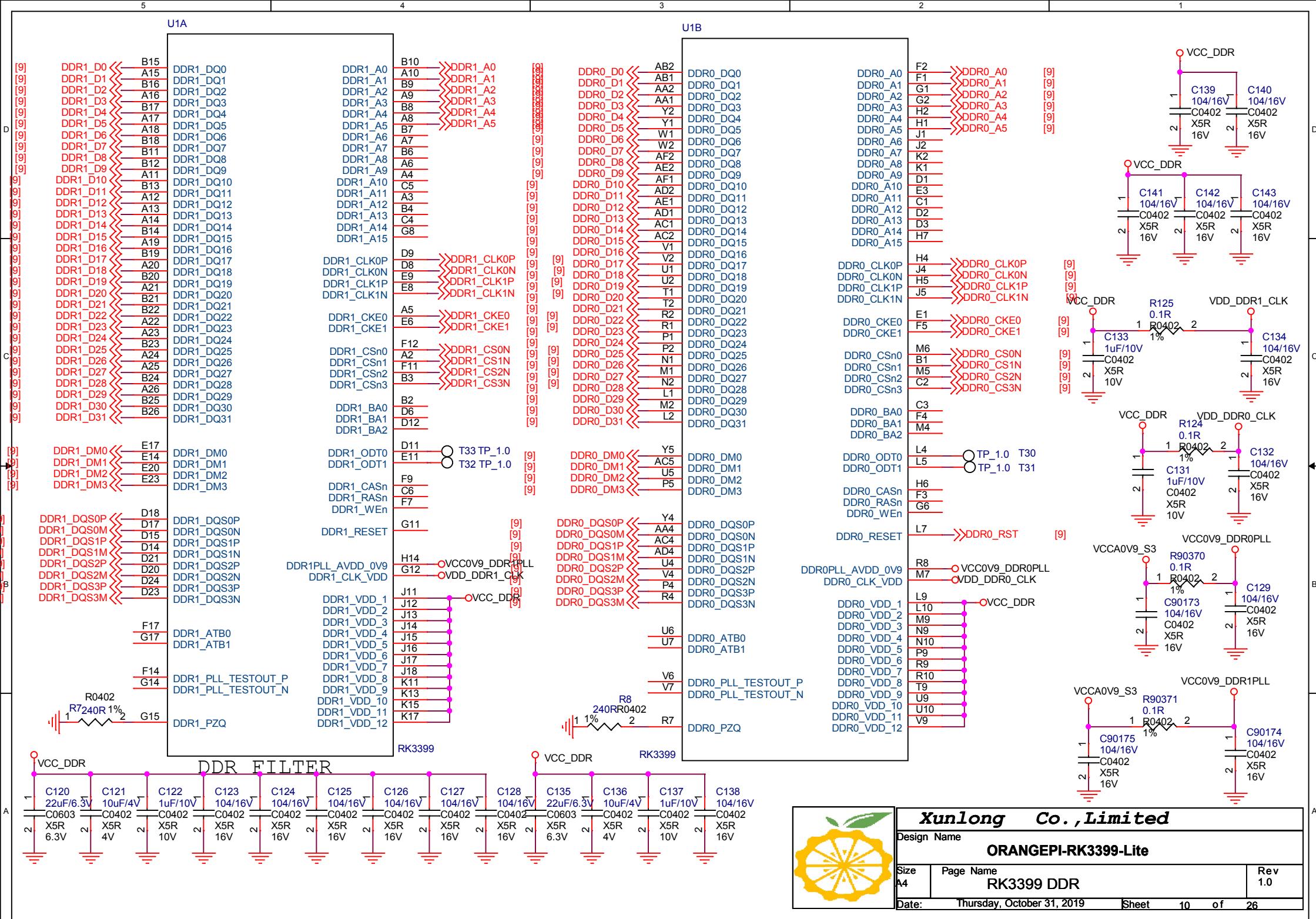
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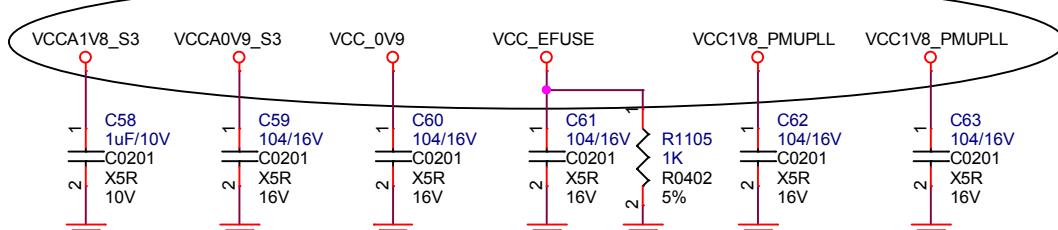
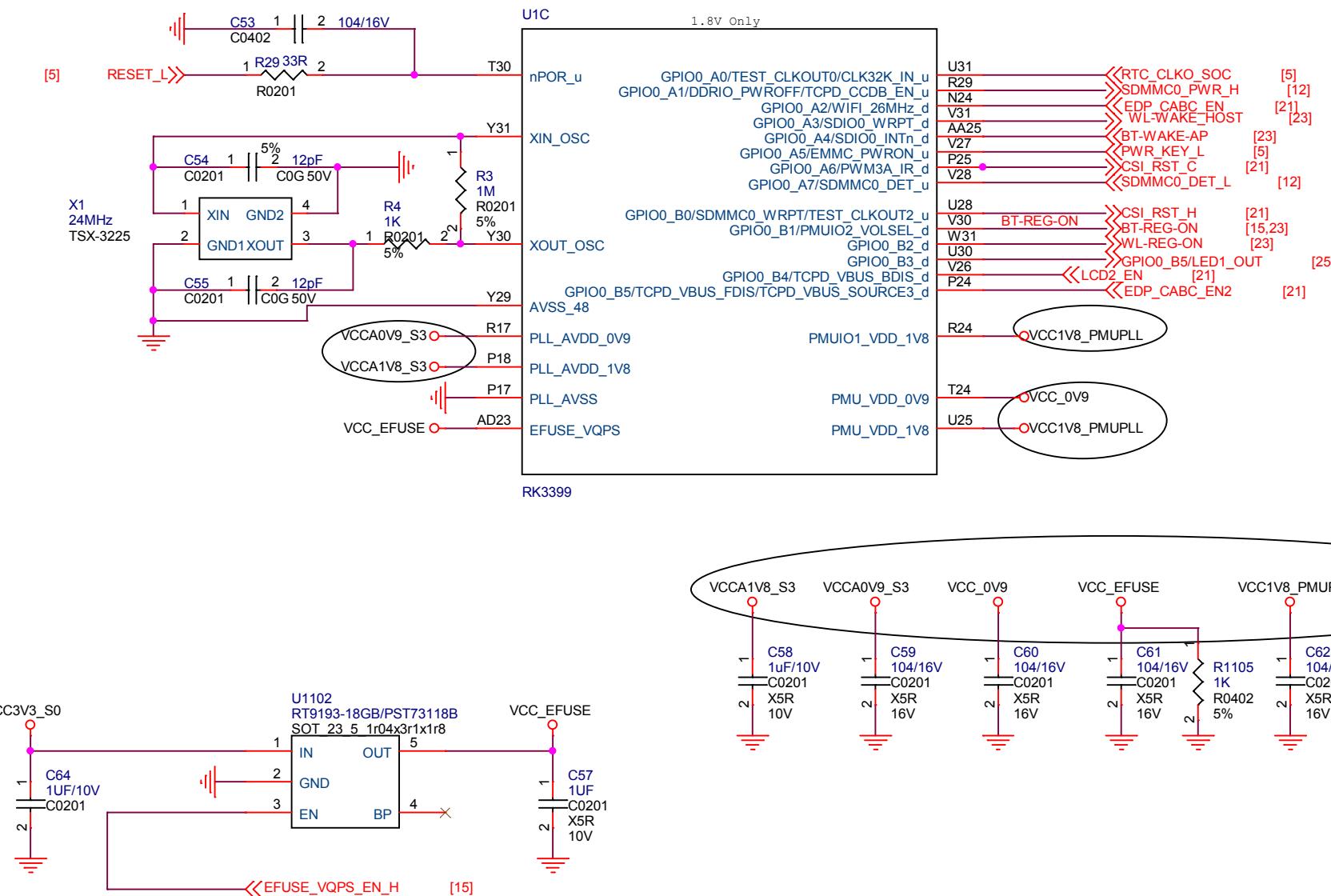
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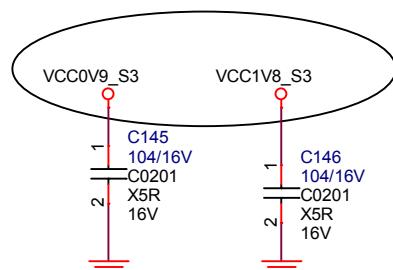
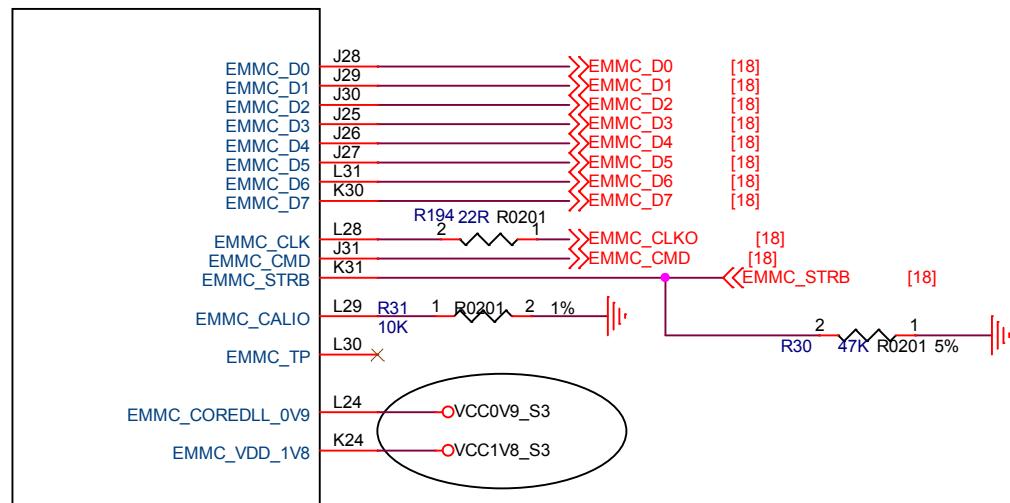
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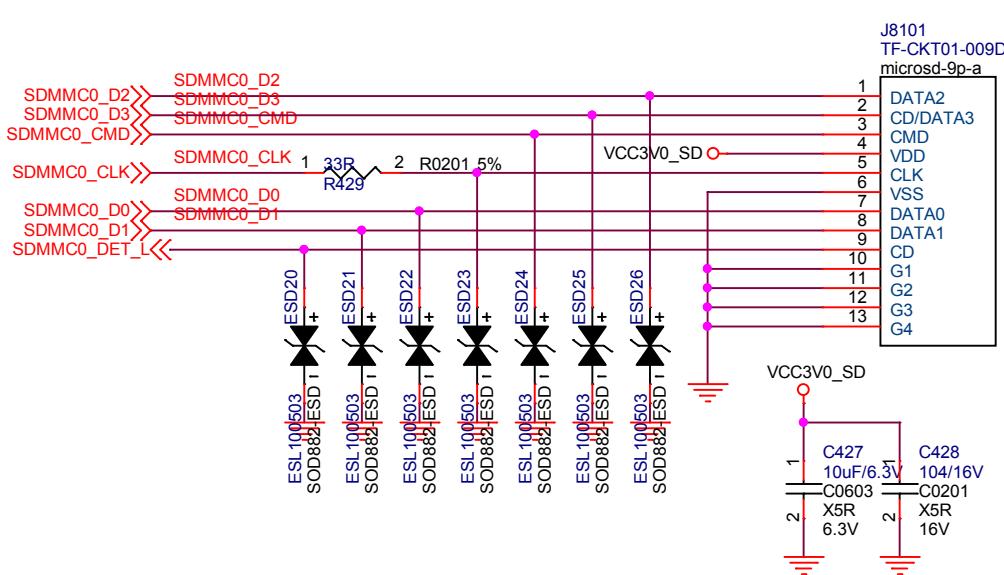
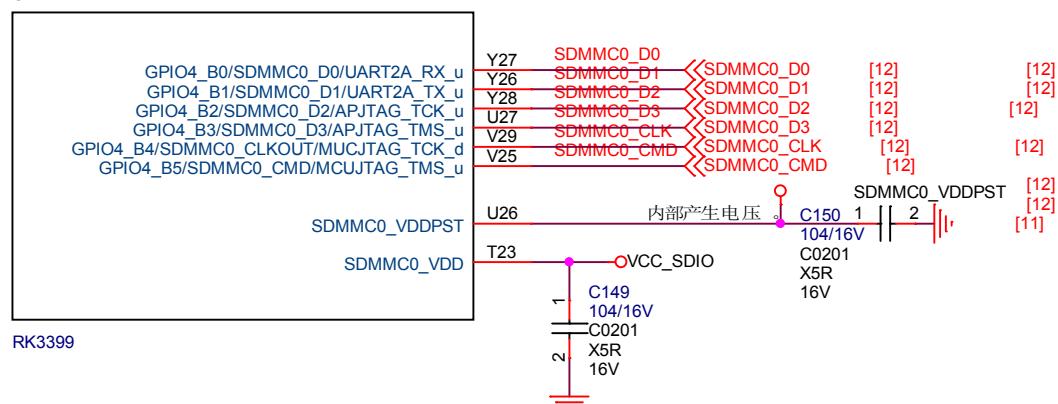
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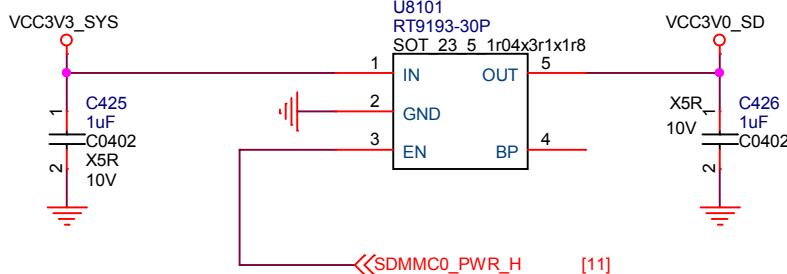
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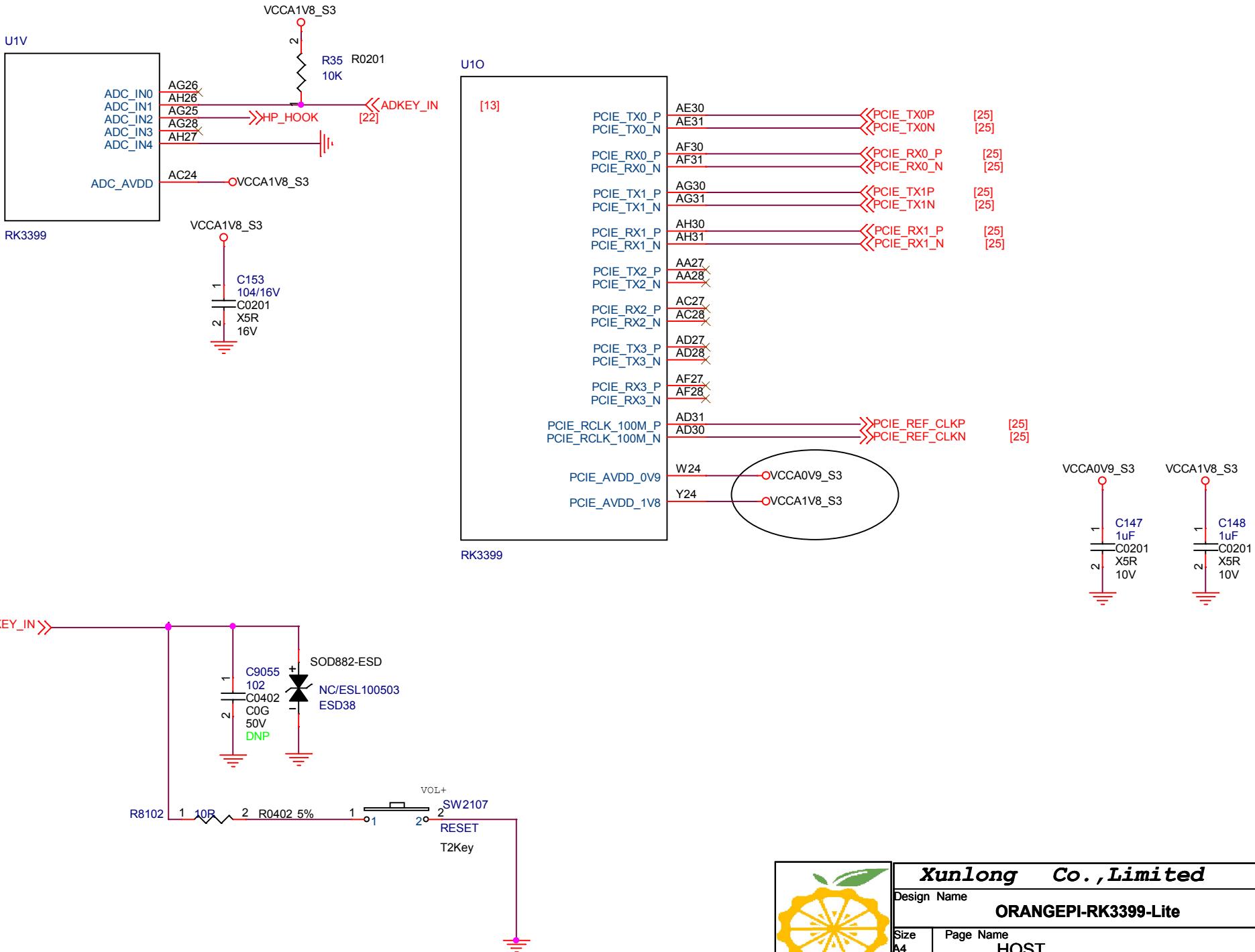
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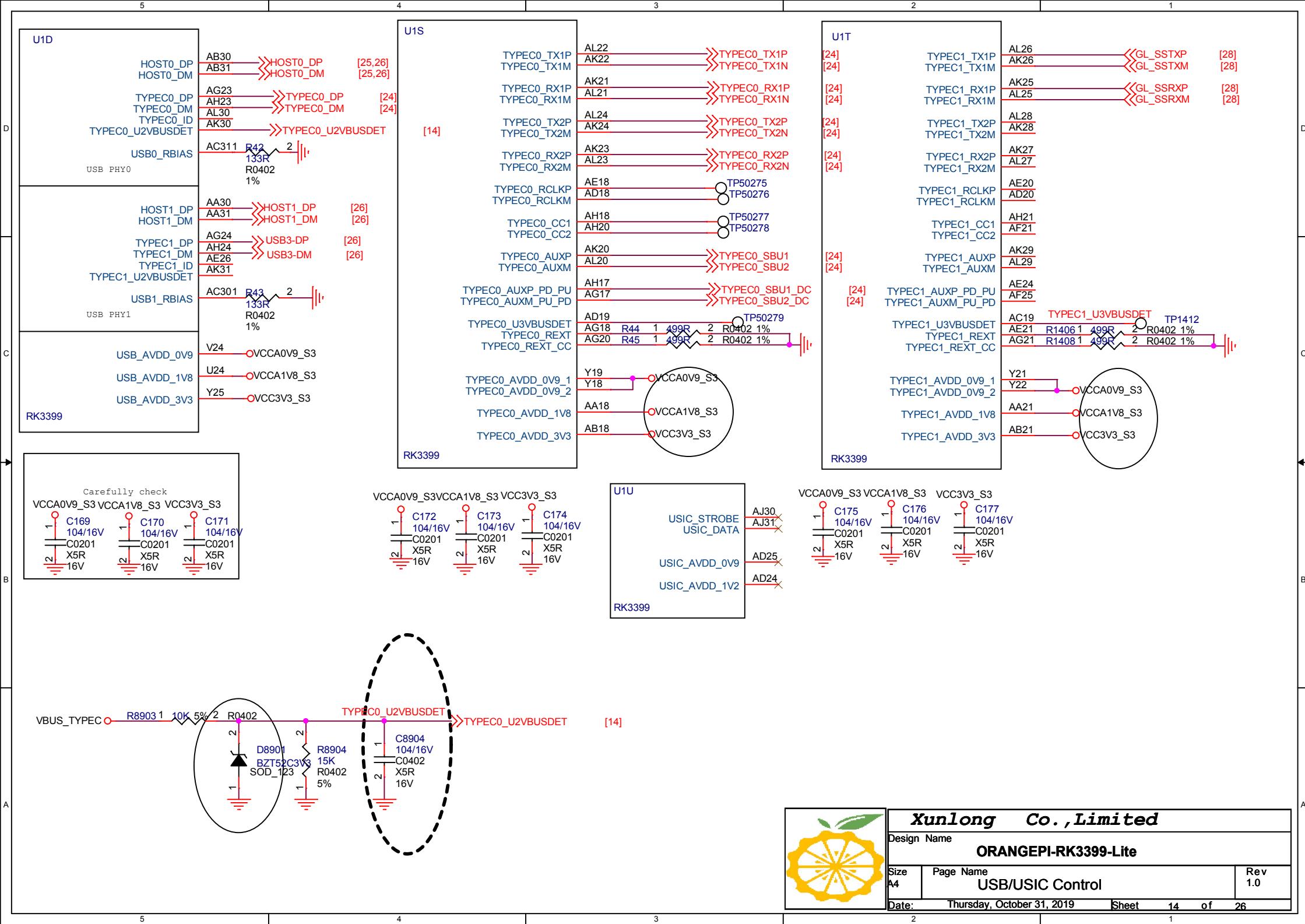
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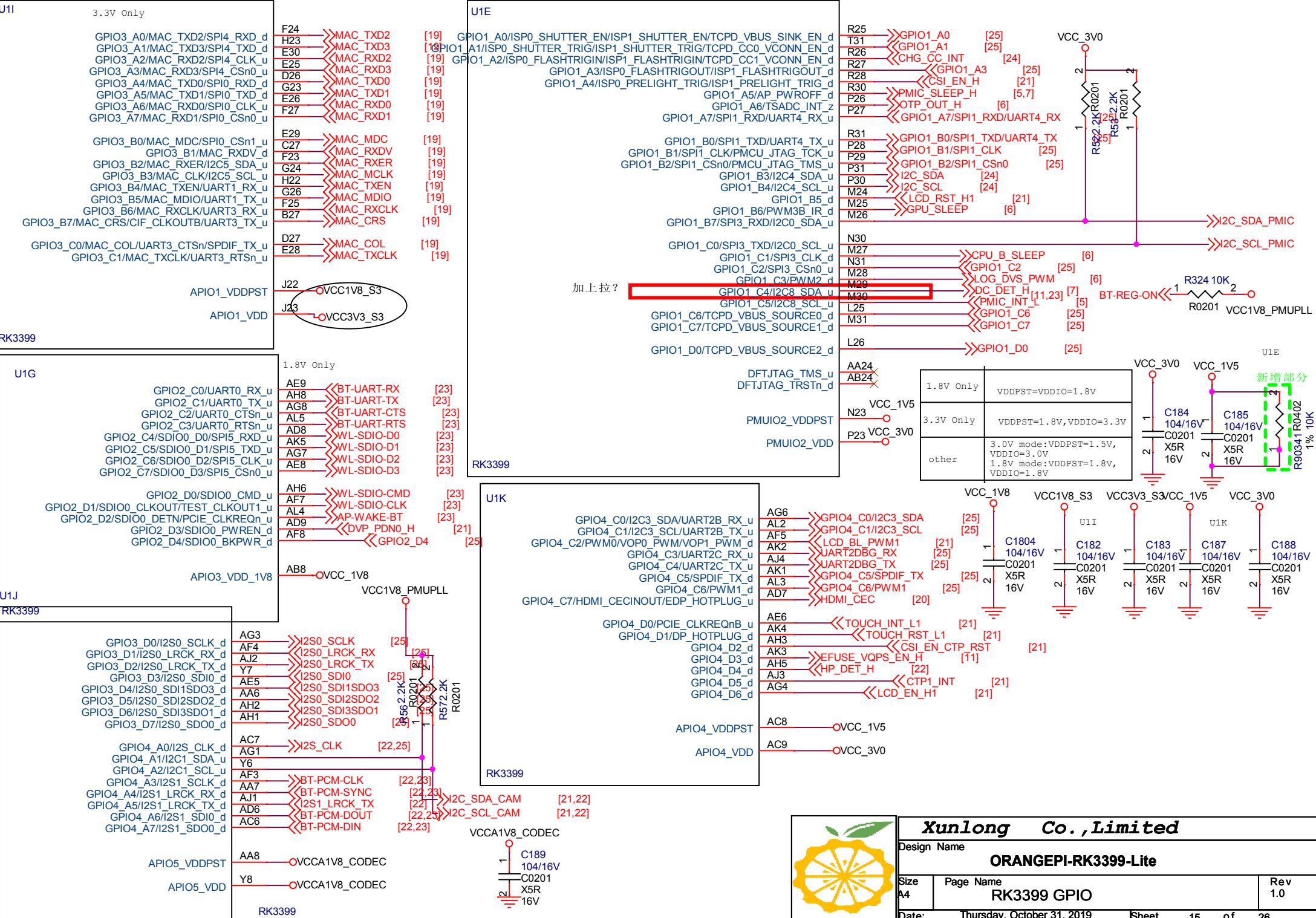
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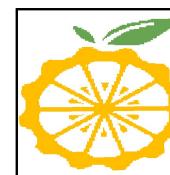
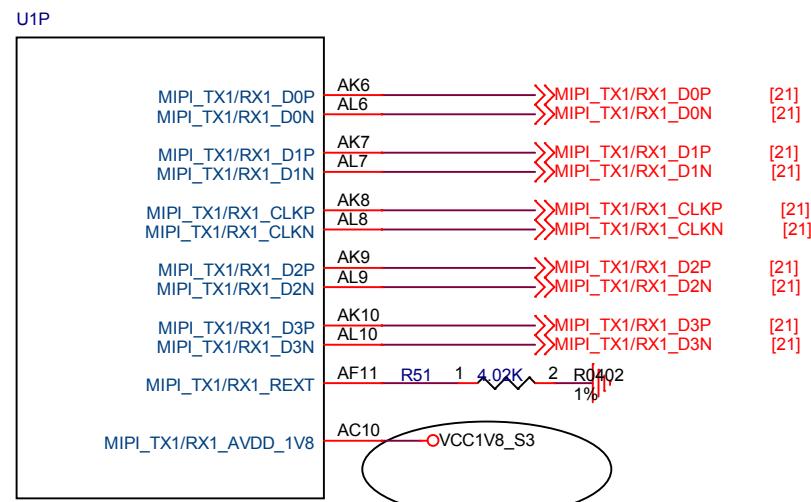
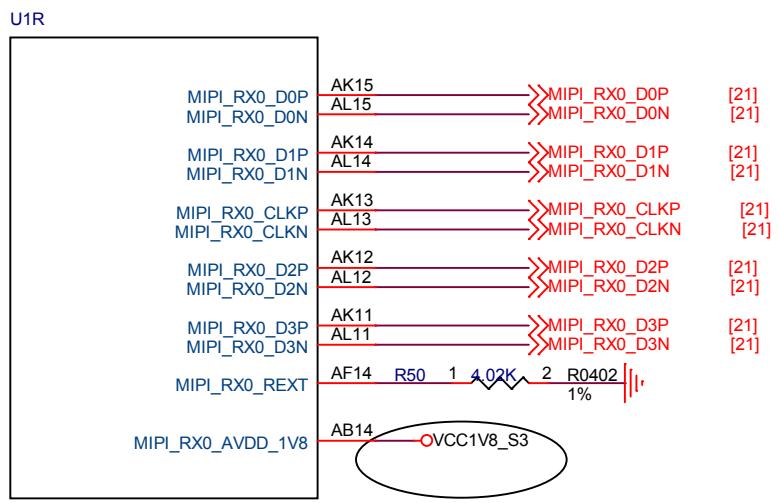
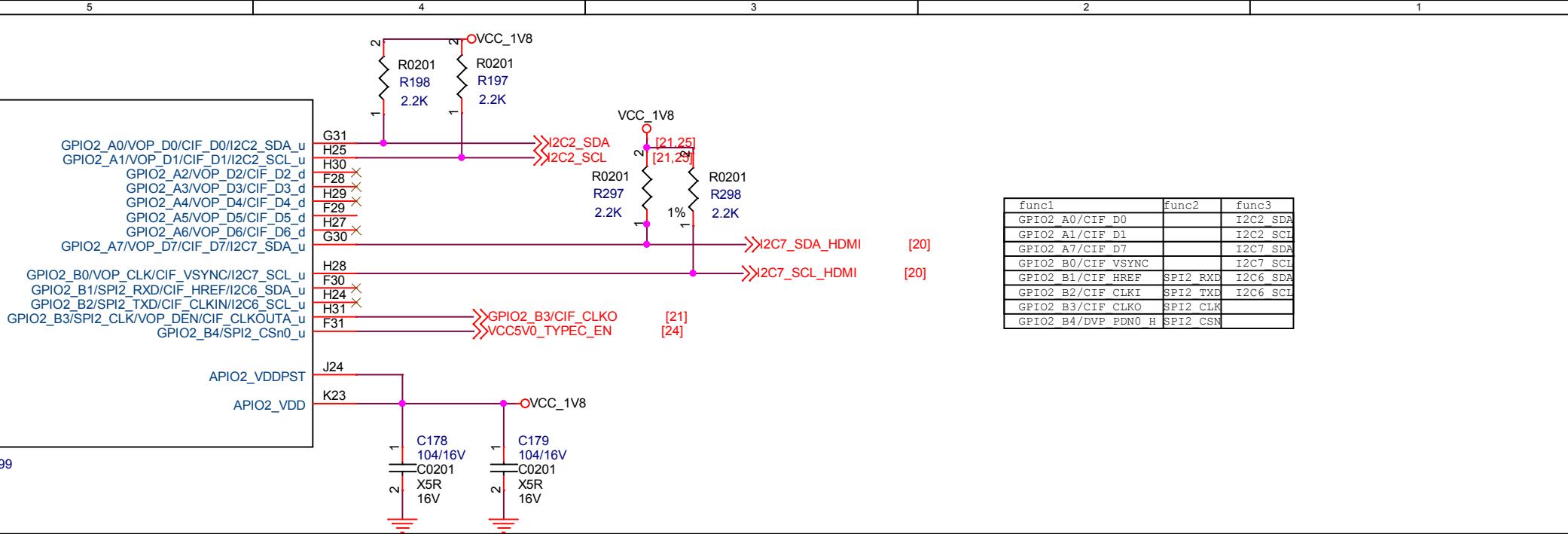
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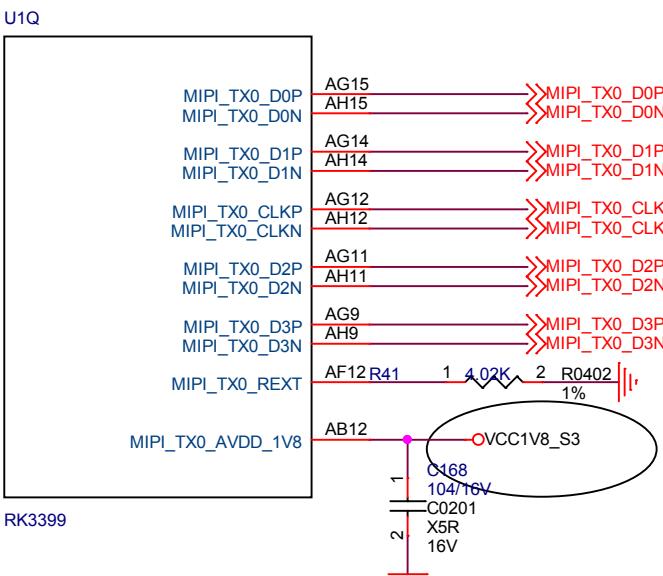
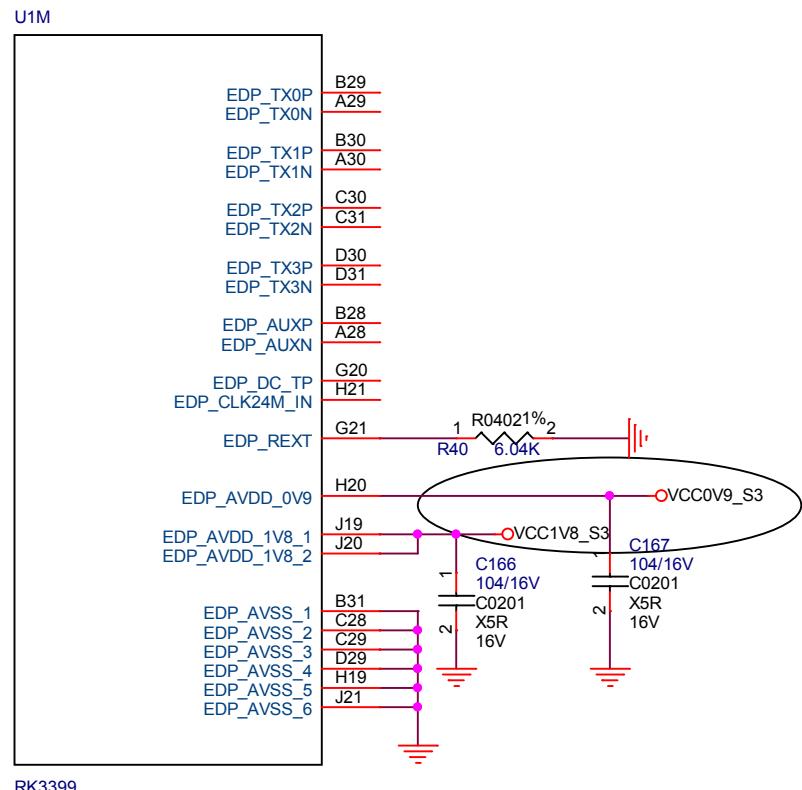
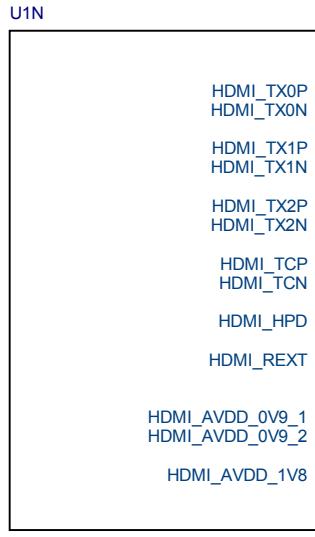
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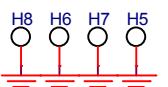
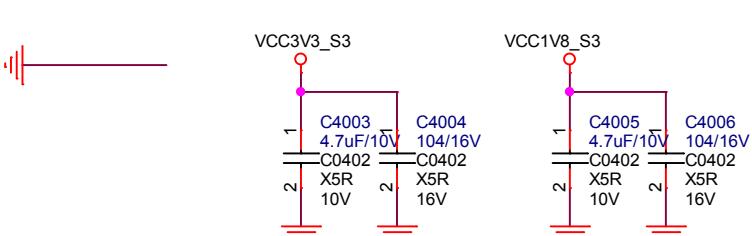
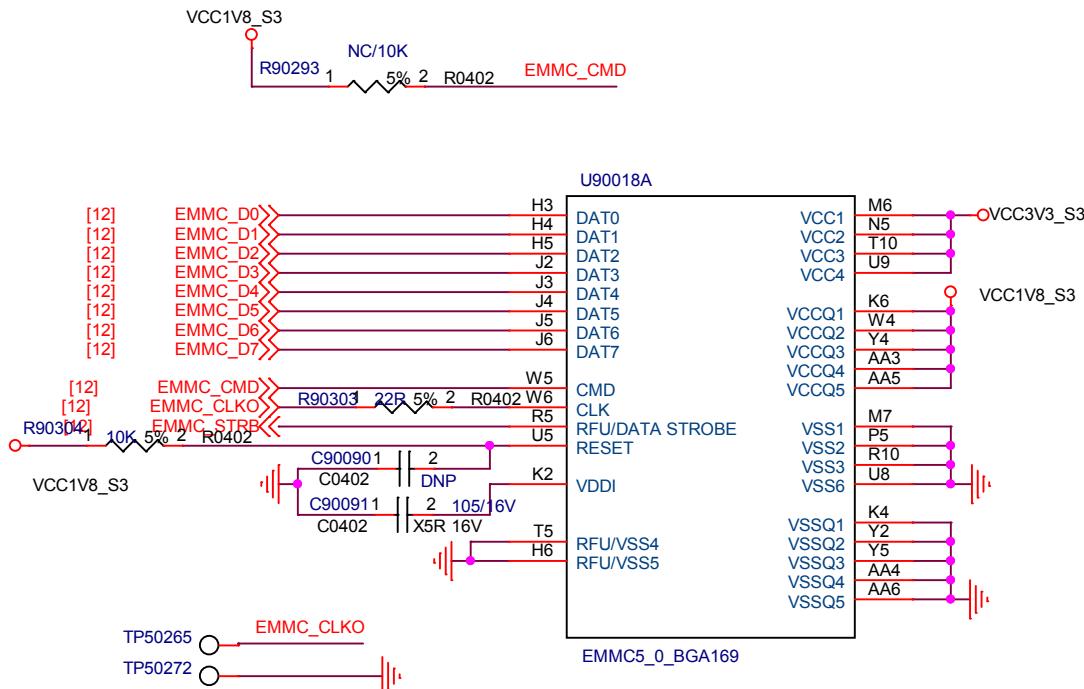
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eMMC FLASH



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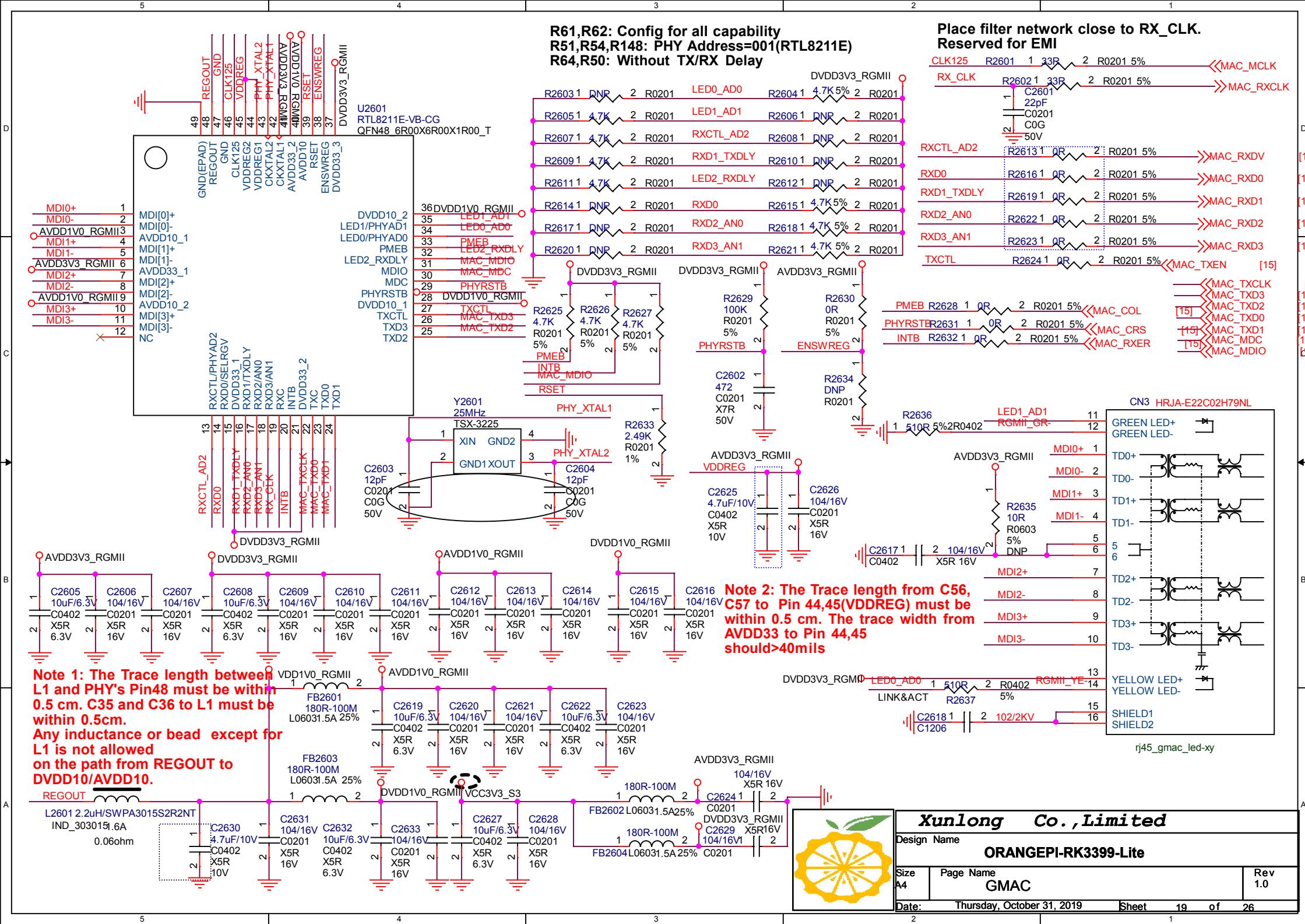
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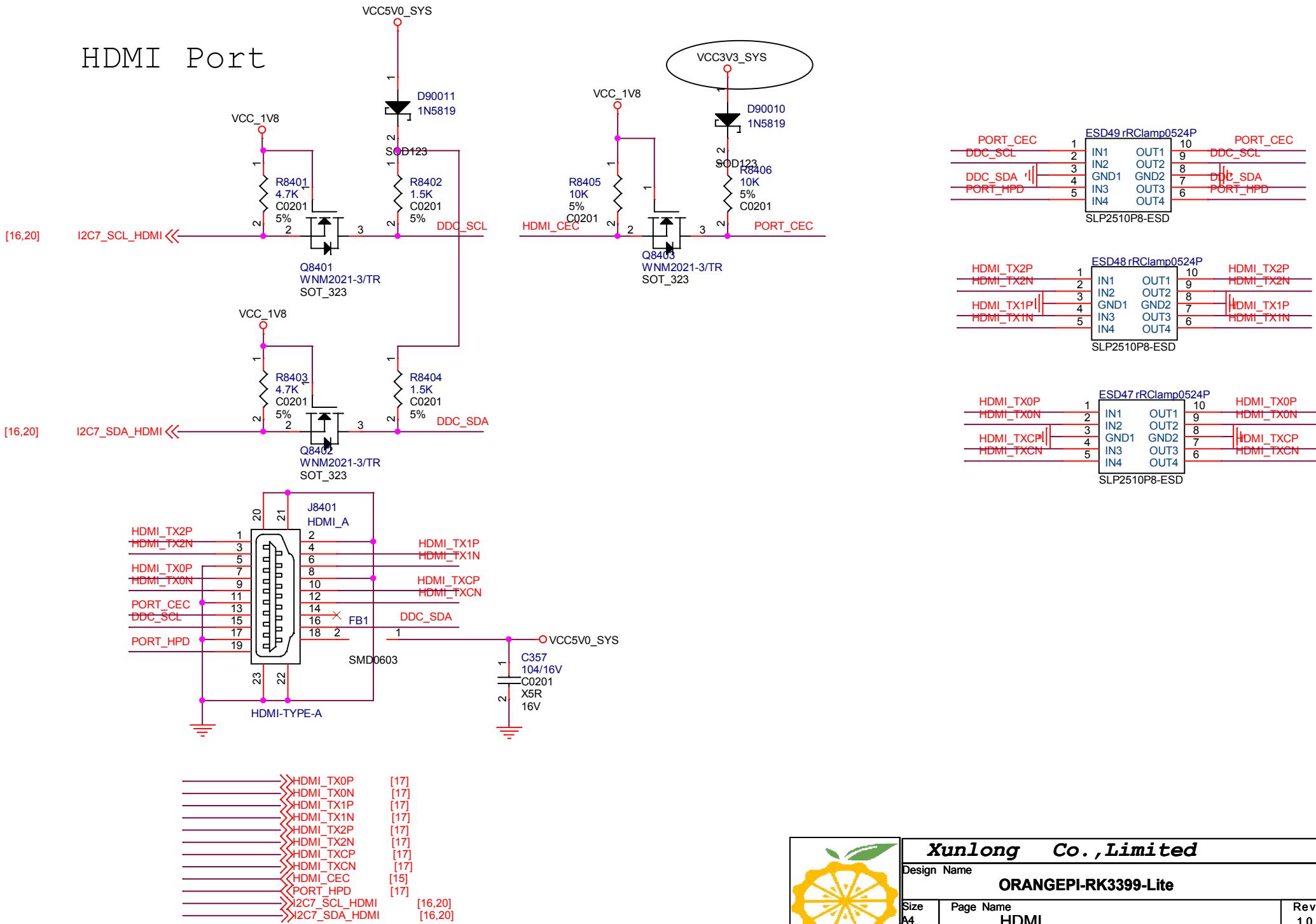
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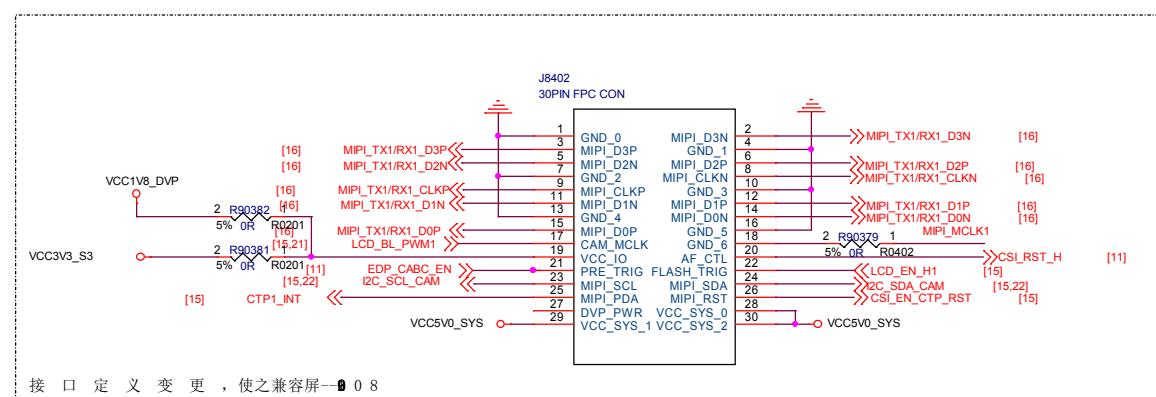
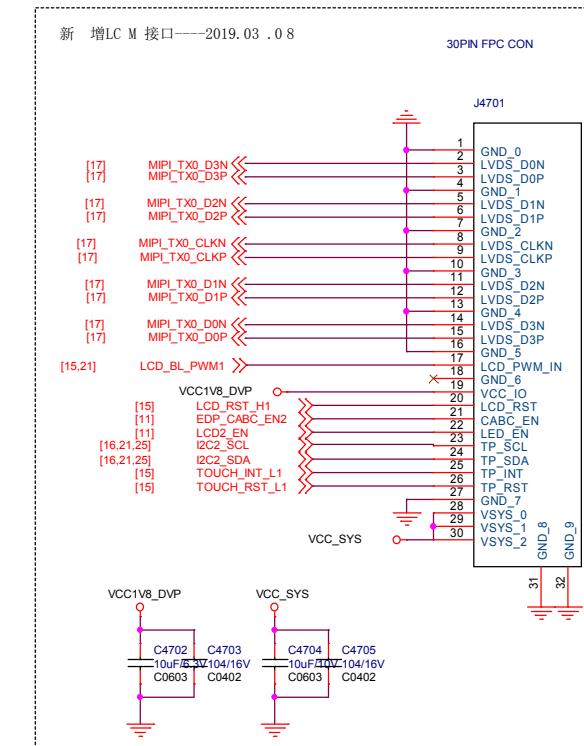
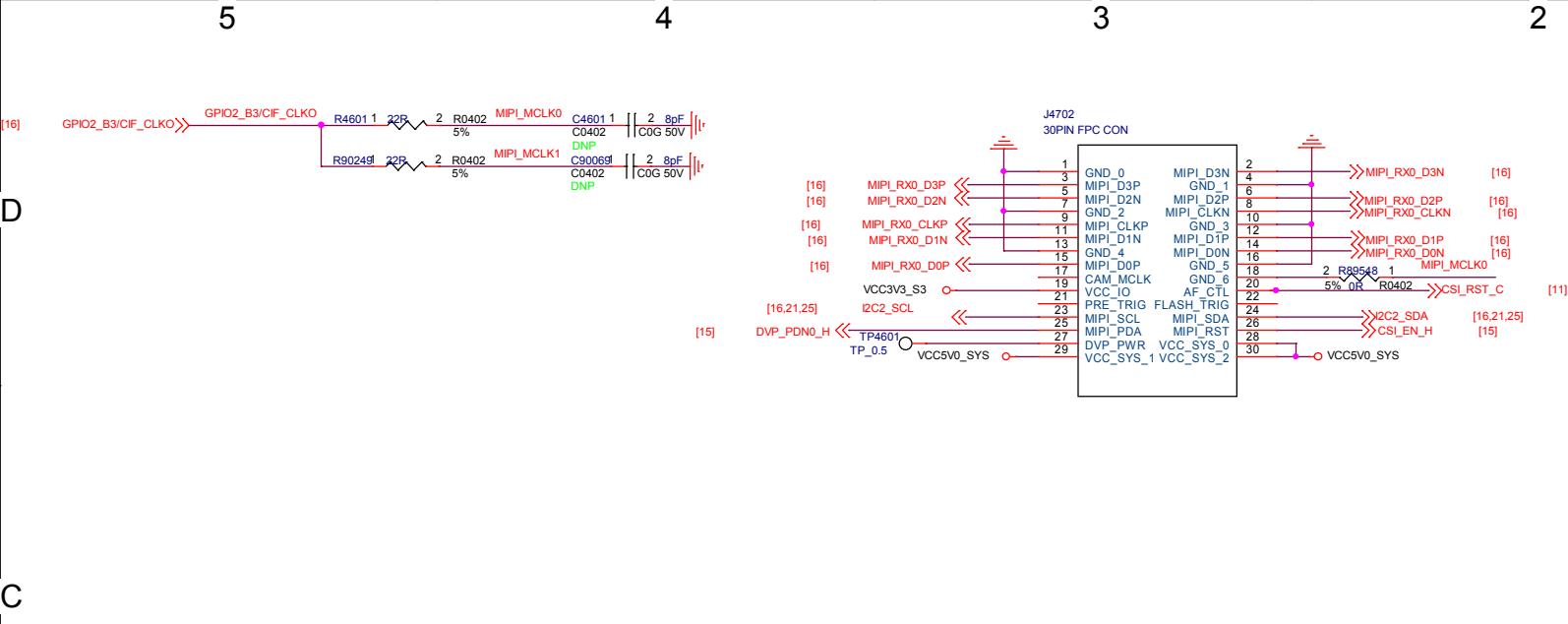
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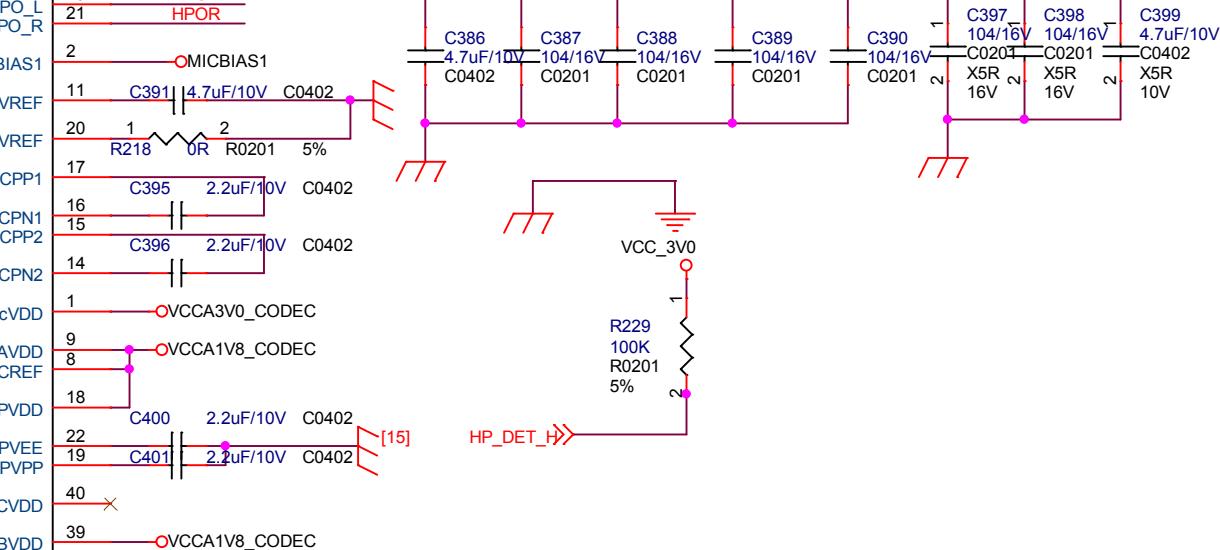
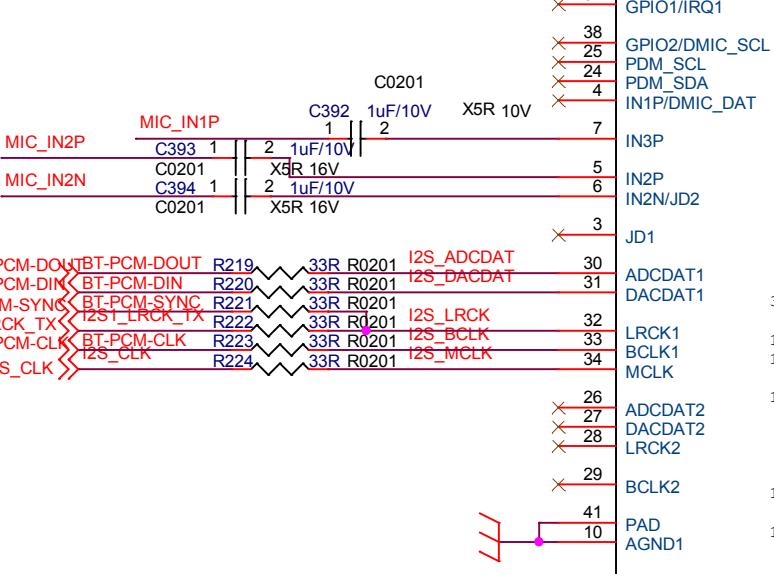
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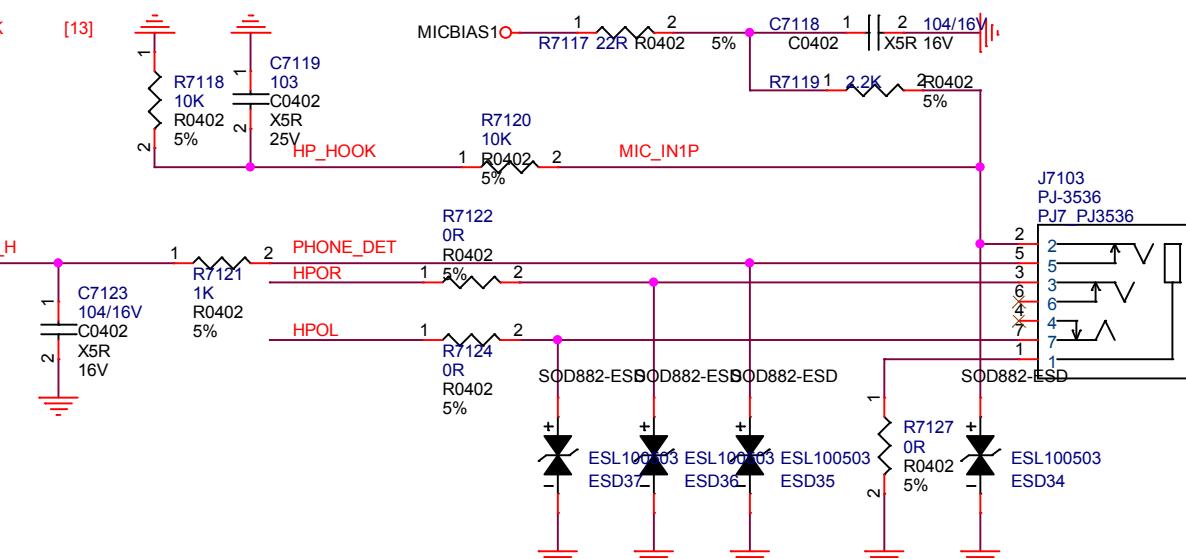
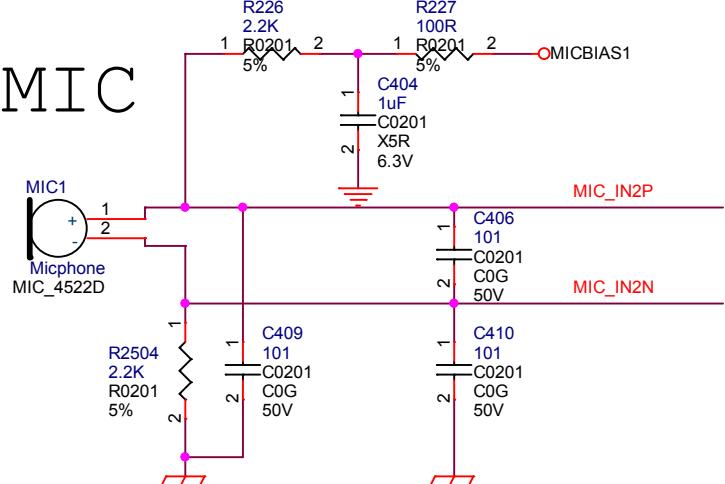
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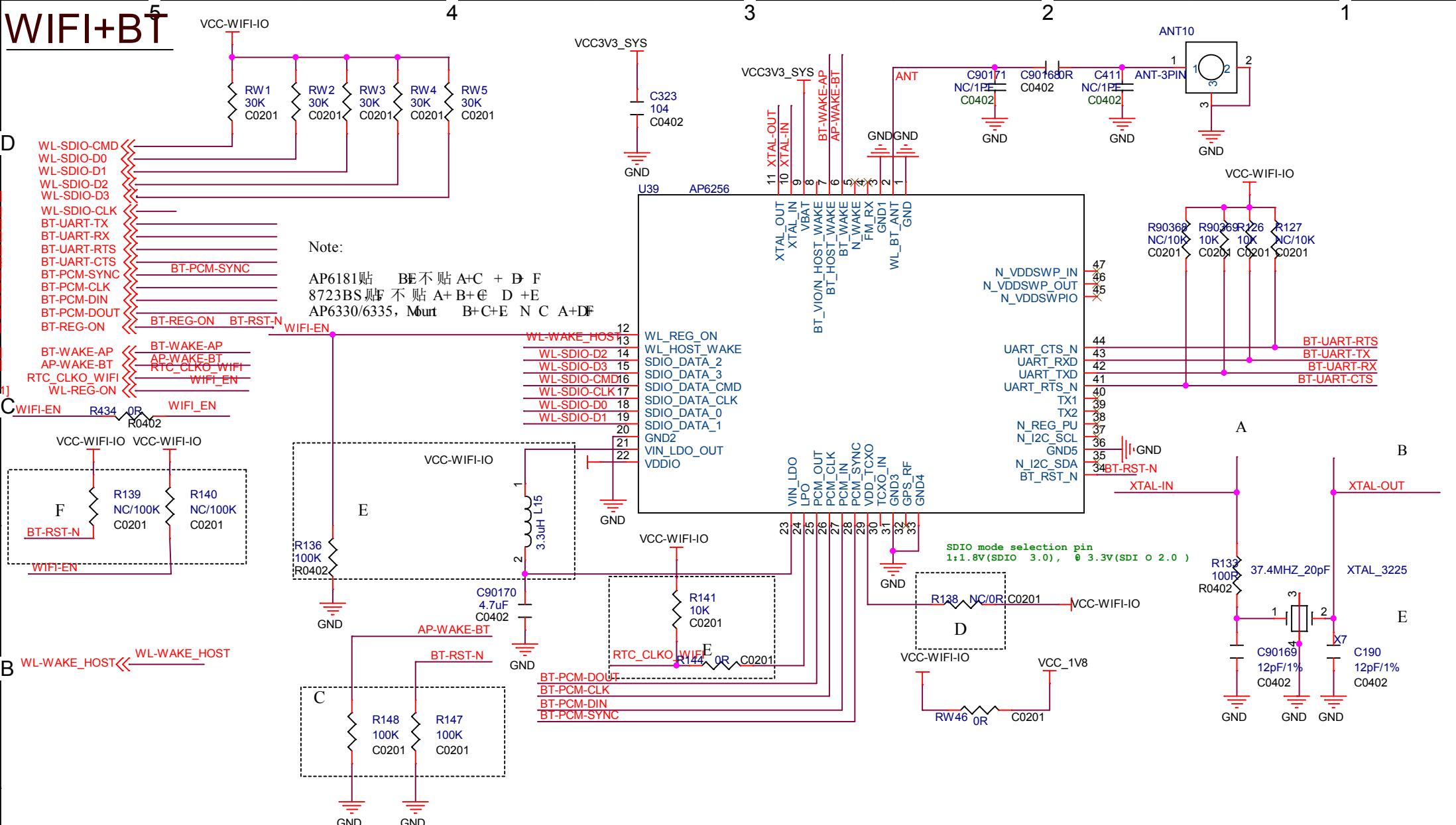
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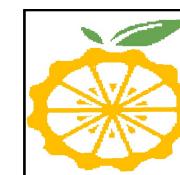
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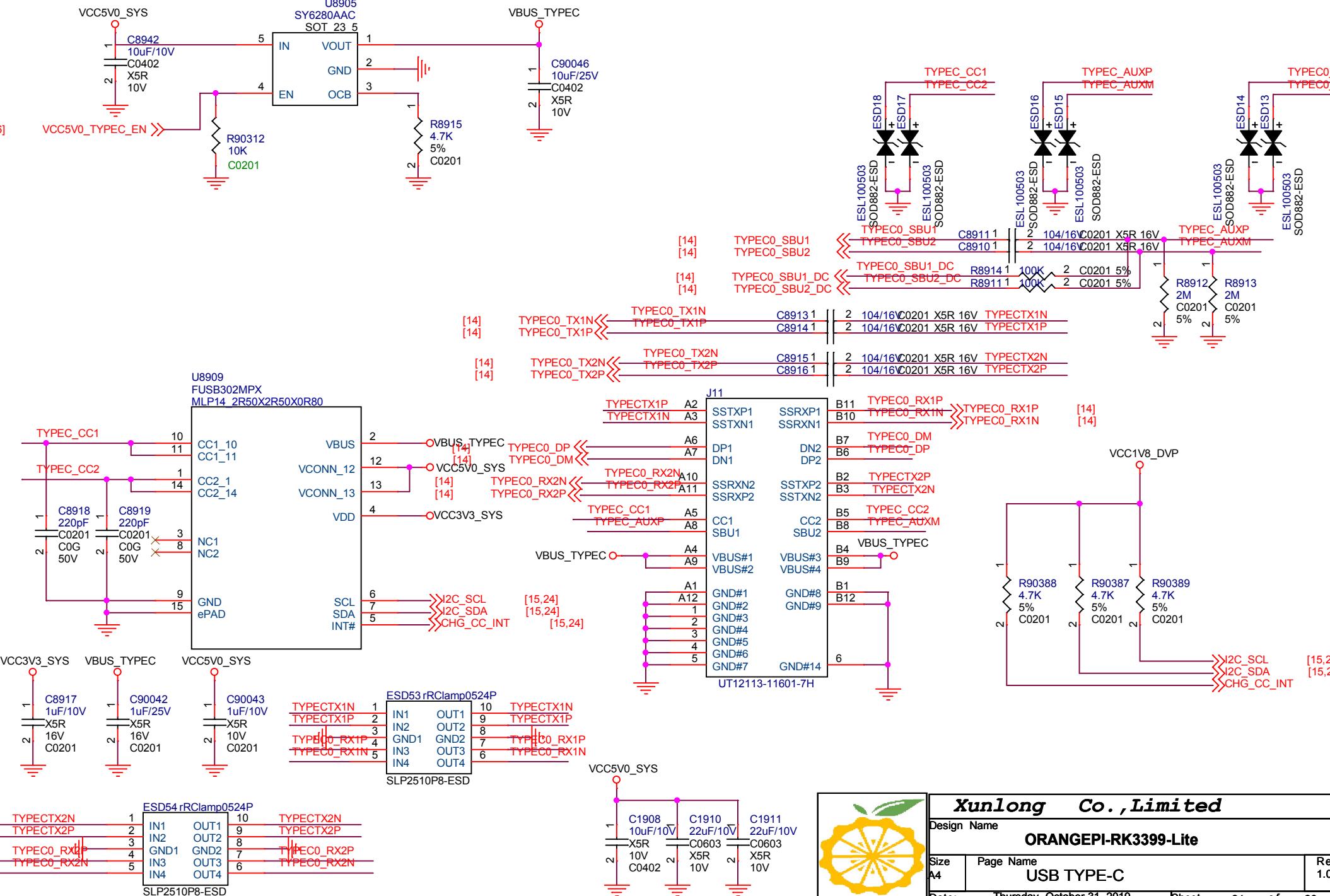


RW51 RW45 RW46 RW50 RW52 RW53

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带蓝牙遥控	IO 为 1.8V	0R	NC	NC	NC	NC	NC
	IO 为 3.3V	0R	0R	0R	NC	NC	NC



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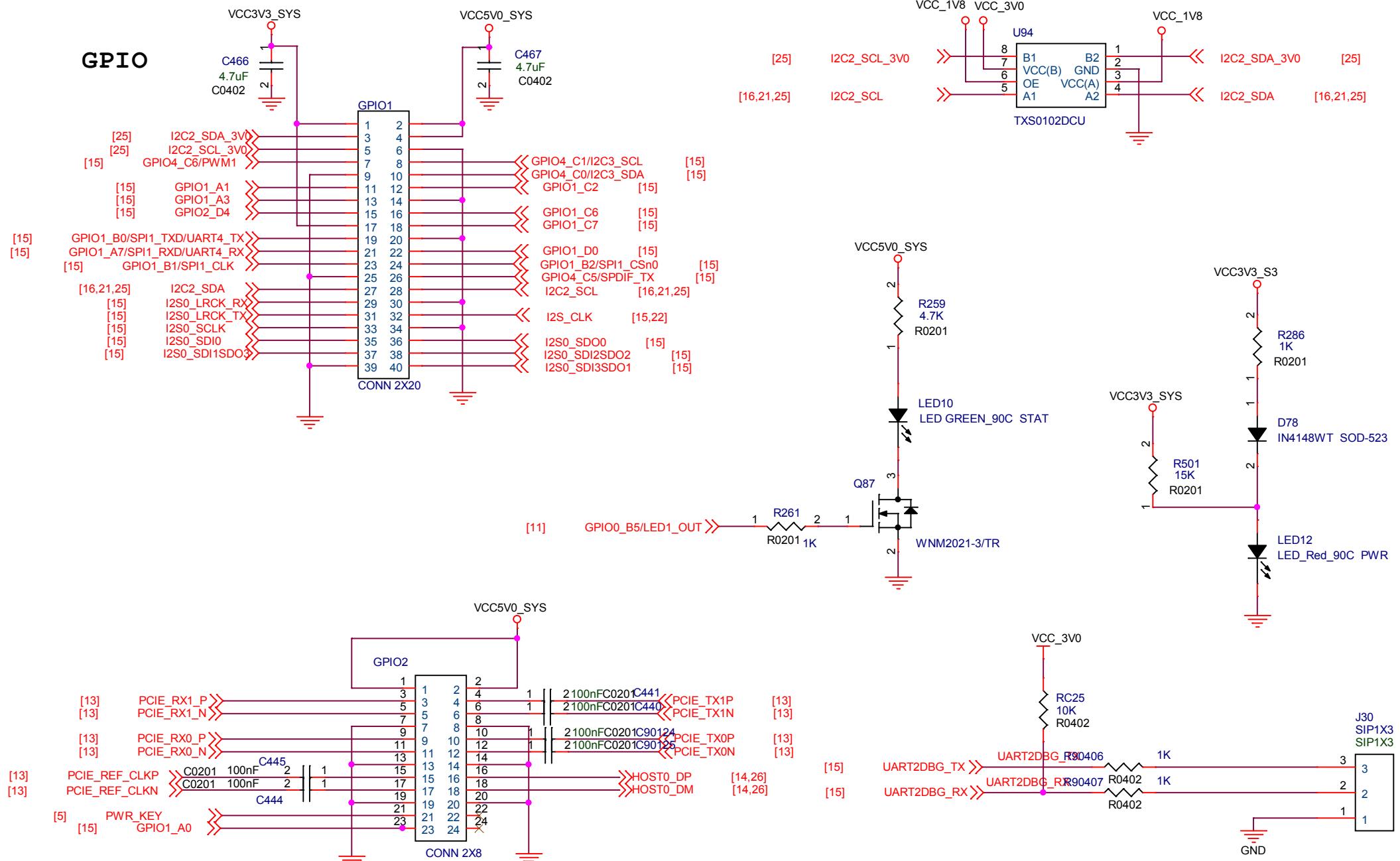
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PCIE , USB**Xunlong Co., Limited**

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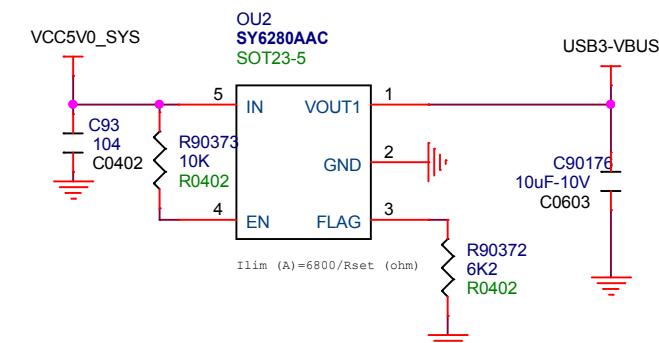
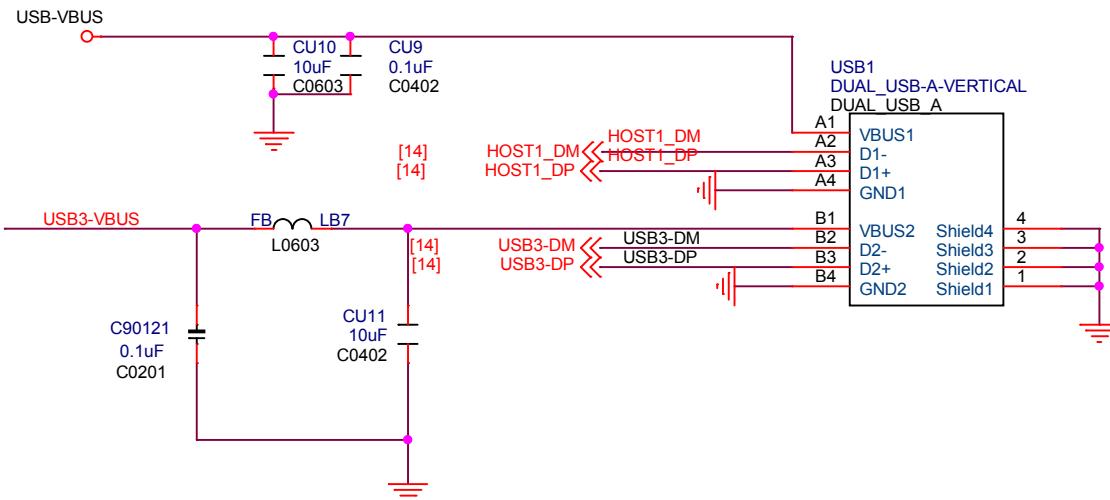
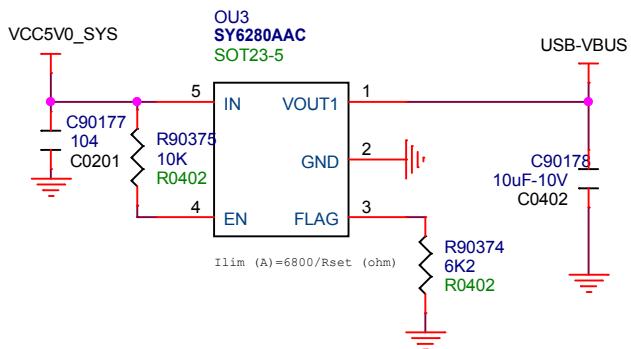
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