Top\_Module Registers

Version : 2.1.234  
Author : Yi Zhang

# 1 Address Map

|  |  |  |
| --- | --- | --- |
| Block | Start Address | Size |
| Instance2 | 0x10000 | 0x10 |
| Instance3 | 0x20000 | 0x10000 |

# 2 Type2 Registers

|  |  |  |
| --- | --- | --- |
| Offset | Instance2 Addr | Register |
| 0x0 | 0x10000 | reg1 |
| 0x8 | 0x10008 | reg2 |

## 2.1 reg1

**Offset :** 0x0  
 **Instance2 Address :** 0x10000  
 **Reset Value :** 0x102  
 **Description :** register 1

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| LSB | MSB | Field | Access | Default | Description |
| 1 | 7 | Field1 | RW | 0x1 | Register1-Field1 |
| 8 | 10 | Field2 | RW | 0x1 | Register1-Field2 |

## 2.2 reg2

**Offset :** 0x8  
 **Instance2 Address :** 0x10008  
 **Reset Value :** 0x202  
 **Description :** register 2

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| LSB | MSB | Field | Access | Default | Description |
| 1 | 7 | Field2 | RW | 0x1 | Register2-Field1 |
| 9 | 12 | Field3 | RW | 0x1 | Register2-Field3 |

# 3 Type1 Registers

|  |  |  |
| --- | --- | --- |
| Offset | Instance3 Addr | Register |
| 0x0 | 0x20000 | reg1 |
| 0x4 | 0x20004 | reg2 |
| 0x1a0 | 0x201a0 | regtt |

## 3.1 reg1

**Offset :** 0x0  
 **Instance3 Address :** 0x20000  
 **Reset Value :** 0x102  
 **Description :** register 1

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| LSB | MSB | Field | Access | Default | Description |
| 1 | 7 | Field1 | RW | 0x1 | Register1-Field1 |
| 8 | 10 | Field2 | RW | 0x1 | Register1-Field2 |

## 3.2 reg2

**Offset :** 0x4  
 **Instance3 Address :** 0x20004  
 **Reset Value :** 0x202  
 **Description :** register 2

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| LSB | MSB | Field | Access | Default | Description |
| 1 | 7 | Field2 | RW | 0x1 | Register2-Field1 |
| 9 | 12 | Field3 | RW | 0x1 | Register2-Field3 |

## 3.3 regtt

**Offset :** 0x1a0  
 **Instance3 Address :** 0x201a0  
 **Reset Value :** 0x5  
 **Description :**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| LSB | MSB | Field | Access | Default | Description |
| 0 | 10 | Field9 | RW | 0x5 | registera2\_field6 |