

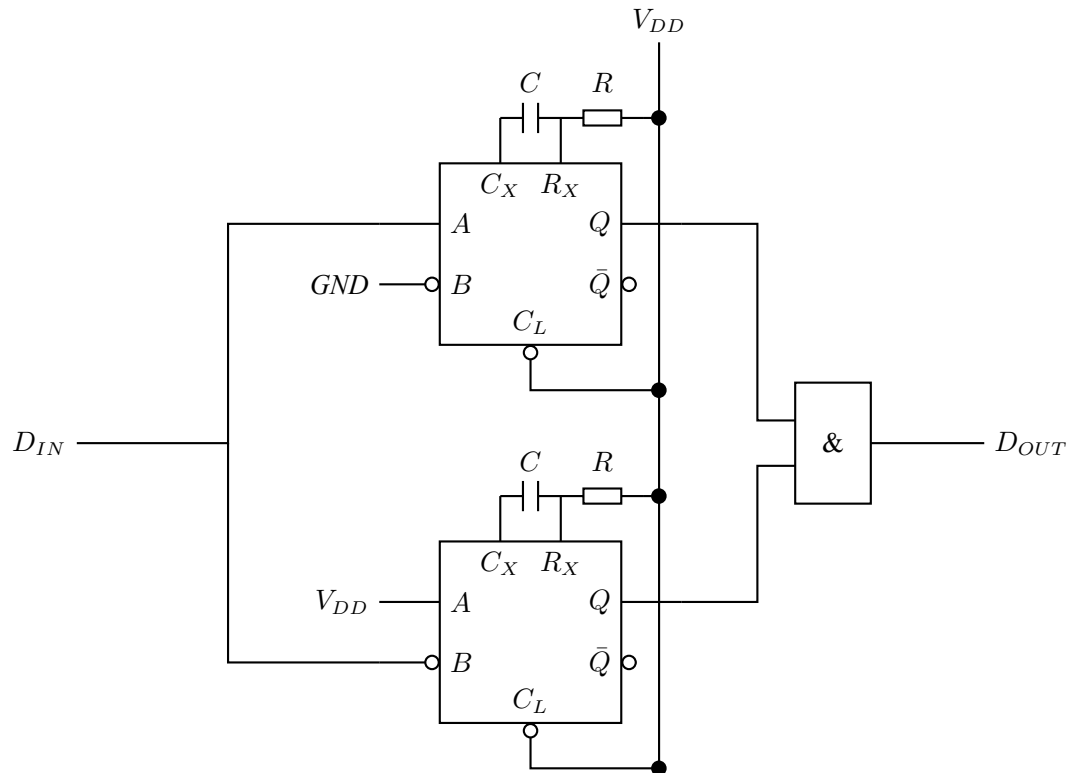
Digital Circuit Fall 2019

Yuxuan Zhang, XJTU, 2160909016

Session 5 - Sequential circuit and unit

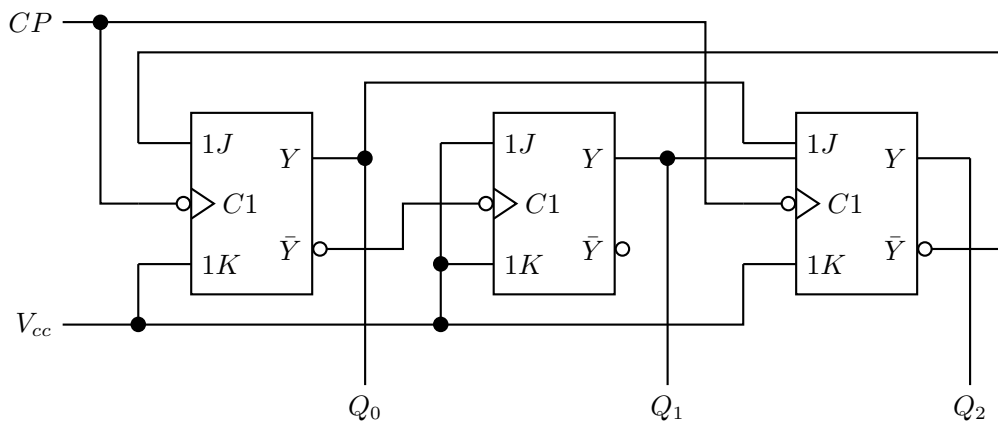
Session 1 Notes

Another method to generate delayed pulse - without losing its length



Session 1 Homework

- **Problem 1 - 8.3** Analyze Logical function of the given circuit.



- Problem 2 - 8.6 .
- Problem 3 - 8.7 .
- Problem 4 - 8.12 .
- Problem 5 - 8.13 .
- Problem 6 - 8.17 .

Session 6 - Sequential circuit and unit

Session 2 Homework

- Problem 1 - 9.1 .
- Problem 2 - 9.5 .
- Problem 3 - 9.8 .
- Problem 4 - 9.13 .