



## 1. Description

### 1.1. Project

Project Name	NUCLEO-WB09KE_cubemx
Board Name	NUCLEO-WB09KE
Generated with:	STM32CubeMX 6.13.0
Date	02/08/2025

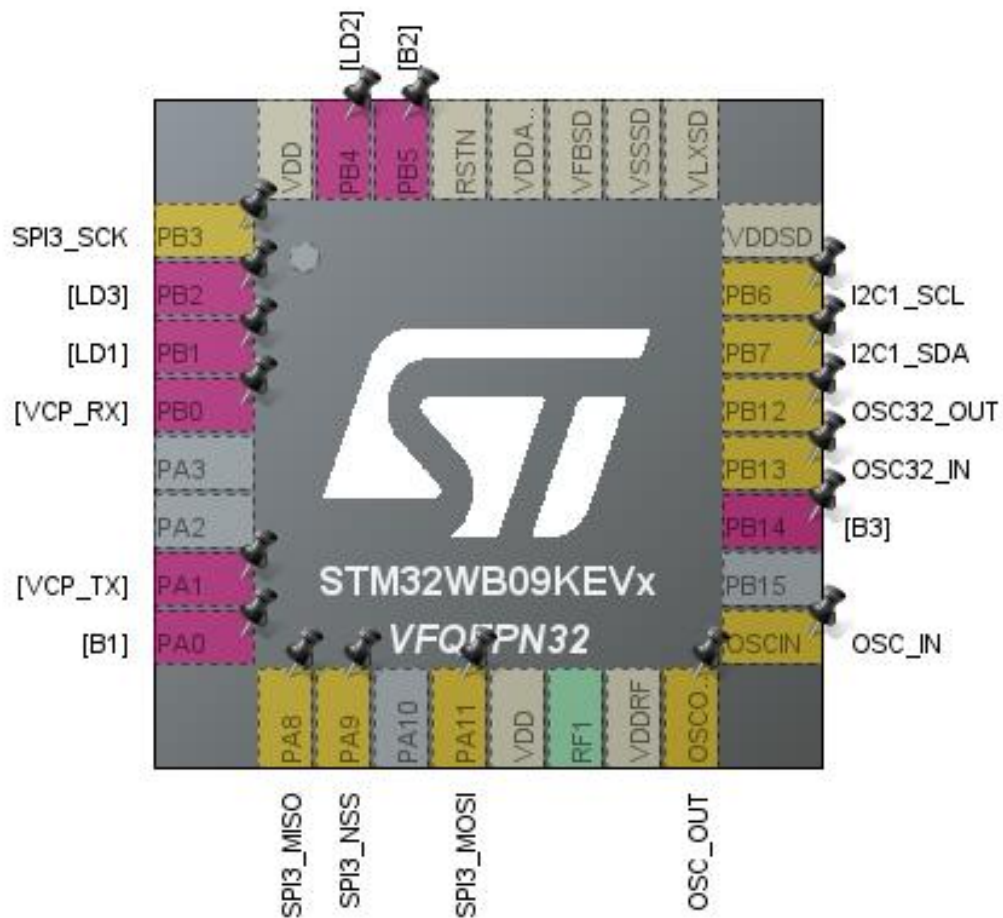
### 1.2. MCU

MCU Series	STM32WB0
MCU Line	STM32WBx9
MCU name	STM32WB09KEVx
MCU Package	VFQFPN32
MCU Pin number	32

### 1.3. Core(s) information

Core(s)	ARM Cortex-M0+
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## 2. Pinout Configuration

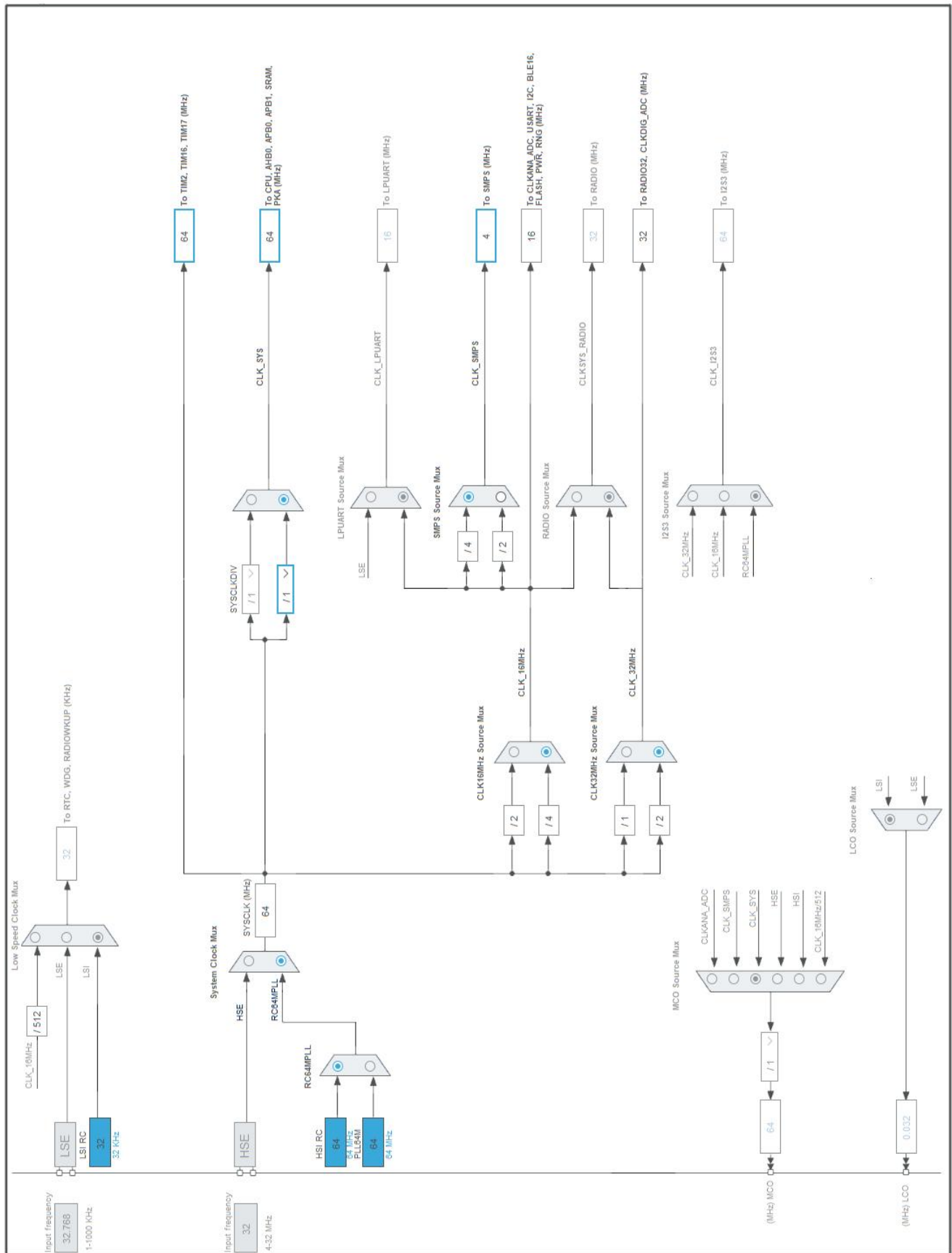


### 3. Pins Configuration

Pin Number VFQFPN32	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PB3 *	I/O	SPI3_SCK	SPI3_SCK
2	PB2	I/O		
3	PB1	I/O		
4	PB0	I/O		
7	PA1	I/O		
8	PA0	I/O		
9	PA8 *	I/O	SPI3_MISO	SPI3_MISO
10	PA9 *	I/O	SPI3_NSS	SPI3_NSS
12	PA11 *	I/O	SPI3_MOSI	SPI3_MOSI
13	VDD	Power		
15	VDDRF	Power		
16	OSCOUT *	MonoIO	RCC_OSC_OUT	OSC_OUT
17	OSCIN *	MonoIO	RCC_OSC_IN	OSC_IN
19	PB14	I/O		
20	PB13 *	I/O	RCC_OSC32_IN	OSC32_IN
21	PB12 *	I/O	RCC_OSC32_OUT	OSC32_OUT
22	PB7 *	I/O	I2C1_SDA	I2C1_SDA
23	PB6 *	I/O	I2C1_SCL	I2C1_SCL
24	VDDSD	Power		
25	VLXSD	Power		
26	VSSSD	Power		
27	VFBSD	Power		
28	VDDA_VCAP	Power		
29	RSTN	Power		
30	PB5	I/O		
31	PB4	I/O		
32	VDD	Power		

\* The pin is affected with a peripheral function but no peripheral mode is activated

## 4. Clock Tree Configuration



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32WB0
Line	STM32WBx9
MCU	STM32WB09KEVx
Datasheet	DS000000_Rev1

### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

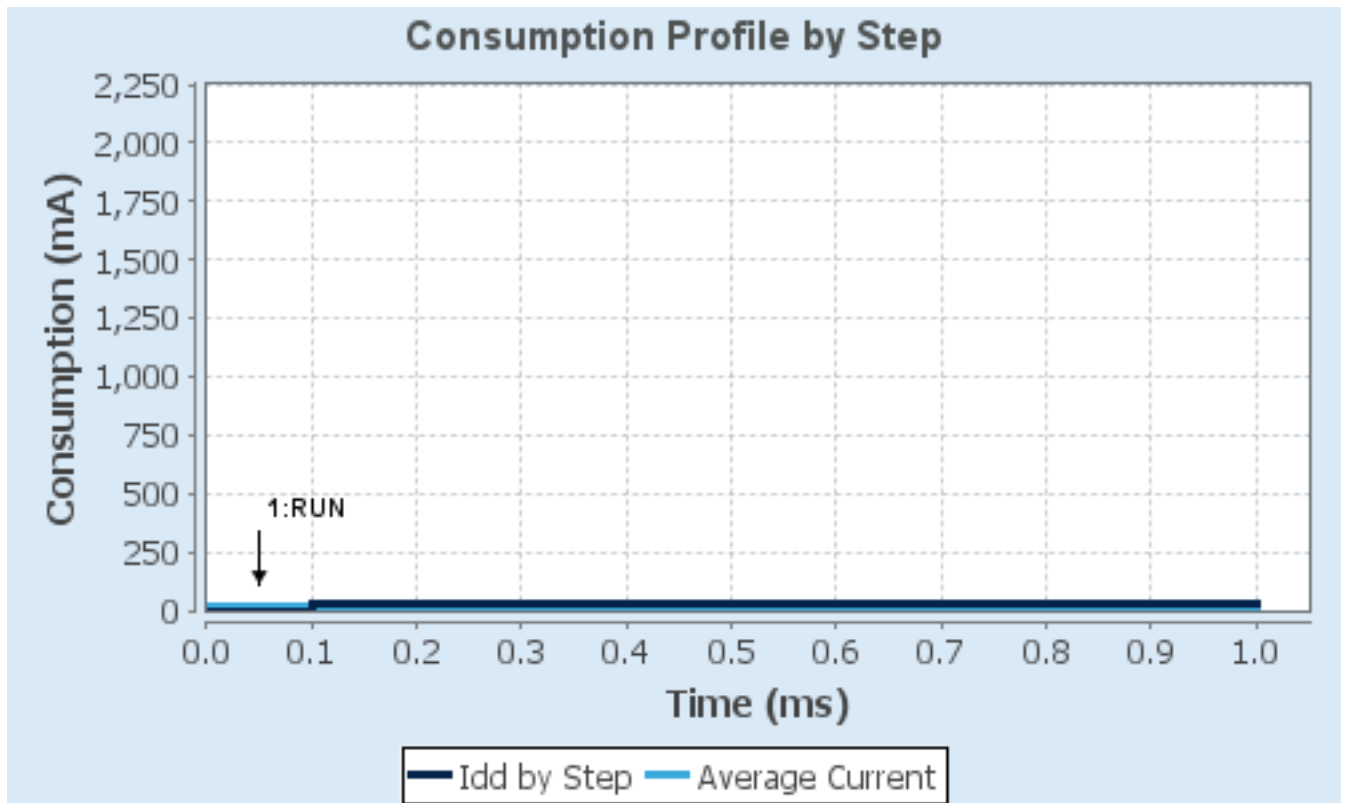
#### 1.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	SHUTDOWN
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	NaN/SMPS	NaN
<b>Fetch Type</b>	FLASH	FLASH
<b>CPU Frequency</b>	64 Hz	0 Hz
<b>Clock Configuration</b>	RC64MPLL ALL RAM RETENTION	ALL_OFF
<b>Clock Source Frequency</b>	32 Hz	32 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	1 nA	25 mA
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	0.0	0.0
<b>Ta Max</b>	125	125
<b>Category</b>	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	22.5 mA
Battery Life	1 day, 7 hours	Average DMIPS	8.0E-6 DMIPS

#### 1.6. Chart





## 2. Software Project

### 2.1. Project Settings

Name	Value
Project Name	NUCLEO-WB09KE_cubemx
Project Folder	D:\TEMP\temp
Toolchain / IDE	MDK-ARM V5.39
Firmware Package Name and Version	STM32Cube FW_WB0 V1.1.0
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x000
Minimum Stack Size	0xC00

### 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO

## 3. Peripherals and Middlewares Configuration

### 3.1. NUCLEO-WB09KE

**mode: Human Machine Interface**

#### 3.1.1. Human Machine Interface:

**Led:**

USER LED BLUE (LD1)	true *
USER LED GREEN (LD2)	true *
USER LED RED (LD3)	true *

**Button:**

USER B1	Mode EXTI *
USER B2	Mode EXTI *
USER B3	Mode EXTI *

**VCOM:**

Virtual Com Port	true *
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**Demonstration code:**

Generate demonstration code	Disabled
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### 3.2. NUCLEO-WB09KE

**mode: Human Machine Interface**

#### 3.2.1. Human Machine Interface:

**Led:**

USER LED BLUE (LD1)	true *
USER LED GREEN (LD2)	true *
USER LED RED (LD3)	true *

**Button:**

USER B1	Mode EXTI *
USER B2	Mode EXTI *
USER B3	Mode EXTI *

**VCOM:**

Virtual Com Port	true *
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**Demonstration code:**

Generate demonstration code	Disabled
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### 3.3. RCC

#### 3.3.1. Parameter Settings:

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##### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled

##### RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	<b>100 *</b>

### 3.4. SYS

**Timebase Source: SysTick**

**\* User modified value**

## 4. System Configuration

### 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
Single Mapped Signals	PB3	SPI3_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	SPI3_SCK
	PA8	SPI3_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	SPI3_MISO
	PA9	SPI3_NSS	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	SPI3_NSS
	PA11	SPI3_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	SPI3_MOSI
	OSCOUT	RCC_OSC_OUT	n/a	n/a	n/a	OSC_OUT
	OSCIN	RCC_OSC_IN	n/a	n/a	n/a	OSC_IN
	PB13	RCC_OSC32_IN	n/a	n/a	n/a	OSC32_IN
	PB12	RCC_OSC32_OUT	n/a	n/a	n/a	OSC32_OUT
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	Very High *	I2C1_SDA
	PB6	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	Very High *	I2C1_SCL

### 4.2. DMA configuration

nothing configured in DMA service

### 4.3. NVIC configuration

#### 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
GPIOA interrupt	true	0	0
GPIOB interrupt	true	0	0
FLASH (CFI) global Interrupt	unused		
RCC interrupt	unused		

#### 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
GPIOA interrupt	false	true	true
GPIOB interrupt	false	true	true

\* User modified value

# 5. System Views

## 5.1. Category view

### 5.1.1. Current

Middleware

System Core	Analog	Timers	Connectivity	Multimedia	Security	Computing	Trace and Debug	Power and Thermal	Utilities	Bsp	Other
CORTEX_M0+ ✓										NUCLEO-WB09KE ✓	
DMA											
GPIO ⚠											
IVIC ✓											
RCC ✓											
SYS ✓											

## 6. Docs & Resources

Type	Link
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_eval_tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval_tools_portfolio.pdf</a>
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Presentations	<a href="https://www.st.com/resource/en/product_presentation/microcontrollers-stm32wb0-series-product-overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers-stm32wb0-series-product-overview.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
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Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an3155-uart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an3155-uart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf</a>

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