



SILICON LABS

BG22 Dual Polarized Antenna Array Radio Board

4x4 Antenna Array


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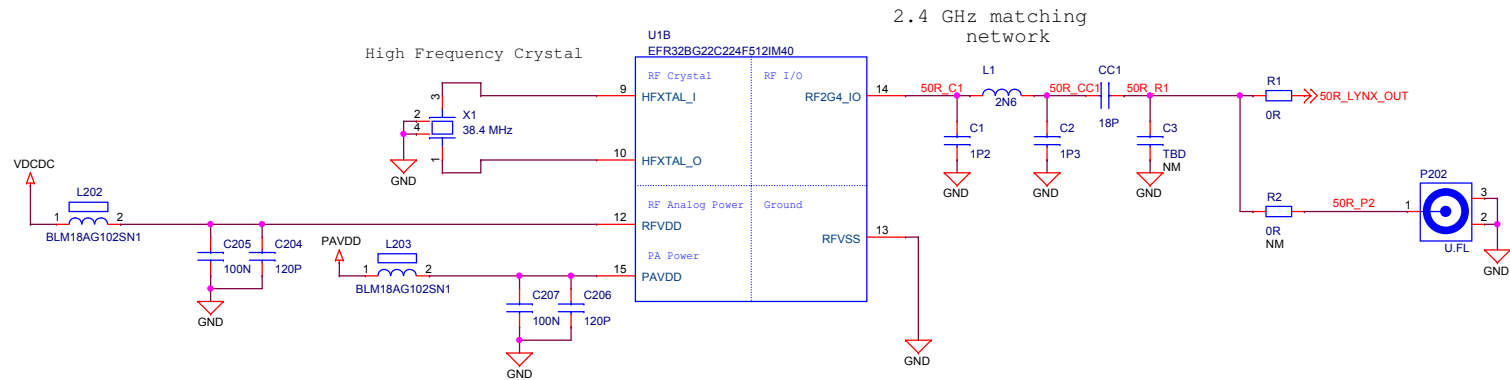
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Revision History

Rev.	Description
A00	Initial production release.

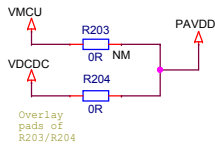
 SILICON LABS		Board Name BG22 Dual Polarized Antenna Array Radio Board	
Designed ADS		Approved RGU	
Page Title Title Page	Board Number BRD4191A		Revision A00
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Antenna & Radio Interface

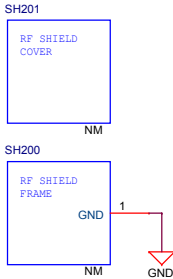


PAVDD Configuration

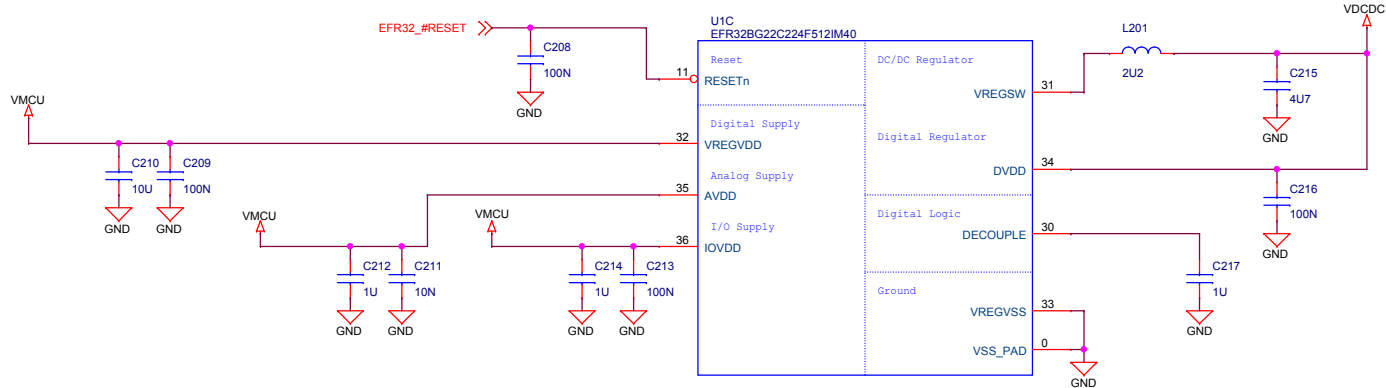
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R203	Mount	Not mount
R204	Not mount	Mount




RF Shielding



Power & Decoupling





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RF, Antenna and Power

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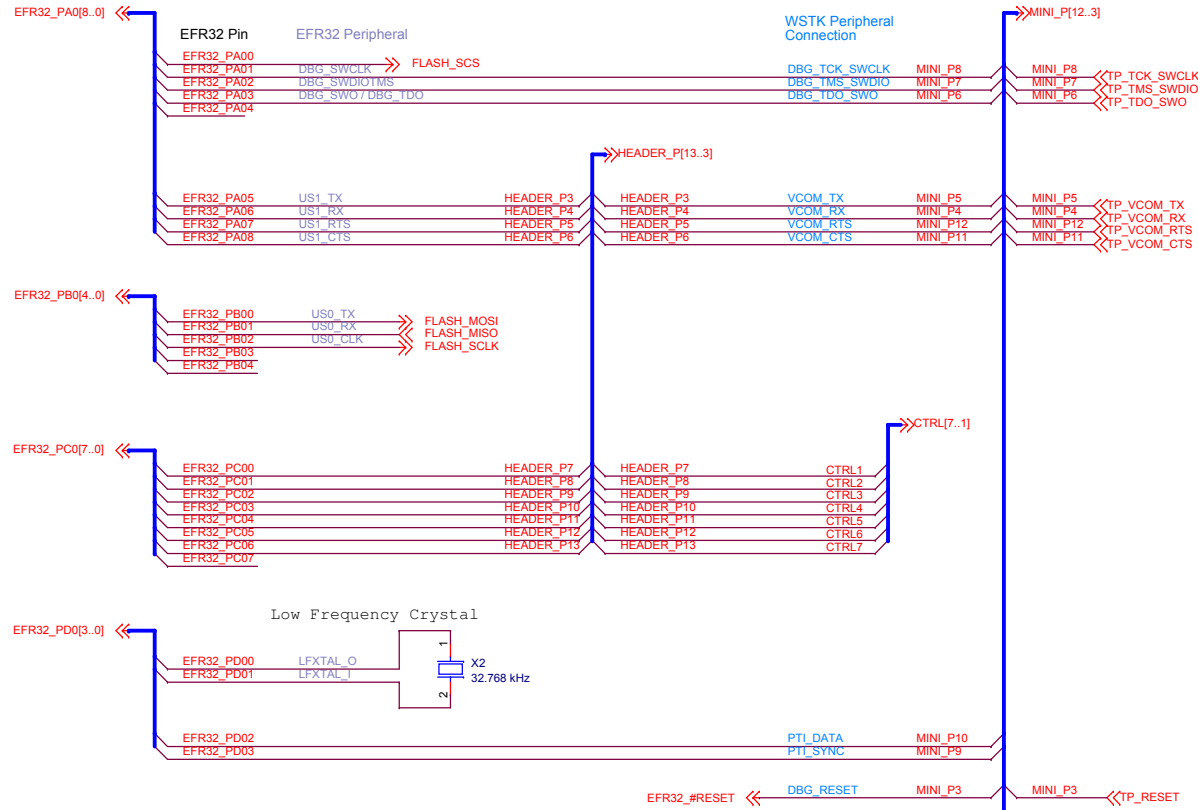
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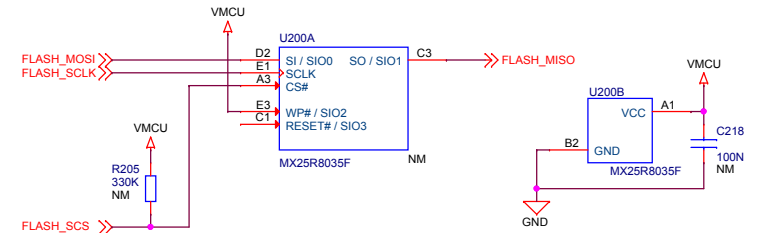
EFR32 Pin Mapping



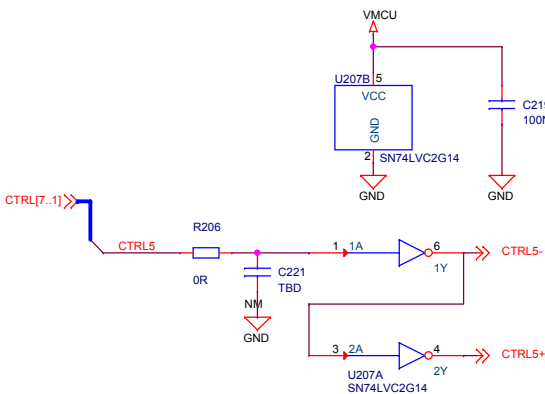
EFR32 I/O Port Pins



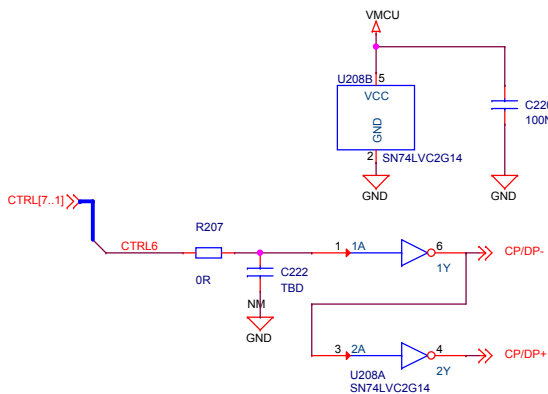
Serial Flash



CHW Selection Signal Buffering

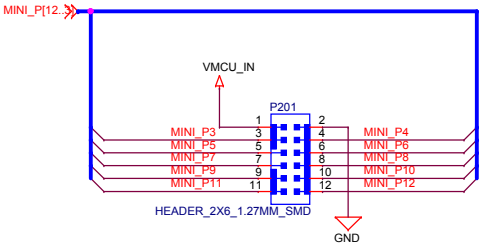


CP/DP Signal Buffering

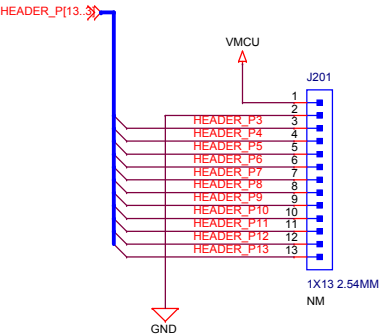


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ADS		I/O Port Connections	
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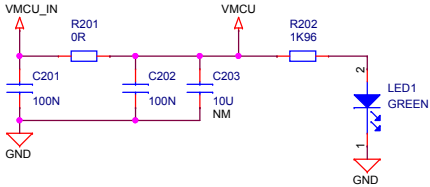
Simplicity Mini+ Connector



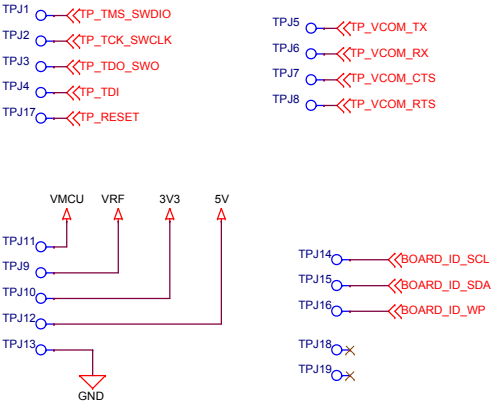
Breakout Header




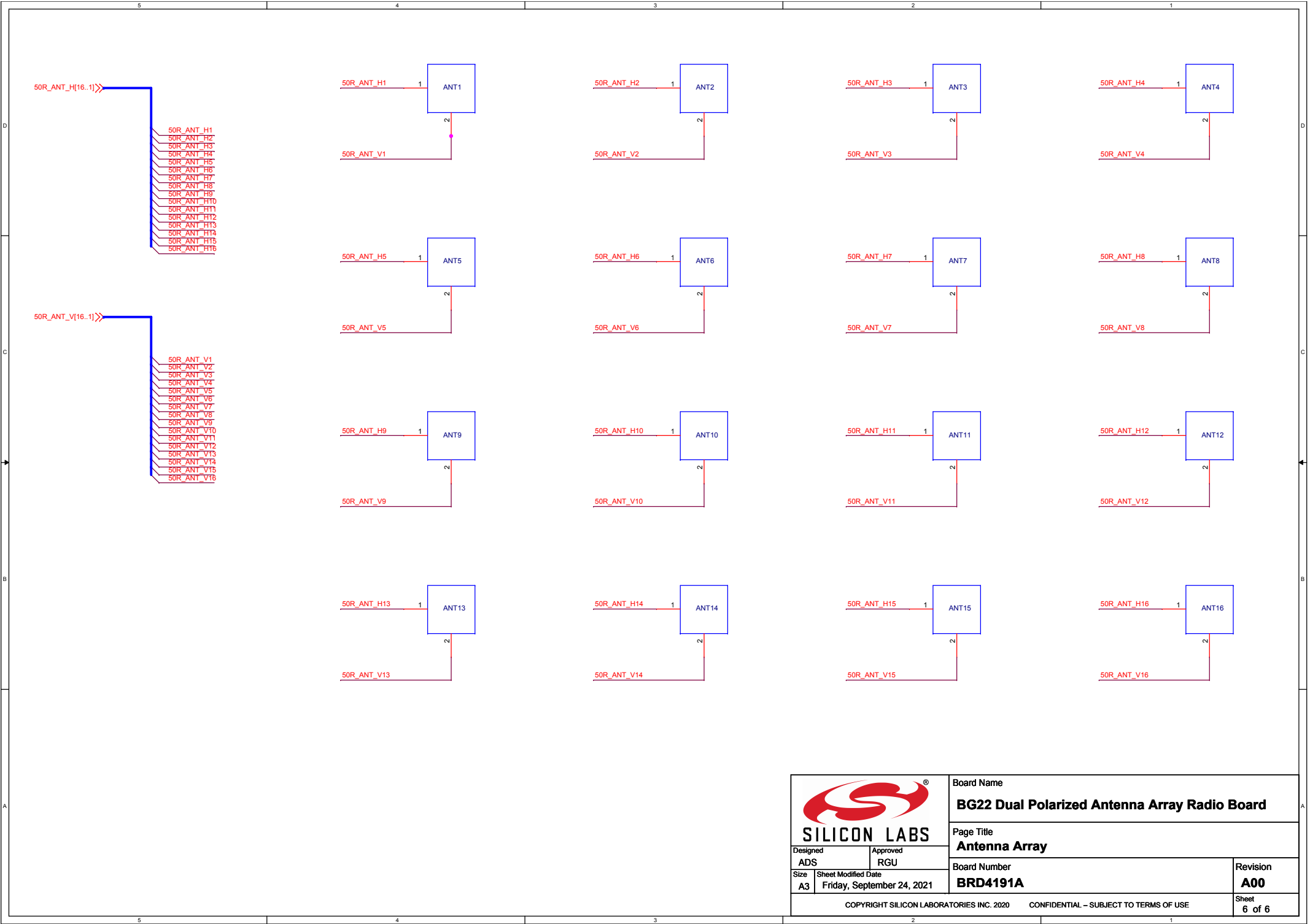
WSTK Power Decoupling




Test Points



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