**A**[**Verilog**](https://en.wikipedia.org/wiki/Verilog)**source code for a**[**traffic light**](https://en.wikipedia.org/wiki/Traffic_light)**controller on**[**FPGA**](https://www.fpga4student.com/2017/08/what-is-fpga-programming.html)**is presented. A sensor on the farm is to detect if there are any vehicles and change the traffic light to allow the vehicles to cross the highway. Otherwise, highway light is always green since it has higher priority than the farm.**

