

# ZHAOYING LI

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📍 COM1-01-15, System and Networking Lab, 13 Computing Drive, Singapore 117417

## EDUCATION

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**National University of Singapore**

*July 2018 - Present*

*Ph.D. in Computer Science*

**Shandong University, China**

*Sept 2014 - June 2018*

*Bachelor of Software Engineering, GPA: 4.4/5*

## PUBLICATIONS

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- **LISA: Graph Neural Network based Portable Mapping on Spatial Accelerators.** **HPCA'22**  
*Zhaoying Li, Dan Wu, Dhananjaya Wijerathne, Tulika Mitra*  
28th IEEE International Symposium on High-Performance Computer Architecture
- **PANORAMA: Divide-and-Conquer Approach for Mapping Complex Loop Kernels on CGRA.** **DAC'22**  
*Dhananjaya Wijerathne, Zhaoying Li, Thilini Kaushalya Bandara, Tulika Mitra*  
59th ACM/IEEE Design Automation Conference, 2022
- **HiMap: Fast and Scalable High-Quality Mapping on CGRA via Hierarchical Abstraction.** **TCAD'22**  
*Dhananjaya Wijerathne, Zhaoying Li, Anuj Pathania, Tulika Mitra, Lothar Thiele*  
IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems
- **Coarse-Grained Reconfigurable Array (CGRA).** **Book Chapter**  
*Zhaoying Li, Dhananjaya Wijerathne, Tulika Mitra*
- **ChordMap: Automated Mapping of Streaming Applications onto CGRA.** **TCAD'21**  
*Zhaoying Li, Dhananjaya Wijerathne, Xianzhang Chen, Anuj Pathania, Tulika Mitra*  
IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems
- **HiMap: Fast and Scalable High-Quality Mapping on CGRA via Hierarchical Abstraction.** **DATE'21**  
*Dhananjaya Wijerathne, Zhaoying Li, Anuj Pathania, Tulika Mitra, Lothar Thiele*  
Design Automation and Test in Europe 2021
- **CASCADE: High Throughput Data Streaming via Decoupled Access/Execute CGRA.** **TECS'19**  
*Dhananjaya Wijerathne, Zhaoying Li, Manupa Karunaratne, Anuj Pathania, Tulika Mitra*  
ACM Transactions on Embedded Computing Systems

## WORK EXPERIENCE

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**EdgeCortex**

*Sept 2020 - Feb 2021*

*Intern*

- Implement a scheduler for neural accelerator.

**Embedded Software Design**

*AY 2018/2019 Semester*

*Graduate Teaching Assistant*

- Design and prototype course project for real-time power-efficient object detection on an embedded platform.

**Computer Organization**

*AY 2019/2020 Semester*

*Graduate Teaching Assistant*

- Lab scoring and lab coordination for compiling score and management

## SKILLS

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**Programming: C++, Java, LLVM, Python, C**