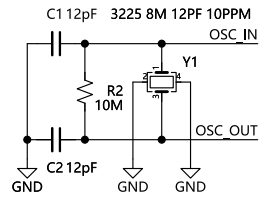
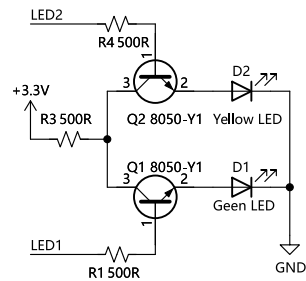


SYS CLOCK

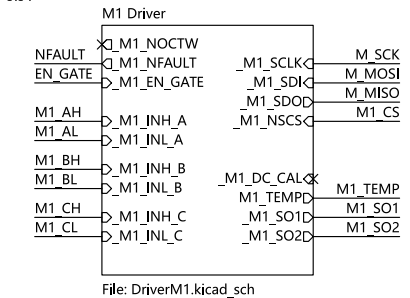
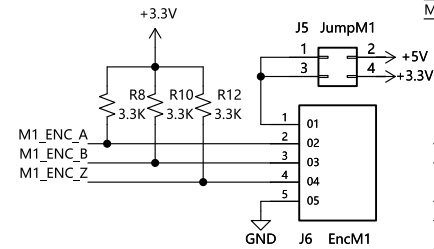
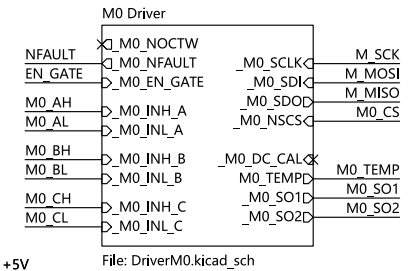
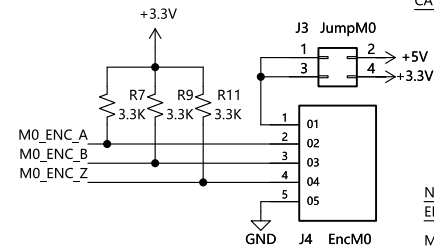
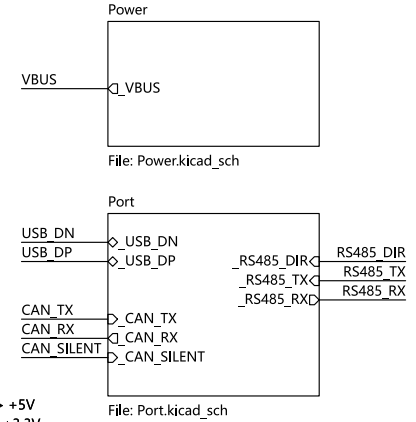
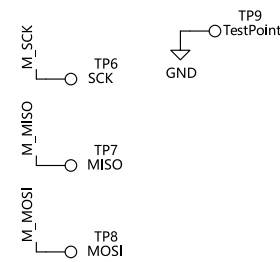
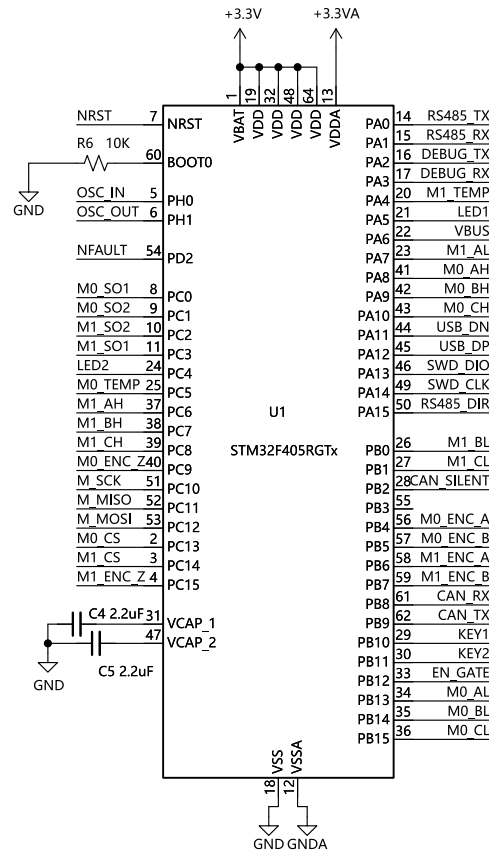
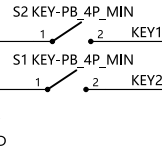
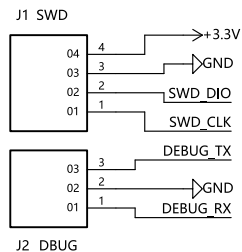


Consult the crystal oscillator data sheet to determine the matching capacitance value, The signal amplitude of the matched capacitance value is typically 100mV-200mV, 12pF-20pF

SYS LED



DEBUG



PMSM/BLDC FOC Diver
Ver 1.0 Designed by
zhbi98

PMSM/BLDC FOC

Sheet: /
File: MotorKit.kicad_sch

Title:

Size: A4
KiCad E.D.A. 9.0.0

Date:

Rev:
Id: 1/5

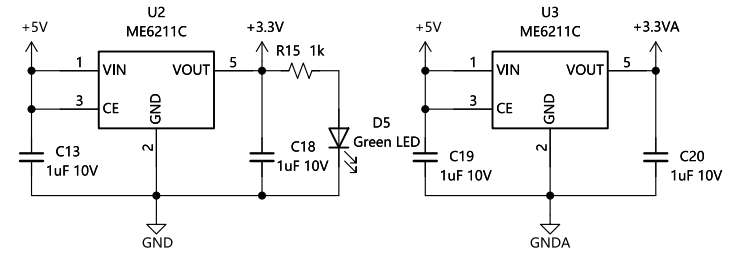
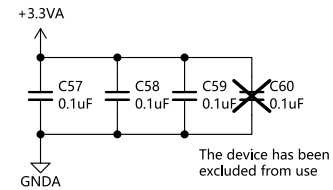
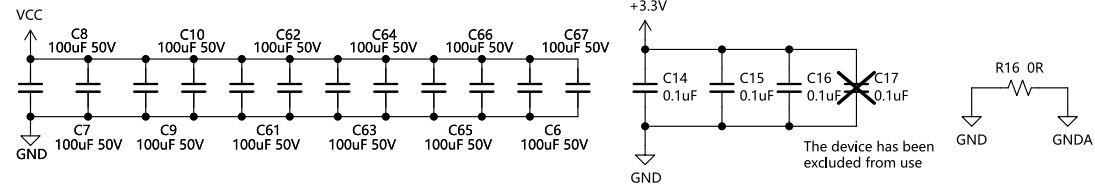


Diagram showing five test points (TP1 to TP5) connected to various power and ground rails:

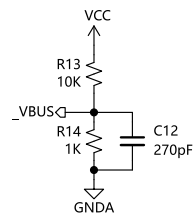
- TP1: Connected to GND.
- TP2: Connected to VCC.
- TP3: Connected to +3.3V.
- TP4: Connected to +3.3VA.
- TP5: Connected to +5V.



POWER FLAGS

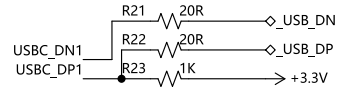
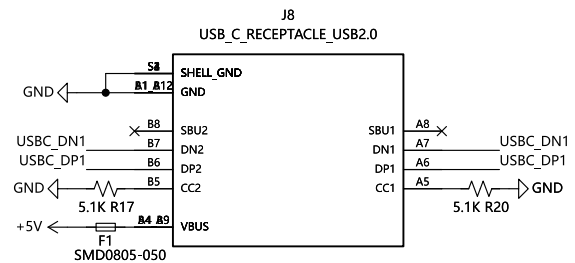
Diagram illustrating various power supply connections to the PWR_FLAG pin:

- +3.3VA
- +3.3V
- +5V
- VCC
- VBUS
- GND
- GNDA

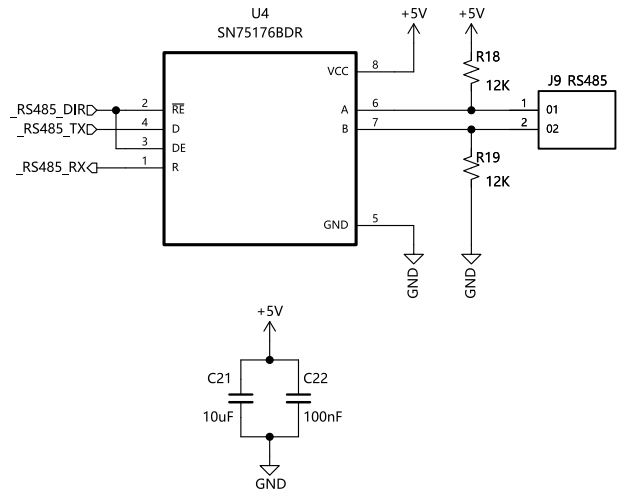


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USB TYPEC 2.0



RS485



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