NOTES:

1、External Interrupt: IRQA(PB0) IRQB(PB6) IRQC(Any GPIO) IRQD(Any GPIO)

2、PC5: Logic-level control for external RX/TX switch. The EM35x baseband controls TX_ACTIVE and drives it high when in TX mode.

3、PC6: Inverted TX_ACTIVE signal

Mode Selection Configuration:

Mode	CSD	CPS	CTX	Current Draw
ShutDown	0	0	0	<1uA
RX Mode w/o LNA	1	0	0	32mA
RX Mode w LNA	1	1	0	38mA
TX Mode10/17/20db	1	1	1	45/90/110mA

ANT_SEL	0	ANT1
	1	ANT2

PA3: SPI CS
PA6: LNA BYPASS
PA7: ANT SELECT
PC5: TX <-> RX Switch

该IO口不能使用 RSV(nCS)

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