5 3 NOTES: 1. External Interrupt: IRQA(PB0) IRQB(PB6) IRQC(Any GPIO) IRQD(Any GPIO) 2. PC5: Logic-level control for external RX/TX switch. The EM35x baseband controls TX ACTIVE and drives it high when in TX mode. 3. PC6: Inverted TX ACTIVE signal Mode Selection Configuration: Mode CPS CTX Current Draw 0 0 <1uA ShutDown RX Mode w/o LNA 0 0 32mA RX Mode w 38mA R1 10R-SR0402 TX Mode10/17/20db 1 45/90/110mA C1 18pF-SC0402 C2 C3 C4 C6 100nF-SC0402 ANT SEL ANT1 U1 II ANT2 1.8V C7 8pF-SC0402 /CS SO /WP VSS VCC /HOLD SCK SI R2 4K7-SR0603 +3.3V xtal-24M +3.3V R7 4K7-SR0603 C10 100nF-SC0402 C11 10pF-SC0402 GND CAT25512 X1 *C12 | 18pF-SC0402 PA6 R3 4K7-SR0603 PA3 R4 4K7-SR0603 ANT1 C13 | 10pF-SC0402 GND TROA SWDIO PC4/JTMS PC3/JTDI 2450AT18B100 RF_TX_ALT PC2/JTDO L2 2.0nH-SL0402 SWCLK JTCK VDD RE PB2 PB1 29 PB1 PA6 VDD_PADS3 PA5 PA4 PA3 W ANT_SEL ANT1 GND5 VDD_IF BIAS_R 1.5nH-SL0402 VB_OUT C17 RF N NC VDD_PADSA PC5 nRST RXN Z 5 0 0 0 0 PAD PA3 10nF-SC0402 SE2432L 49 EPAD C20 4K7-SR0603 C21 8pF-SC0402 108-112005 R10 4K7-SR0603 R11 4K7-SR0603 PA3: SPI CS PA6: LNA BYPASS PA7: ANT SELECT VCC PA1 该IO口不能使用 RSV(nCS) PC5: TX <-> RX Switch PB1 nRST GND JTCK 17 18 20 19 20 22 21 22 24 23 24 26 25 26 28 J14x2 <Title> Document Number Size A3