keil代码连接地址修改

原创 zhj失落之地 于 2017-10-11 14:08:19 发布 2114 收藏 3 版权

分类专栏： uboot相关

bootload拷贝APP代码到DRAM的某一地址，然后跳转运行APP.

bootload部分代码如下：

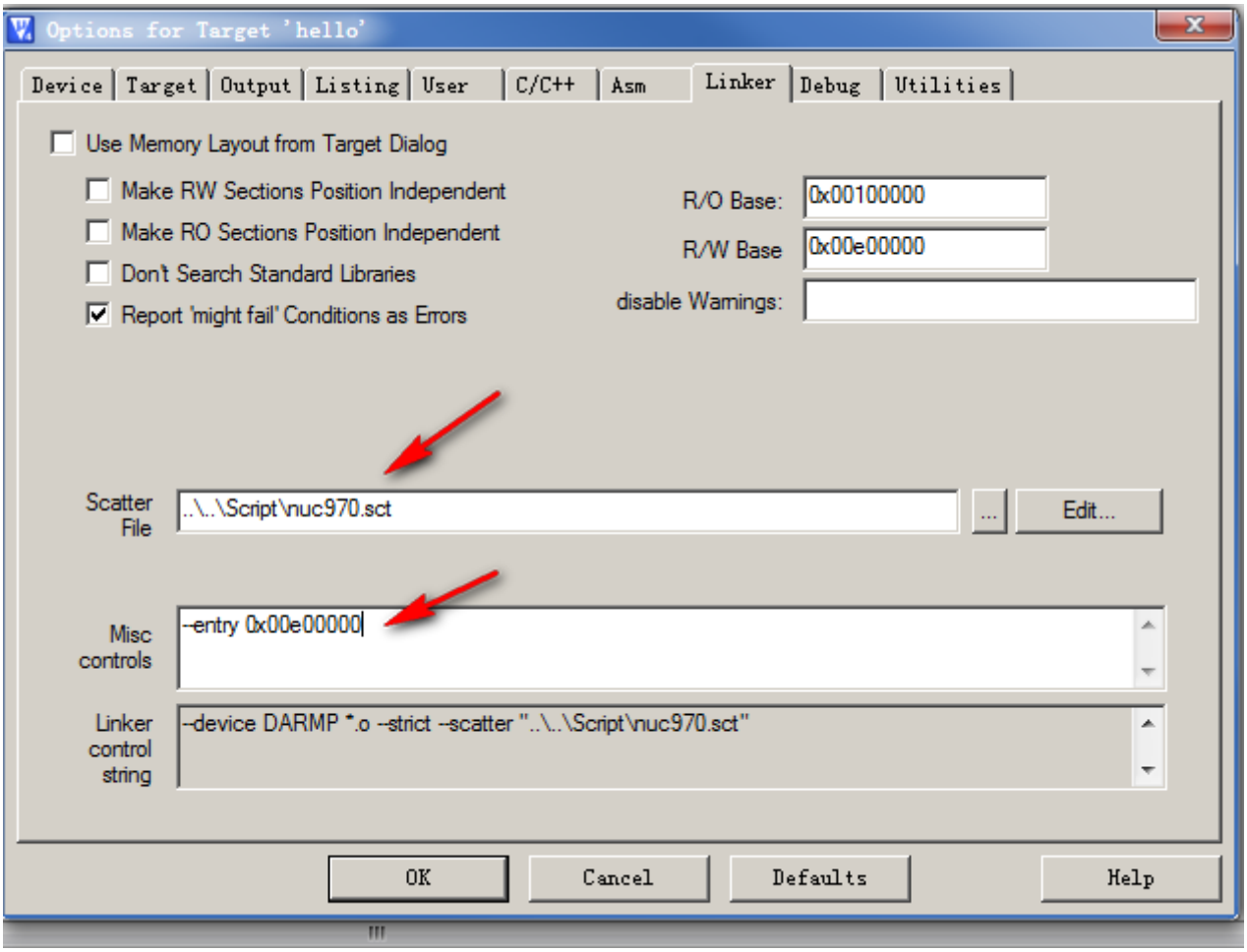
```
84  void copy_nand2ram_boot(struct mtd_info *mtd)
85  {
86      struct nand_chip *this_chip = mtd->priv;
87      __attribute__((noreturn)) void (*image)(void);
88
89      /*
90       * Load image from NAND into RAM
91       */
92      nand_image_load(mtd, CONFIG_NAND_IMAGE_OFFS, CONFIG_NAND_IMAGE_SIZE, (unsigned char *)CONFIG_NAND_IMAGE_DST);
93      sysprintf("CONFIG_NAND_IMAGE_OFFS = 0x%x\r\n",CONFIG_NAND_IMAGE_OFFS);//0x100000
94      sysprintf("CONFIG_NAND_IMAGE_SIZE = %d\r\n",CONFIG_NAND_IMAGE_SIZE);//(500 * 1024)
95      sysprintf("CONFIG_NAND_IMAGE_DST = 0x%x\r\n",CONFIG_NAND_IMAGE_DST);//0x0e00000  ddr 14M的位置
96      if (this_chip->select_chip)
97          this_chip->select_chip(mtd, -1);
98
99      /*
100      * Jump to  image
101      */
102     sysprintf("Jump to image......");
103
104     image = (void *)(CONFIG_NAND_IMAGE_DST);
105
106     (*image)();
107 }
```

跳转之后执行一个裸机代码。

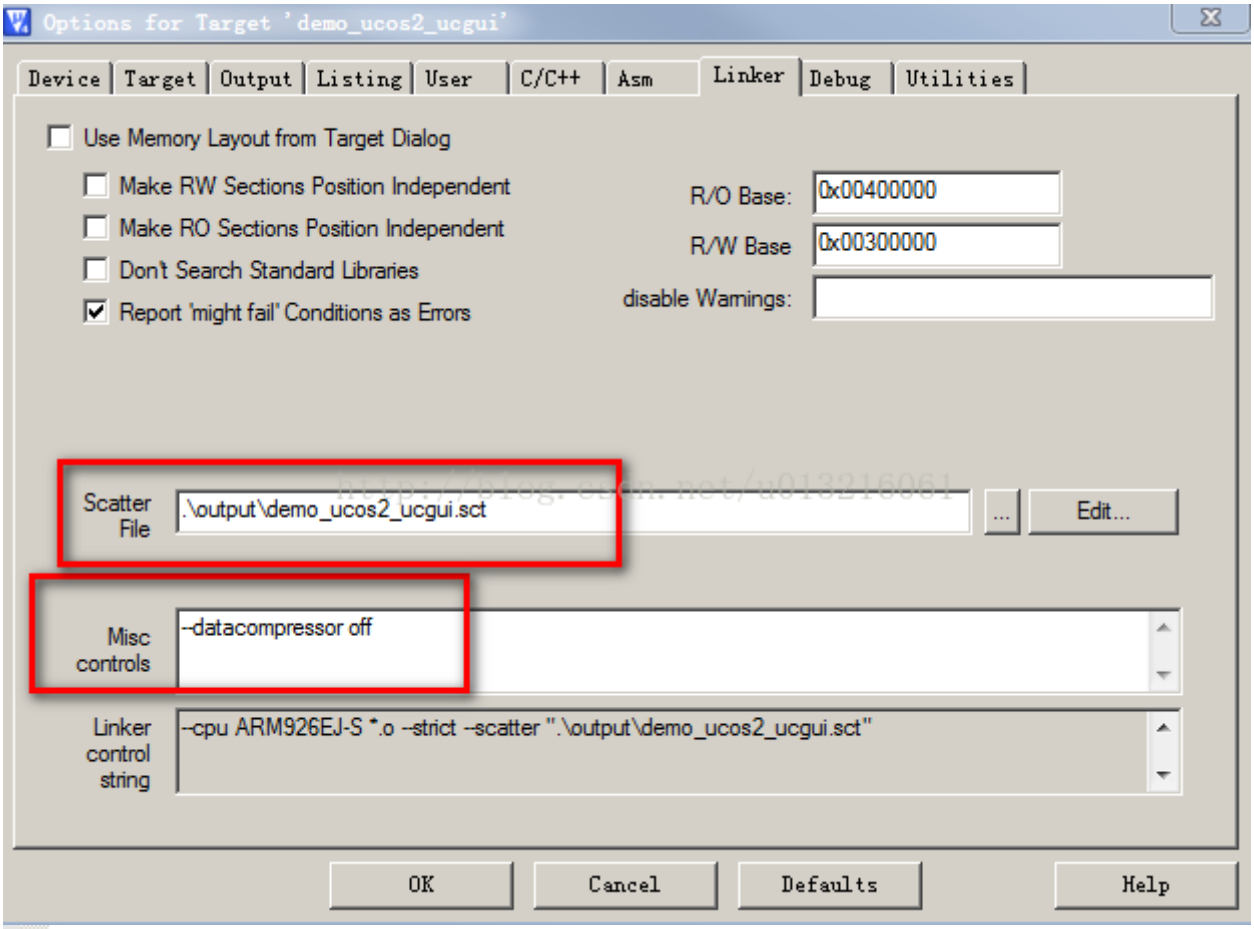
修改keil的链接地址

nuc970.sct | main.c | sys_nuc970.s | startup_NUC970.s | system_nuc970.c | sys_uart.c | sys_timer.c

```
1
2
3   LR_IROM1 0x00e00000 0x800000  {    ; load region size_region  8m
4       ER_IROM1 0x00e00000 0x800000  {  ; load address = execution address
5           *.o (NUC_INIT, +First)
6           *(InRoot$$Sections)
7           .ANY (+RO)
8       }
9       RW_RAM1 +0 {  ; RW_RAM1 start address is after ER_ROM1
10          .ANY (+RW +ZI)
11      }
12  }
13
```


Options for Target 'hello'
Device
Target
Output
Listing
User
C/C++
Asm
Linker
Debug
Utilities
Use Memory Layout from Target Dialog
Make RW Sections Position Independent
Make RO Sections Position Independent
Don't Search Standard Libraries
Report 'might fail' Conditions as Errors
R/O Base:
0x00100000
R/W Base
0x00e00000
disable Warnings:
Scatter File
..\..\Script\nuc970.sct
...
Edit...
Misc controls
--entry 0x00e00000
Linker control string
--device DARMP *.o --strict --scatter "..\..\Script\nuc970.sct"
OK
Cancel
Defaults
Help

方法二




```
#define VPOST_VBUFF_SIZE       (0x200000)
#define VPOST_OSDBUFF_SIZE     (0x80000)

ROM_LOAD 0x200000
{
    ROM +0
    {
        startup_NUC970.o (NUC_INIT, +First)
        anon$$obj.o
    }

    ER_APP +0
    {
        main_*.o (+RO)
        standalone.o (+RO)
    }

    ER_N329BSP +0
    {
        wb_*.o (+RO)
        w55fa92_*.o (+RO)
    }

    ER_UCOS2 +0
    {
```