

高頻高壓半導世界

—四大金刚的蜘蛛擂台

FoM of Si, GaAs, SiC, and GaN

王不老說半導

四大金刚

- 佛教四大天王→中国的"四大金刚", 为"风调雨顺"的化身
- 东方持国天王掌碧玉琵琶, 职调
南方增长天王掌青光宝剑, 职风
西方广目天王掌紫金龙花狐貂,
职顺, 北方多闻天王掌混元米伞,
职雨
- 老上海四大金刚:**大饼、油条、
豆浆、粢饭**
- 那麼納米半导世界呢?



<https://buzzly.net/p/4K9jtAHh/>



<https://kknews.cc/food/3eyg8xa.html>

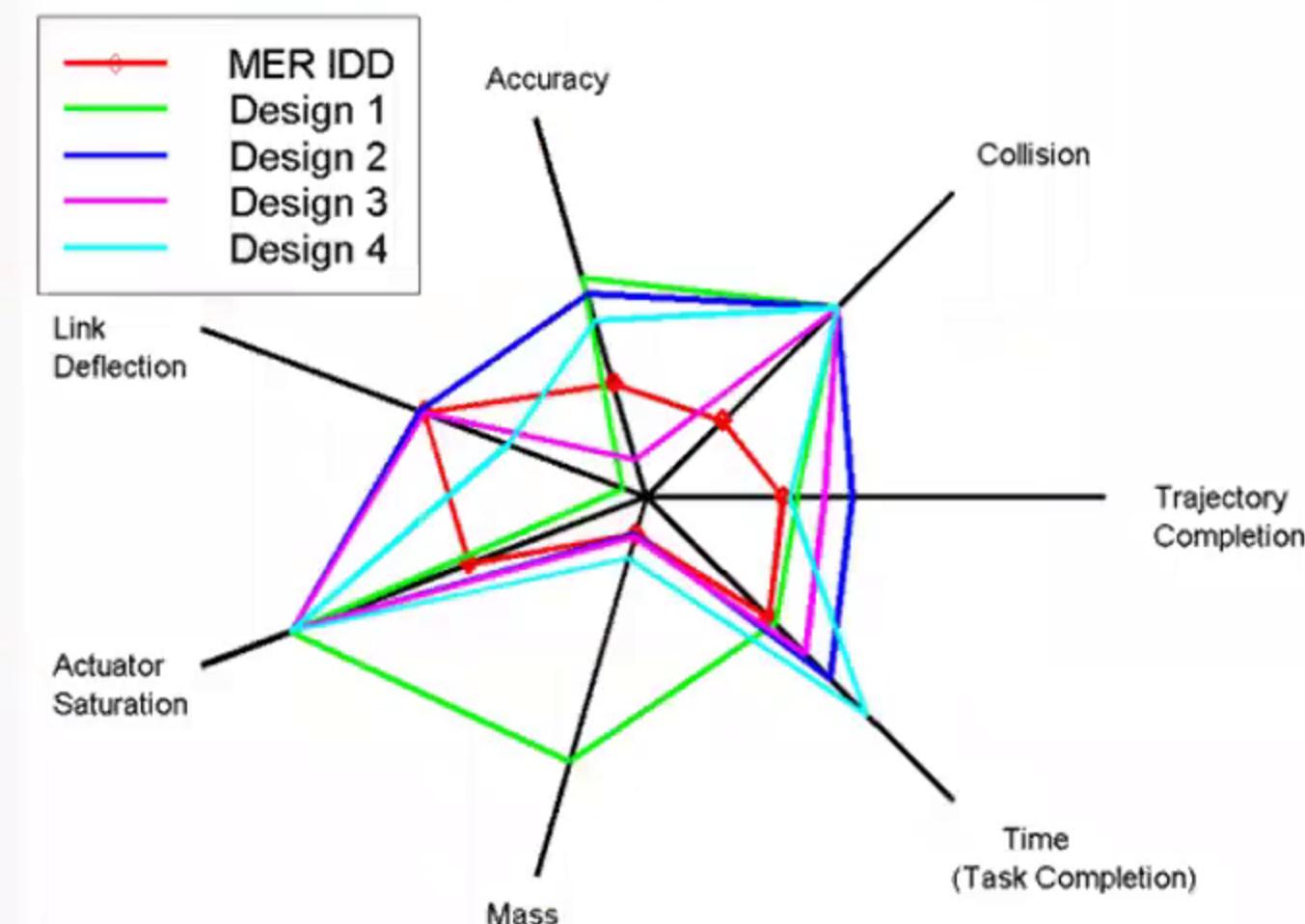
高功率高頻半導江湖的四大金剛

Properties	Si	4H-SiC	GaAs	GaN	Application
Crystal Structure	Diamond	Hexagonal	Zincblende	Hexagonal	
Energy Gap : E_G (eV)	1.12	3.26 3x	1.43	3.5	high temp. operation, emission wavelength
Electron Mobility: μ_n (cm^2/Vs)	1400	900	8500	1250	High frequency devices
Hole Mobility: μ_p (cm^2/Vs)	600	100	400	200	
Breakdown Field; E_B (V/cm) $\times 10^6$	0.3	3 10x	0.4	3	Power devices
Thermal Conductivity (W/cmK)	1.5	4.9 3x	0.5	1.3	High heat dissipation
Saturation Drift Velocity: v_s (cm/s) $\times 10^7$	1	2.7 3x	2	2.7	High frequency devices
Relative Dielectric Constant: ϵ_s	11.8	9.7	12.8	9.5	
p, n Control	Good	Good	Good	Average	
Thermal Oxide	Good	Good	Behind	Behind	MOS structure

插播: FoM乃是蜘蛛擂台裁判

- FoM = Figure-of-Merit = 品質因數 = 蜘蛛擂台上的裁判
 - 產品優劣比較，必須上此擂台一較長短
 - 只是吹噓自己，不敢上蜘蛛擂台者，必是个大忽悠
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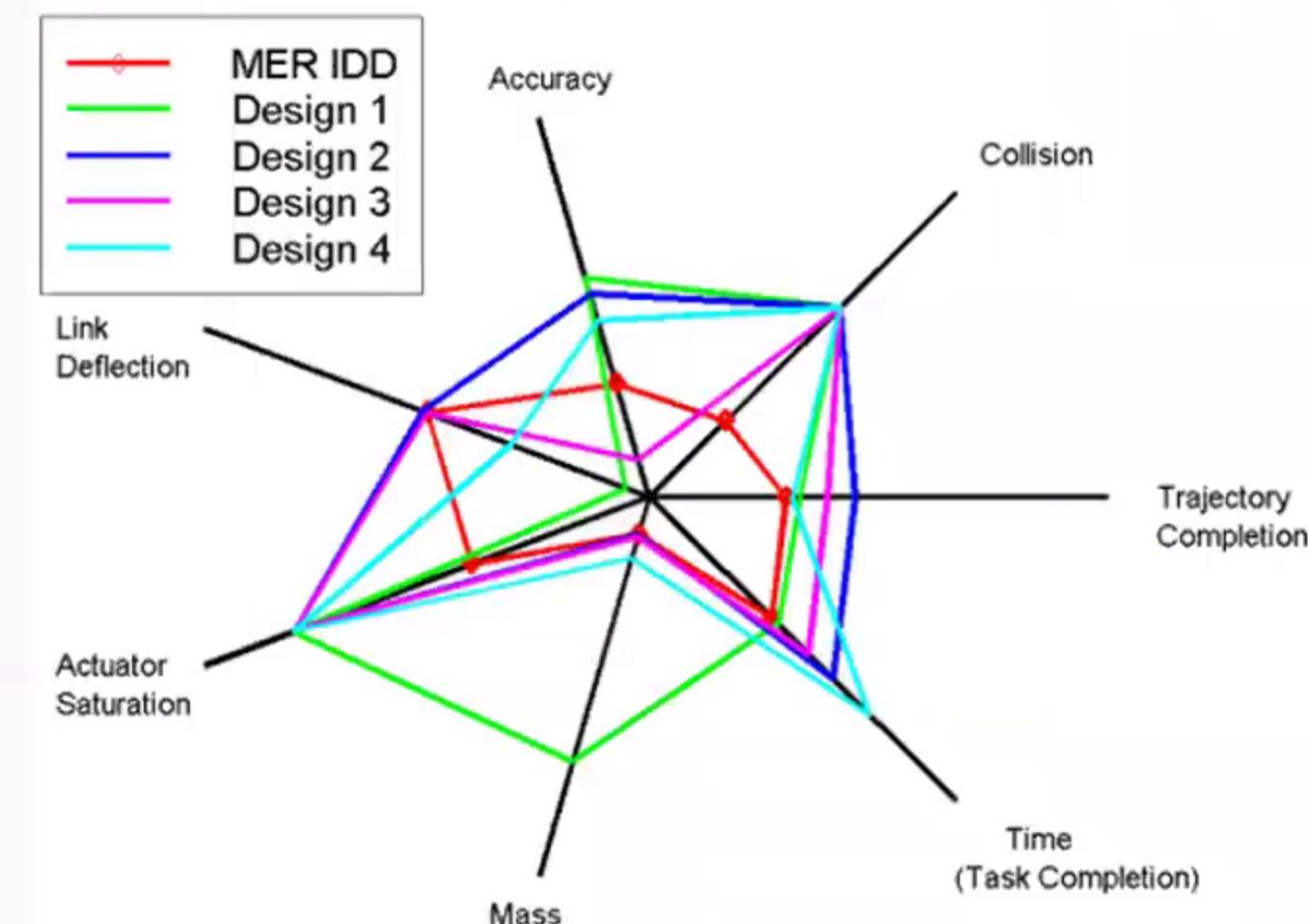
Radar chart: 雷达图;蜘蛛网图



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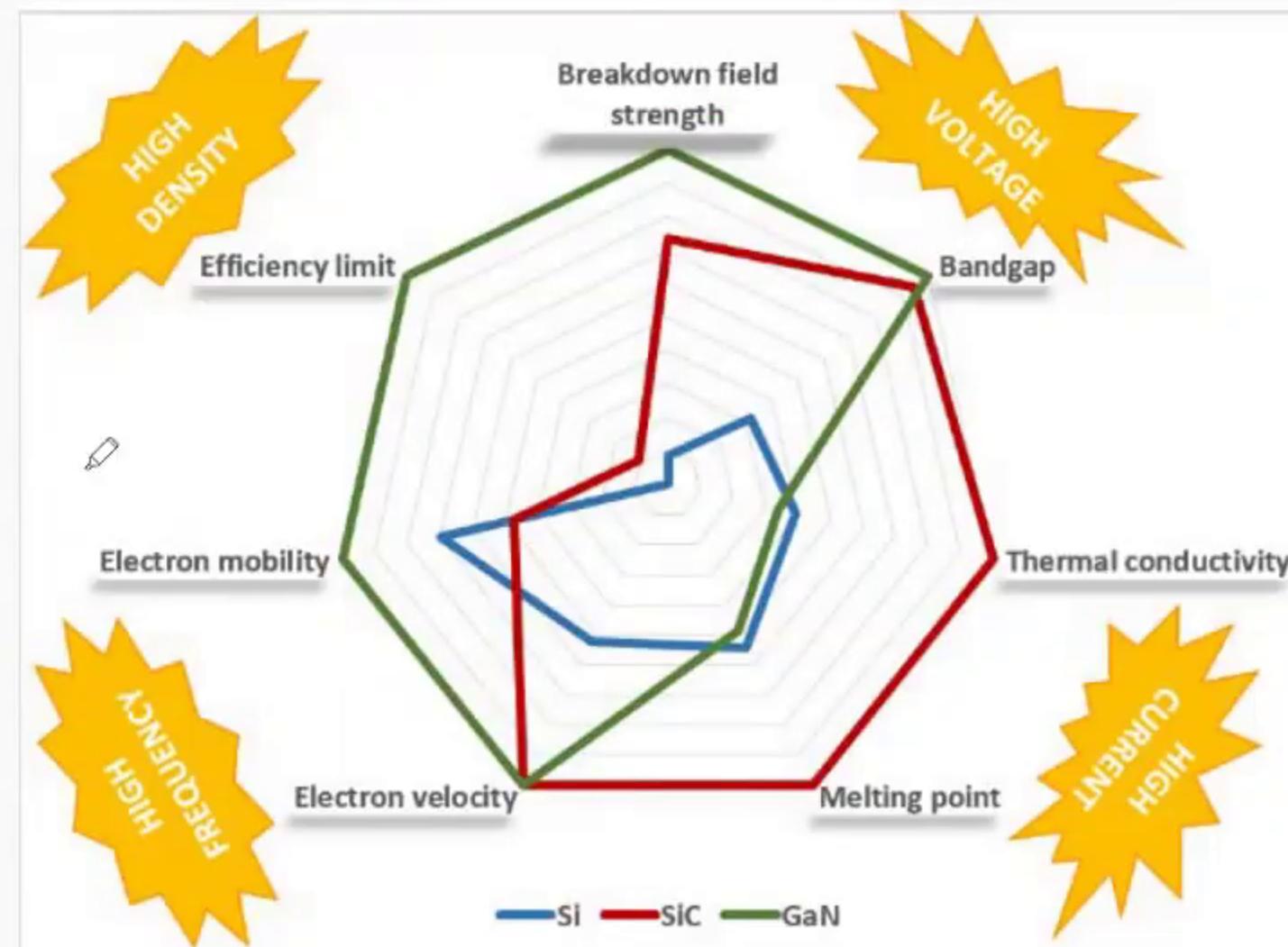
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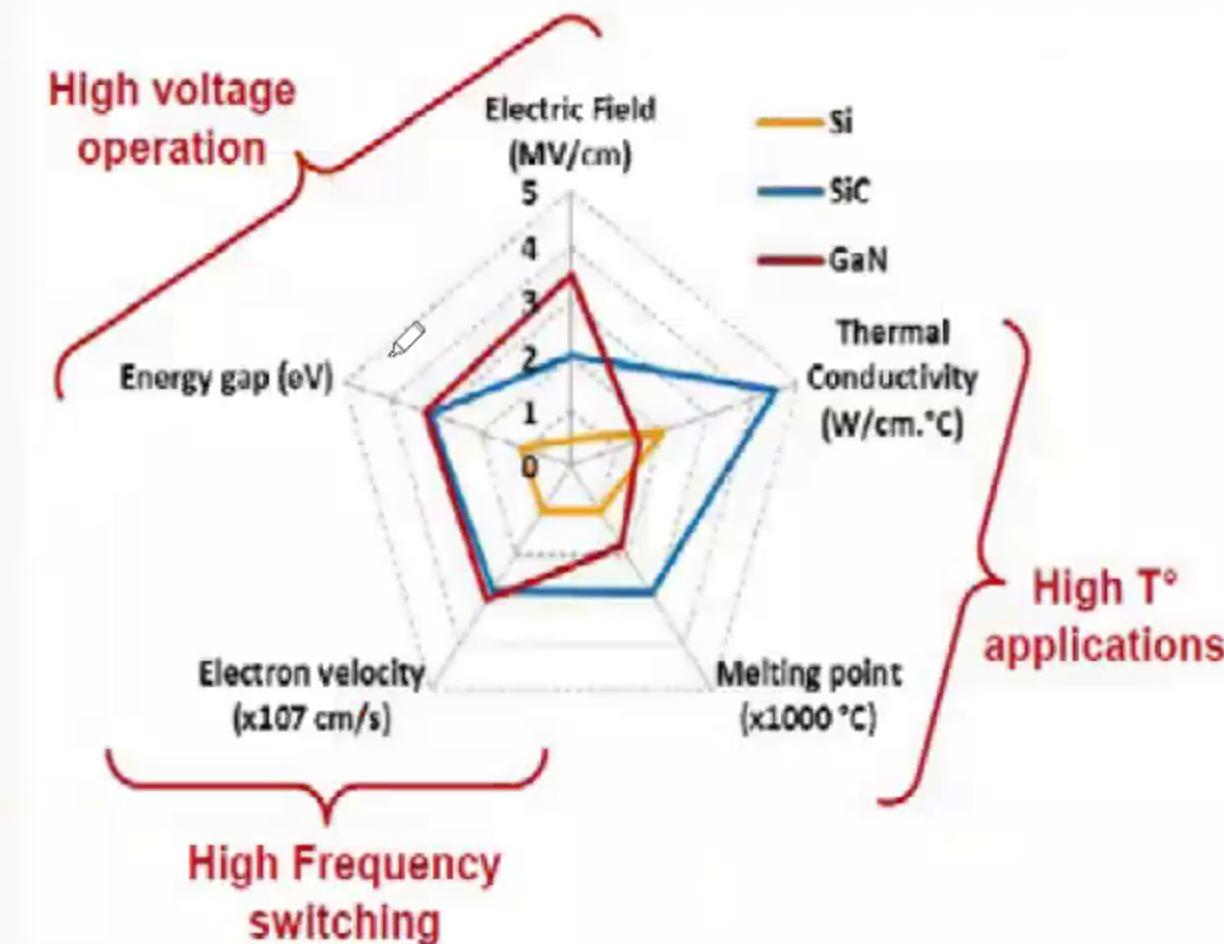
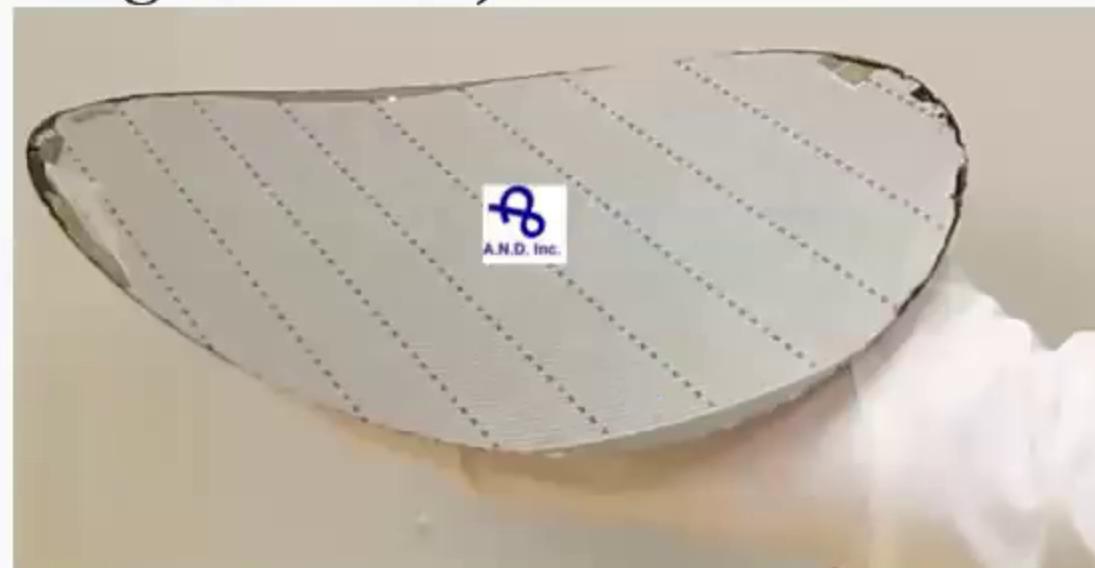
Si, SiC and GaN的蜘蛛擂台

- 比較各家材料本身的特質，自然是各有所長，也各有所短



Si, SiC and GaN的蜘蛛擂台

- SiC for high Temp application
- GaN ~ reach high voltage, but requires bulk GaN substrate
- Silicon may not compete at the high frequency range (but AND begs to differ)



試問：如何找出Si, GaAs and GaN的FoM?

- 解答：為了避免各說各話，必須找出一個可以綜合相關參數的蜘蛛擂台裁判(FoM)如下：

- 先找出個墊底的(Si)
- 再找出你認為重要的相關參數集

3. $FoM = \frac{\text{新材料參數集}}{\text{墊底的硅參數集}}$

- Johnson (低壓考量) = $\frac{VE_{crit}}{(VE_{crit})_{Si}}$

- Baliga (加碼高頻) = $\frac{\mu E^2 crit}{(\mu E^2 crit)_{Si}}$
(E_{crit}重要性被加權了)

	Si	GaAs	GaN
Bandgap (eV)	1.1	1.42	3.4
Critical field, E_{crit} (MV/cm)	0.3	0.4	3.3
Mobility, μ (cm ² /V/s)	1350	8500	2000
Peak velocity, v_{peak} (10 ⁷ cm/s)	1	2	2.5
Baliga FOM $\propto \mu * E_{crit}^2$ (normalized to Si)	1	11	179
Johnson FOM $\propto v_{peak} * E_{crit}$ (normalized to Si)	1	2.7	27.5

Table I: Baliga and Johnson FOMs for Si, GaAs and GaN. GaN has the highest FOMs making it advantageous for high voltage and RF.

B. J. Baliga, "Power semiconductor device figure of merit for high-frequency applications," EDL, 10(10), pp. 455-457, 1989

試問：如何找出Si, GaAs and GaN的FoM?

- 解答：但FoM會隨時空改變
 - 現代芯片由於縮放比例(scaling)越來越小，許多熱傳甚至電性，都可能不一樣了，例如GaAs在變小以後，DOS(become discrete)竟然不夠， μ , v 的優勢變小
 - 最終，當然是整體成本性能考量(i.e., PPAC)才是幕後大老闆

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試問: GaN的Baliga FoM為何?

- 解答:
- $$\text{Baliga FoM} = \frac{\mu E_{crit}^2}{(\mu E_{crit})_{Si}} = \frac{2000 \times 3.3^2}{1350 \times 0.3^2} = 179$$
- 然而, Baliga又在其最新2019著作將以上公式改了(E_{crit} 更重)
- $$\text{Baliga FoM (2019)} = \frac{\epsilon \mu E_{crit}^3}{(\epsilon \mu E_{crit})_{Si}} = \frac{9.5 \times 2000 \times 3.3^3}{11.7 \times 1350 \times 0.3^3} = 1601 \text{ (Mamamia, 好大)}$$

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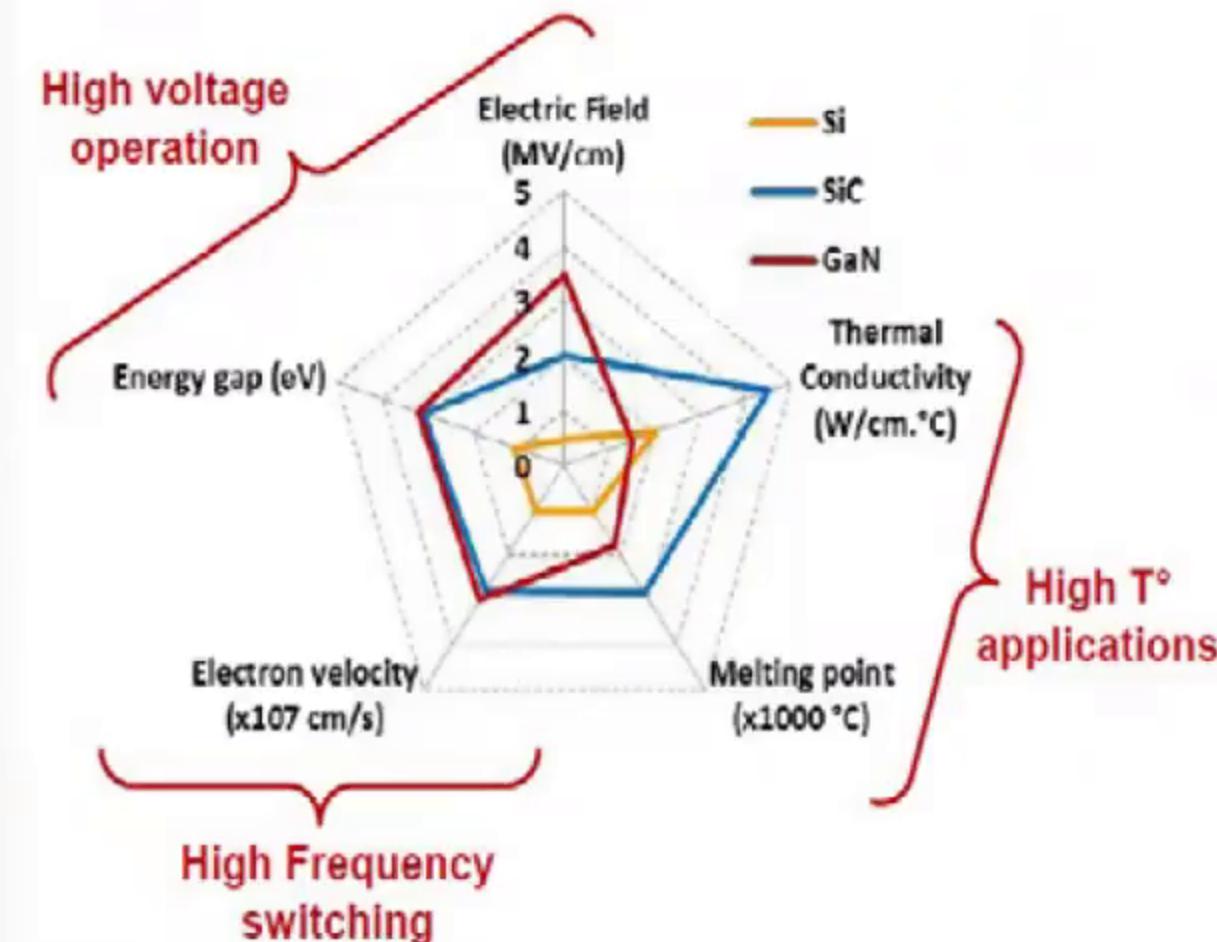
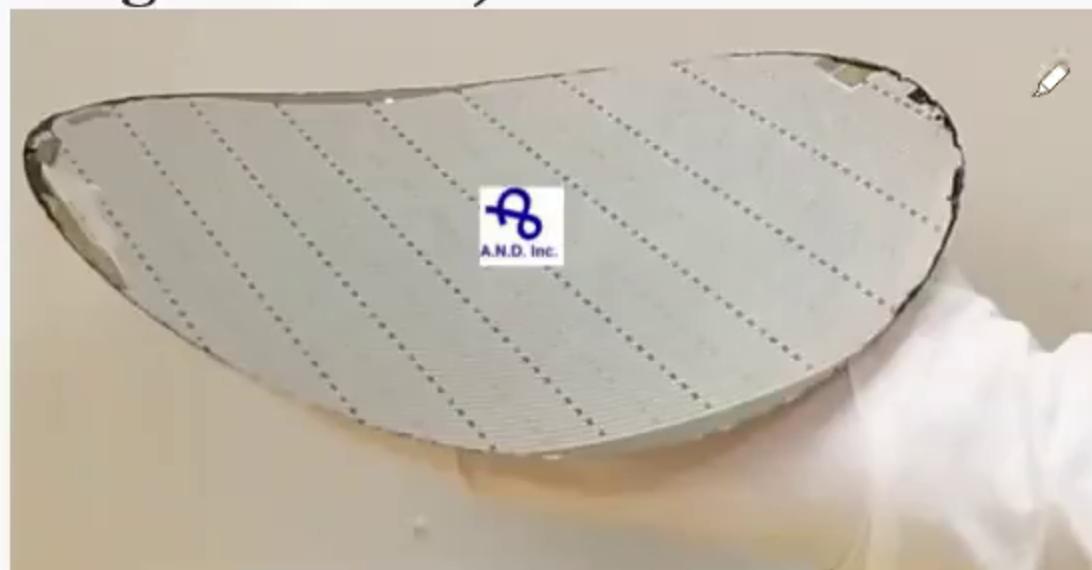
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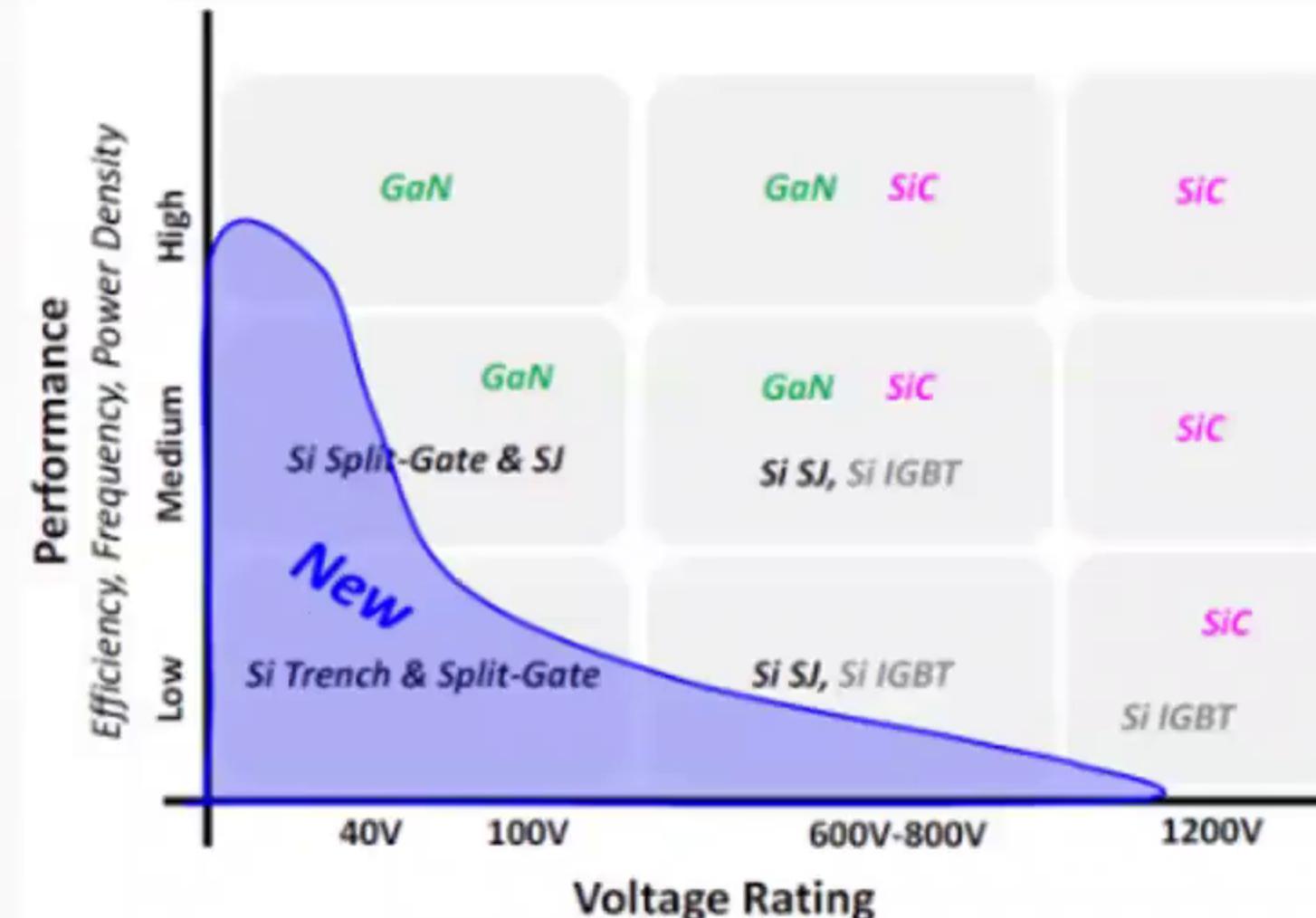
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目前四大金刚半導板塊仍動盪不安

- 代有能人輩出，各板塊並非固定，隨時有對手虎視眈眈，欲取而代之
- 其實若是能用老朋友**硅**，因其技術非常成熟，大家還是願意用他的
- 最近有一家初創公司(AND, applied novel devices)宣稱其**硅**半導產品，竟可以其超薄體特色(30um)侵蝕**GaN**與**SiC**部分市場



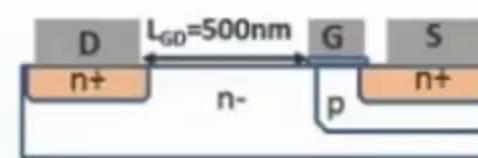
<https://www.powerelectronicsnews.com/new-silicon-power-mosfet-technology-rivals-gan-with-near-zero-reverse-recovery-and-low-on-resistance/>

預告：英特爾想要以GaN一統功率半導江山

- 英特爾以為左下各式功率半導皆可由HKMG的GaN MOS取代

Power electronics, RF PA, LNA, RF Switches

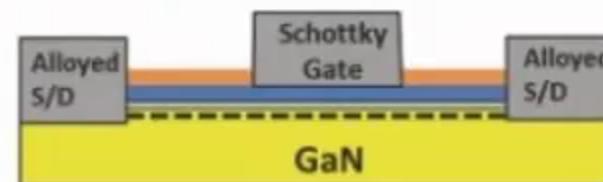
Si EDMOS



Si/SOI Stacked Transistor



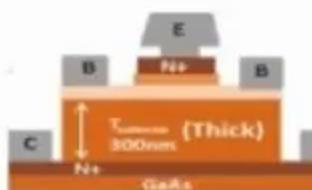
GaN Schottky HEMT



GaAs HEMT

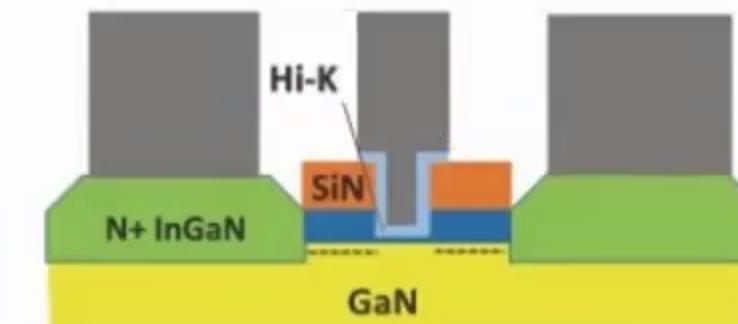


GaAs HBT



- Depletion mode
- Gate leakage
- Alloyed S/D

Enhancement-mode
High-K GaN NMOS Transistor



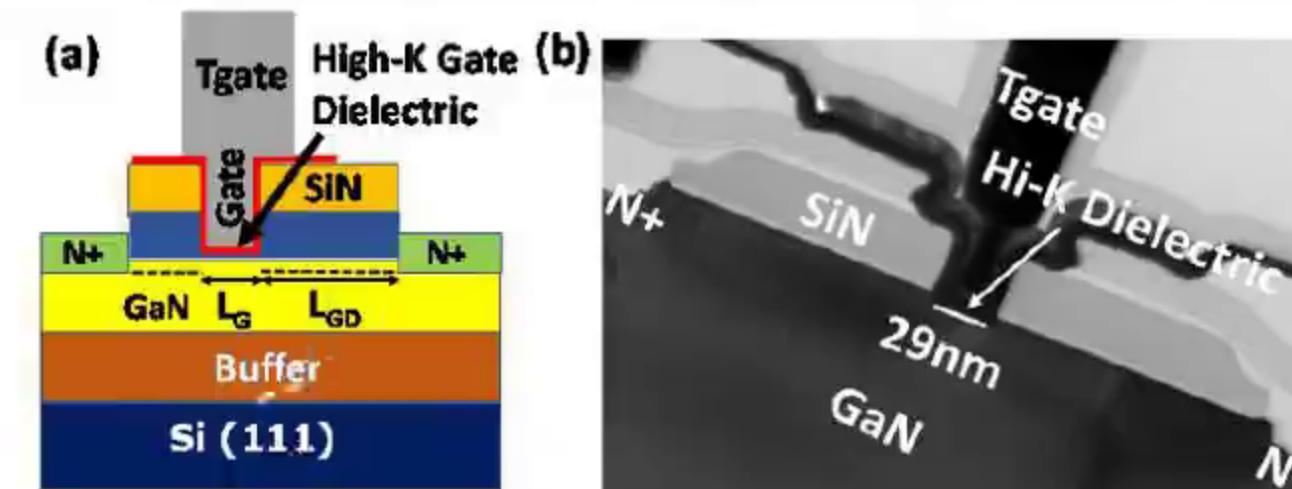
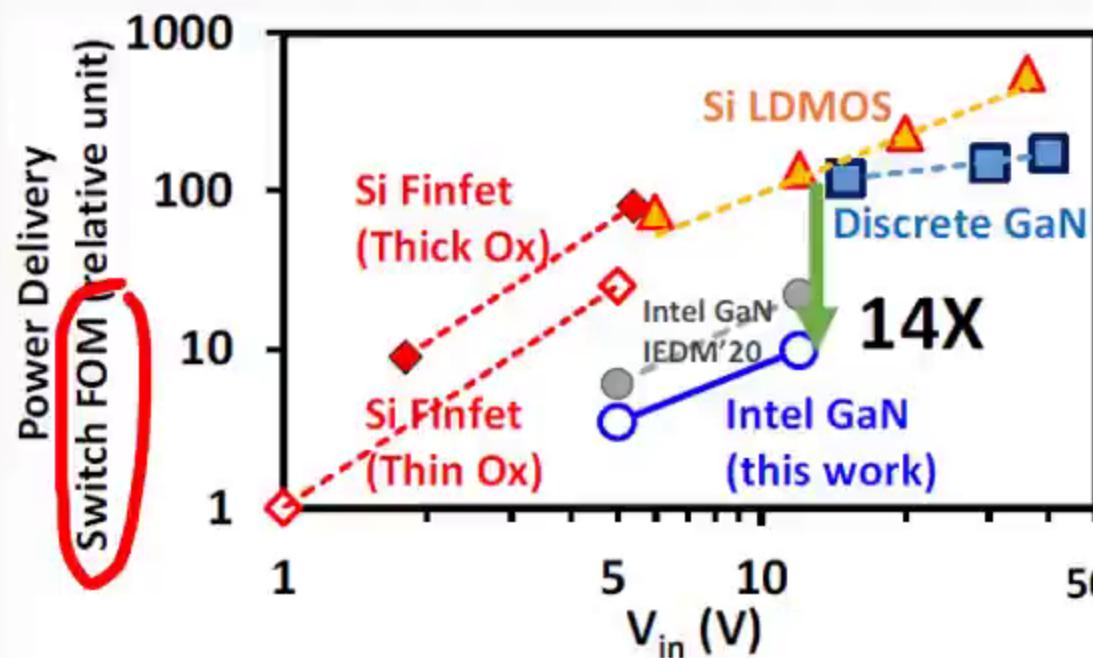
H.W.Then et al., VLSI 2015

SoC Compatible

- E-mode
- High-K metal gate
- Low leakage
- Scalable

預告:英特爾想要以GaN一統功率半導江山

- CR (Then等) 已經研究十年
- 世界第一個做到 HKMG GaN-nMOS on 300mm-Si(111)



E-mode high-k GaN Transistor

Fig.1 (a) schematic of the E-mode high-k gate dielectric GaN transistor of this work, (b) TEM micrographs of the E-mode high-k GaN transistors of this work showing short channel length of 29nm.

H. W. Then et al., "Advanced Scaling of Enhancement Mode High-K Gallium Nitride-on-300mm-Si(111) Transistor and 3D Layer Transfer GaN-Silicon Finfet CMOS Integration," IEDM'21, 11.1.1

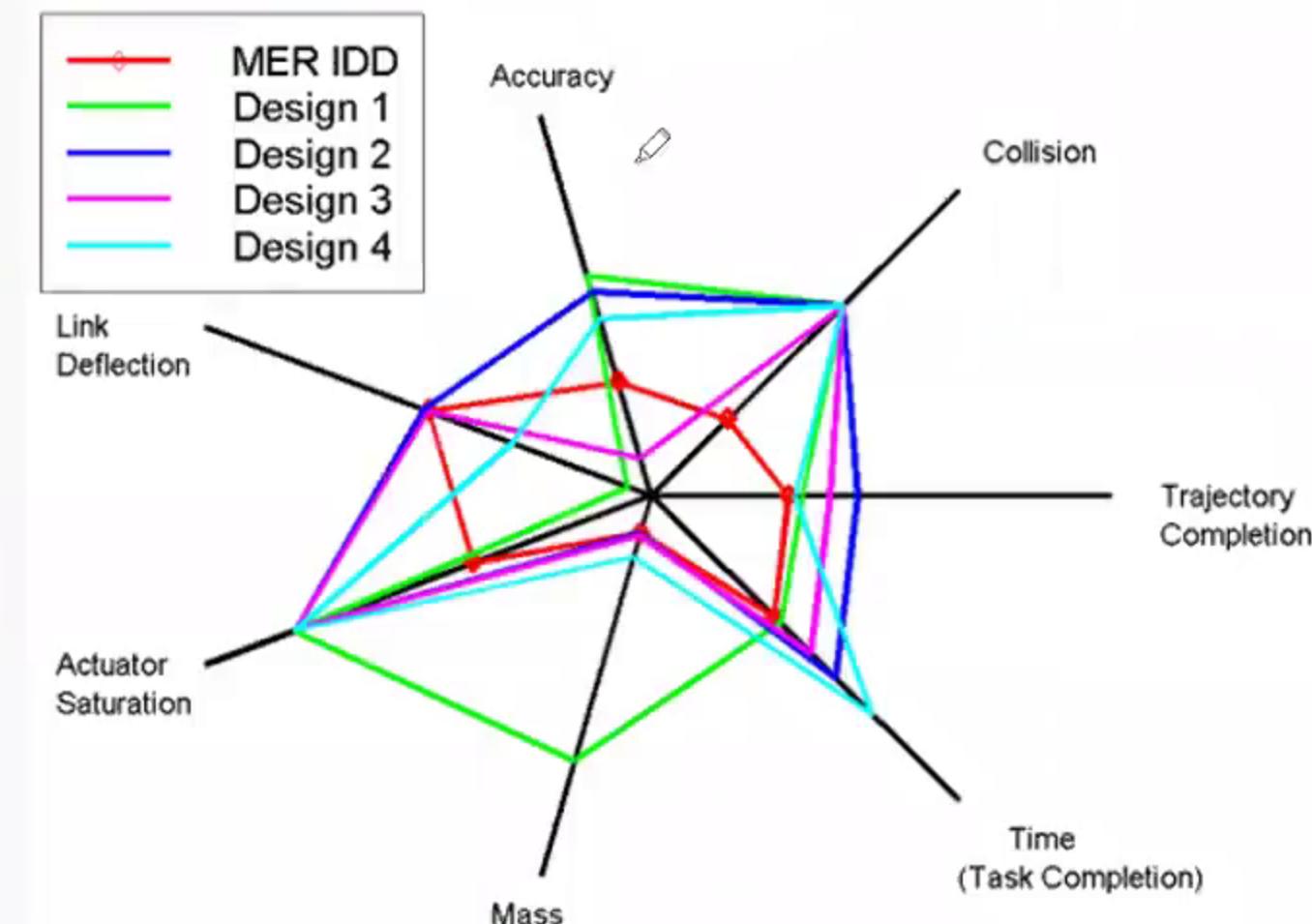
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