mrprob2

MizzouRisc Problem set 2

All questions below deal with MizzouRisc processor and assembler.

1)Write the code snippet that you need to load 31A00(H) into R11

2) Without using processor instructions, write the line of code so that location 32000 contains the value AFACE(H)

3) Suppose that R8 has AFACE(16) and R9 has 11000(H), what is the result of executing the instruction

Sto R8, (#4R9)

4) Create the machine language representation of, that is assemble

and R4,R5, R6

5) We want to modify the Sub instruction to accommodate all addressing modes except any immediate mode. Please layout the instructions as you would expect it to be organized for MizzouRisc.