

Chen Jinfan | Curriculum Vitae

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Education Background

Swiss Federal Institute of Technology in Zürich (ETHZ)

Master in Computer Science

2021.02 - now

Swiss Federal Institute of Technology in Zürich (ETHZ)

Bachelor in Computer Science

2017.09 - 2021.02

Babelsberger Filmgymnasium Potsdam

Senior High

2014.09 - 2017.09

Research Experience

Jinfan worked on meta-learning for bayesian optimization, and the [result](#) was published on NeurIPS21 with him as the third author.

Jinfan has contributed to the RTL of an RPC-DRAM memory controller. A RISC-V chip with that controller has been taped out in TSMC65nm. The paper "A Fully-Digital Reduced Pin Count (RPC) Memory Controller and PHY for a Linux-Capable 65nm RISC-V SoC" is on submission.

Jinfan has worked on a new tensor abstraction for automatic communication-avoiding distributed deep learning with Prof. Torsten Hoefler, and the resulting first-author paper, "AutoDDL: Automatic Data and Operator Parallelism for Distributed Deep Learning with Asymptotically Optimal Communication," is on submission to ATC'23.

Jinfan has developed a software framework for efficient in-memory processing with Prof. Onur Mutlu, and the resulting first-author paper, "SmallTable: A Software Framework For Productive And Efficient In-Memory Processing," is on submission to PACT'23.

Other Experience

Jinfan was a member of ETHZ Student Supercomputing Team [Racklette](#). He was in charge of the machine learning sub-task at ISC20, and the team placed second in the machine learning sub-task.

Jinfan was a research intern at [Oneflow](#) from 2021.06 until 2021.12, where he worked on theory, implementation, and experiment for large-scale model training with billions of parameters on hundreds of GPUs.

Jinfan was a teaching assistant for the course "Digital Circuits and Computer Architecture".

Technical Strength

Programming Language: Python, C

Language Proficiency

German (EU Standard Language Test C1)

English (EU Standard Language Test C1)