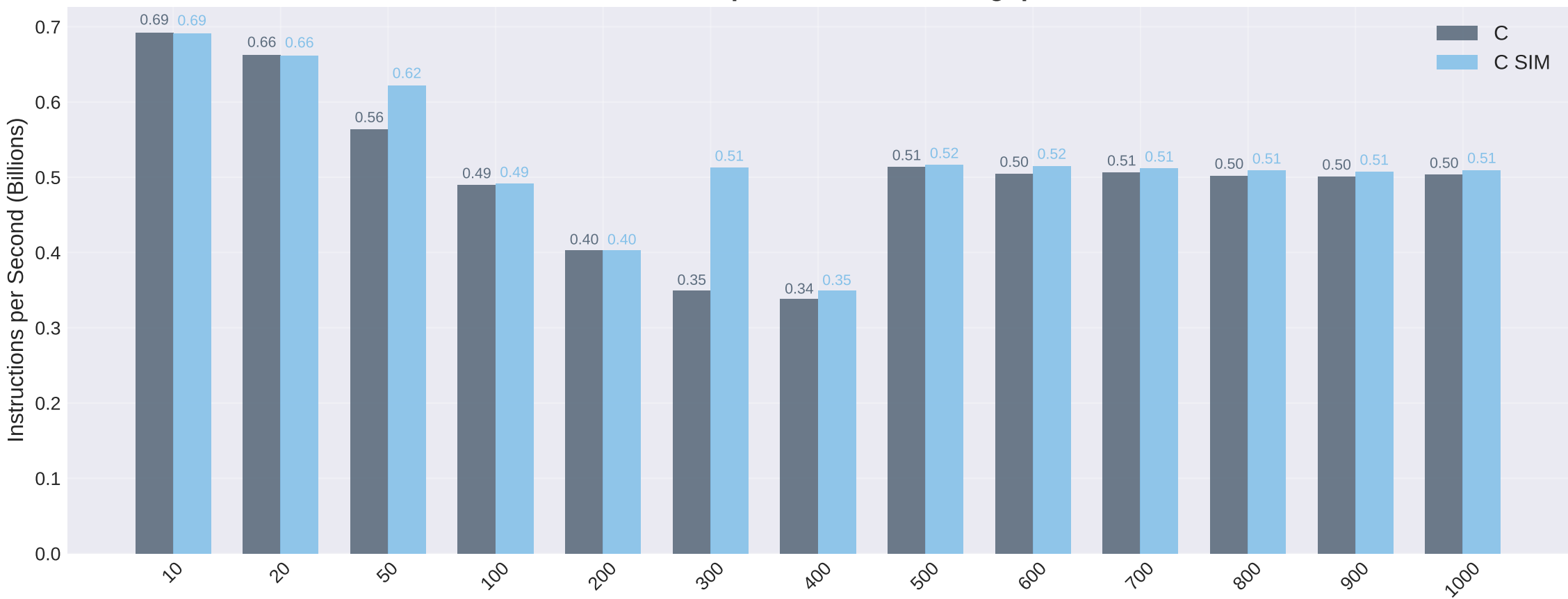
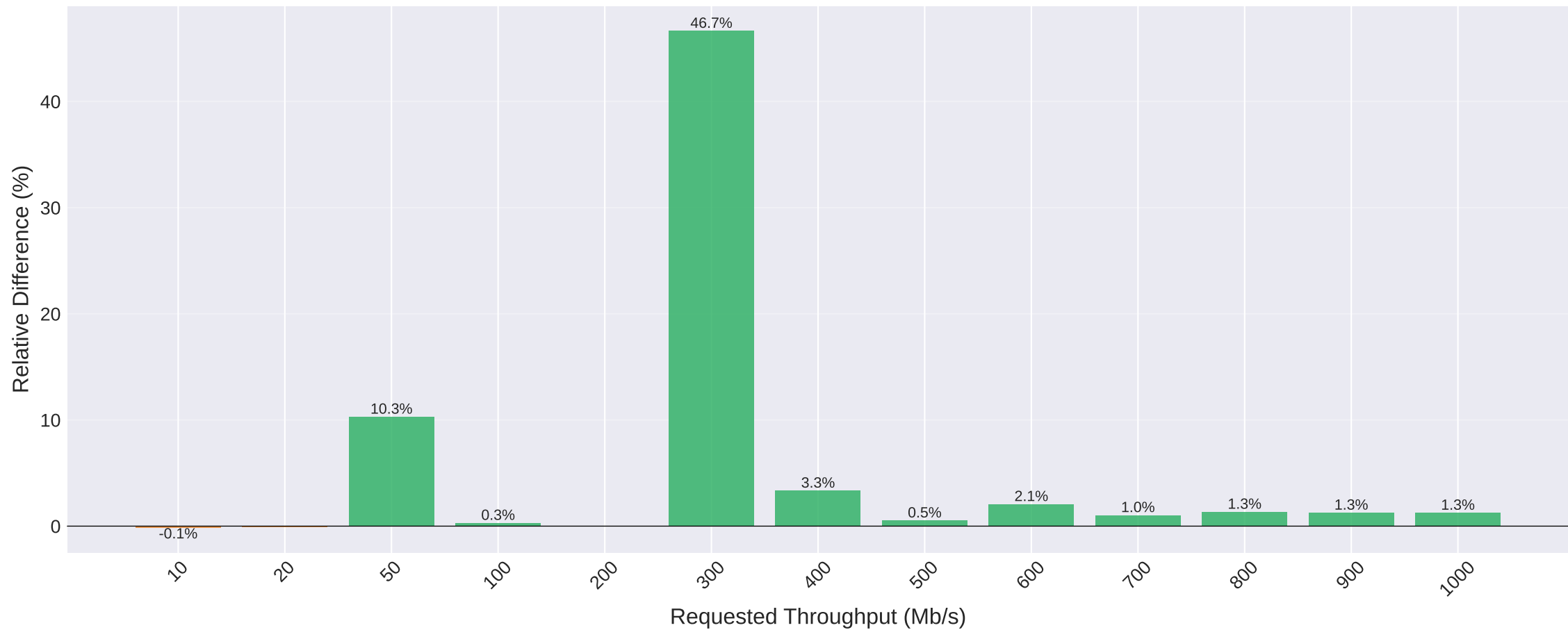


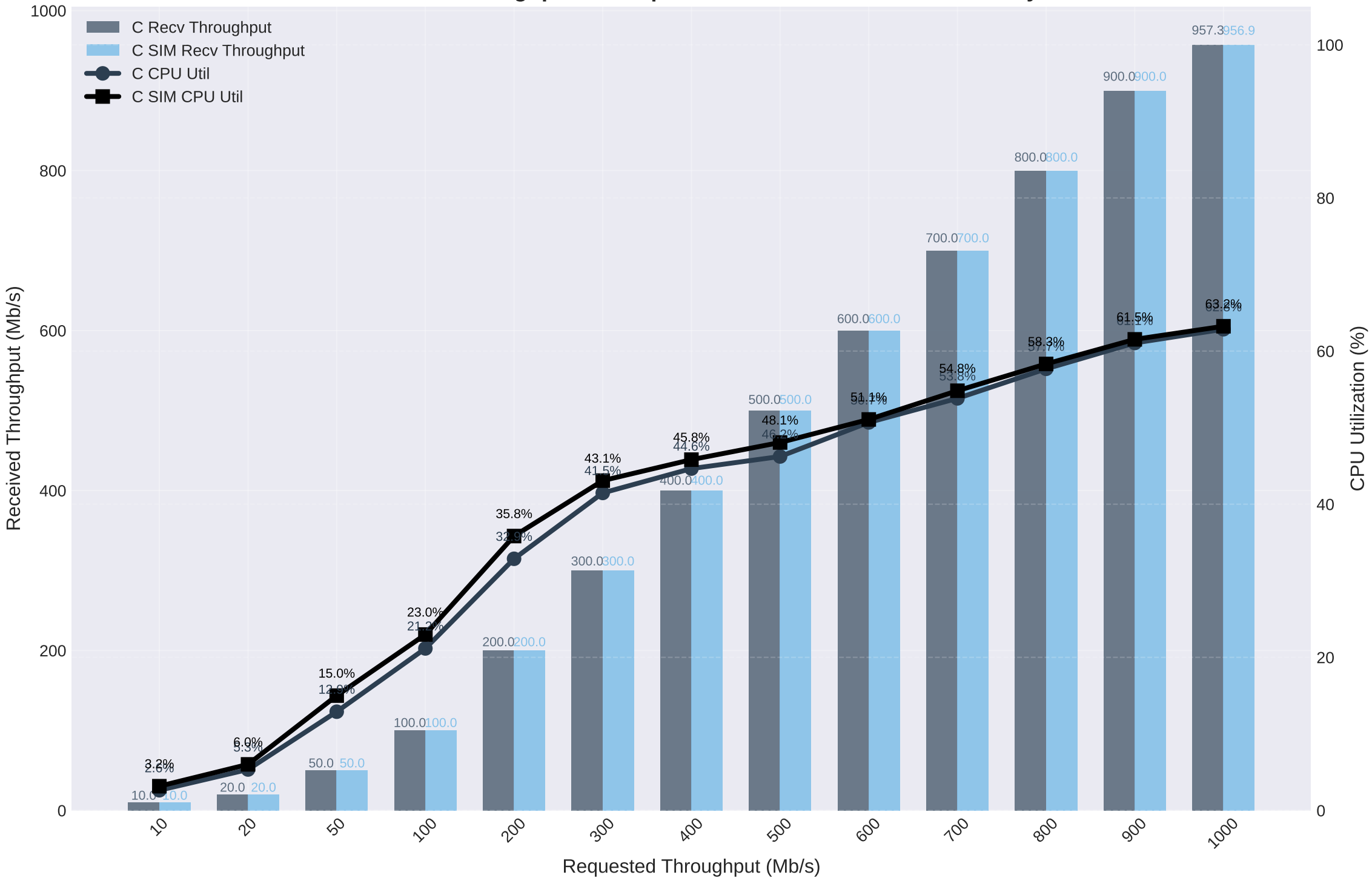
Instructions per Second vs Throughput



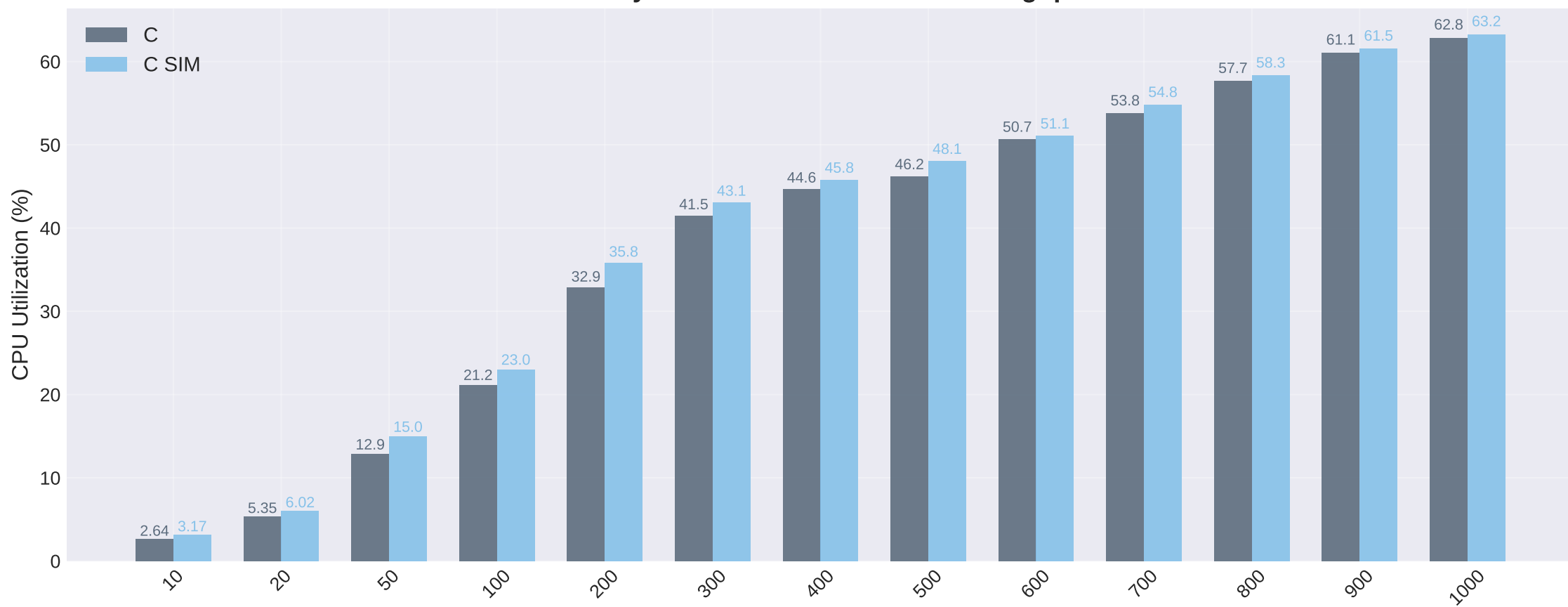
Relative Difference (C SIM vs C)



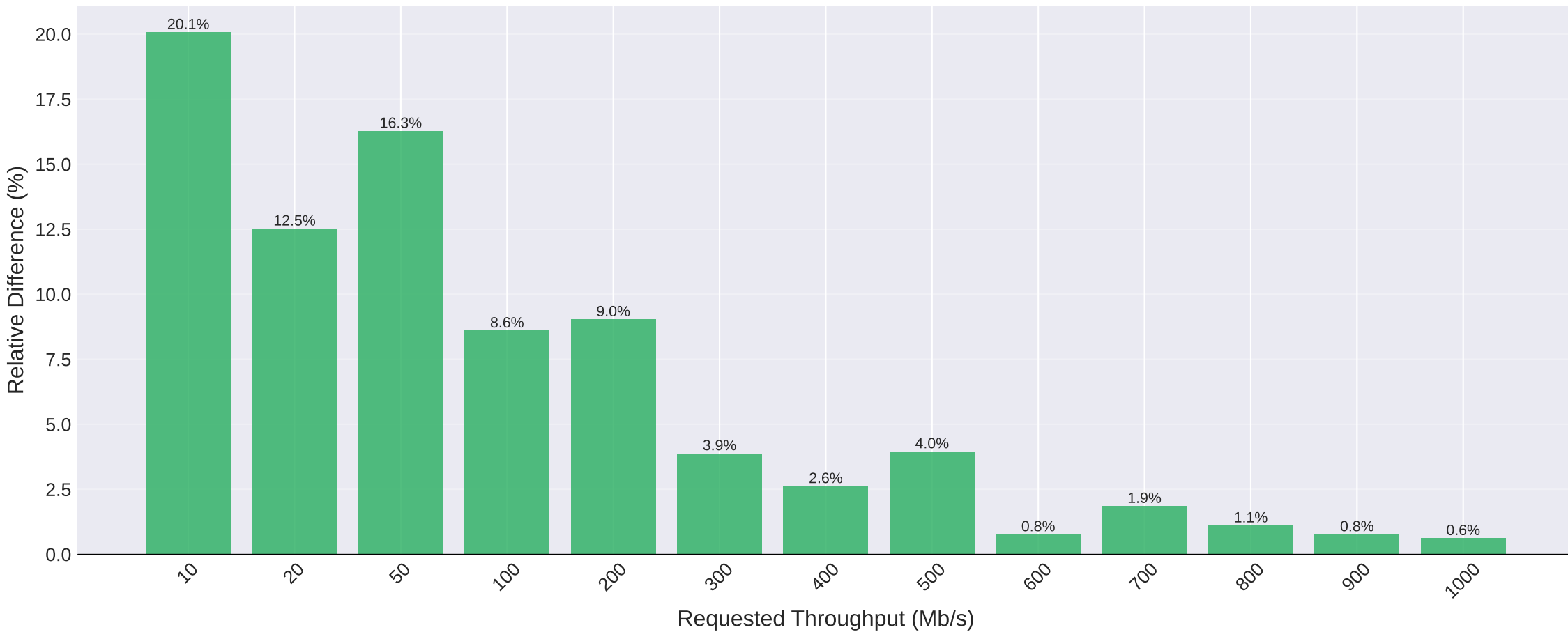
Received Throughput vs Requested with CPU Utilization Overlay



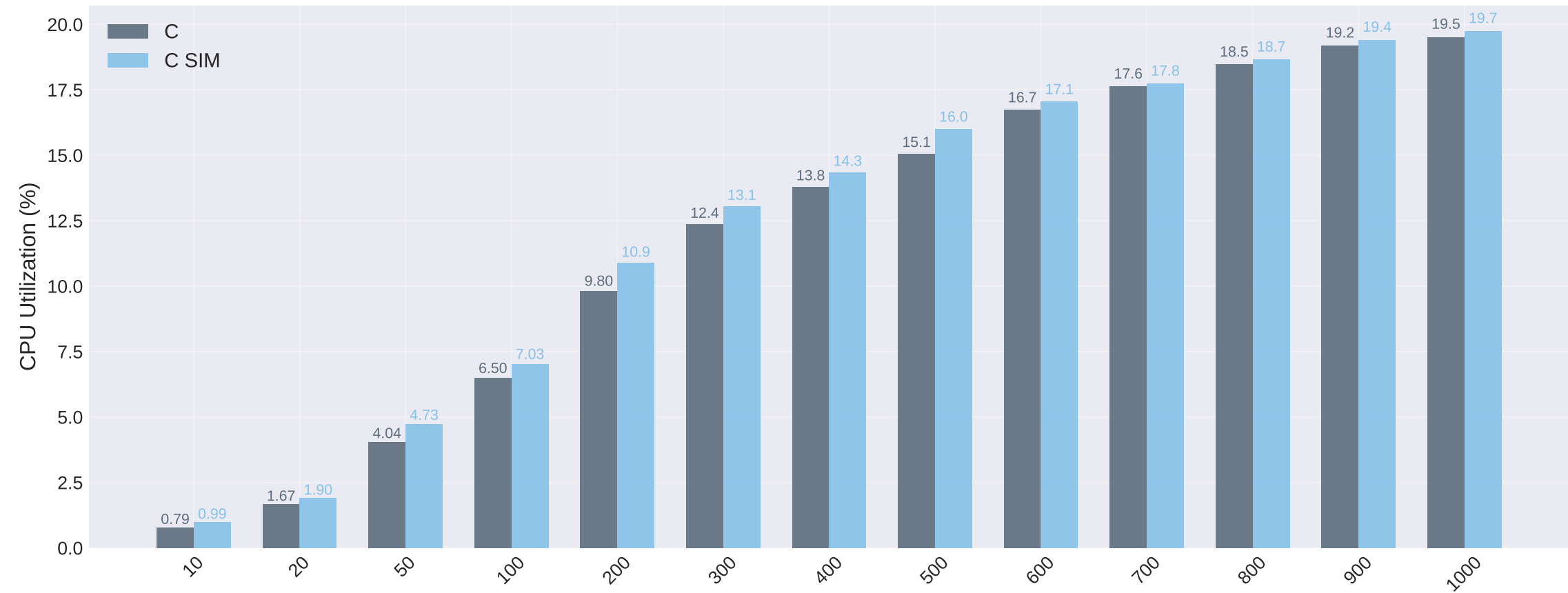
Total System CPU Utilization vs Throughput



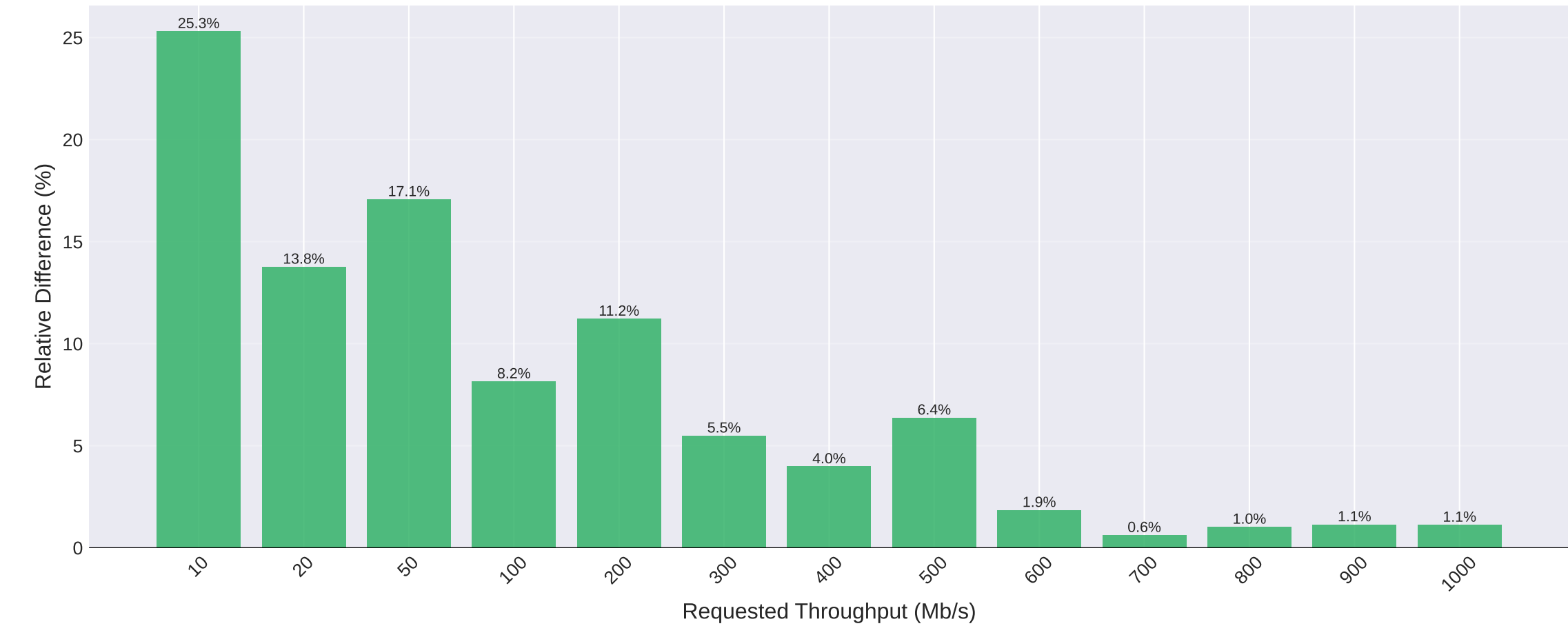
Relative Difference (C SIM vs C)



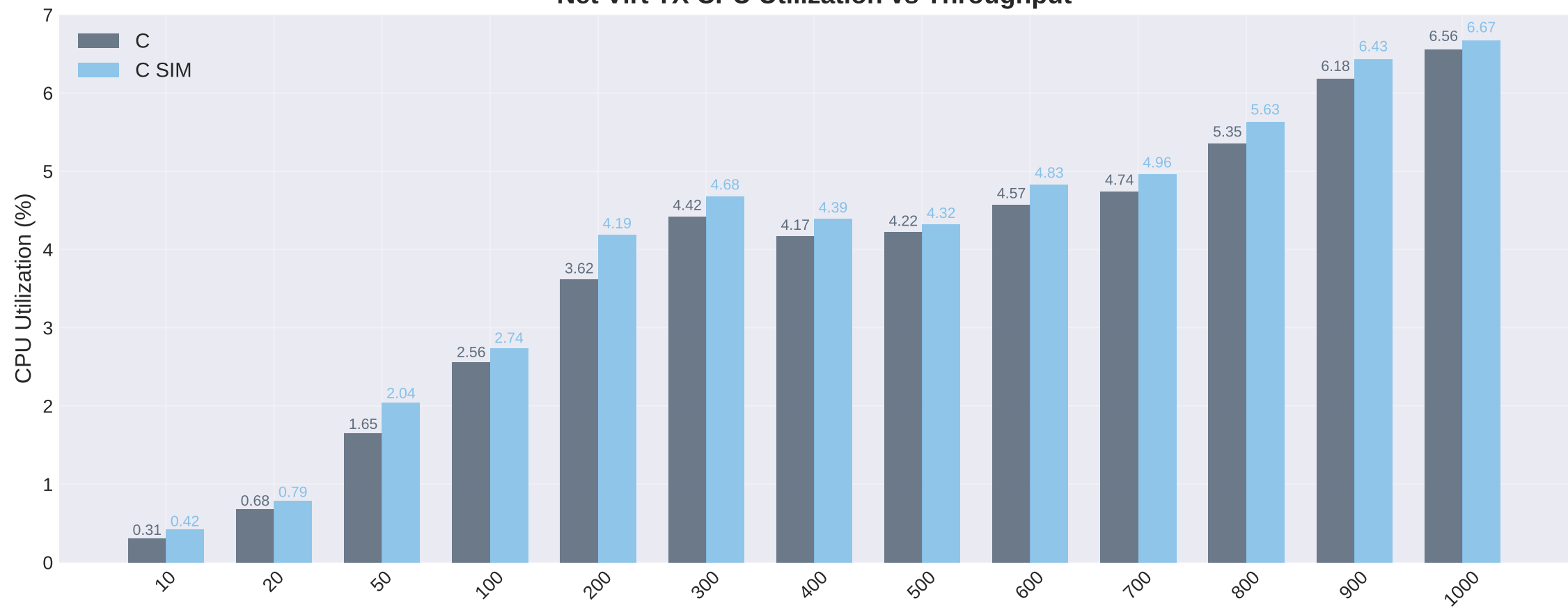
Ethernet Driver CPU Utilization vs Throughput



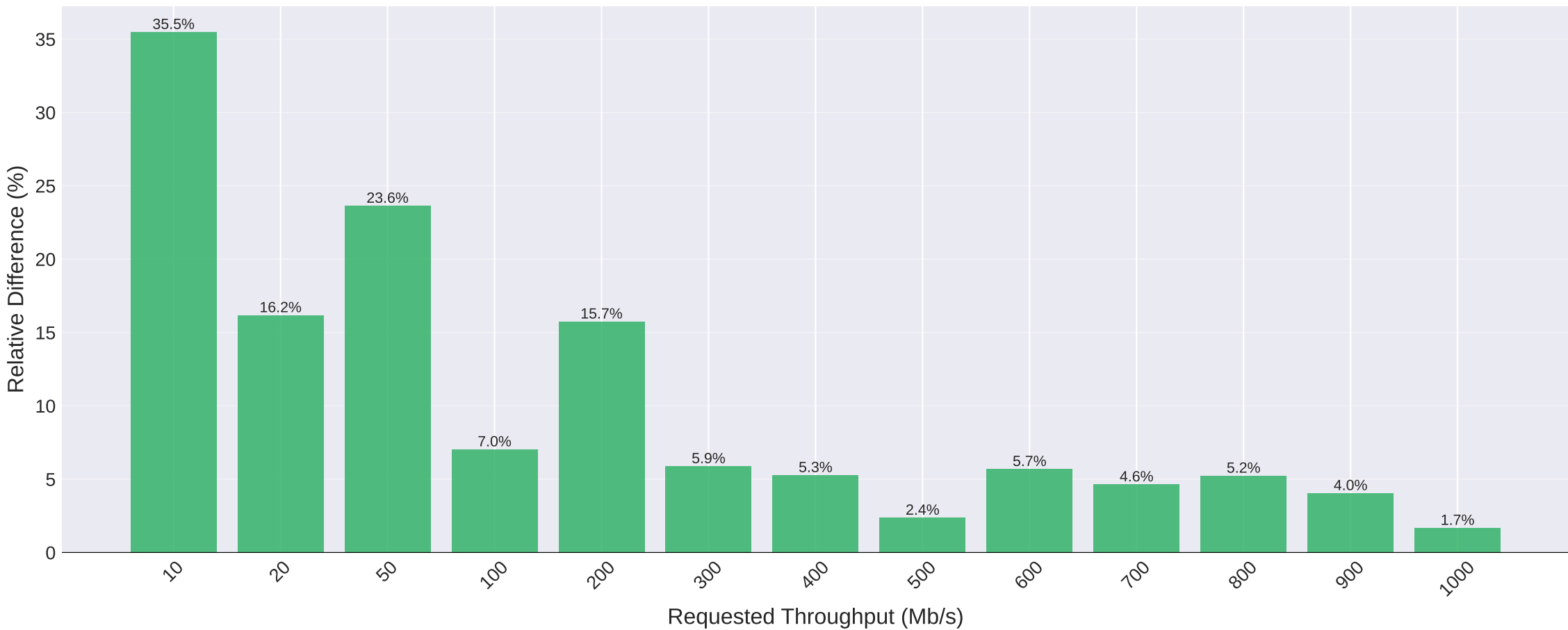
Relative Difference (C SIM vs C)



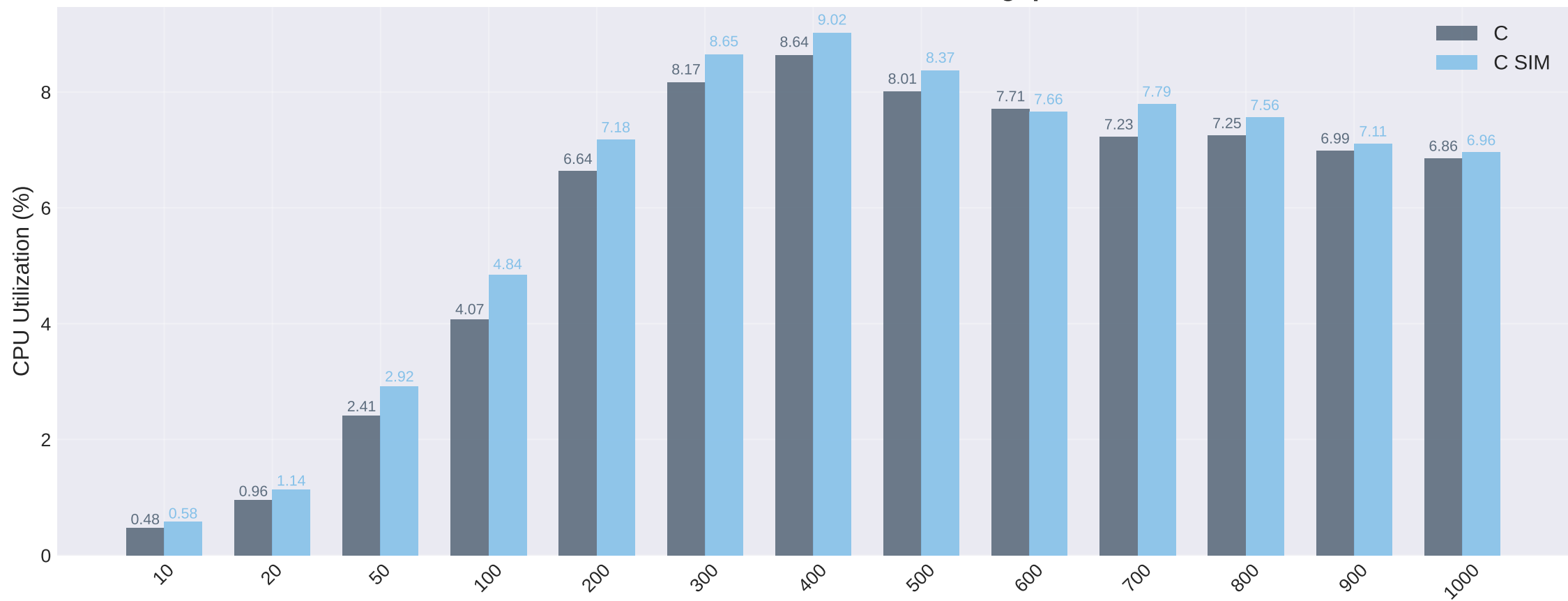
Net Virt TX CPU Utilization vs Throughput



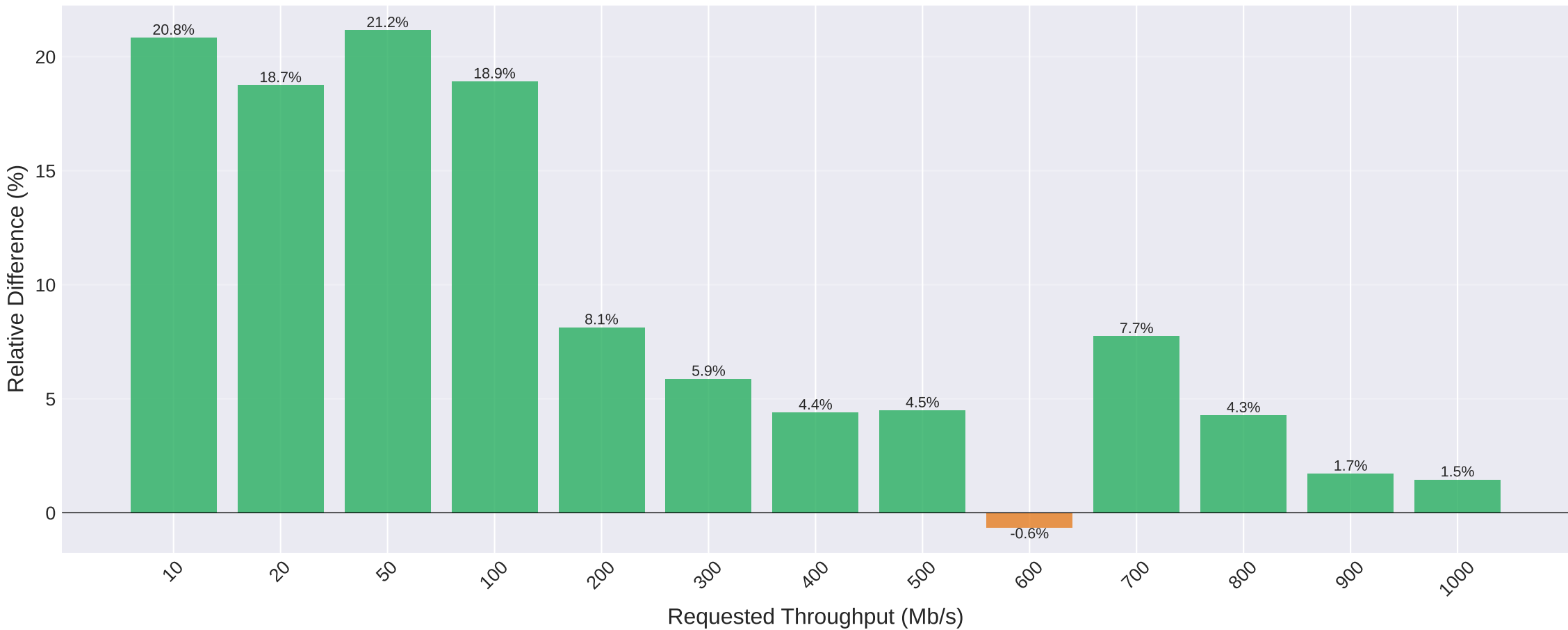
Relative Difference (C SIM vs C)



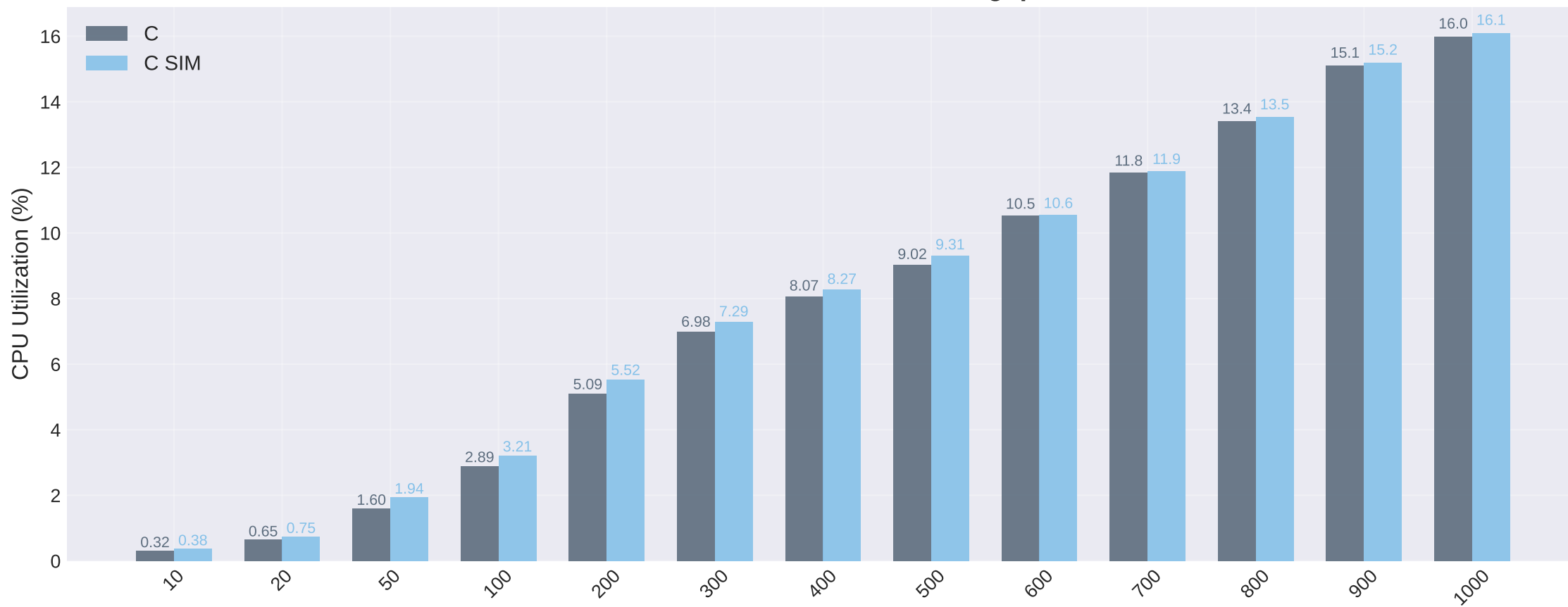
Net Virt RX CPU Utilization vs Throughput



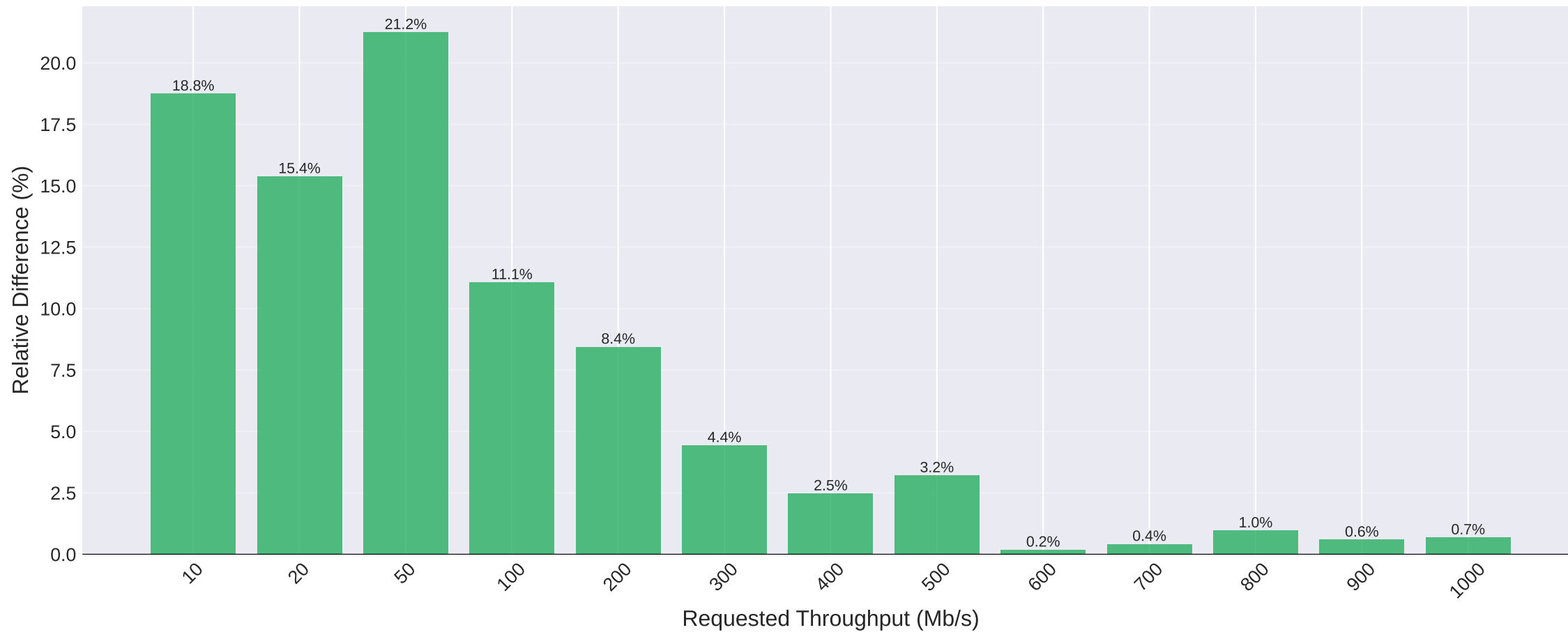
Relative Difference (C SIM vs C)



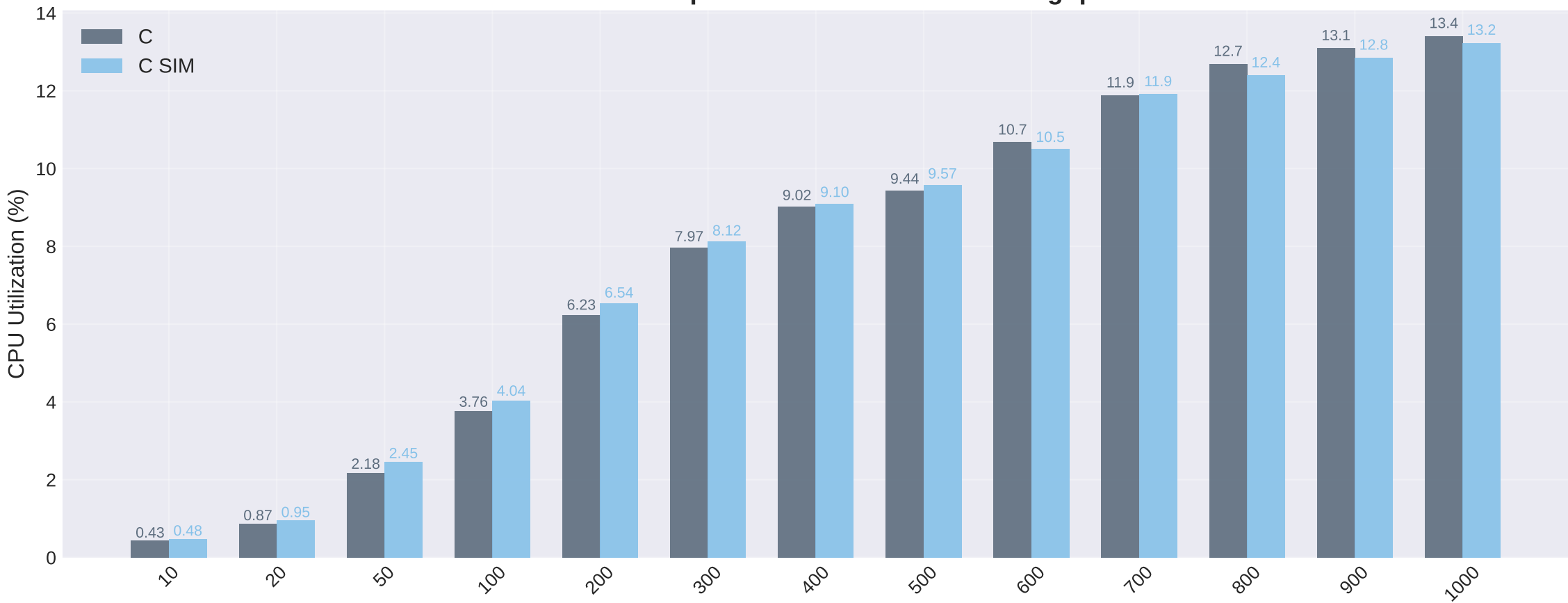
Client0 CPU Utilization vs Throughput



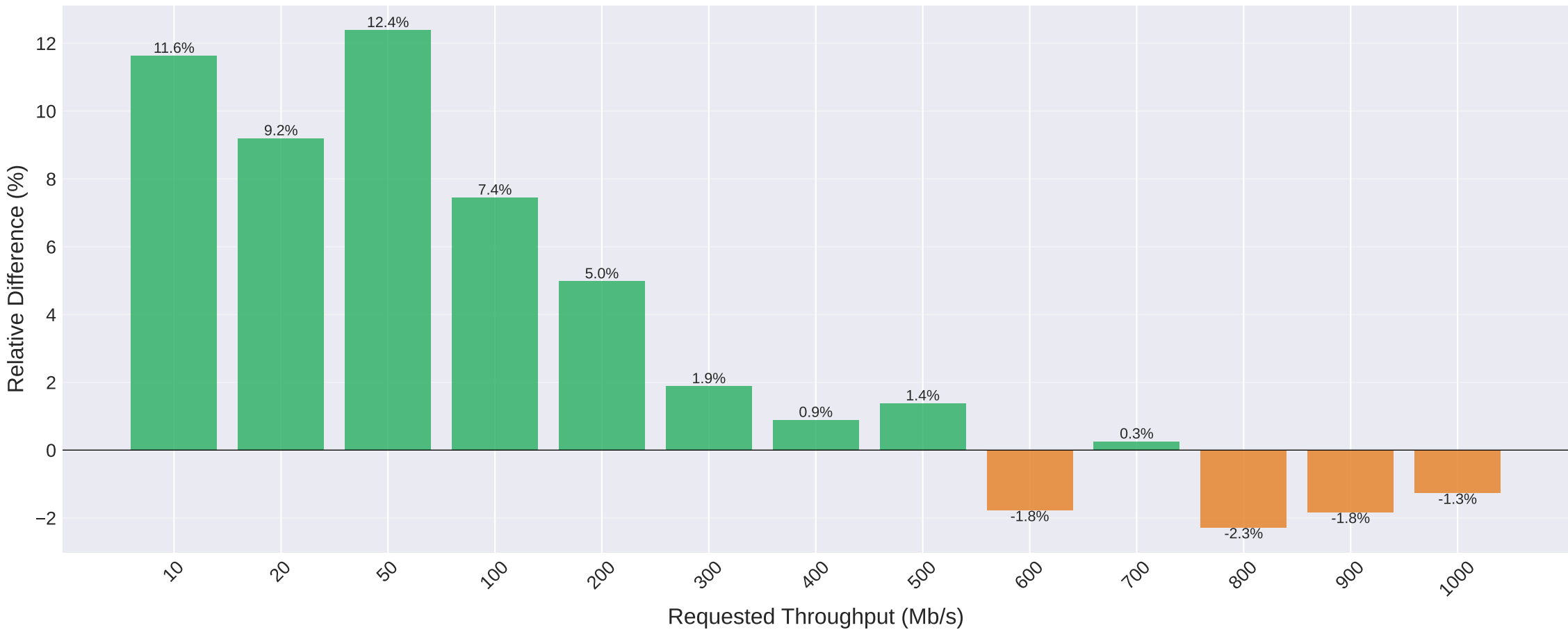
Relative Difference (C SIM vs C)



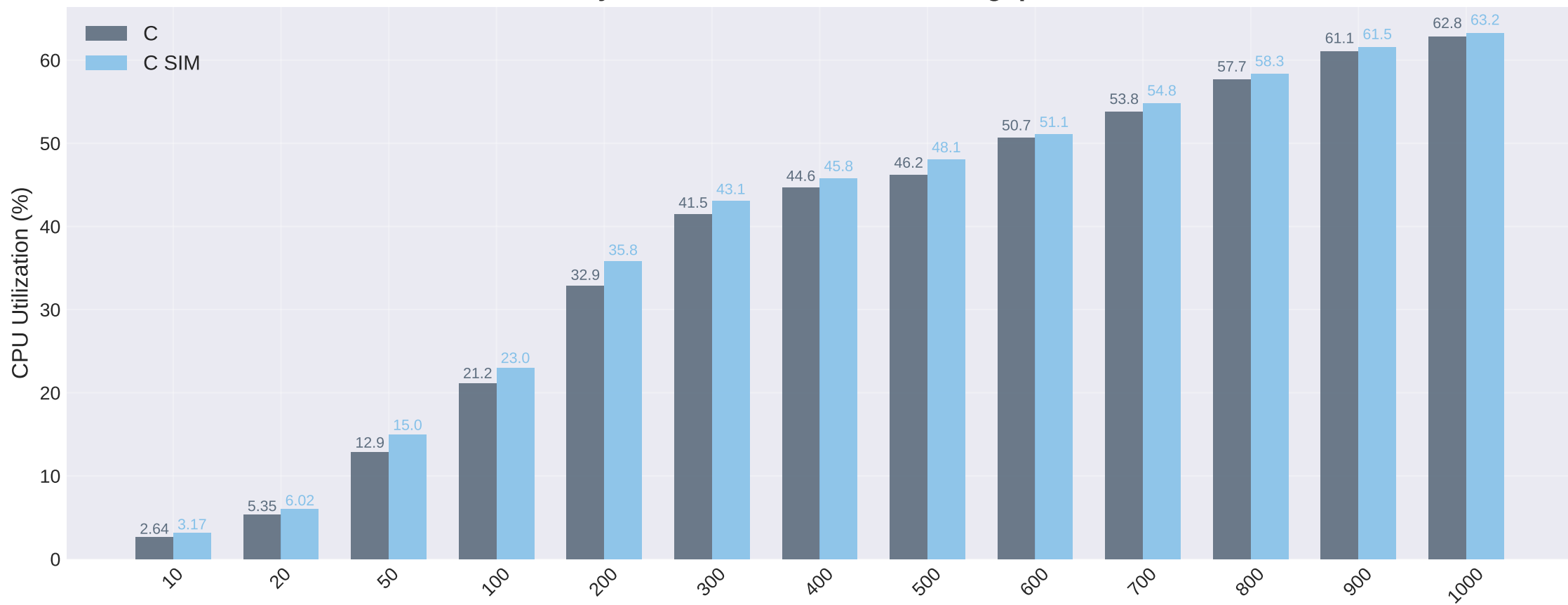
Client0 Net Copier CPU Utilization vs Throughput



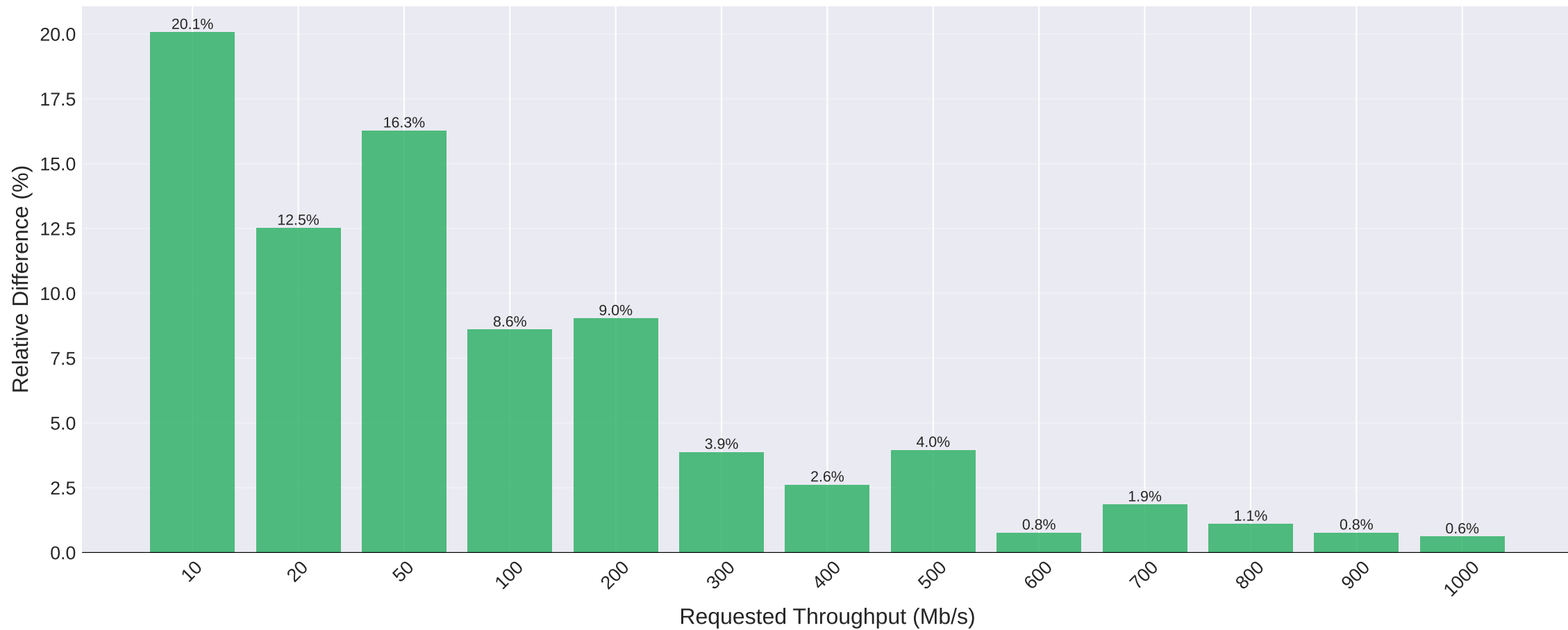
Relative Difference (C SIM vs C)



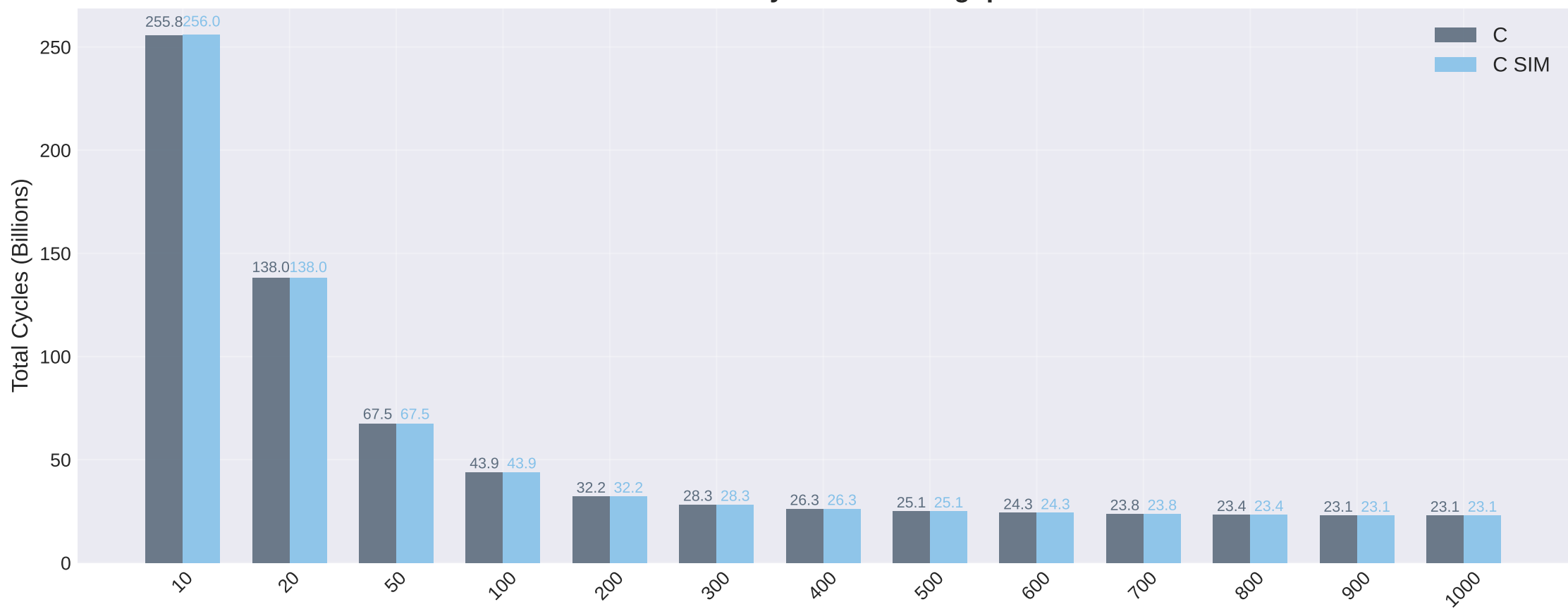
System CPU Utilization vs Throughput



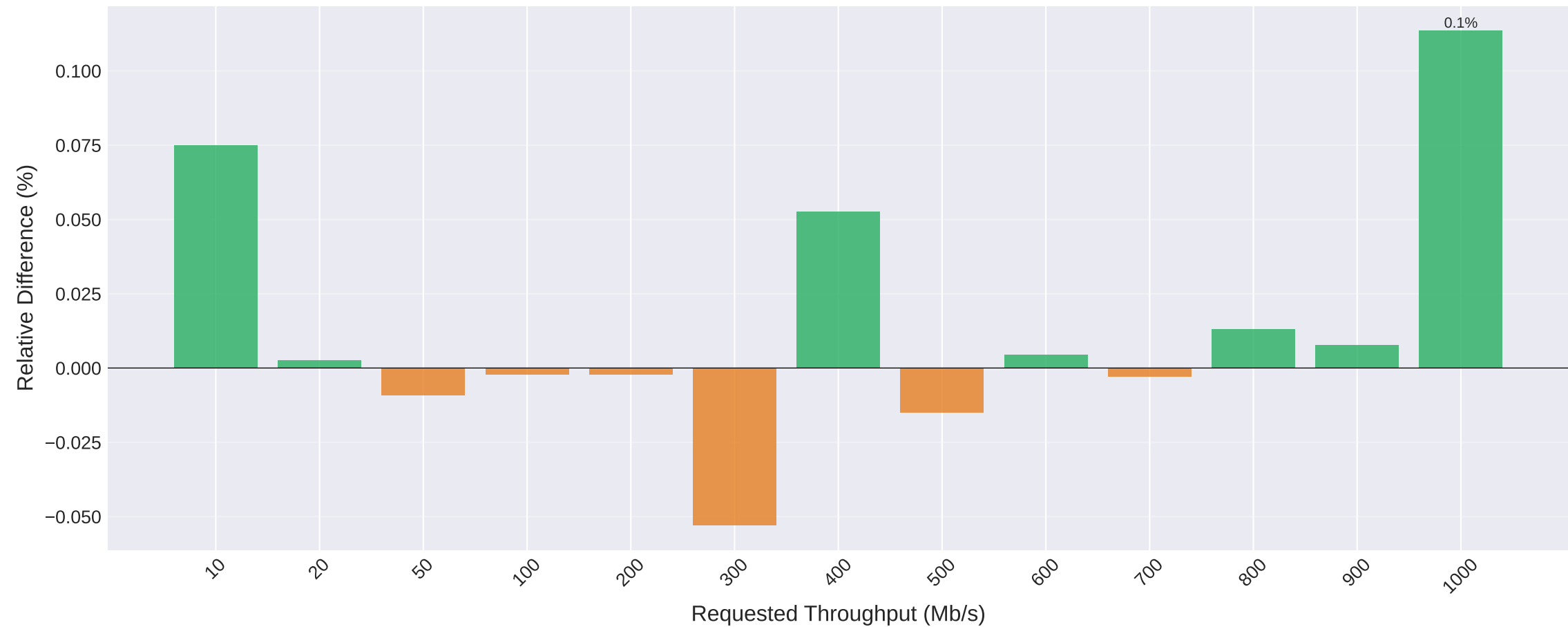
Relative Difference (C SIM vs C)



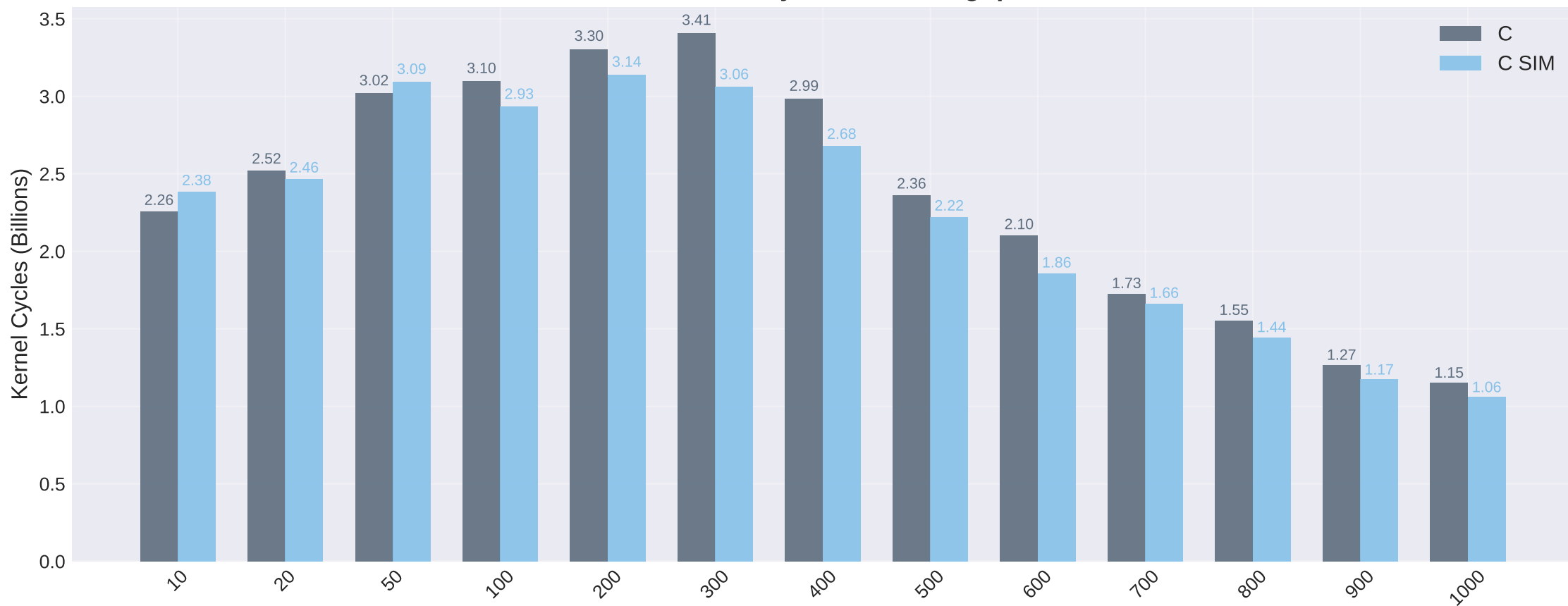
Total CPU Cycles vs Throughput



Relative Difference (C SIM vs C)



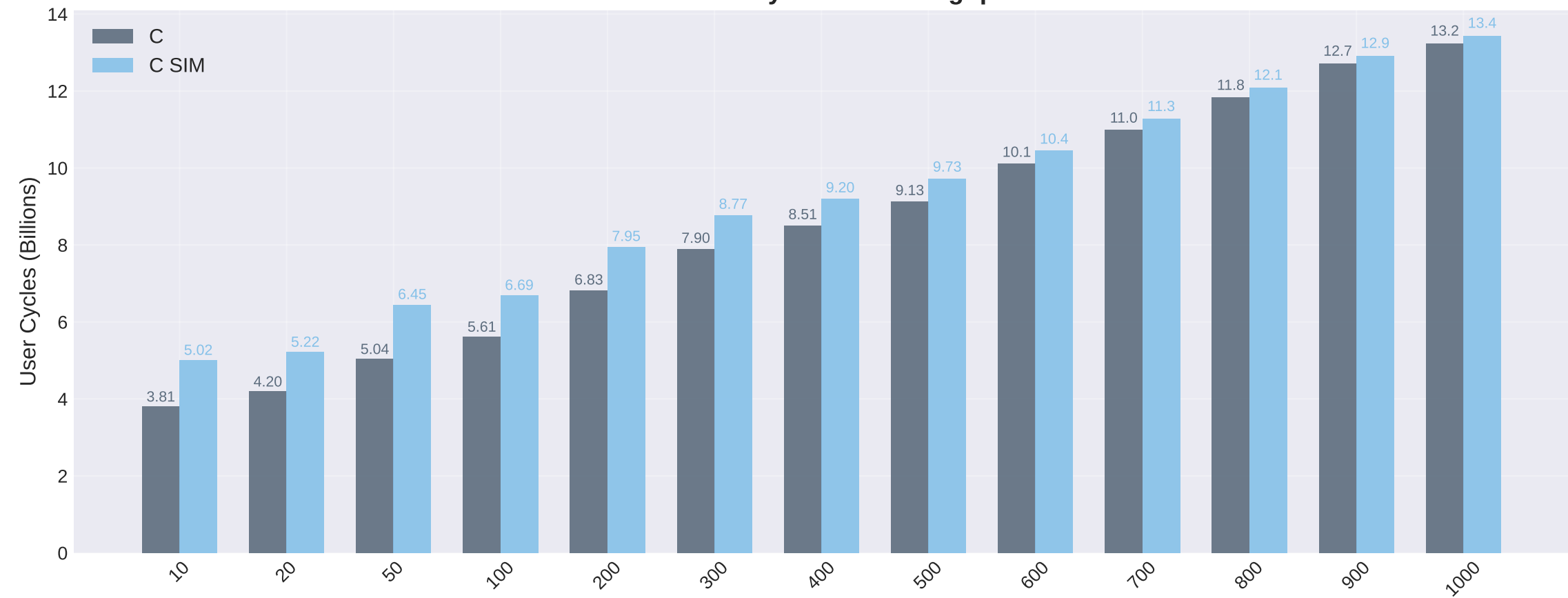
Kernel CPU Cycles vs Throughput



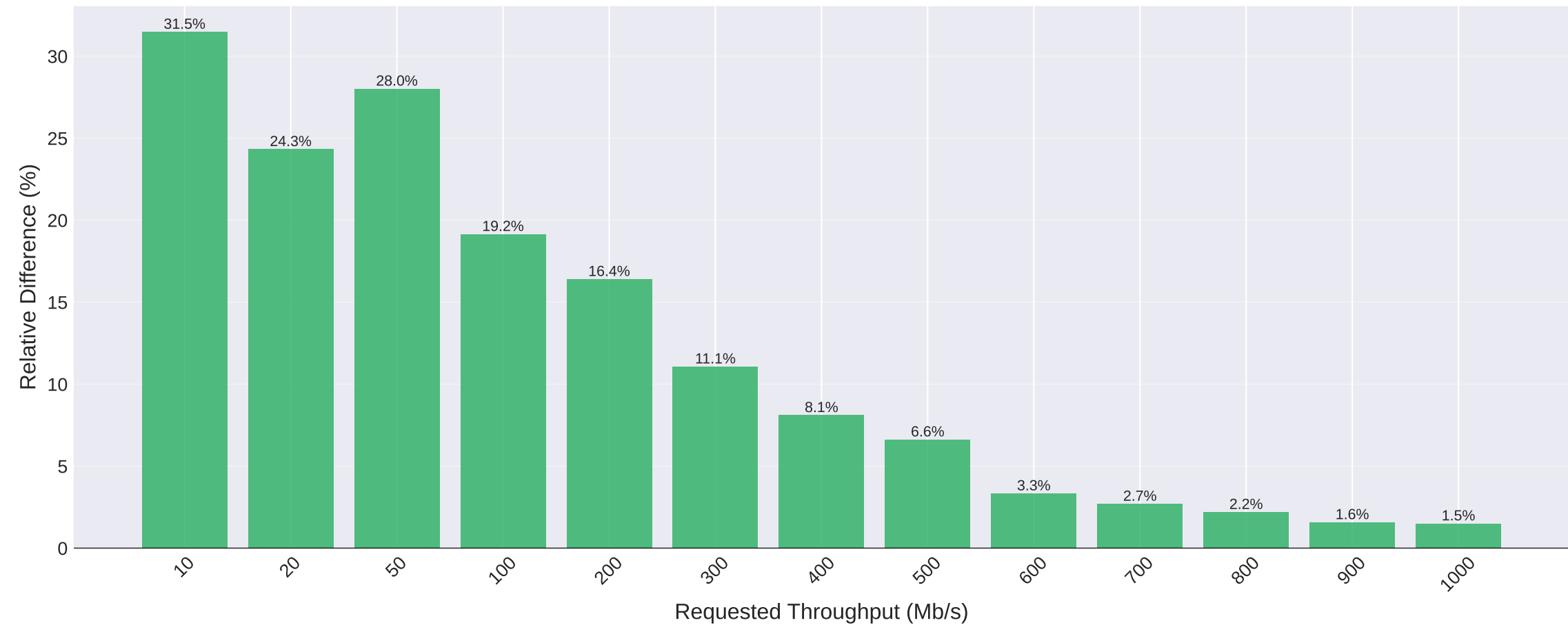
Relative Difference (C SIM vs C)



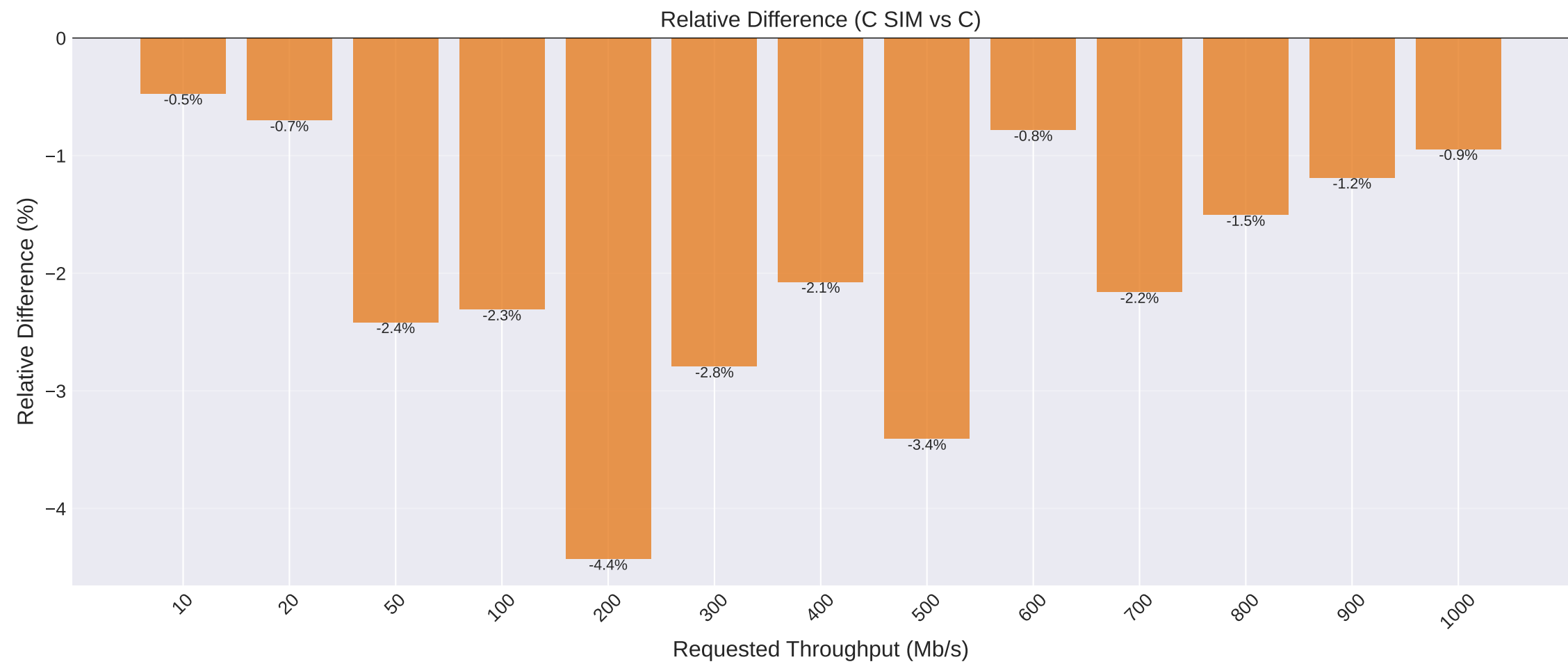
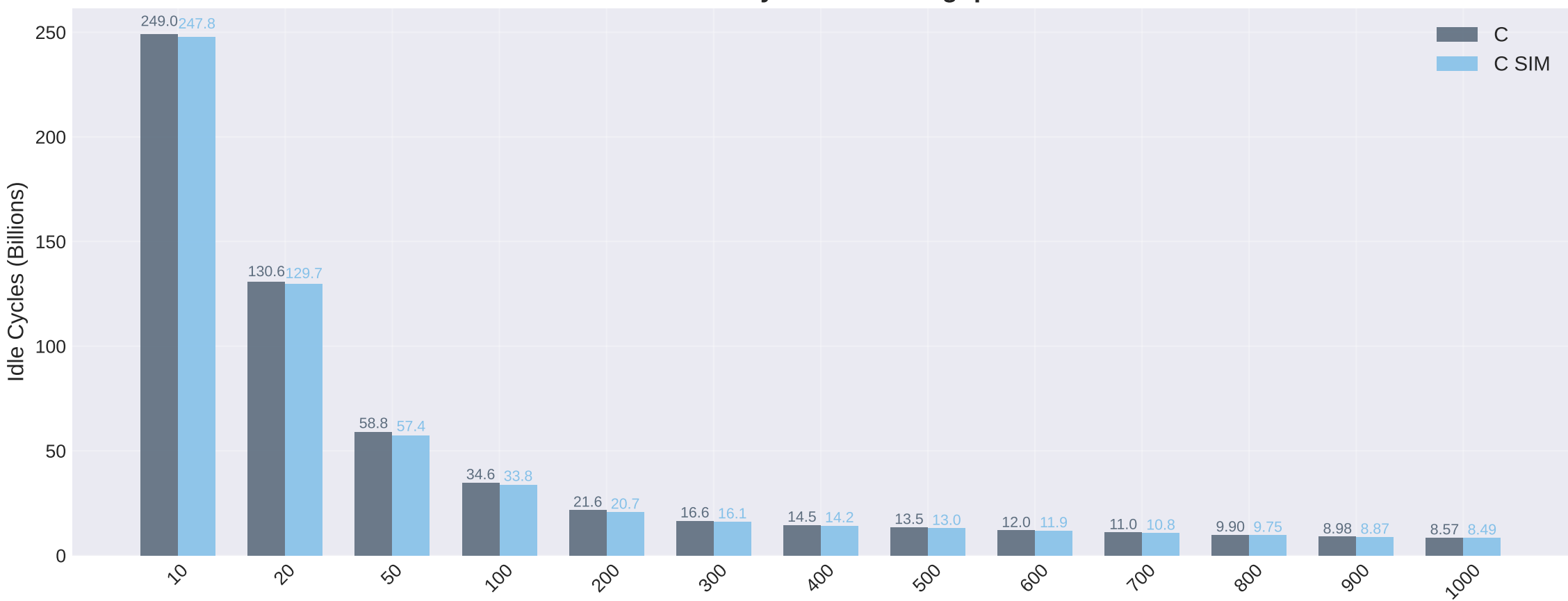
User CPU Cycles vs Throughput



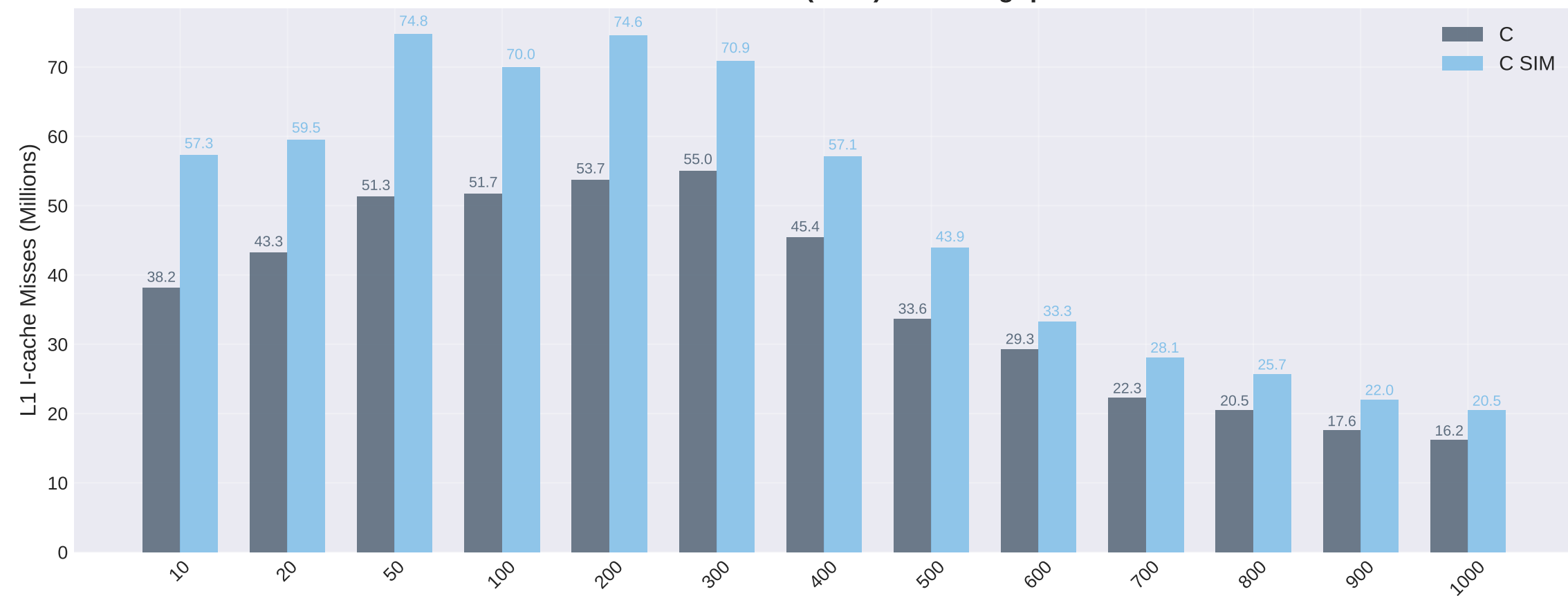
Relative Difference (C SIM vs C)



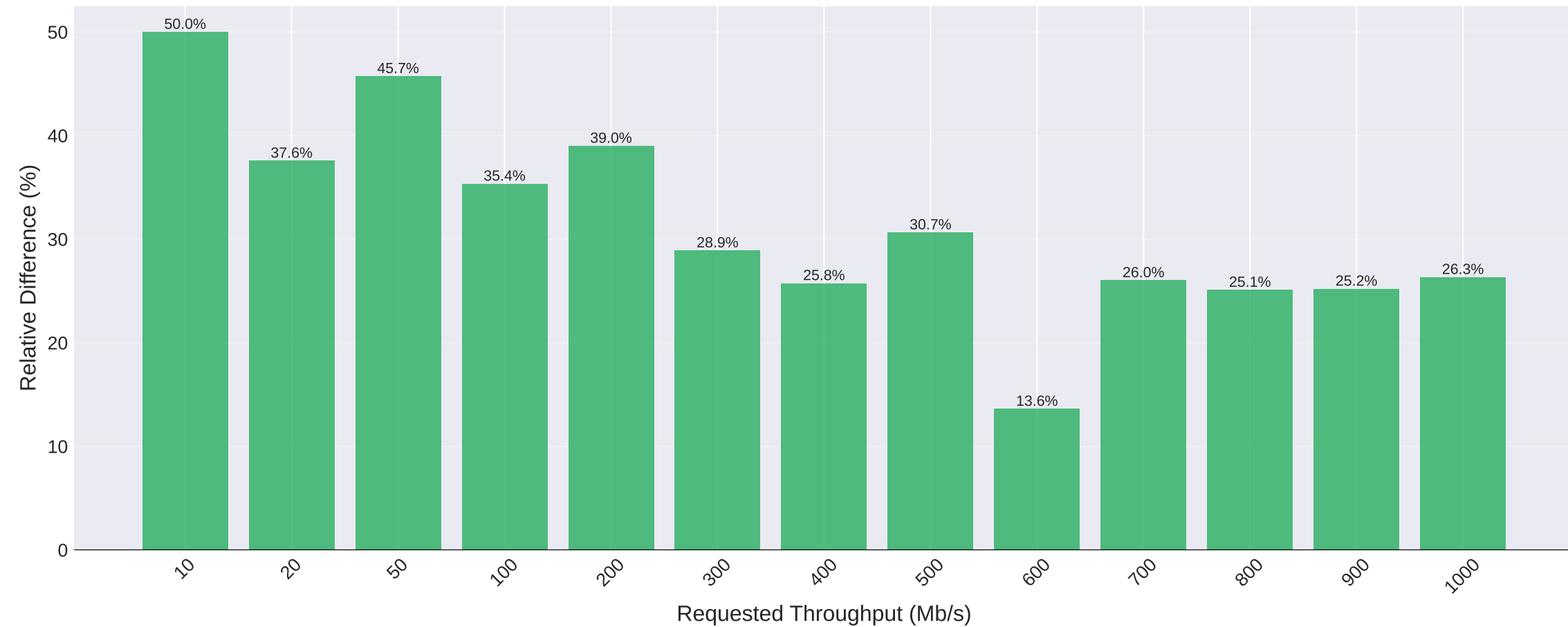
Idle CPU Cycles vs Throughput



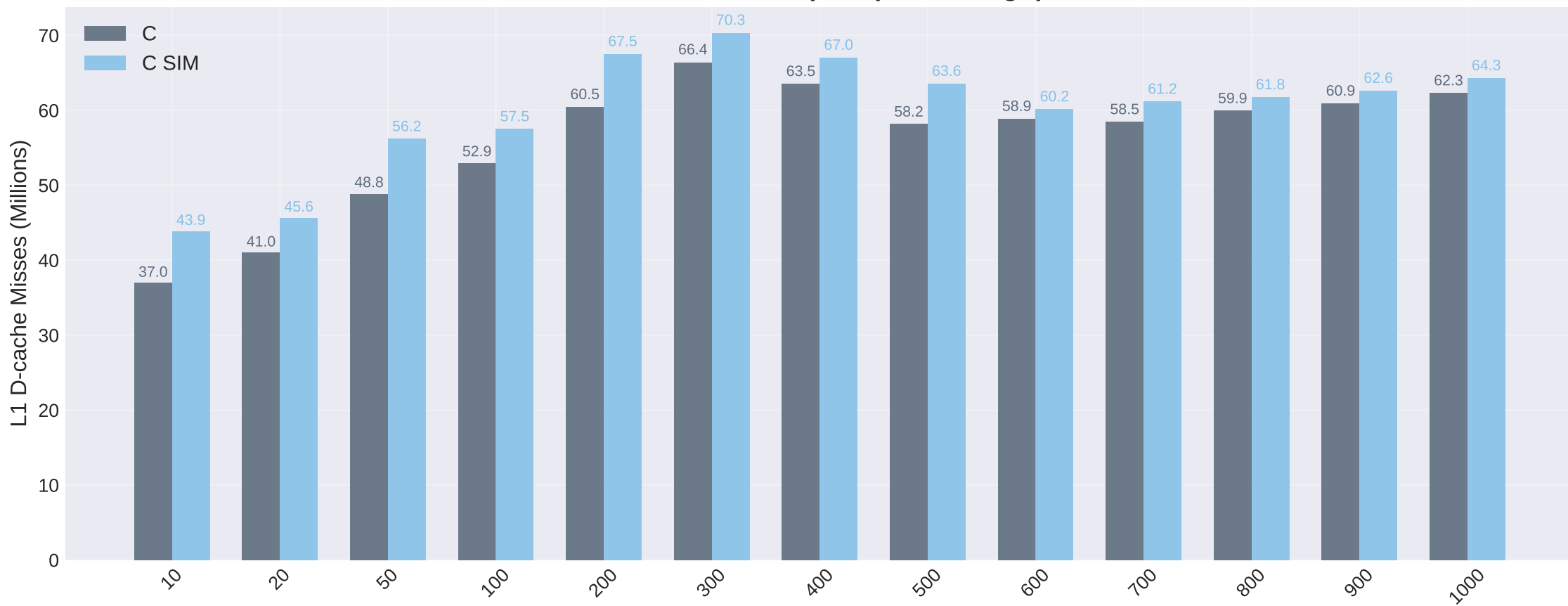
L1 I-cache Misses (Total) vs Throughput



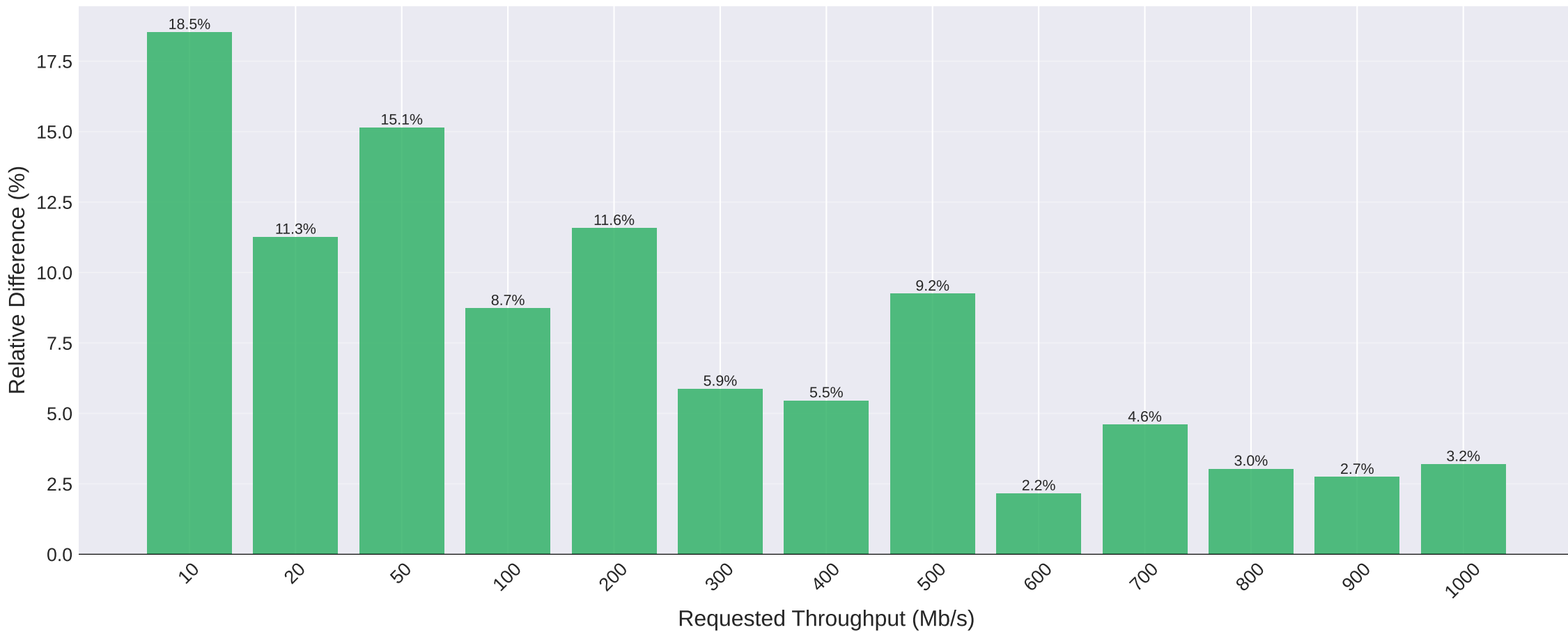
Relative Difference (C SIM vs C)



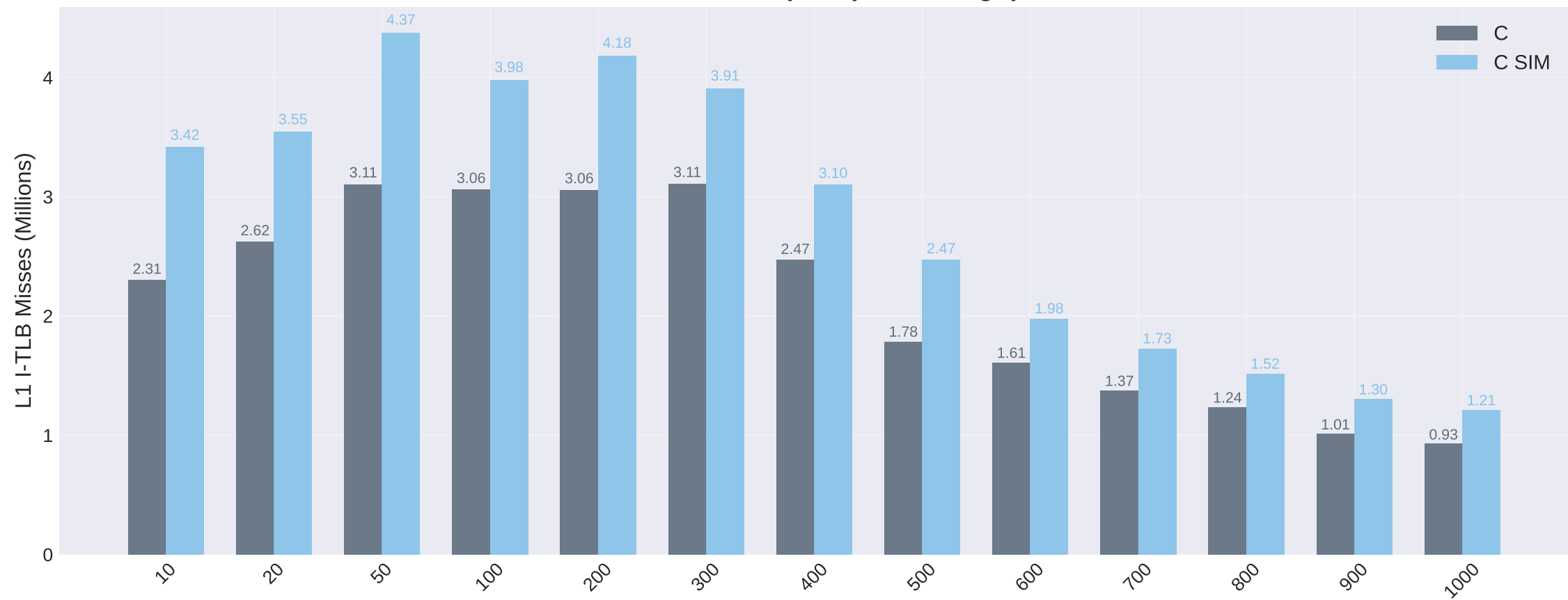
L1 D-cache Misses (Total) vs Throughput



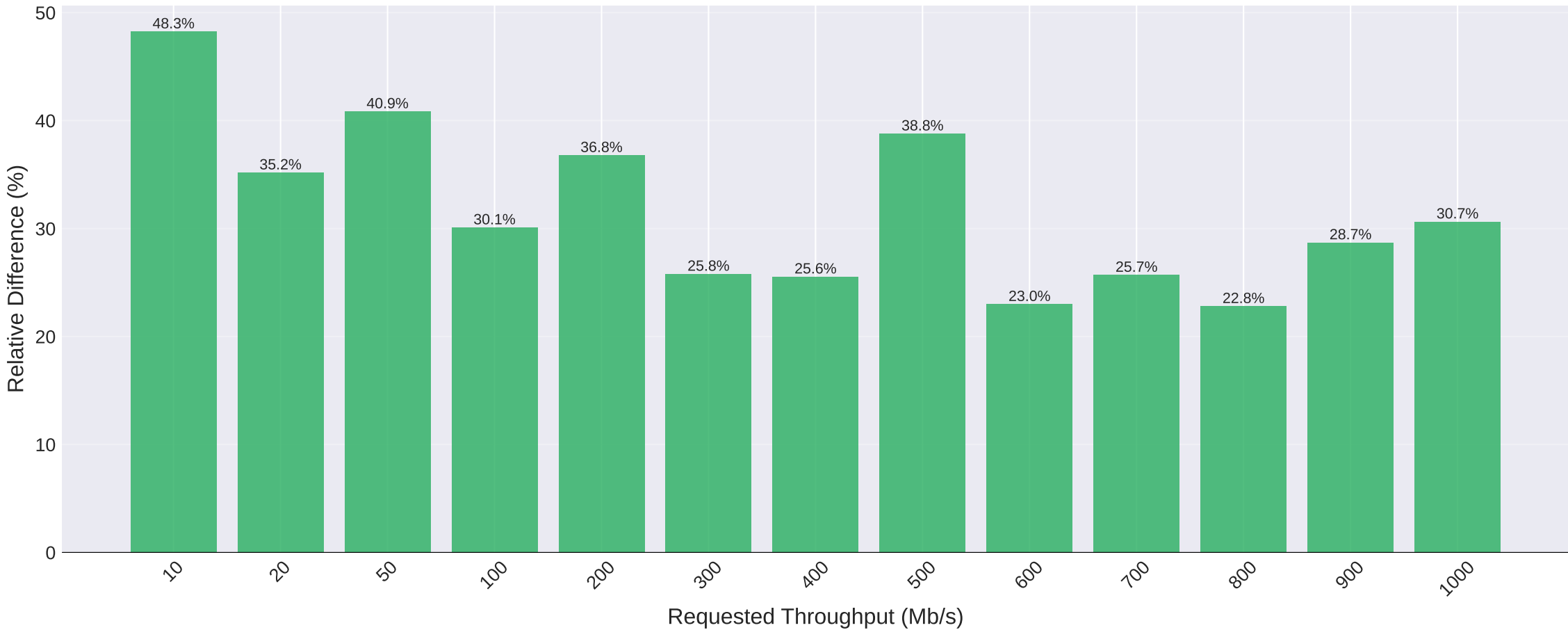
Relative Difference (C SIM vs C)



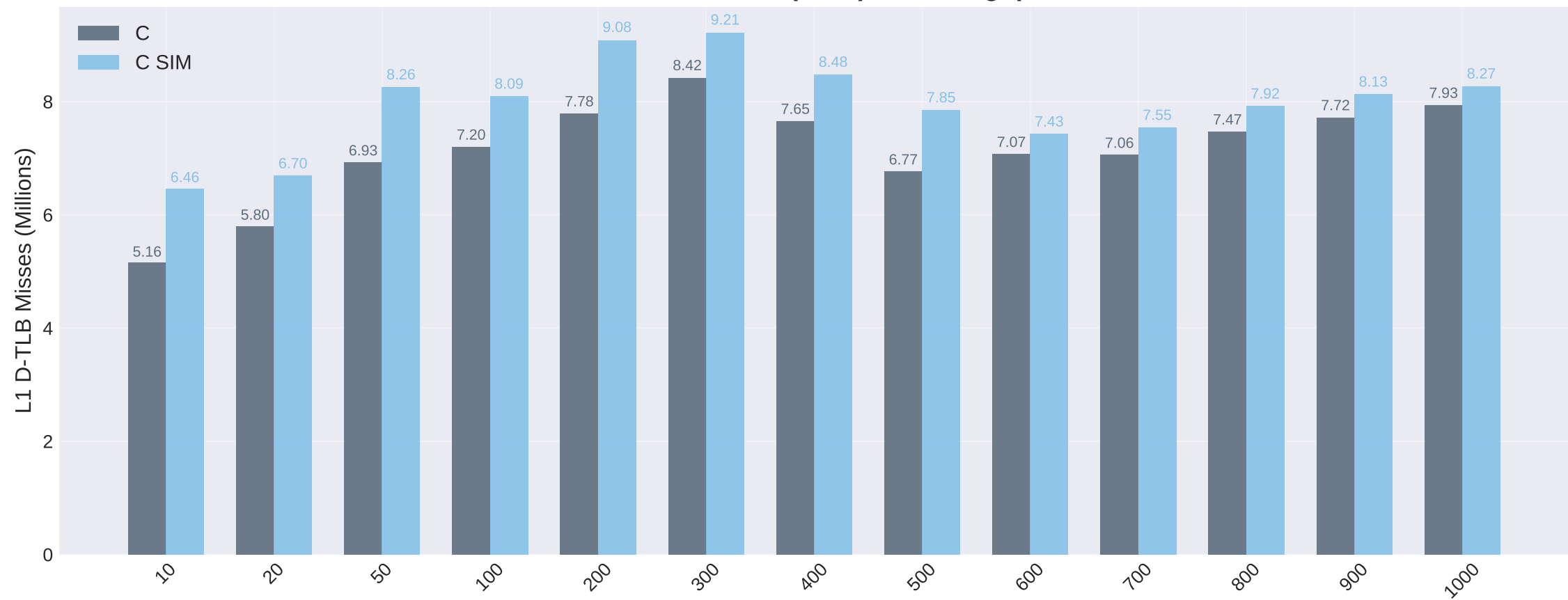
L1 I-TLB Misses (Total) vs Throughput



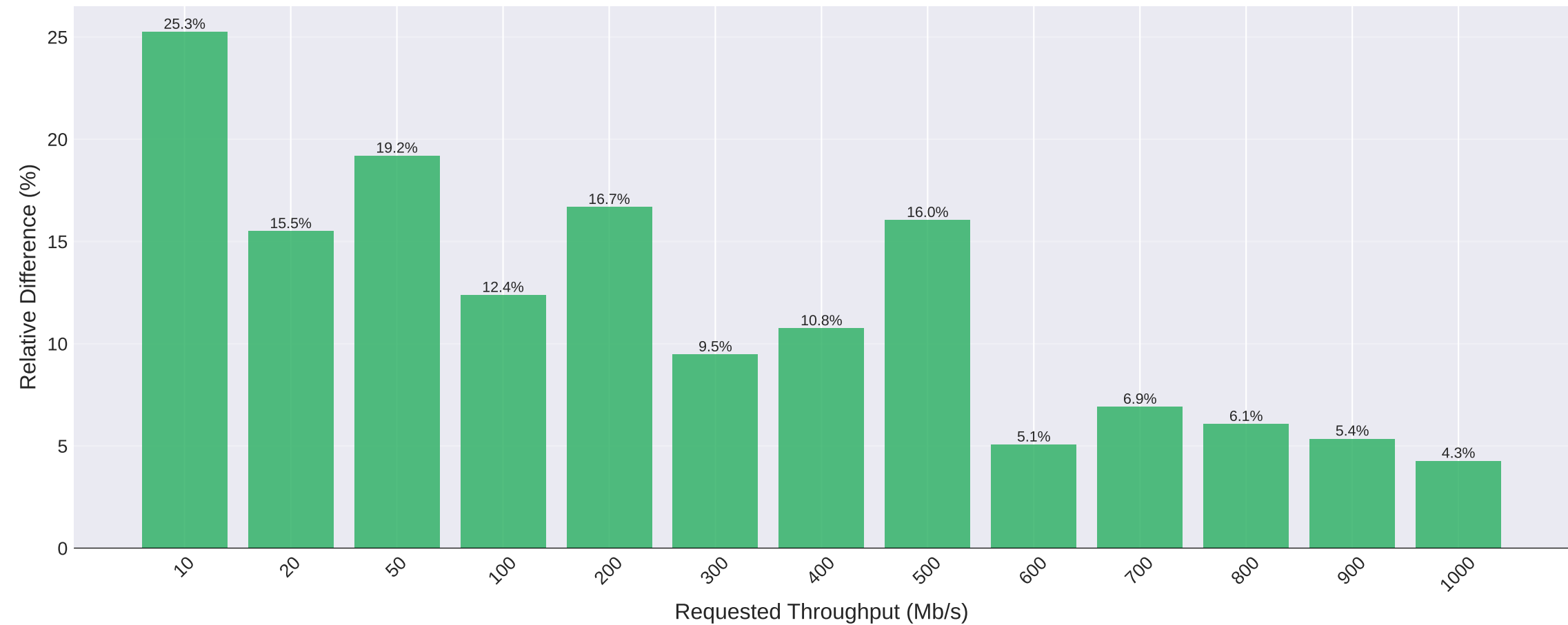
Relative Difference (C SIM vs C)



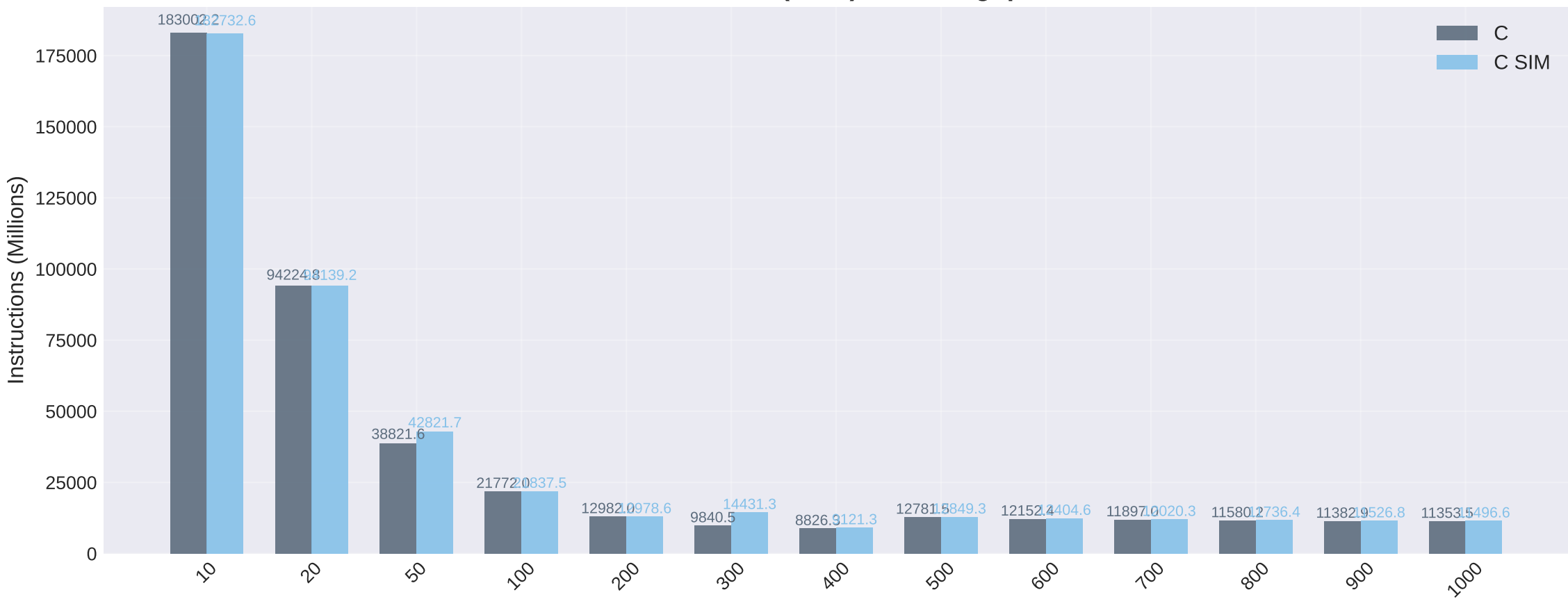
L1 D-TLB Misses (Total) vs Throughput



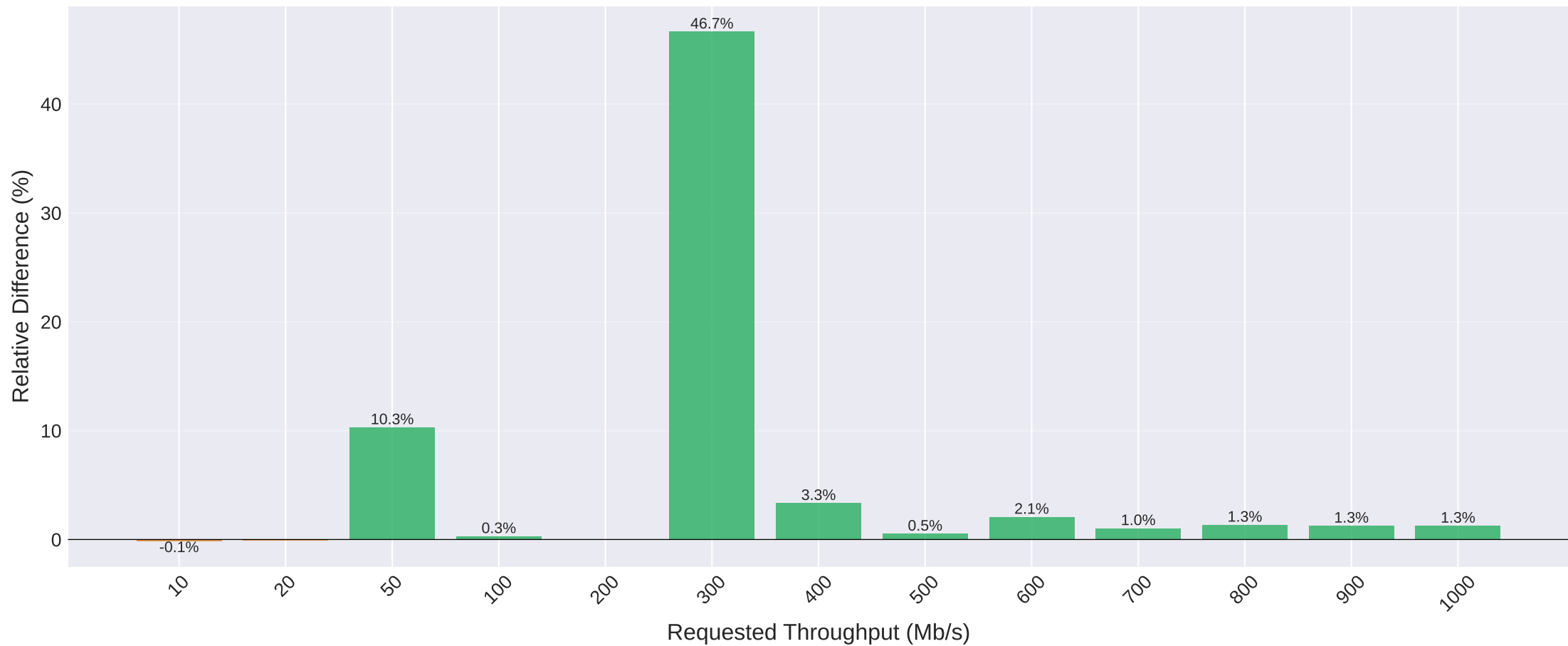
Relative Difference (C SIM vs C)



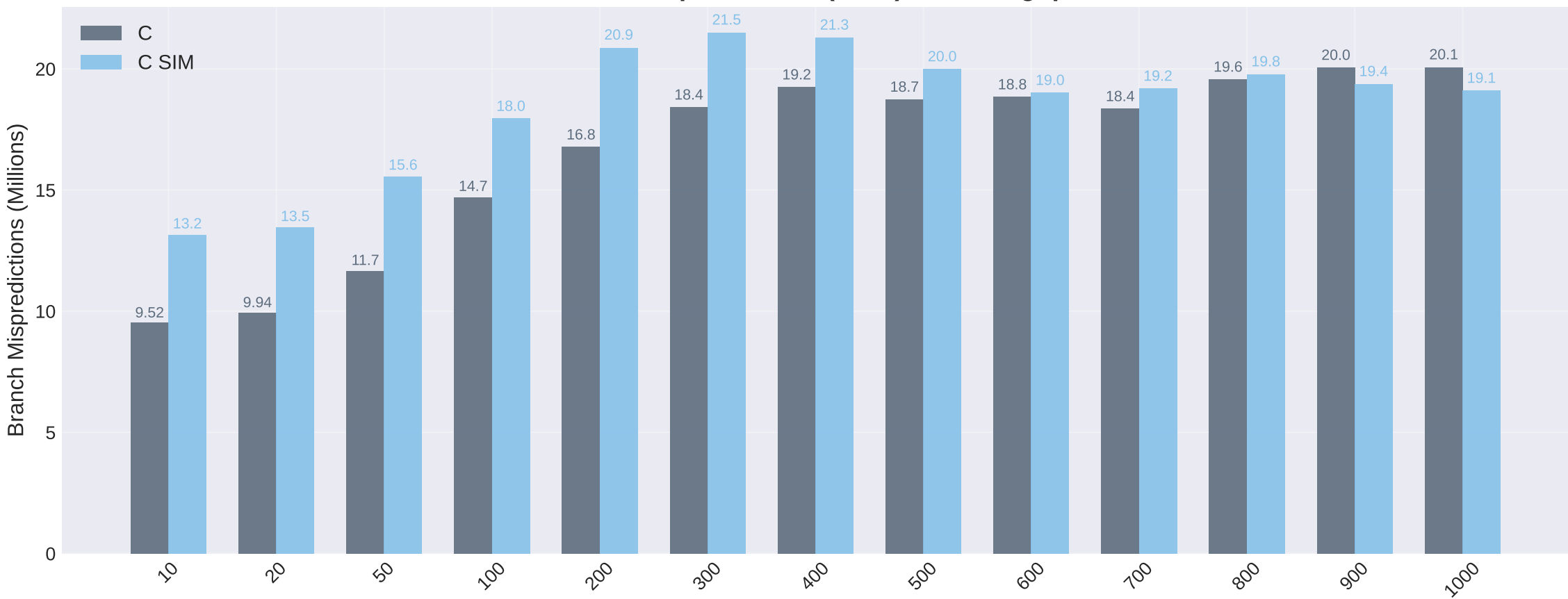
Instructions (Total) vs Throughput



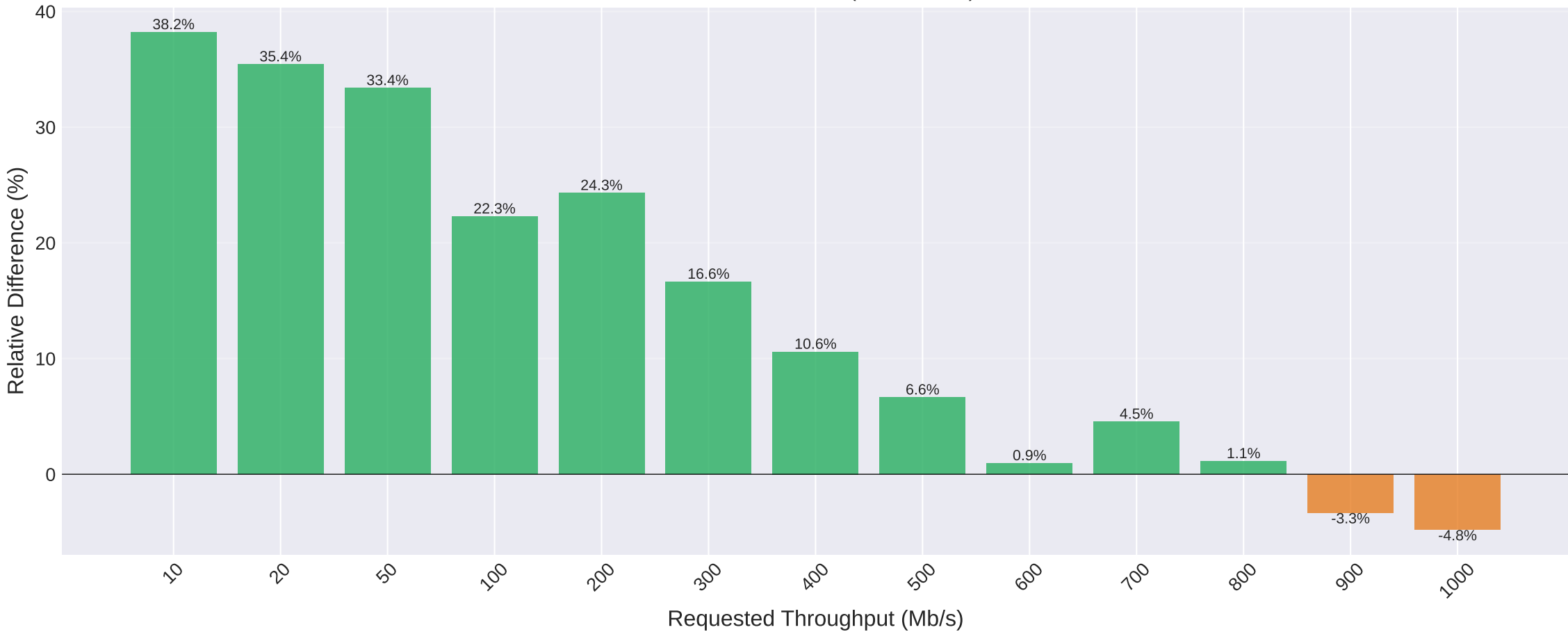
Relative Difference (C SIM vs C)



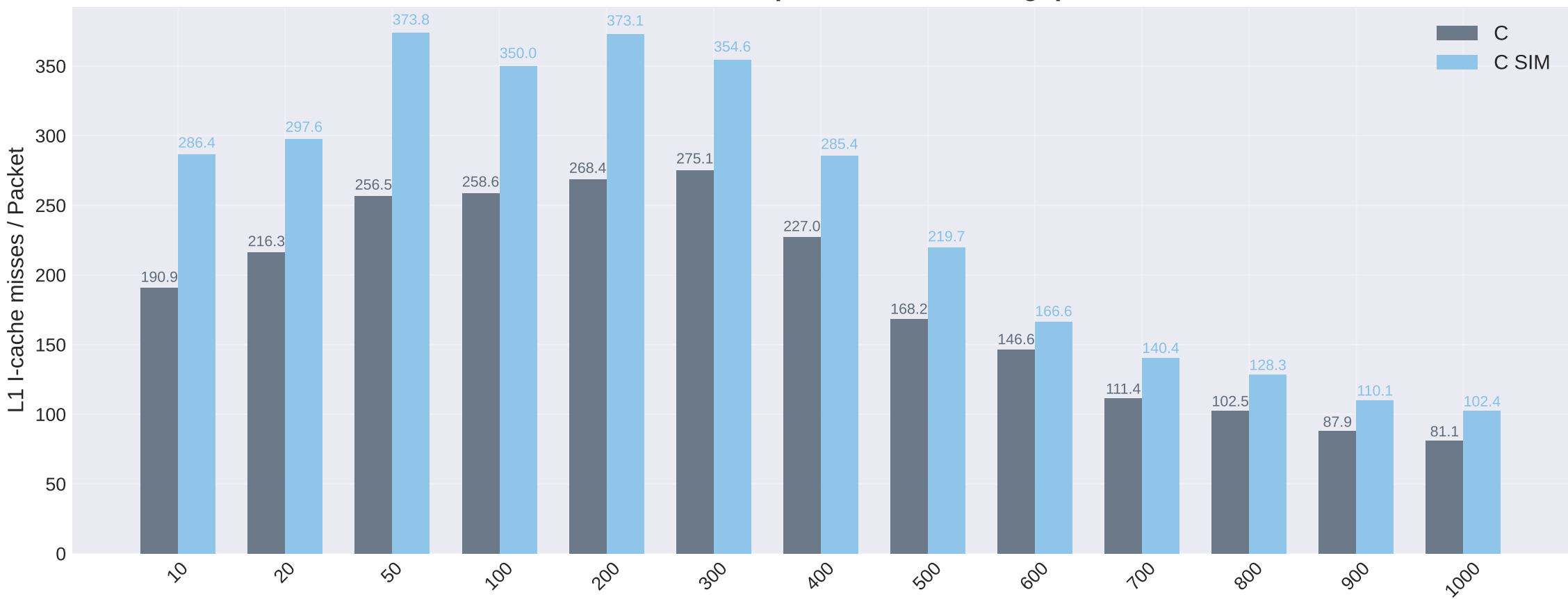
Branch Mispredictions (Total) vs Throughput



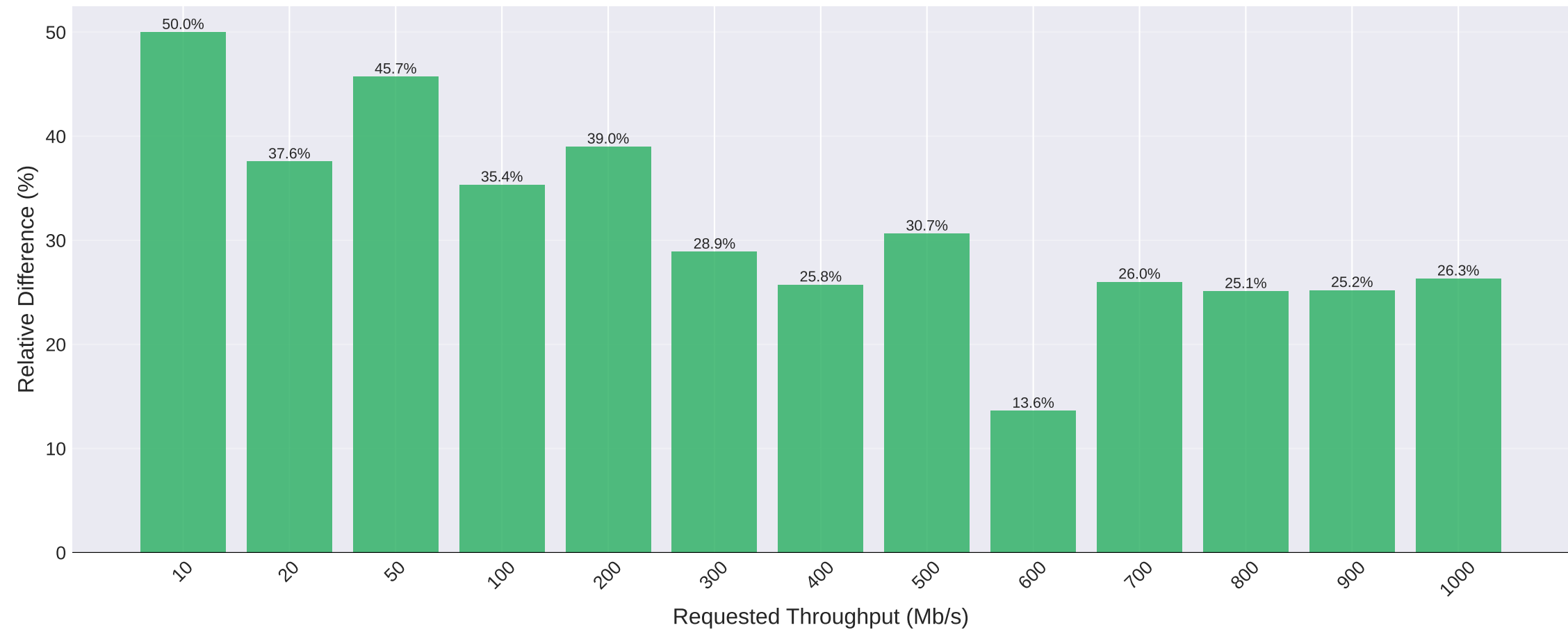
Relative Difference (C SIM vs C)



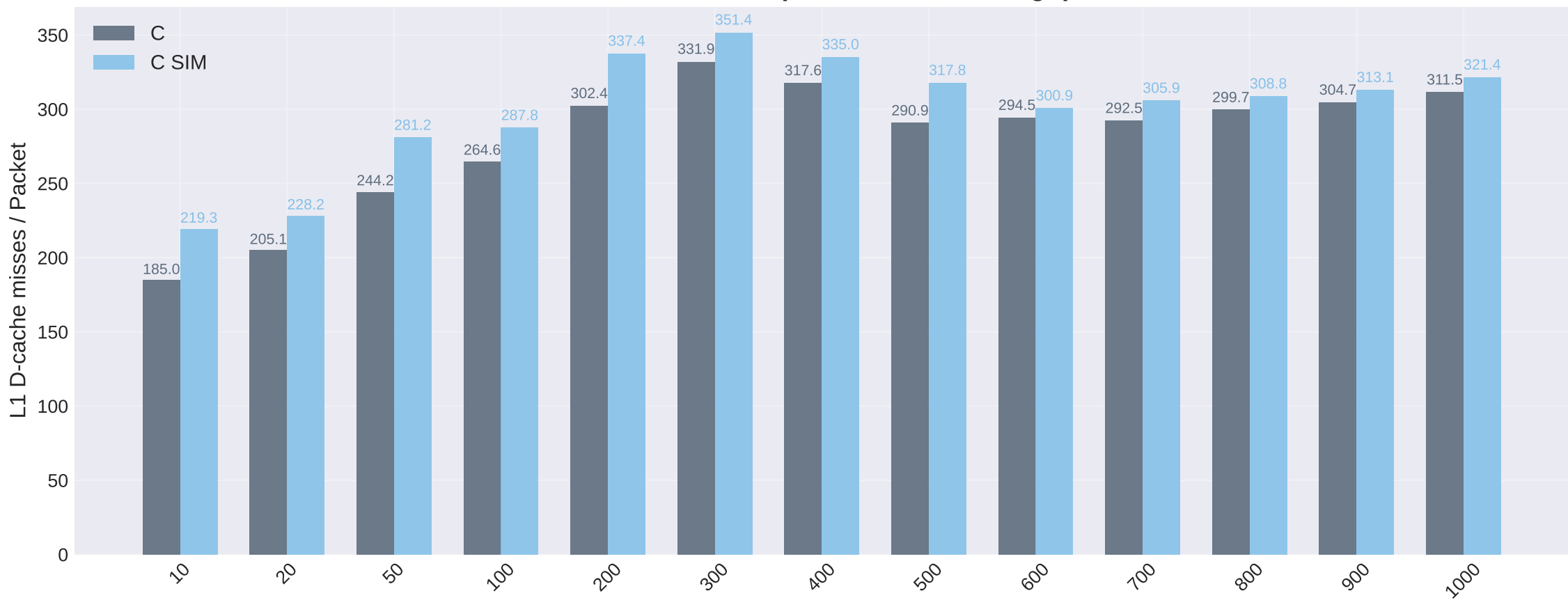
L1 I-cache Misses per Packet vs Throughput



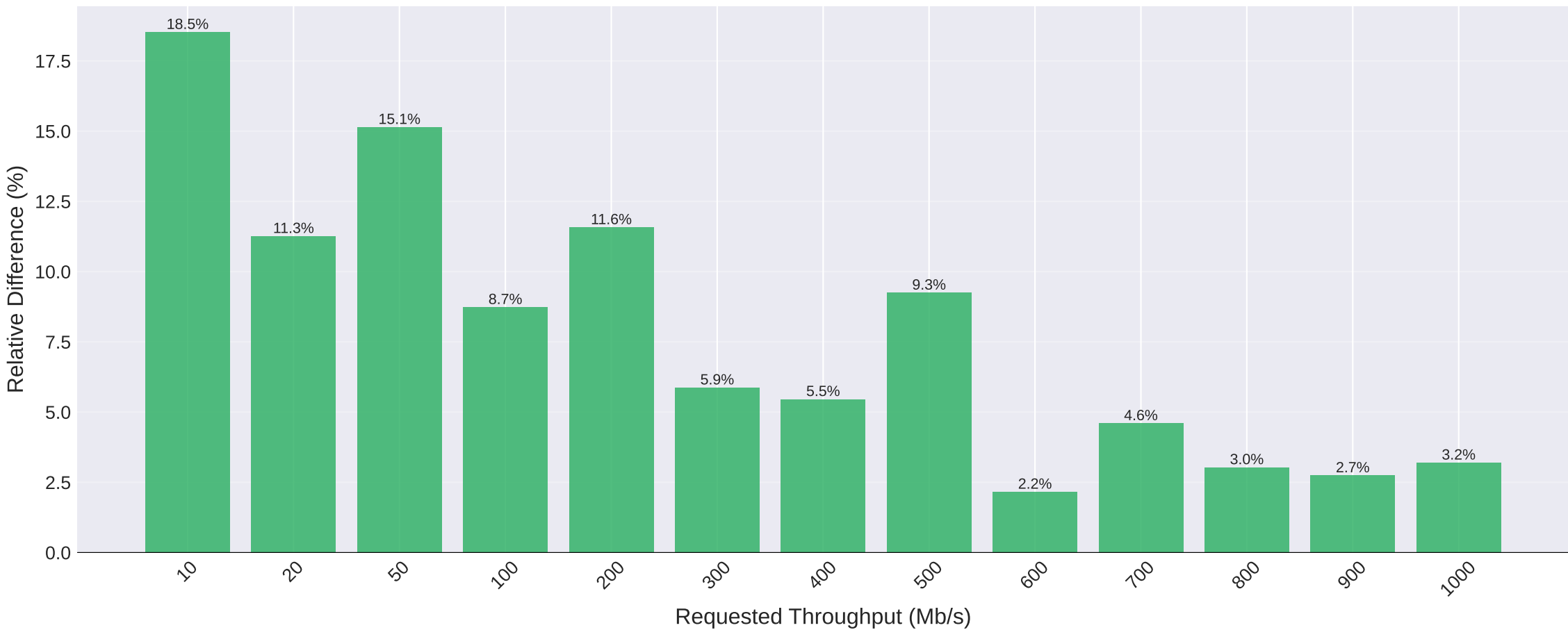
Relative Difference (C SIM vs C)



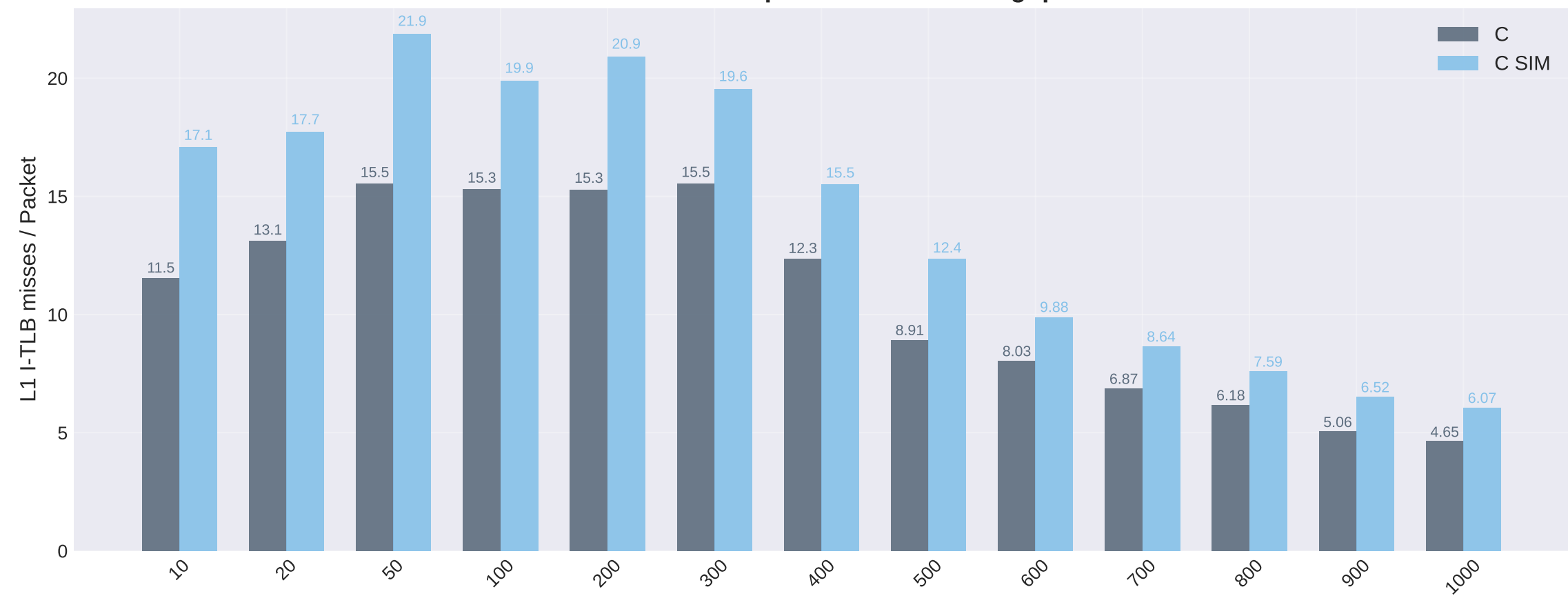
L1 D-cache Misses per Packet vs Throughput



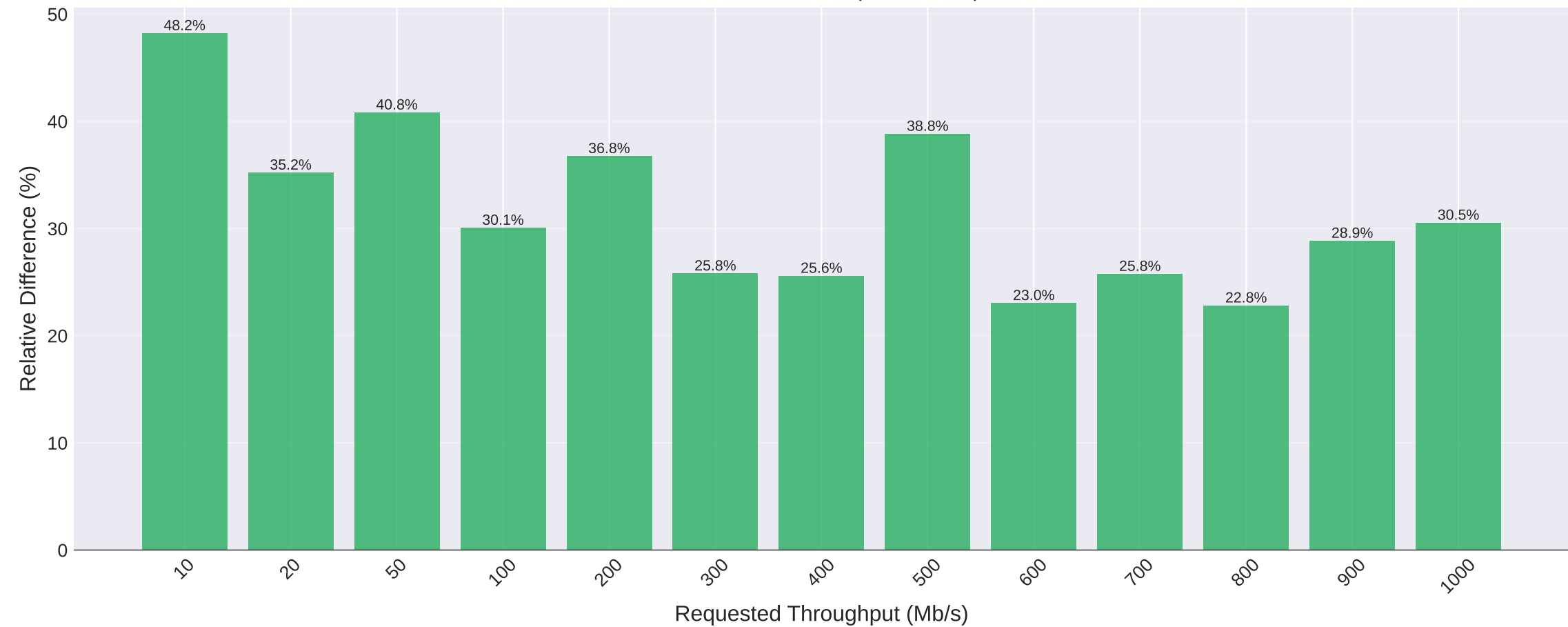
Relative Difference (C SIM vs C)



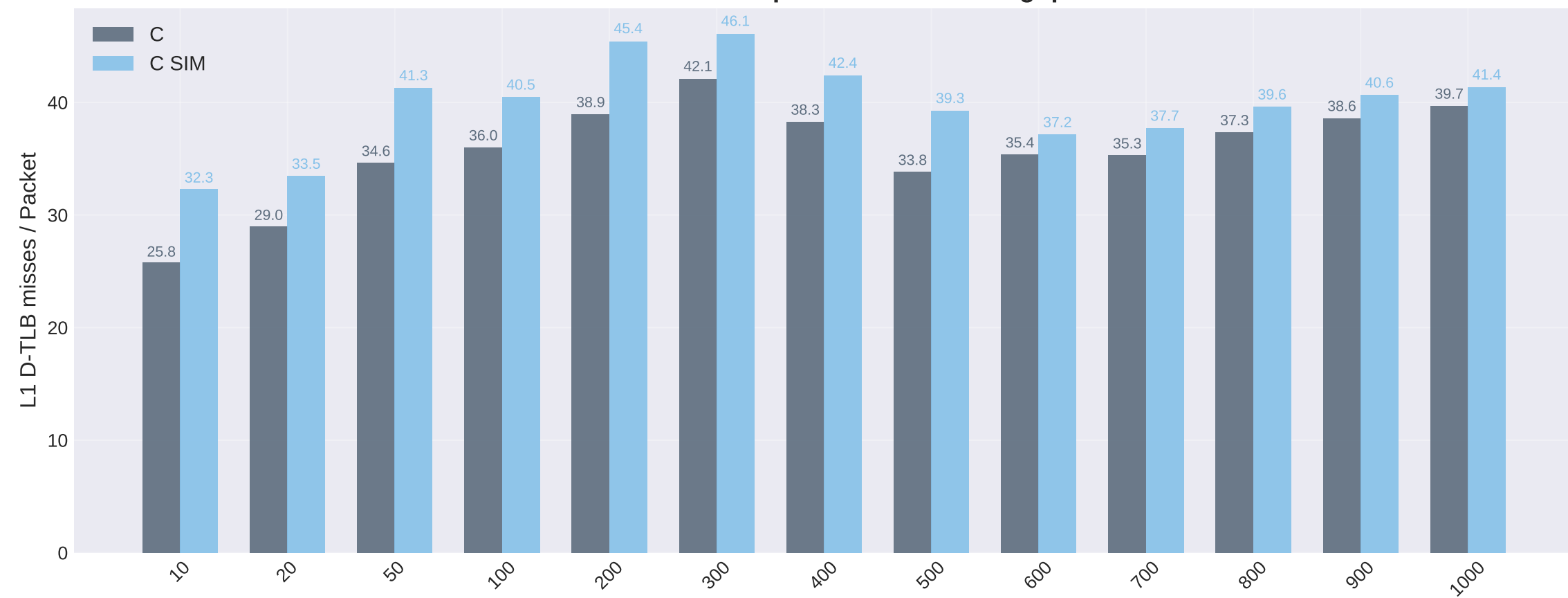
L1 I-TLB Misses per Packet vs Throughput



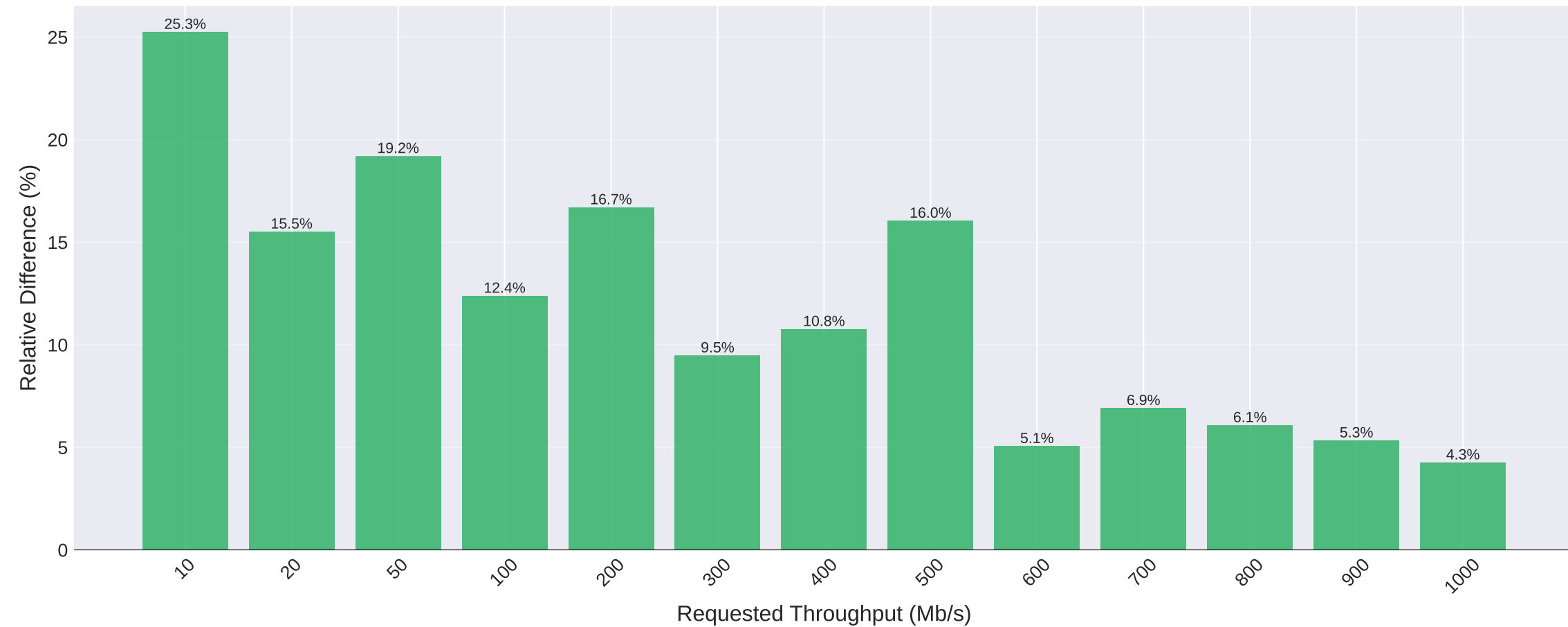
Relative Difference (C SIM vs C)



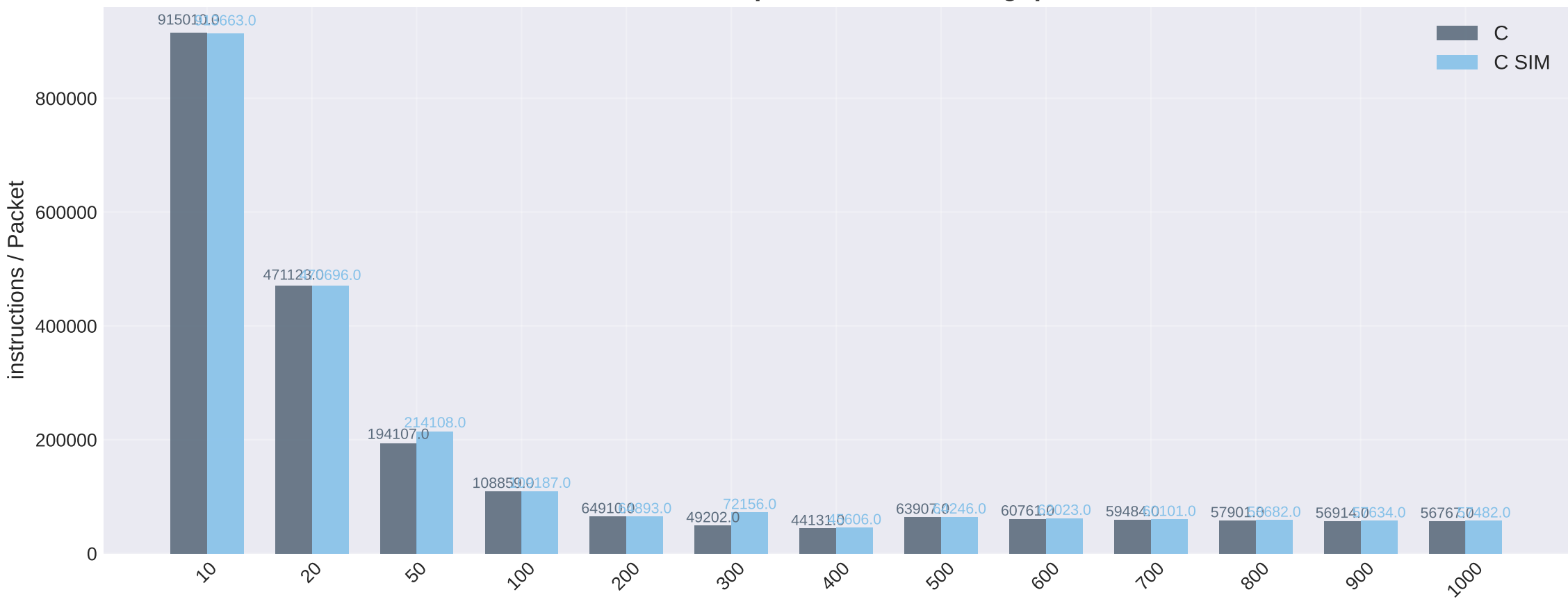
L1 D-TLB Misses per Packet vs Throughput



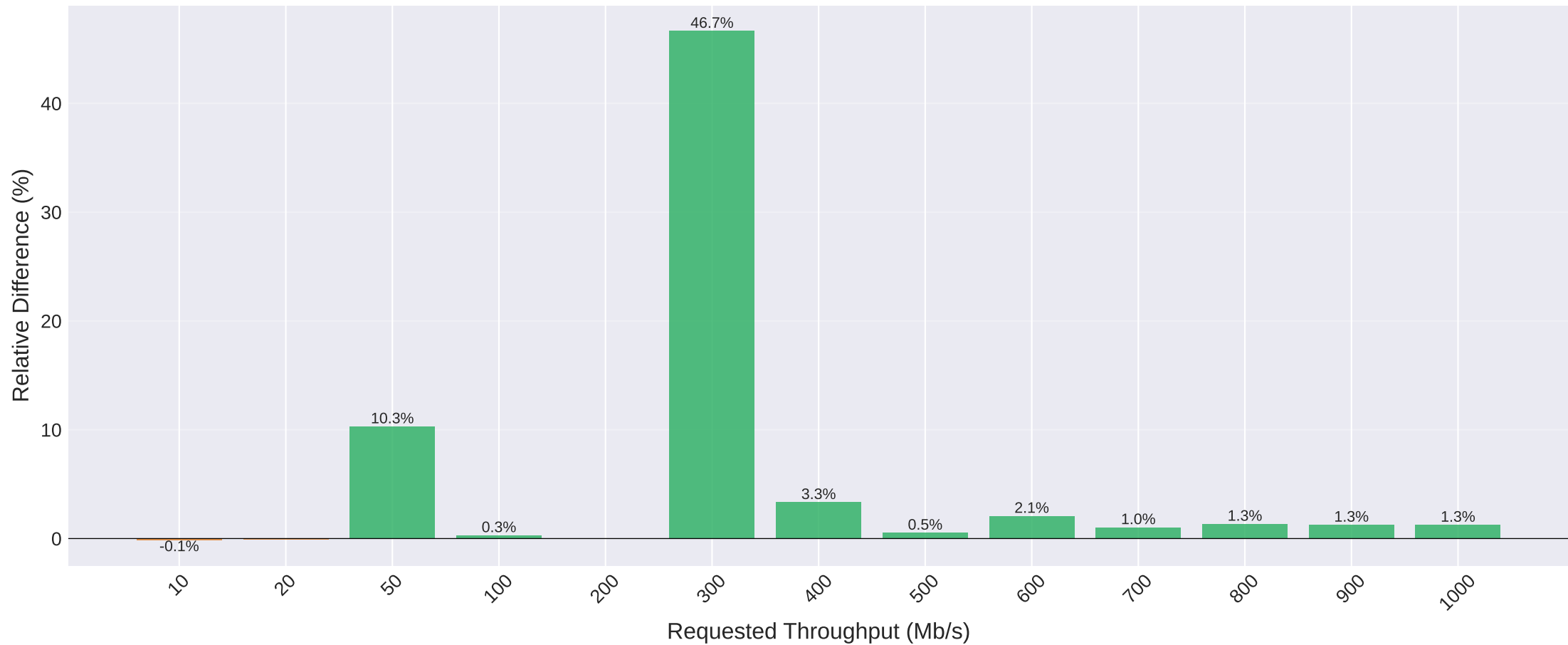
Relative Difference (C SIM vs C)



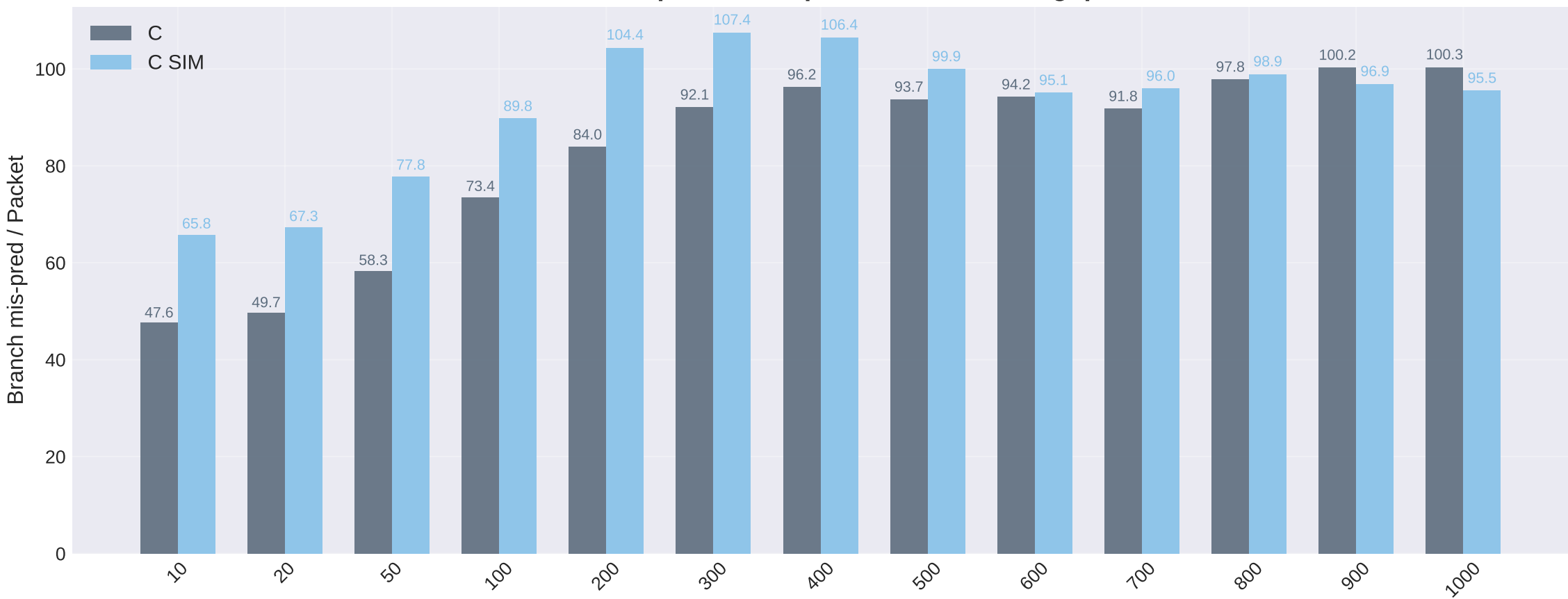
Instructions per Packet vs Throughput



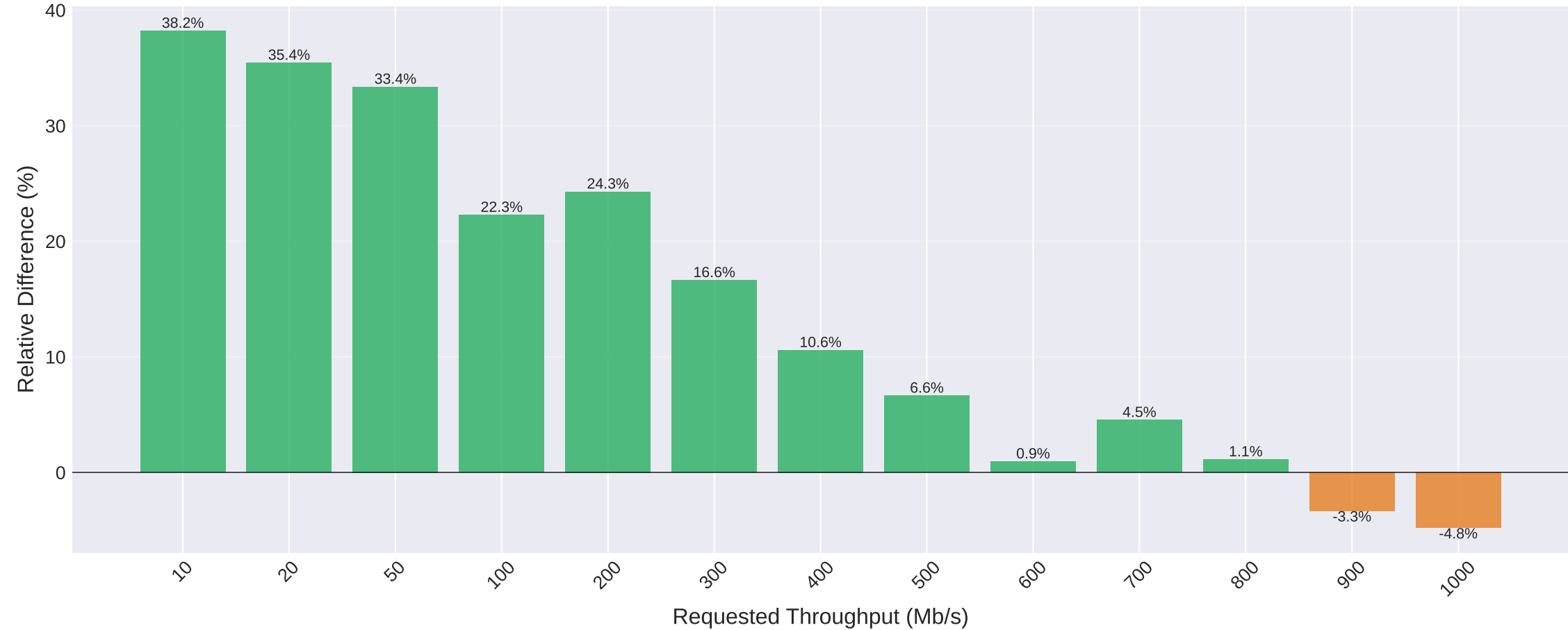
Relative Difference (C SIM vs C)



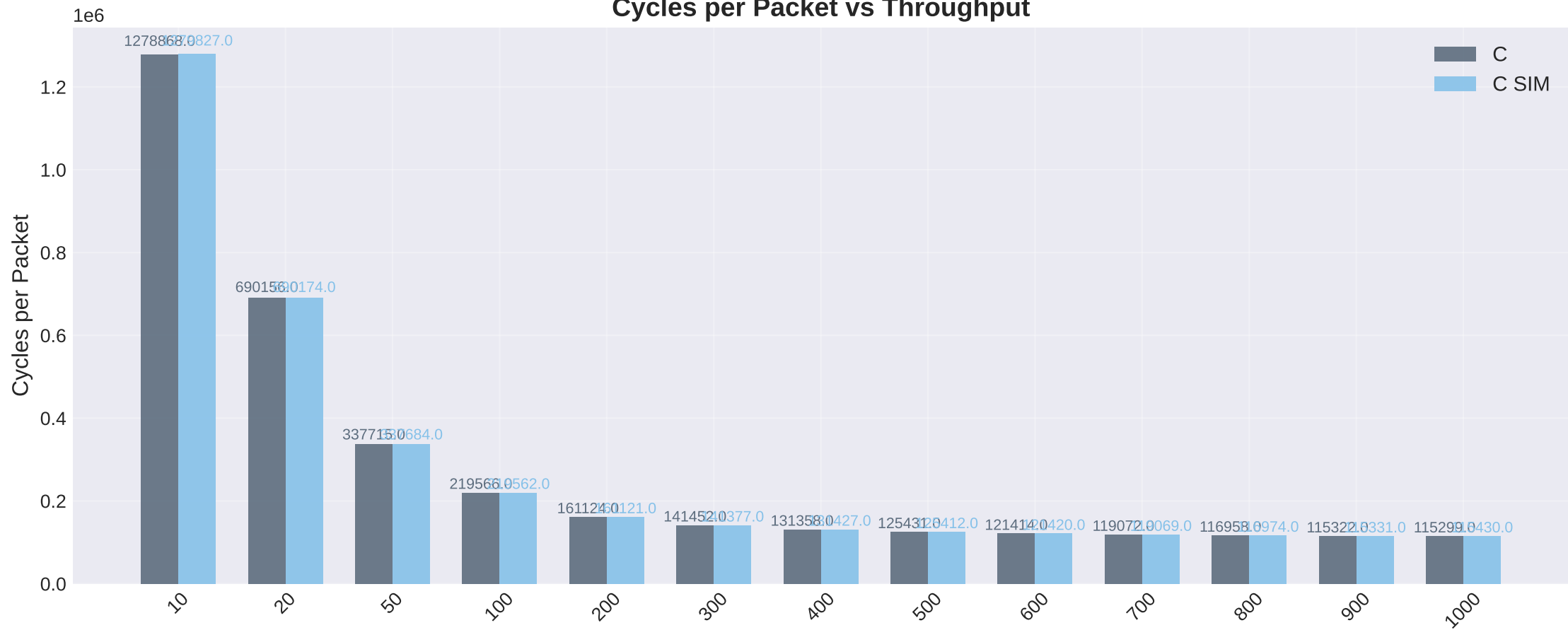
Branch Mispredictions per Packet vs Throughput



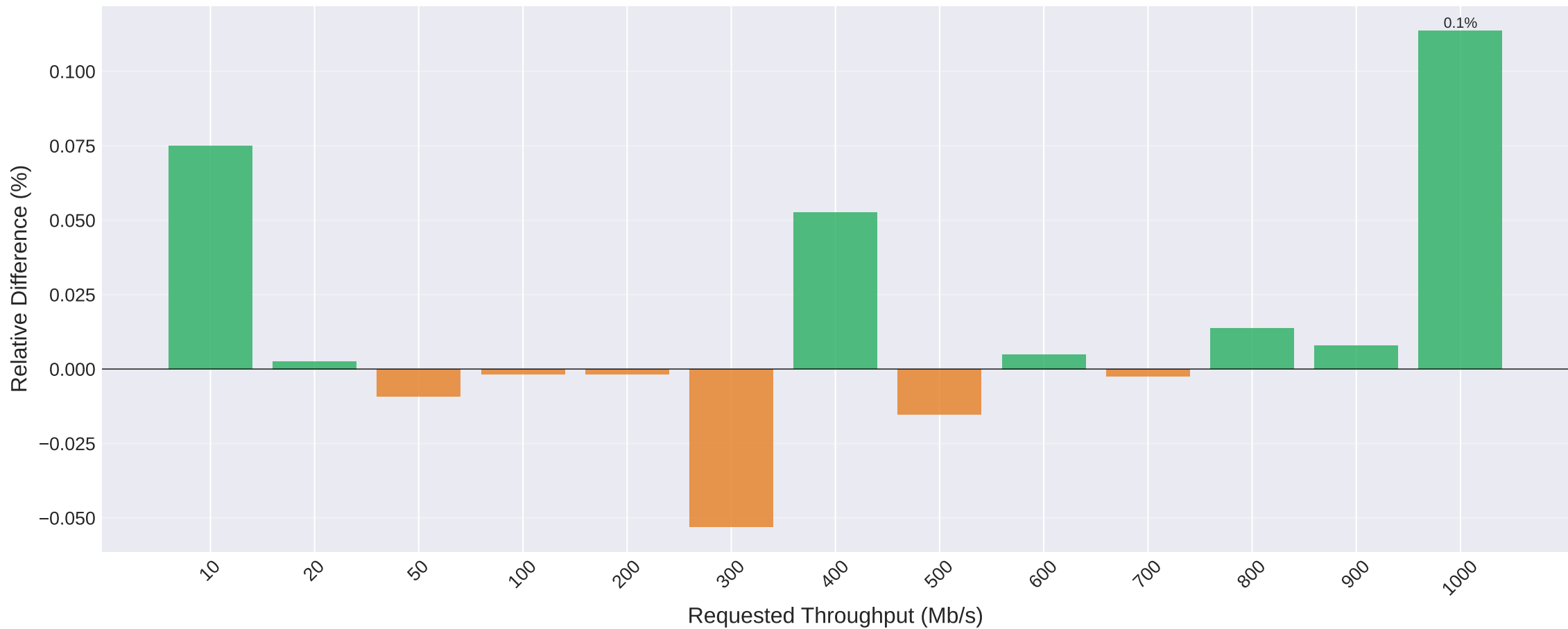
Relative Difference (C SIM vs C)



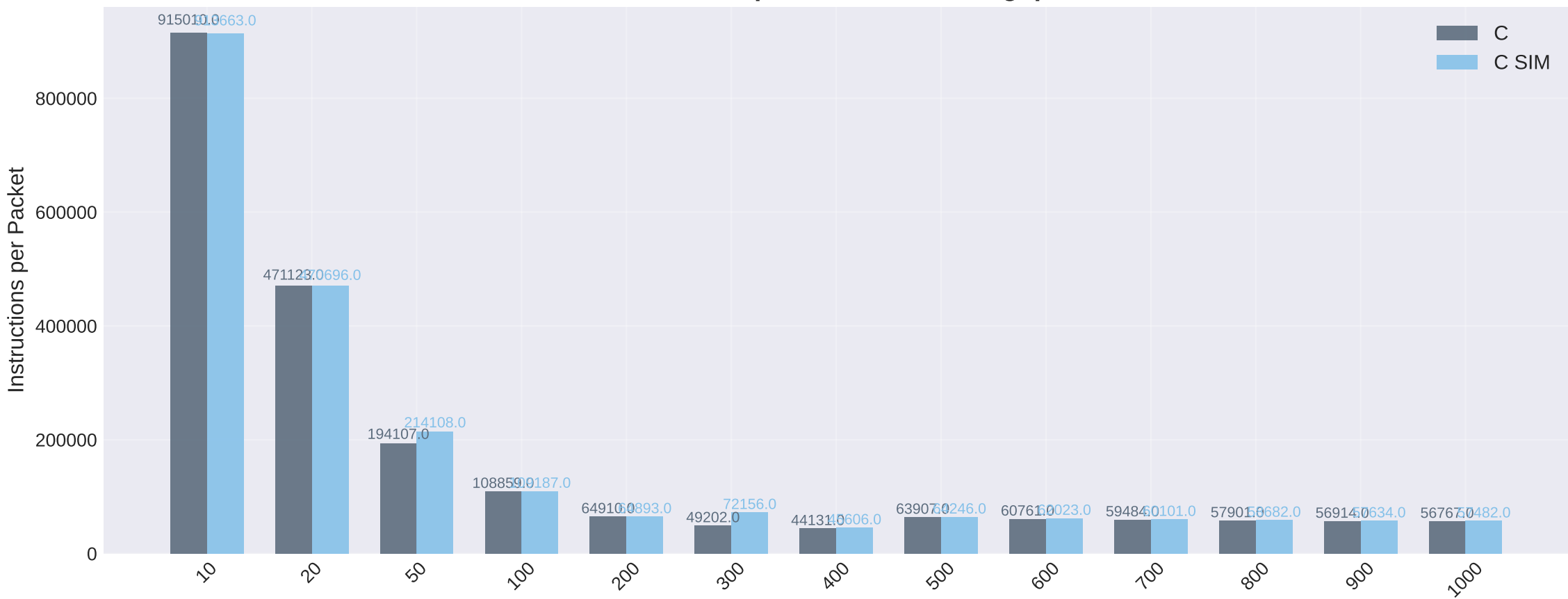
Cycles per Packet vs Throughput



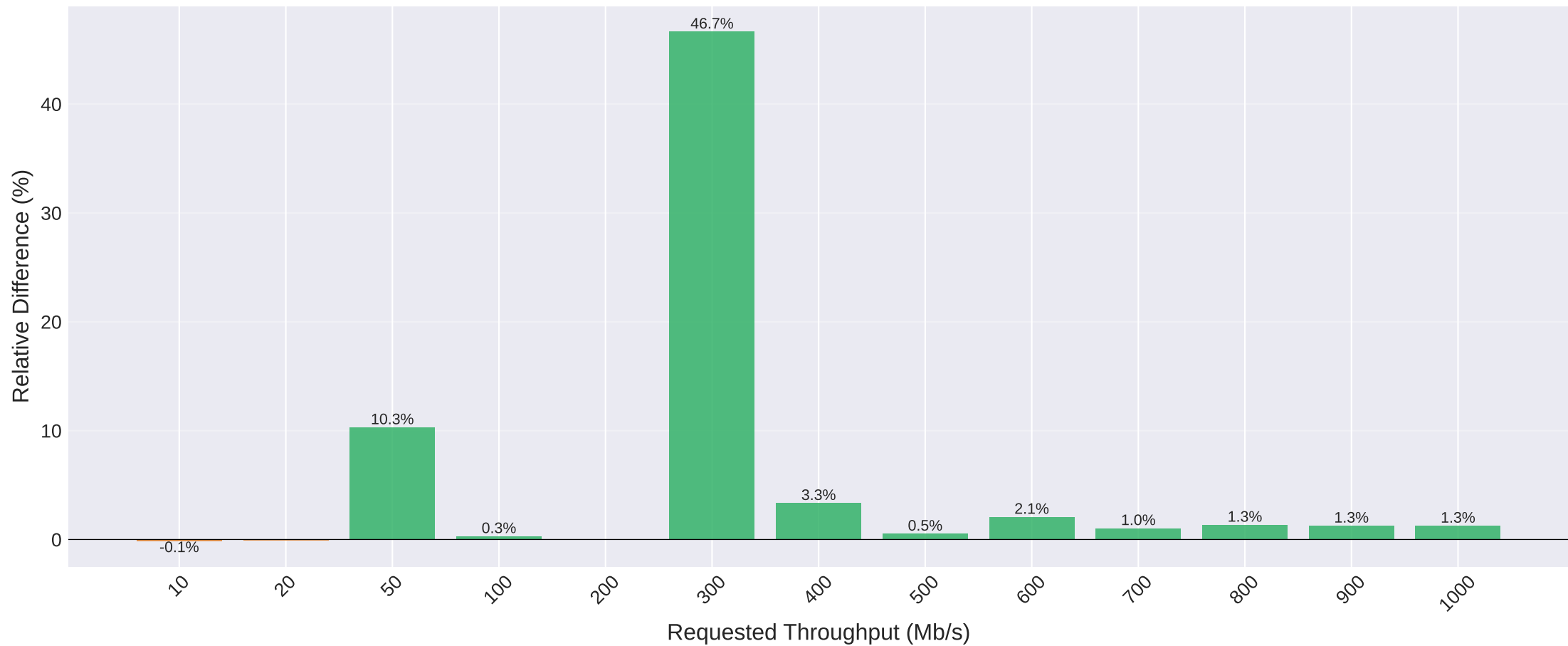
Relative Difference (C SIM vs C)



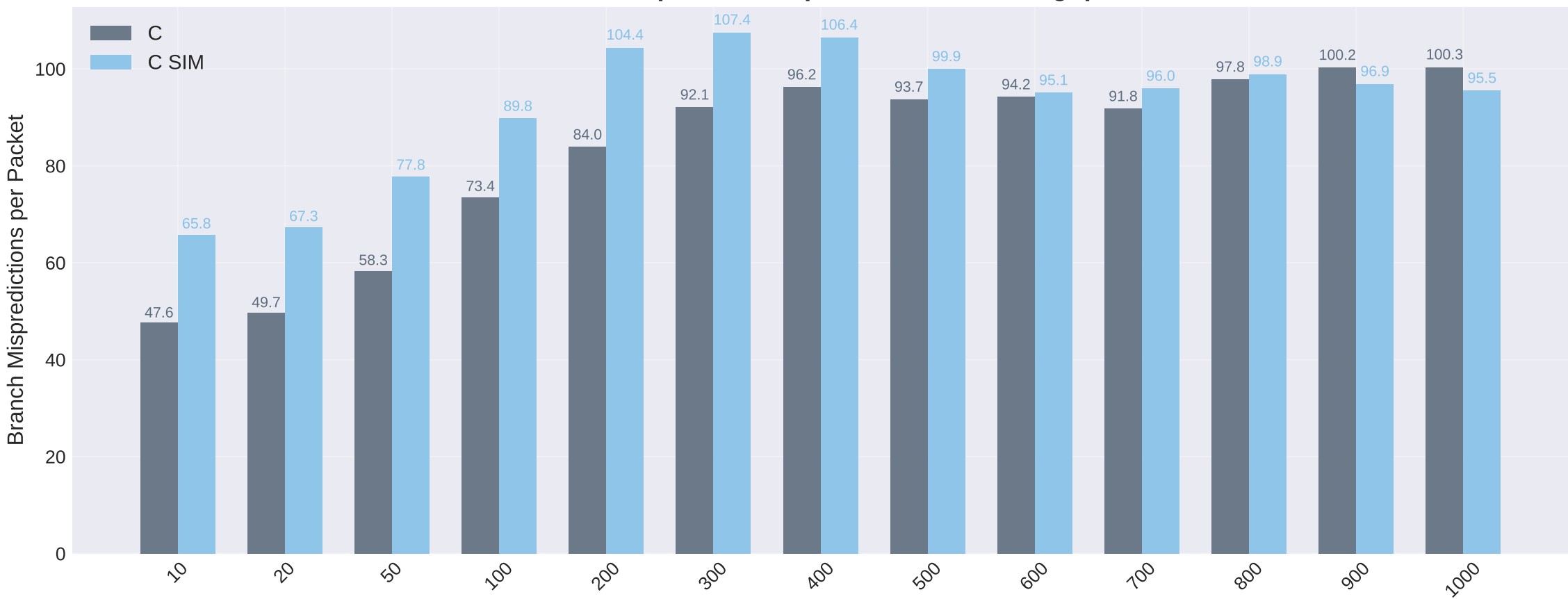
Instructions per Packet vs Throughput



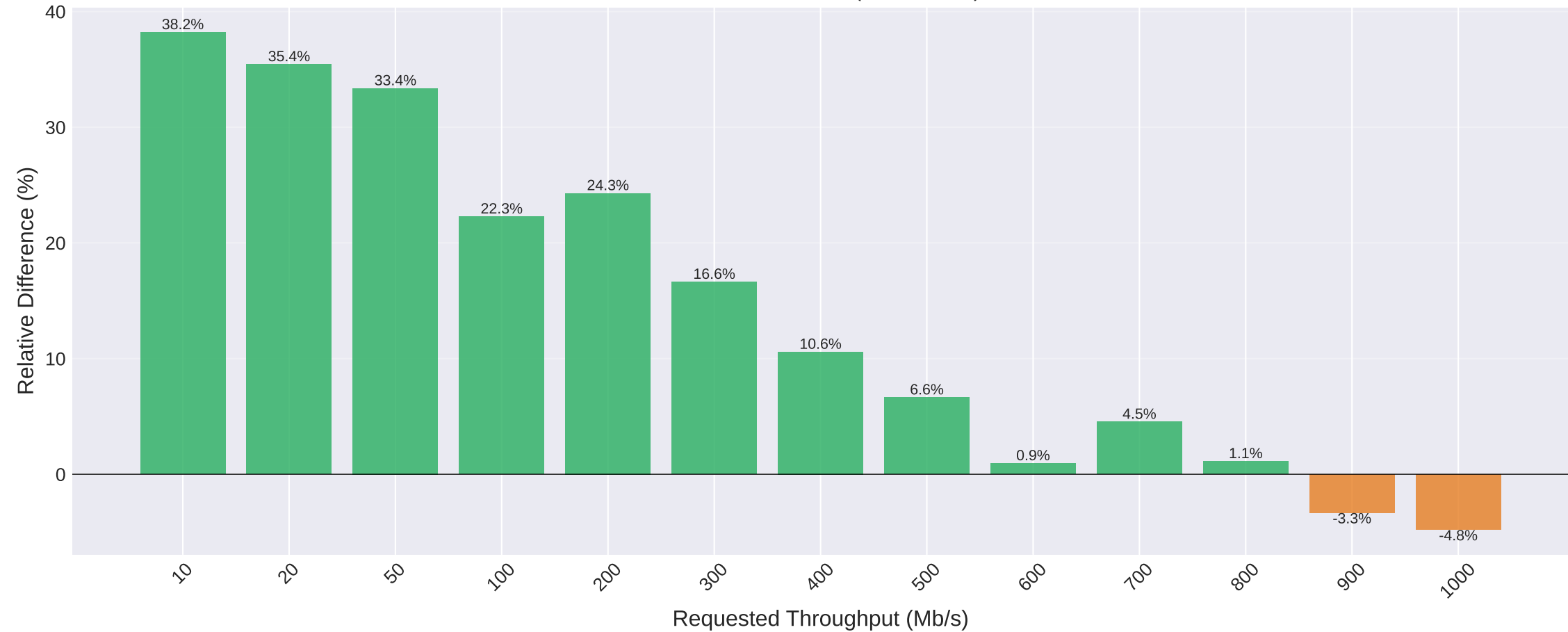
Relative Difference (C SIM vs C)



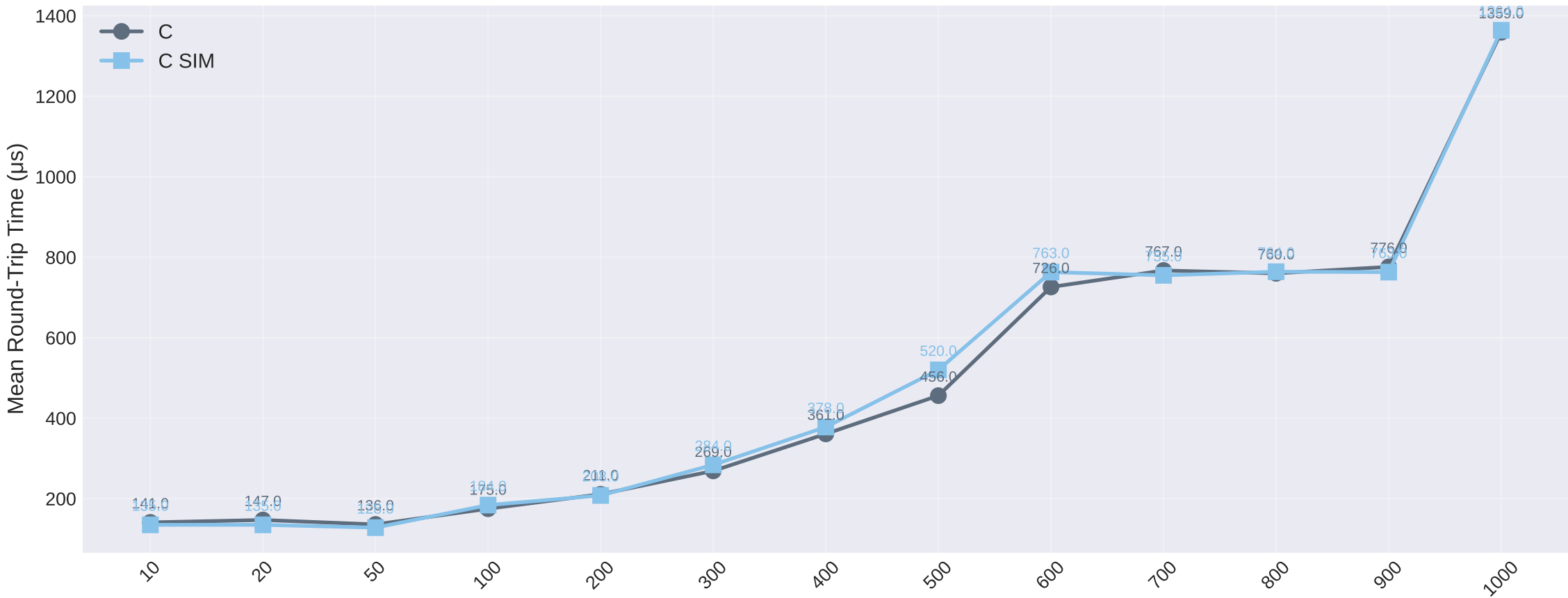
Branch Mispredictions per Packet vs Throughput



Relative Difference (C SIM vs C)



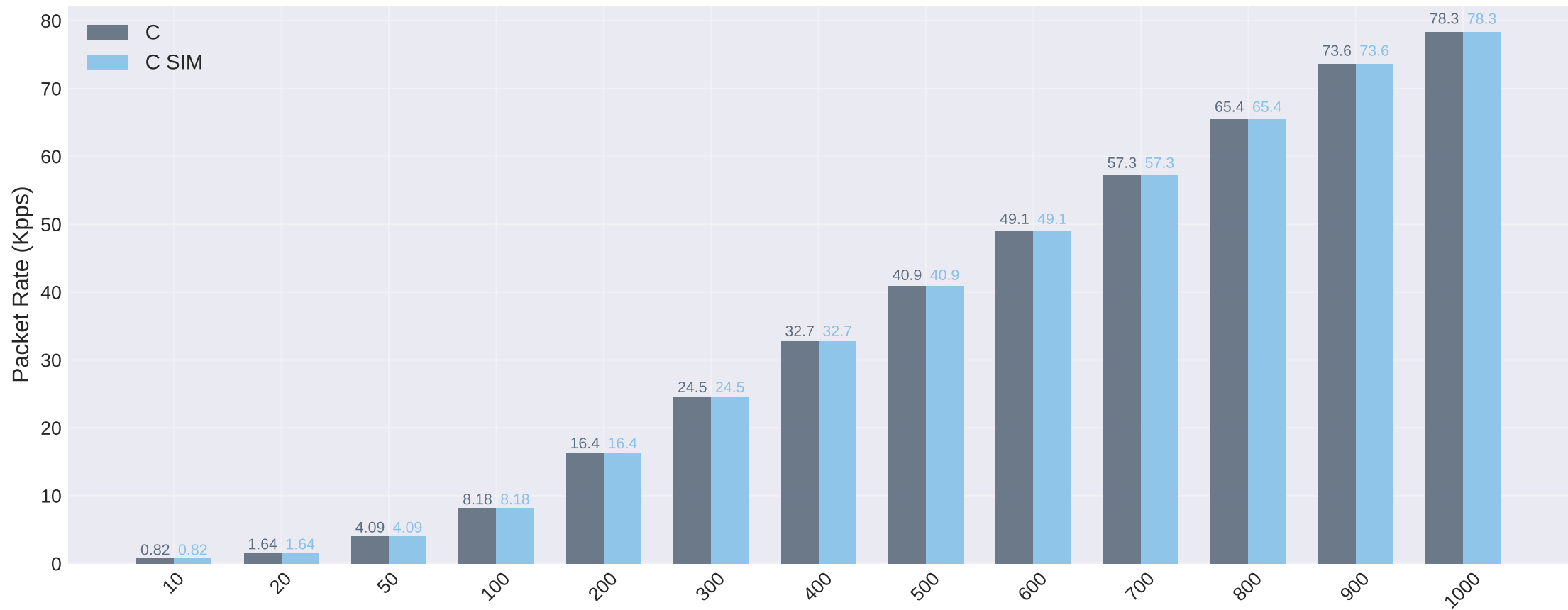
Mean Round-Trip Time (μ s) vs Throughput



Relative Difference (C SIM vs C)



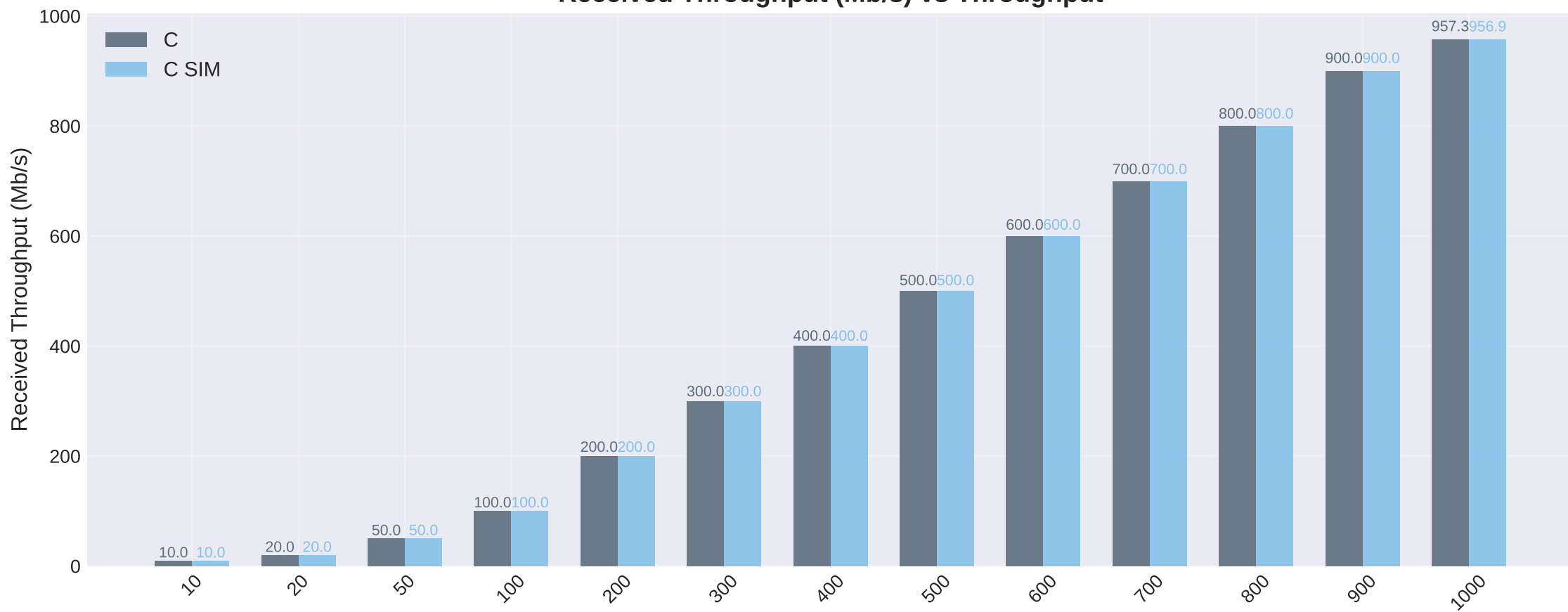
Packet Rate (packets/s) vs Throughput



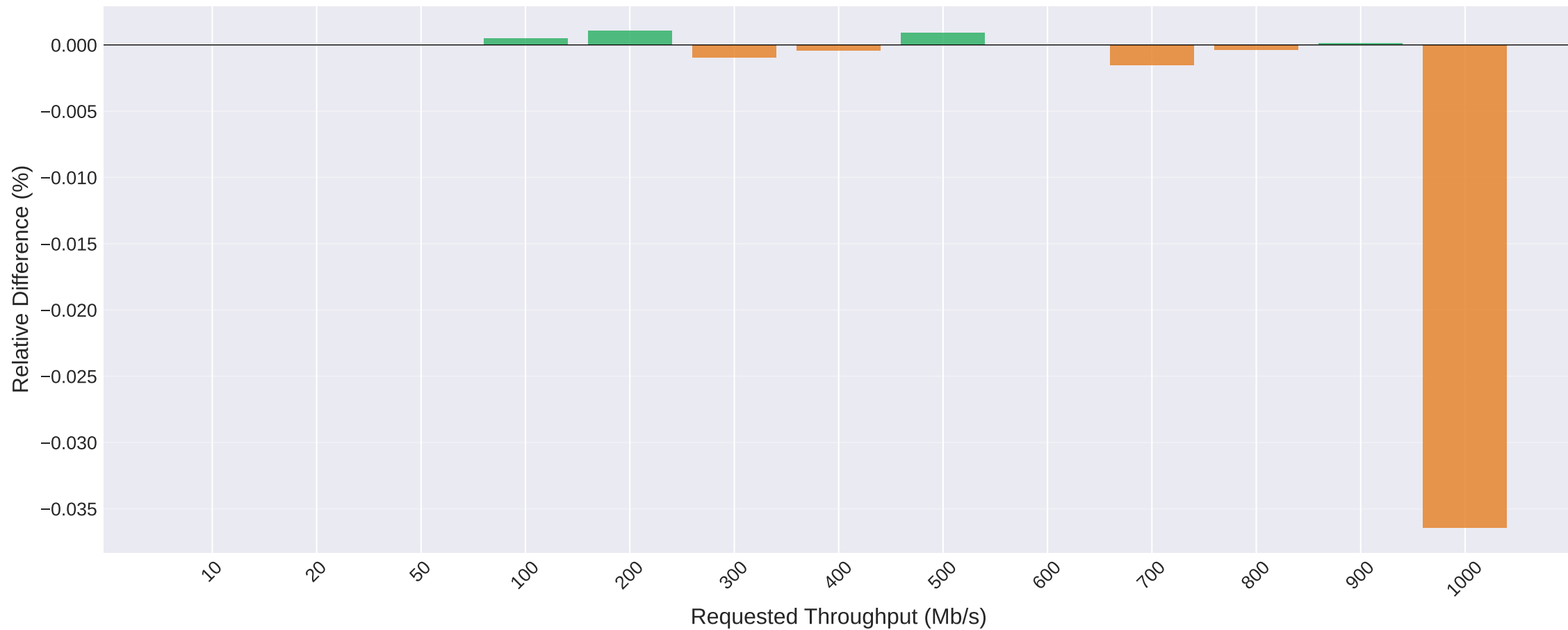
Relative Difference (C SIM vs C)



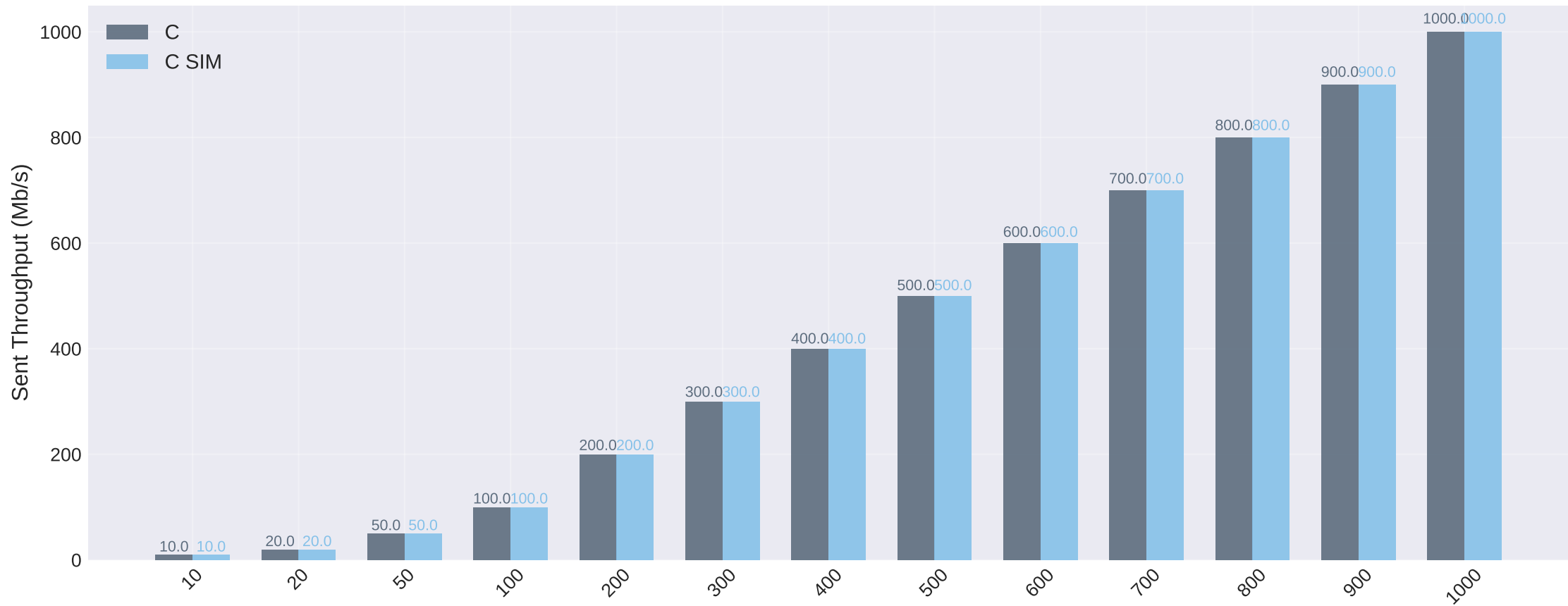
Received Throughput (Mb/s) vs Throughput



Relative Difference (C SIM vs C)



Sent Throughput (Mb/s) vs Throughput



Relative Difference (C SIM vs C)

