

Requested Throughput (Mb/s)

NO

-35.0%

-35

SO

Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.7957.0 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.7% 61.5% 600.0600.0 600 CPU Utilization (%) 58.2% 54.5% 54.2% 500.0500.0 46:3% 42.6% 400 300.0300.0 200.0200.0 200 20 100.0100.0

Requested Throughput (Mb/s)

NOO

400

600

100

900

200

700

go

900

2000

50.0 50.0

50

2.9%

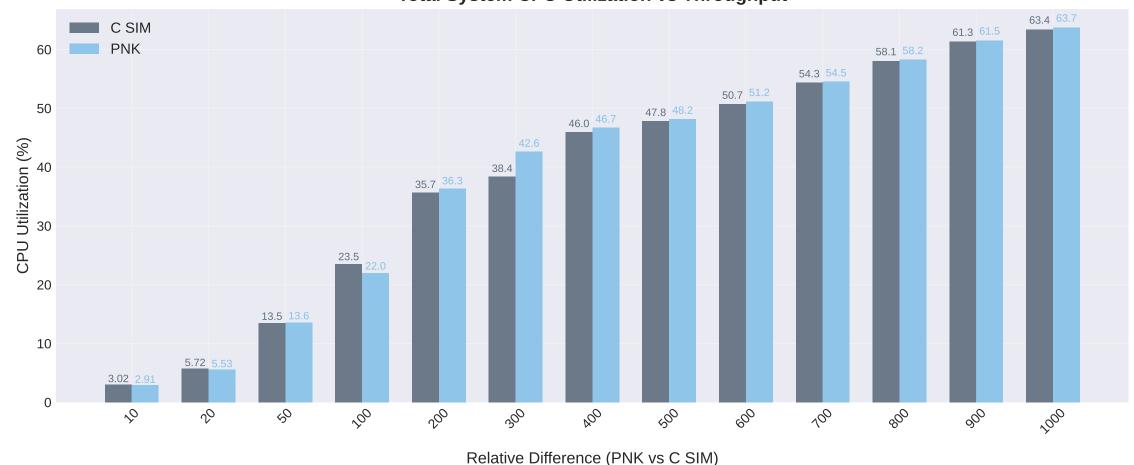
30

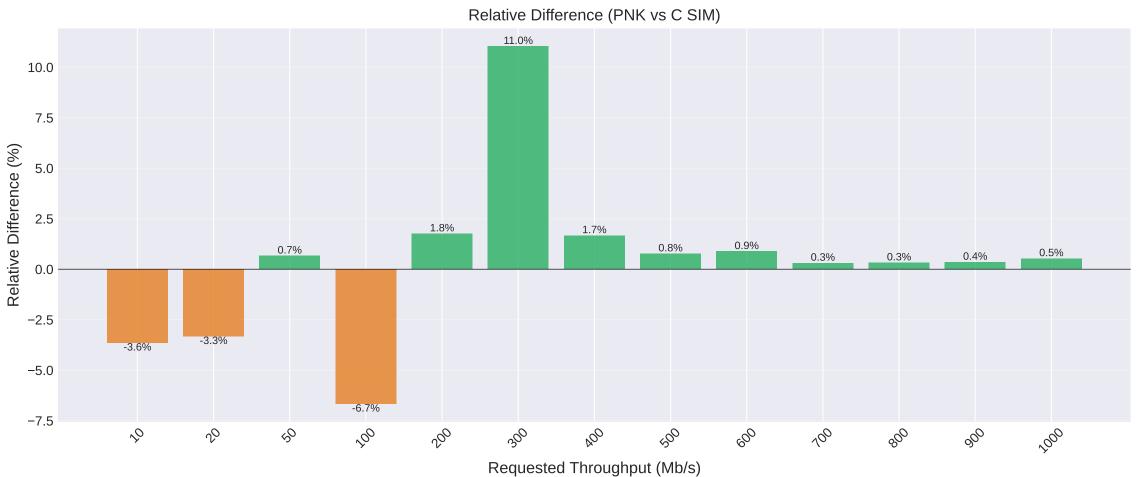
0

20.0 20

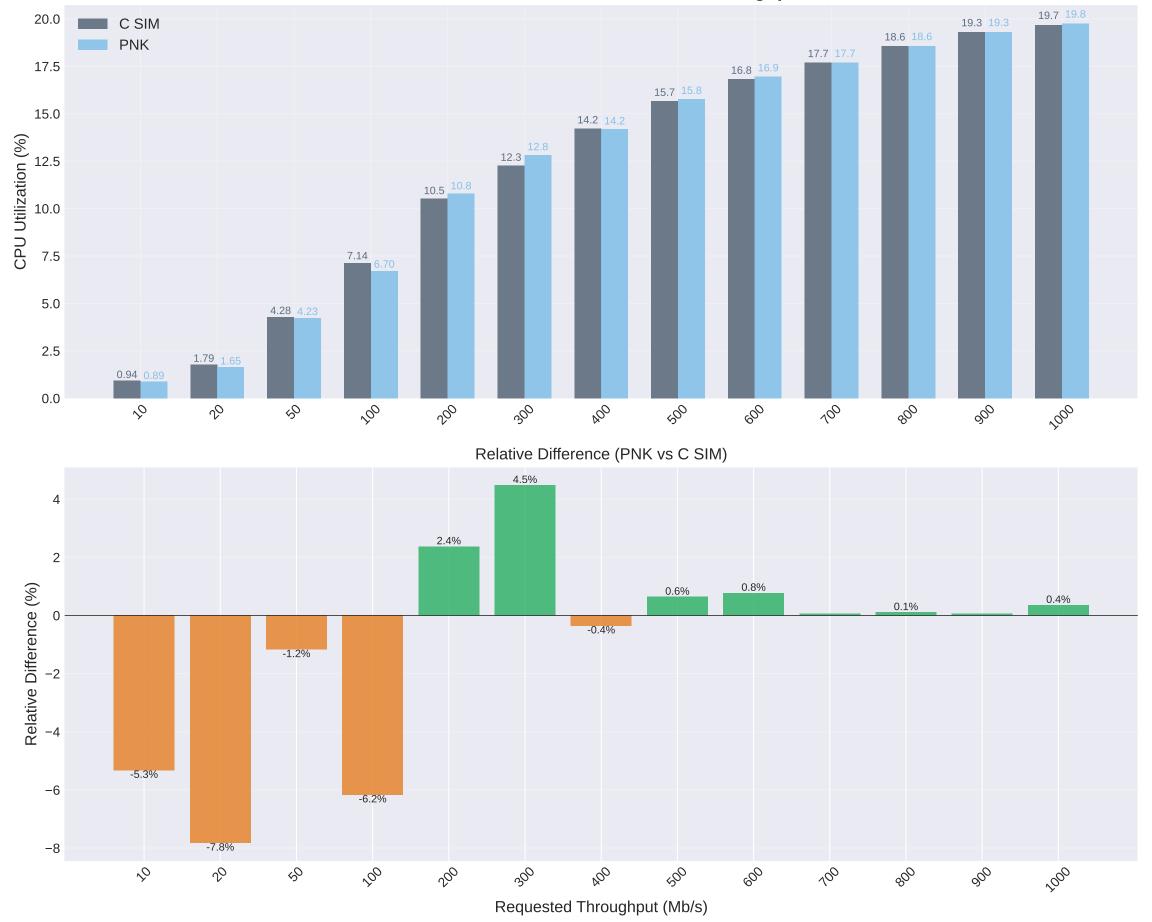
20

Total System CPU Utilization vs Throughput

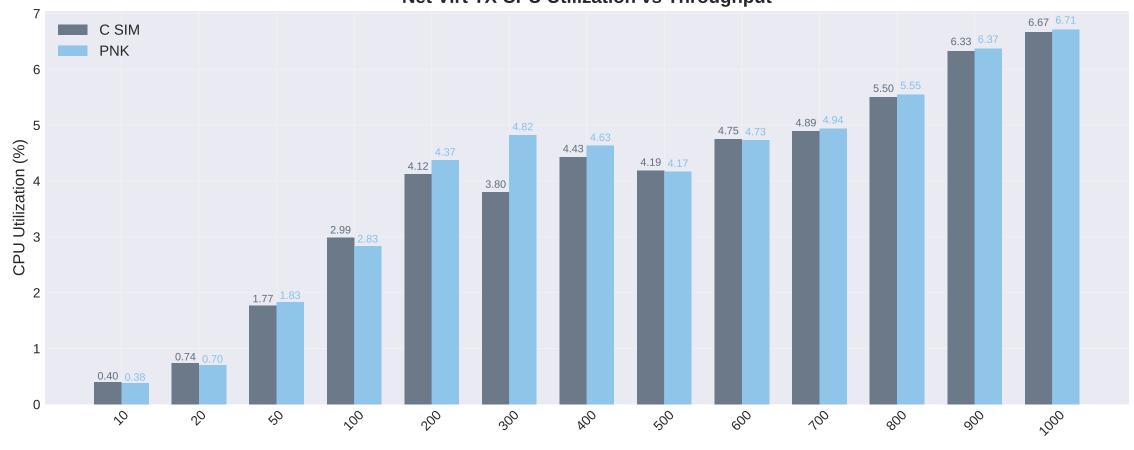


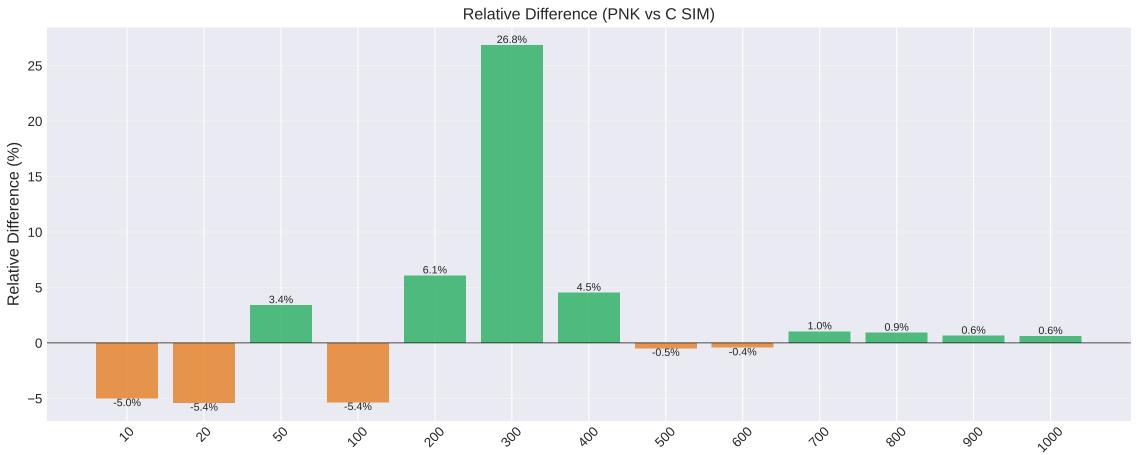


Ethernet Driver CPU Utilization vs Throughput

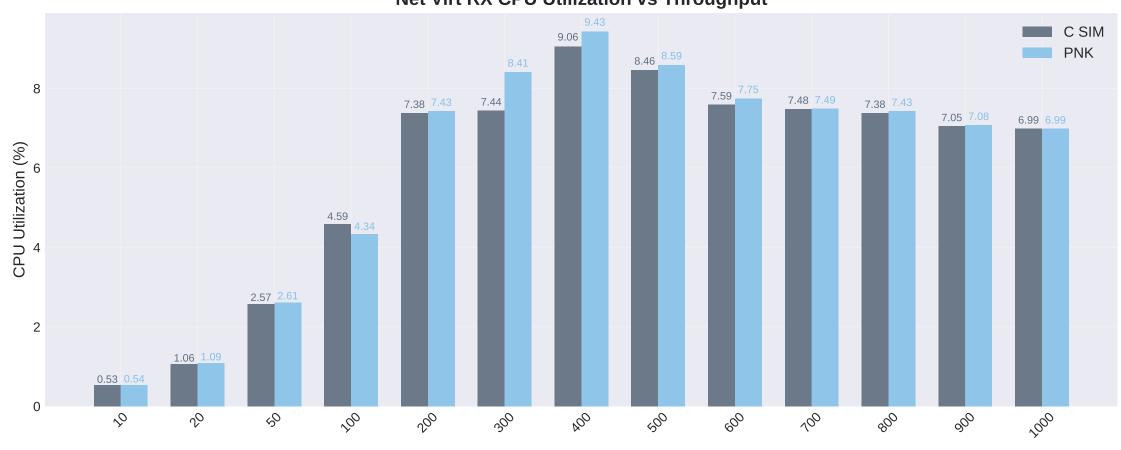


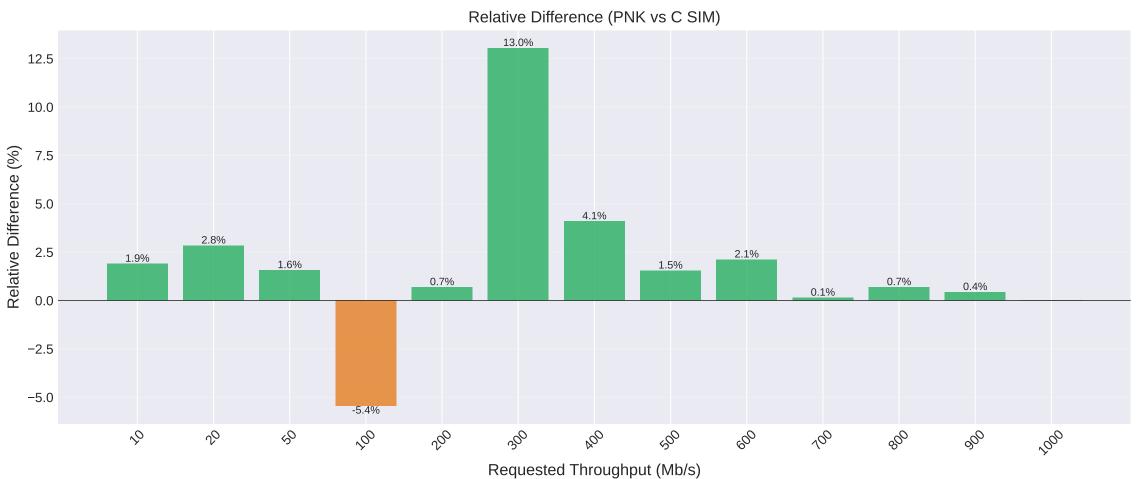




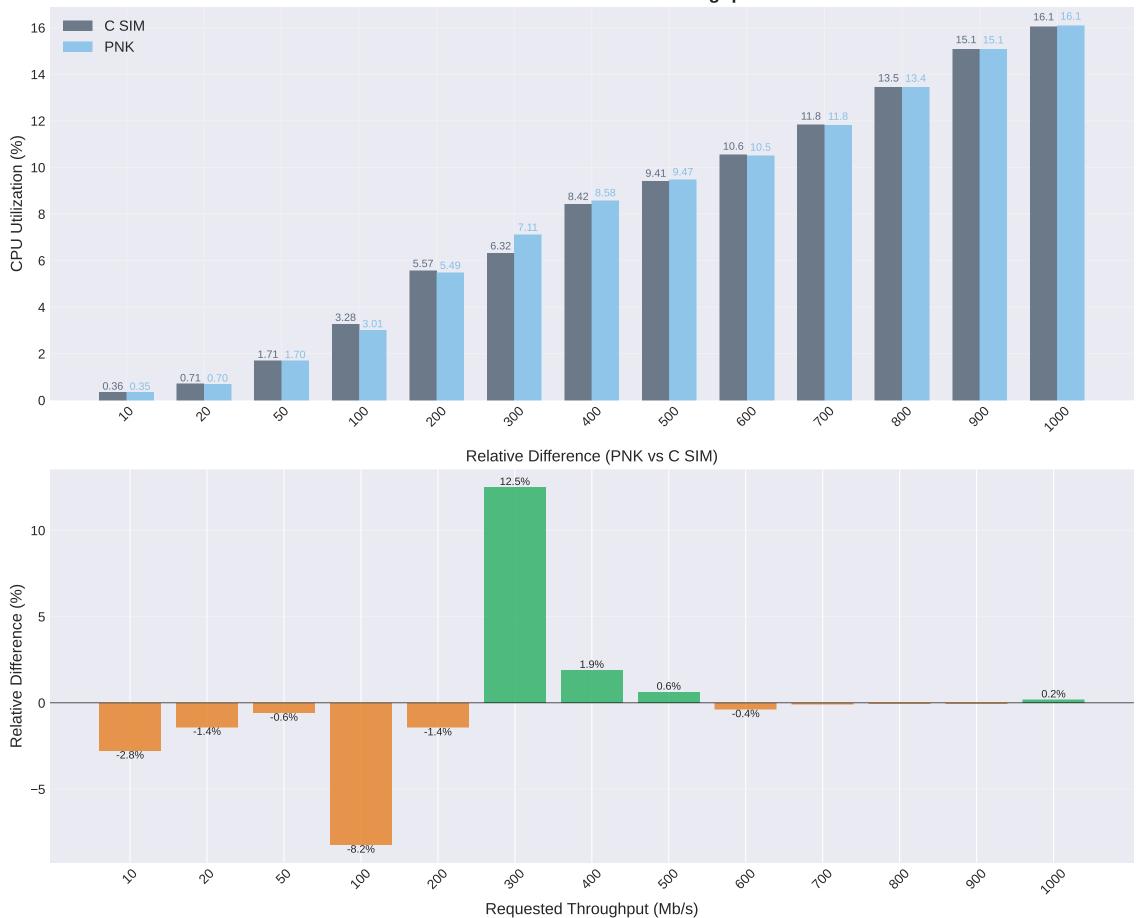


Net Virt RX CPU Utilization vs Throughput

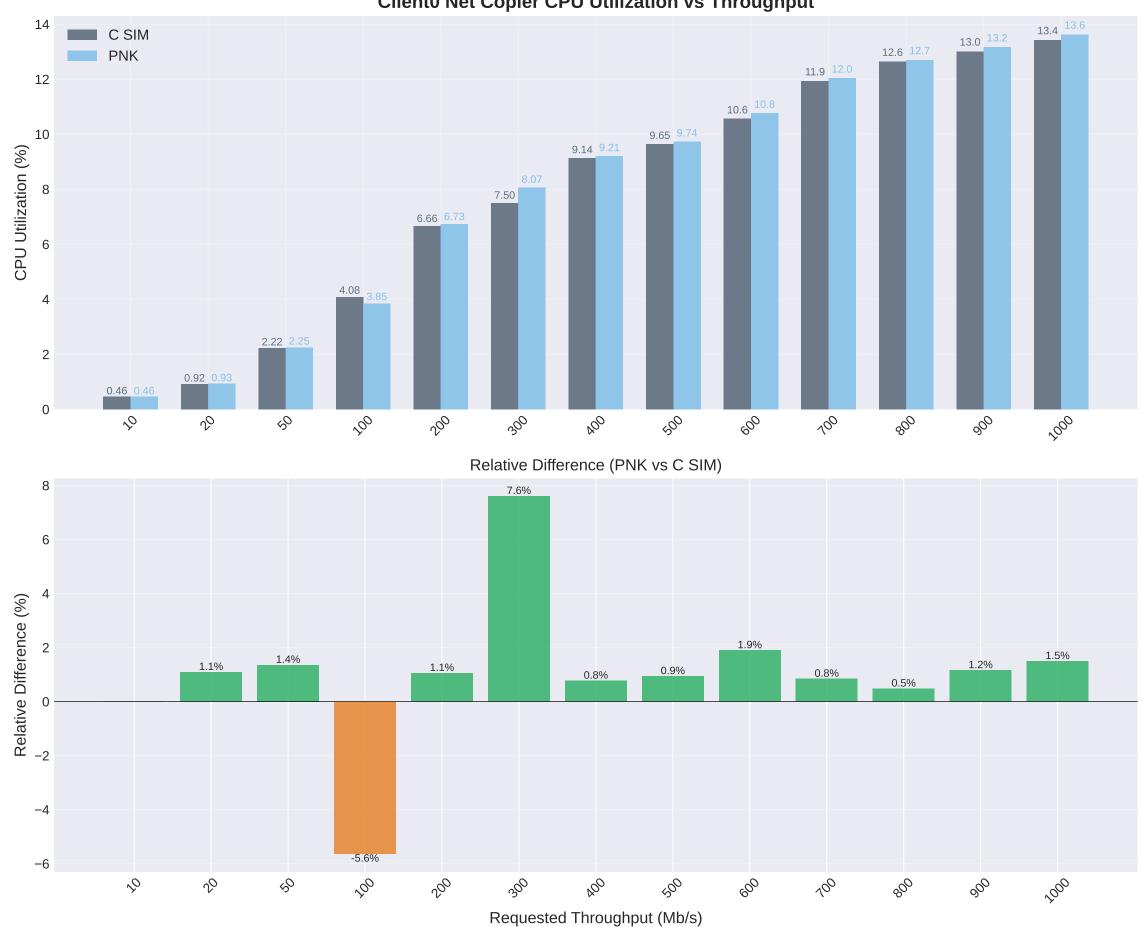




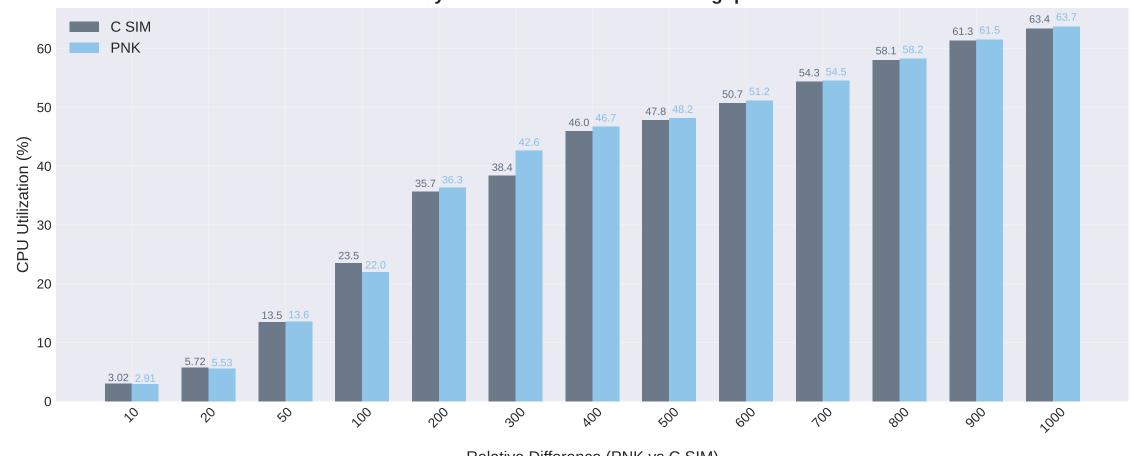
Client0 CPU Utilization vs Throughput

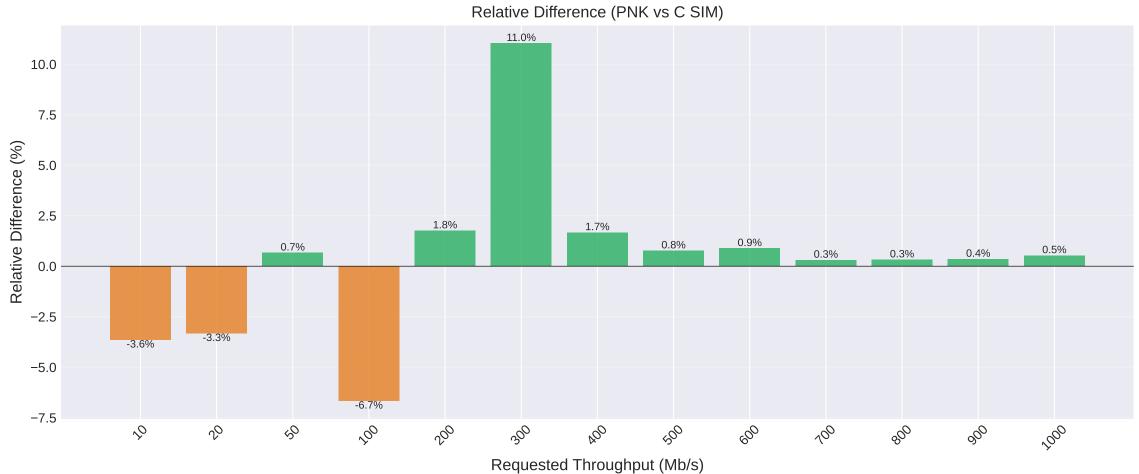


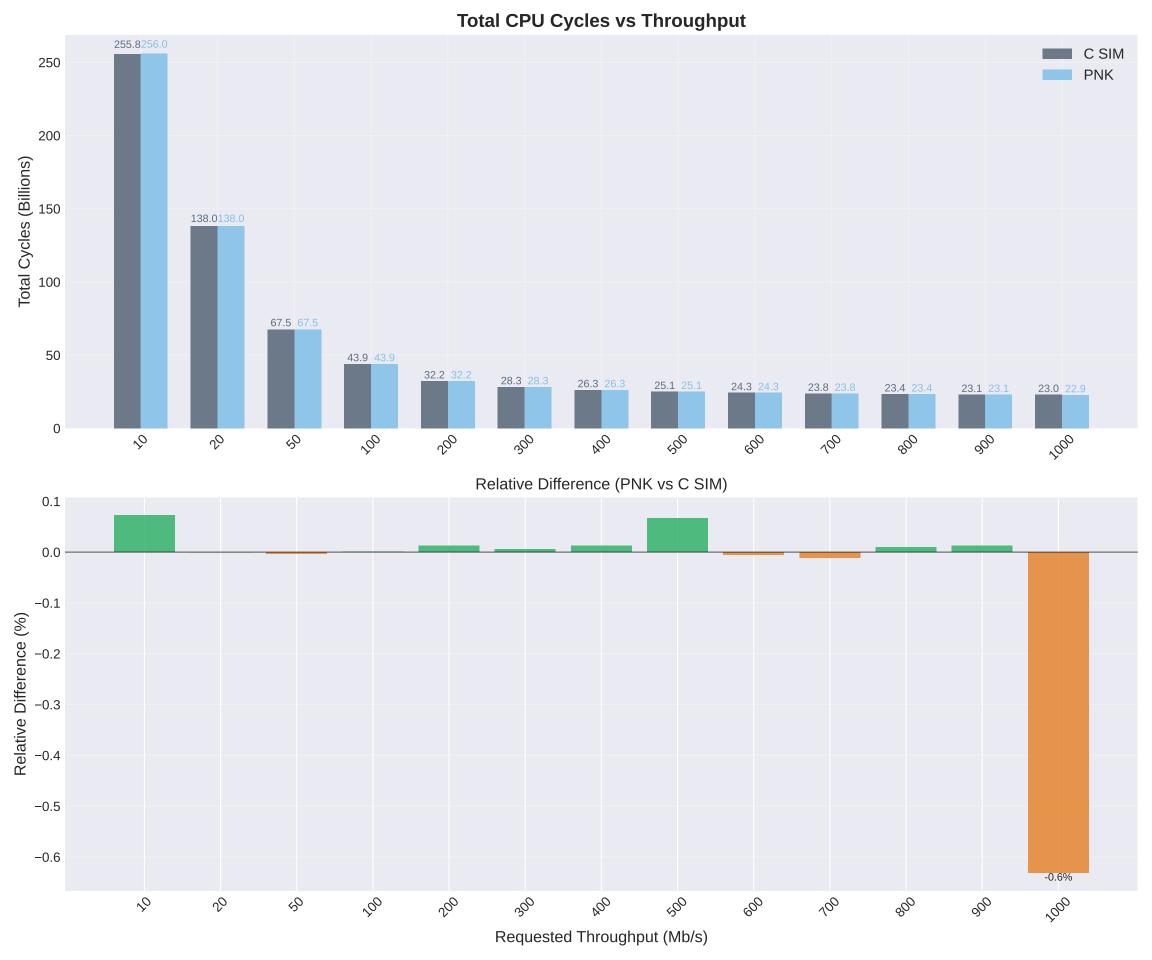
Client0 Net Copier CPU Utilization vs Throughput



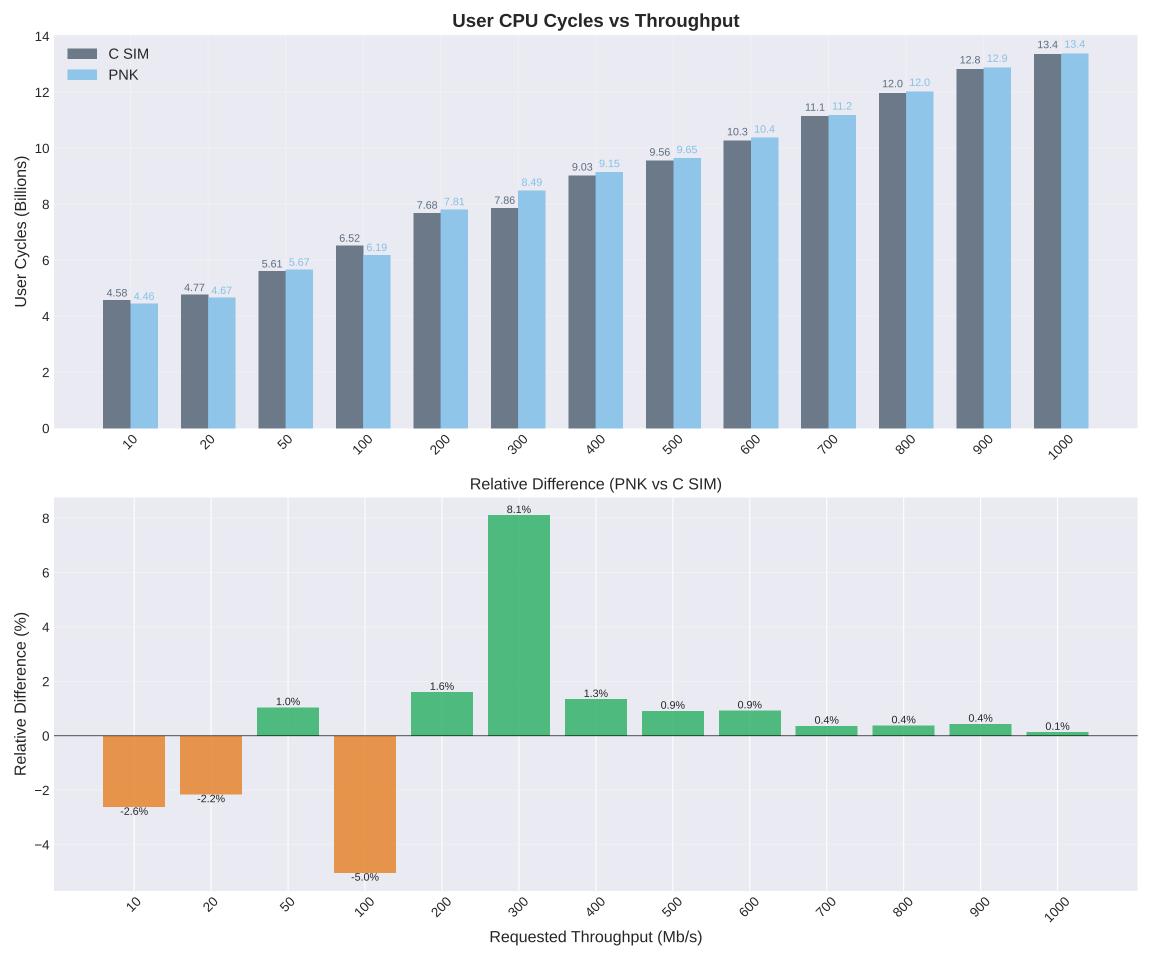
System CPU Utilization vs Throughput





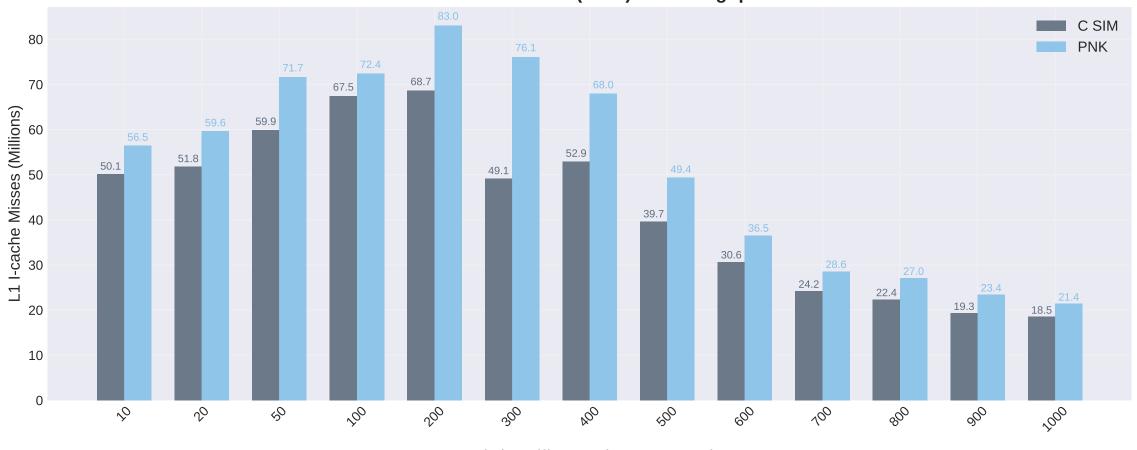


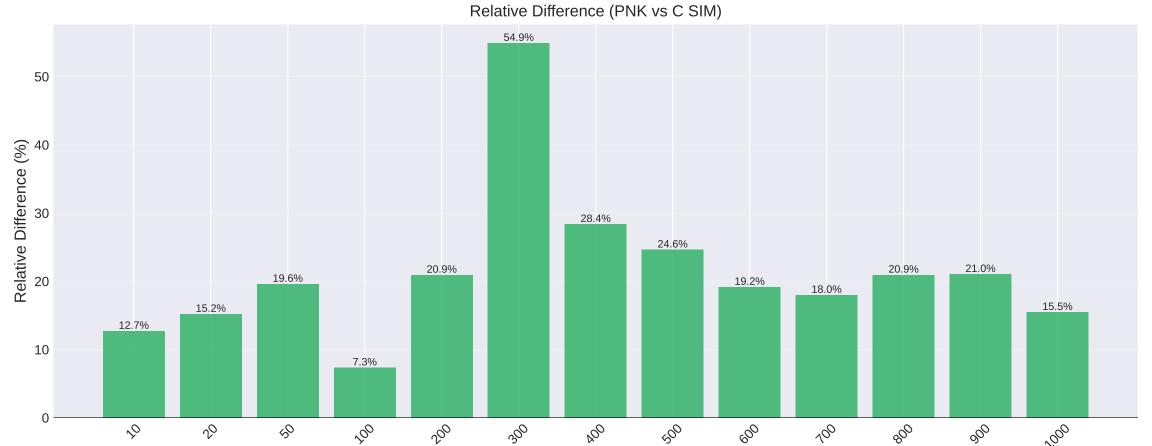
Kernel CPU Cycles vs Throughput 3.38 3.43 3.5 C SIM 3.20 PNK 2.89 2.96 2.94 3.0 2.90 2.89 2.73 2.49 Kernel Cycles (Billions) 2.40 2.33 2.34 1.95 1.96 1.69 1.69 1.50 1.50 1.22 1.21 1.12 1.09 0.5 0.0 200 \$ 700 300 NOO 400 600 700 800 900 2000 20 SO Relative Difference (PNK vs C SIM) 17.2% 15 Relative Difference (%) 2.5% 1.5% 0.5% 0.5% -0.1% -0.2% -0.5% -0.6% -2.8% -4.2% -4.2% -5 -8.2% \$0 20 60 200 200 300 NOO 400 600 100 900 Requested Throughput (Mb/s)

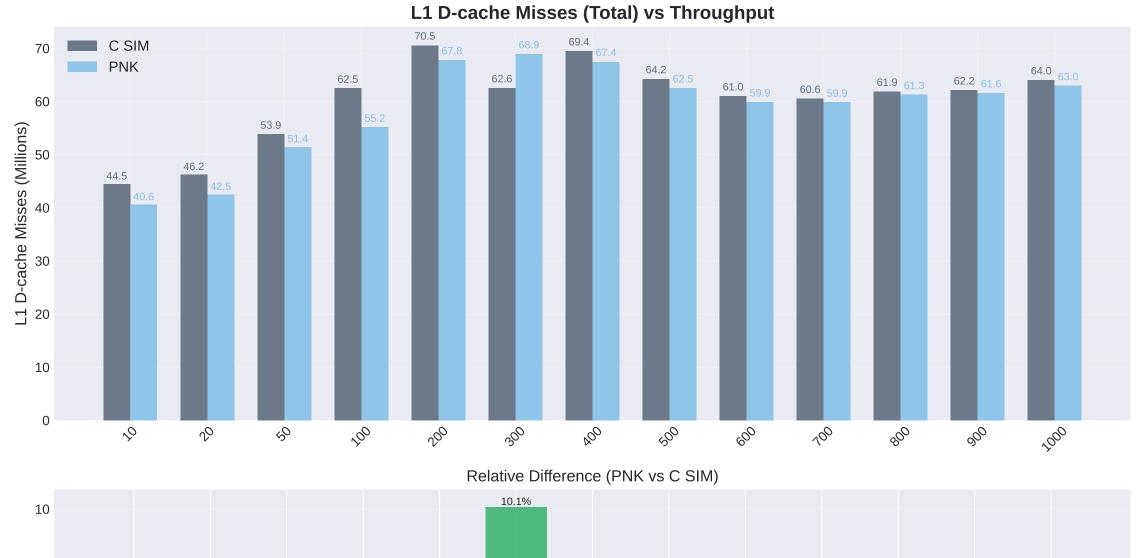


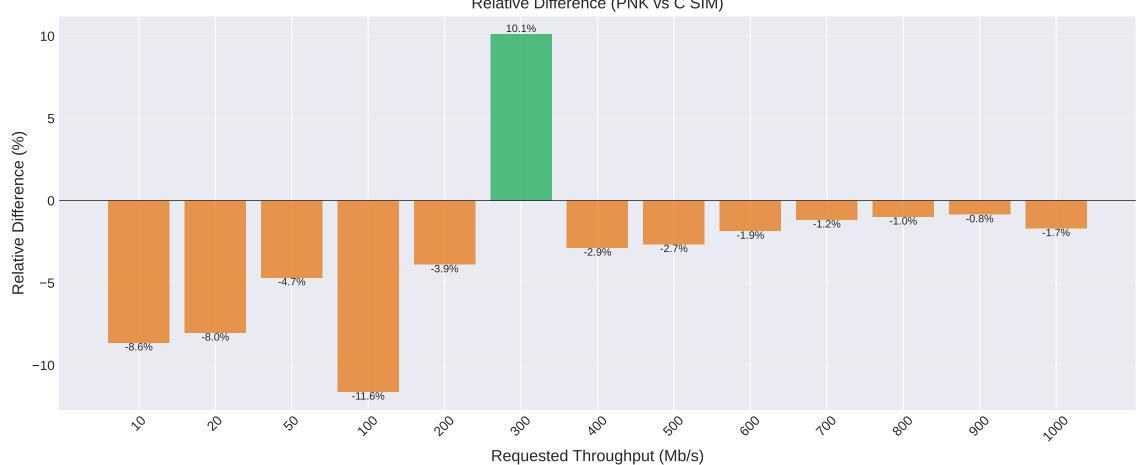
Idle CPU Cycles vs Throughput 248.1248.5 250 C SIM PNK 200 Idle Cycles (Billions) 130.1130.4 58.4 58.4 50 33.6 34.3 20.7 20.5 17.4 16.2 14.2 14.0 13.1 13.0 12.0 11.9 10.9 10.8 9.81 9.77 8.43 8.30 8.92 8.87 0 \$0 200 200 300 NOO 400 600 100 800 900 2000 20 50 Relative Difference (PNK vs C SIM) 2.1% 2 0.2% 0.2% 0 -0.1% -0.4% Relative Difference (%) -0.4% -0.5% -0.6% -0.9% -1.0% -1.4% -1.5% -6 -6.9% 30 20 SO 100 200 300 NOO 500 600 700 900

L1 I-cache Misses (Total) vs Throughput

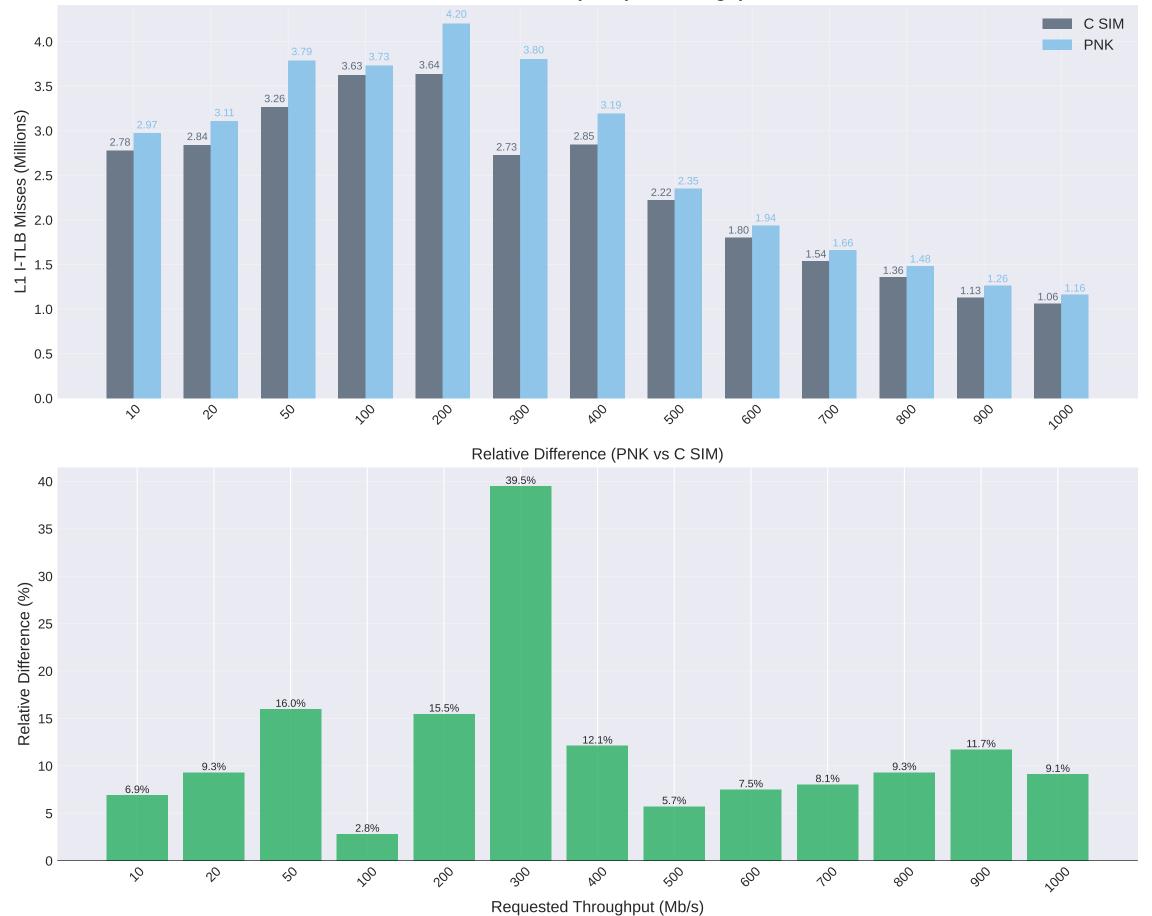




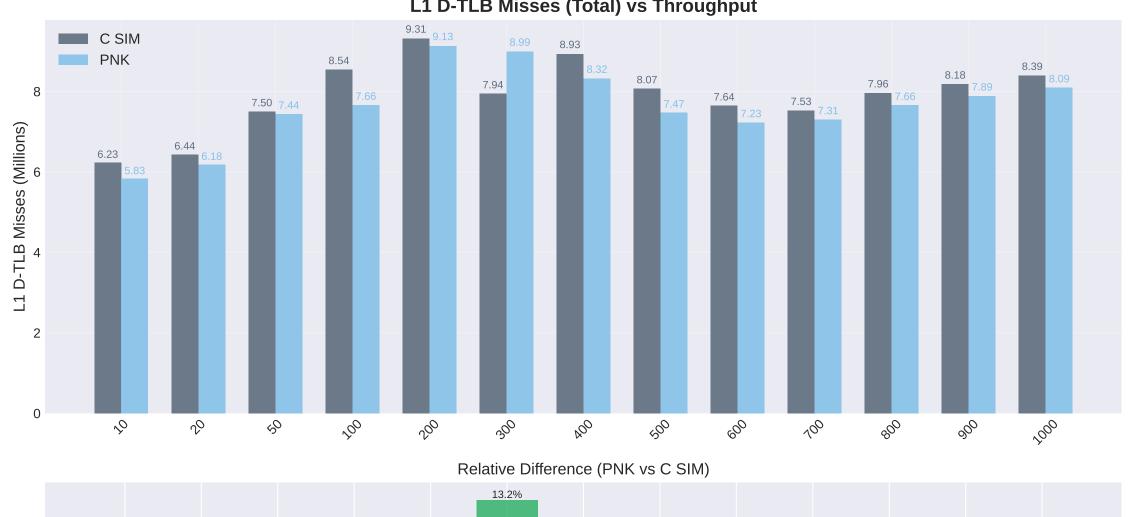


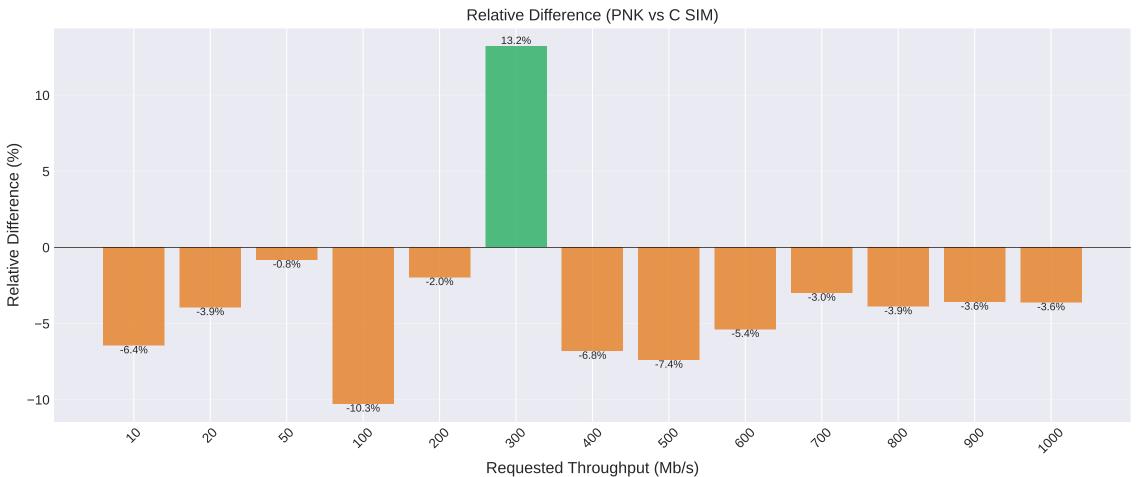


L1 I-TLB Misses (Total) vs Throughput

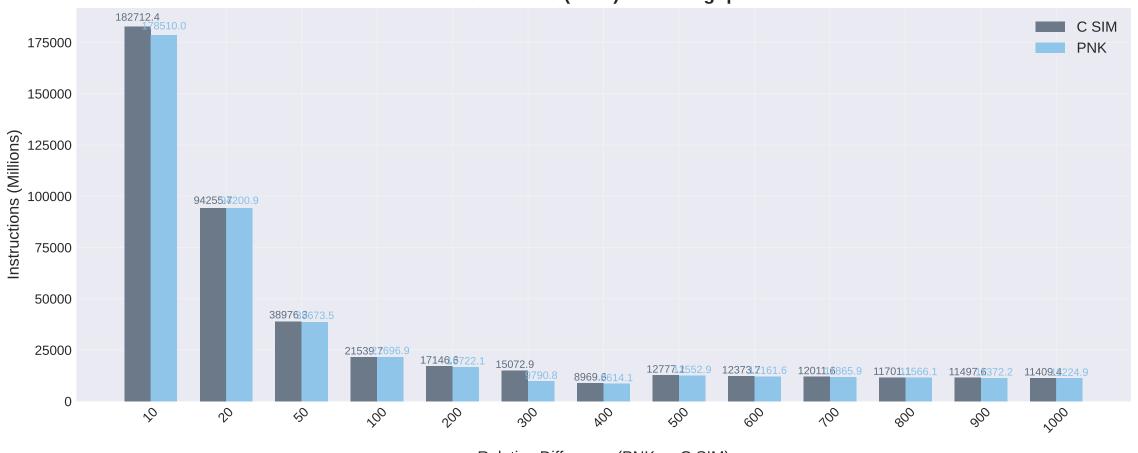


L1 D-TLB Misses (Total) vs Throughput

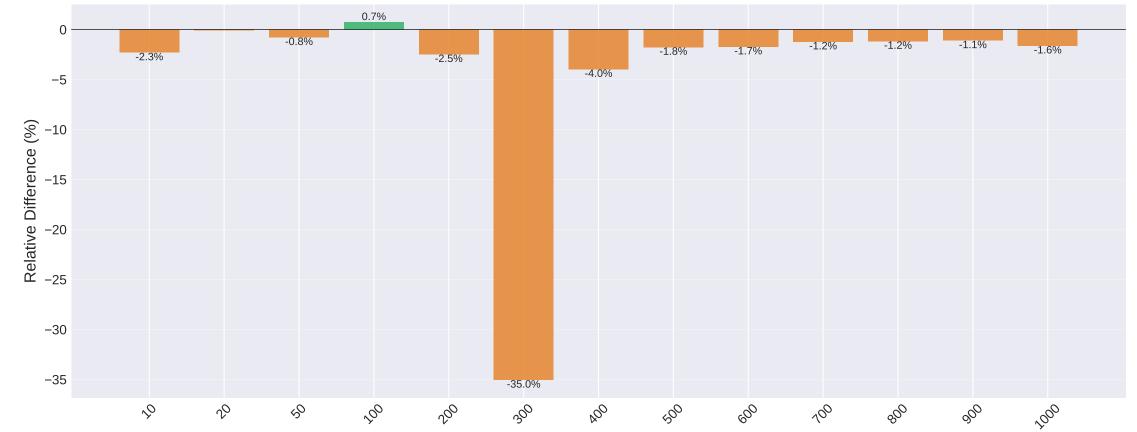






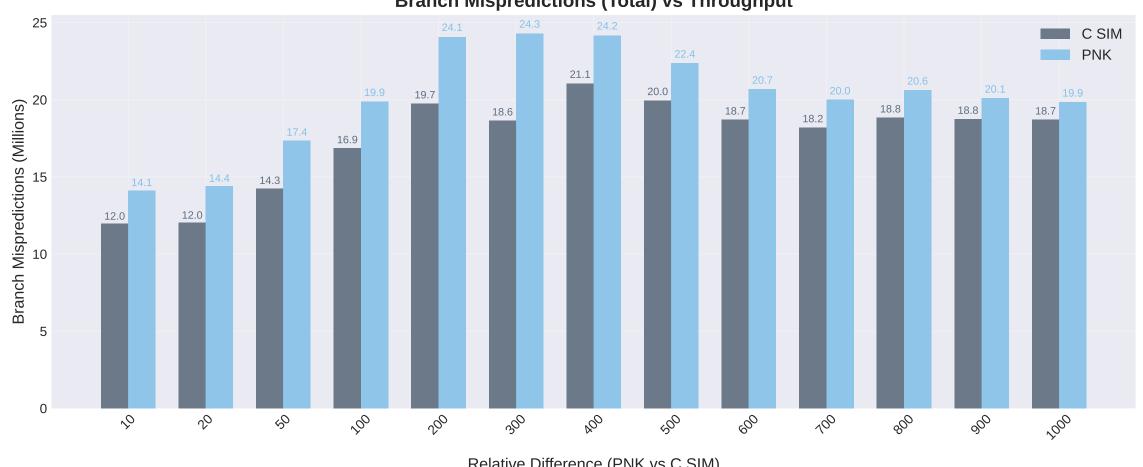


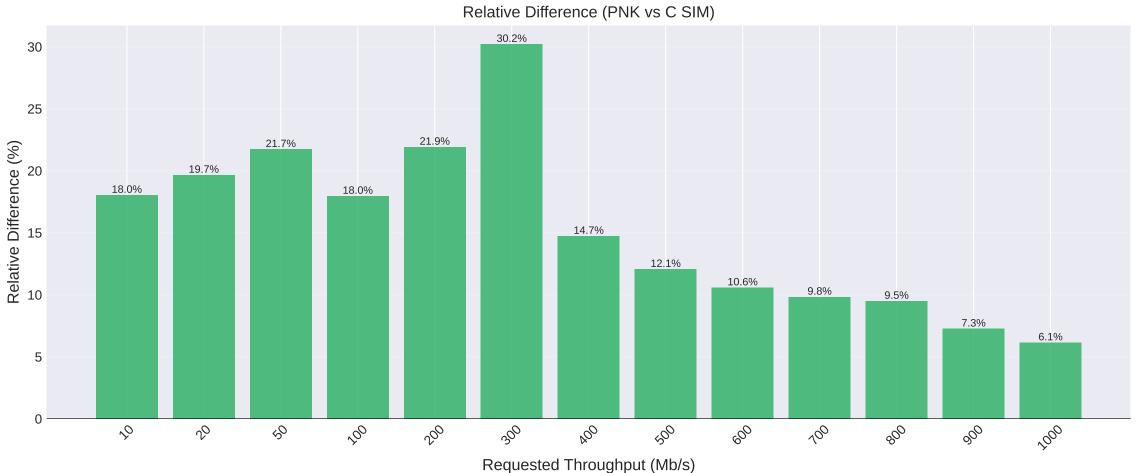




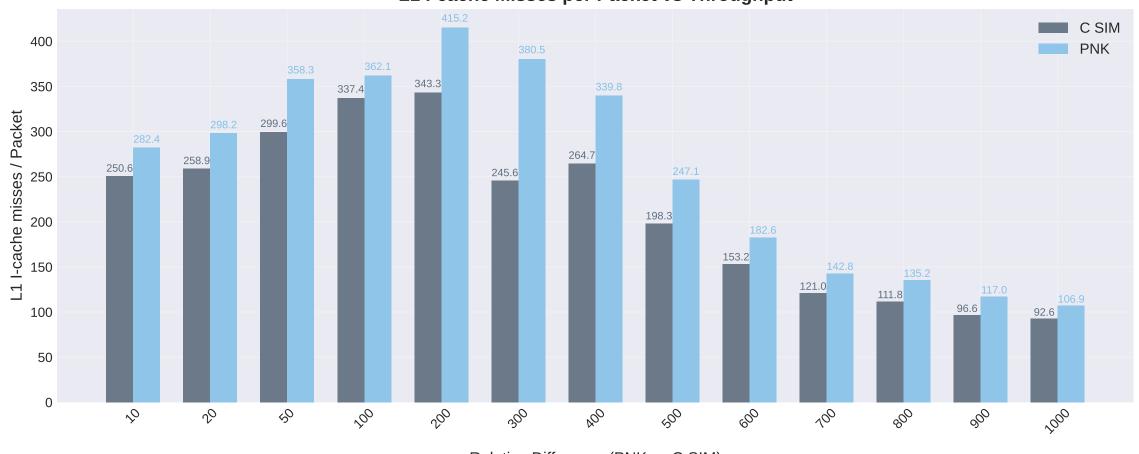
Requested Throughput (Mb/s)

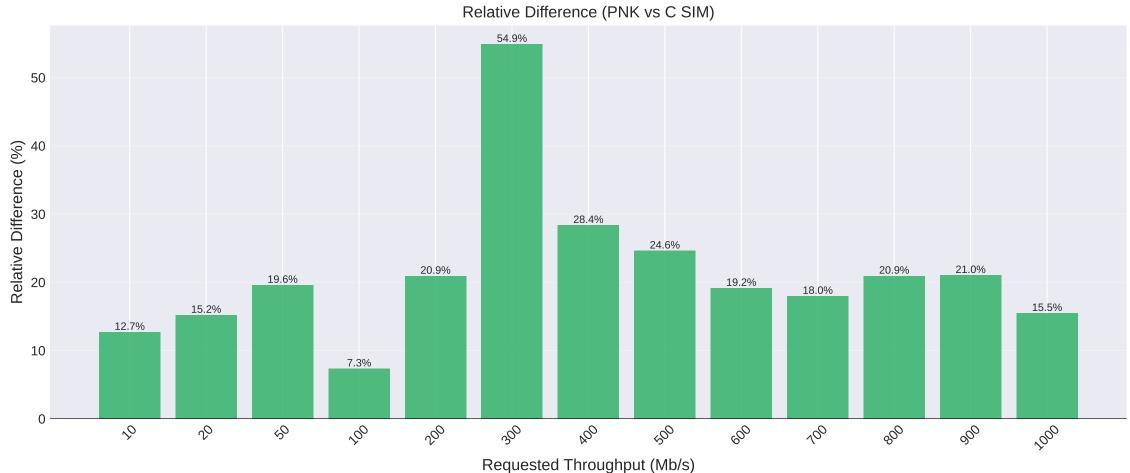
Branch Mispredictions (Total) vs Throughput



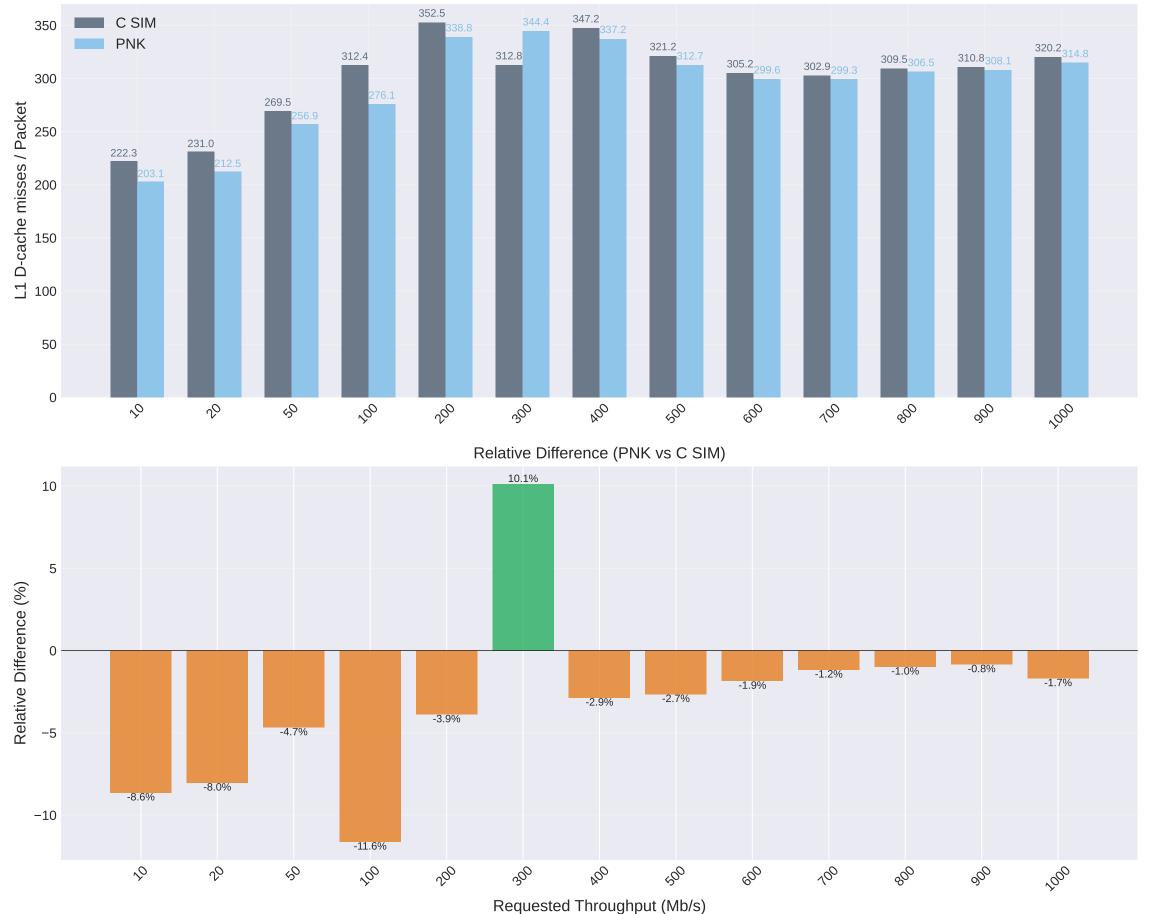


L1 I-cache Misses per Packet vs Throughput

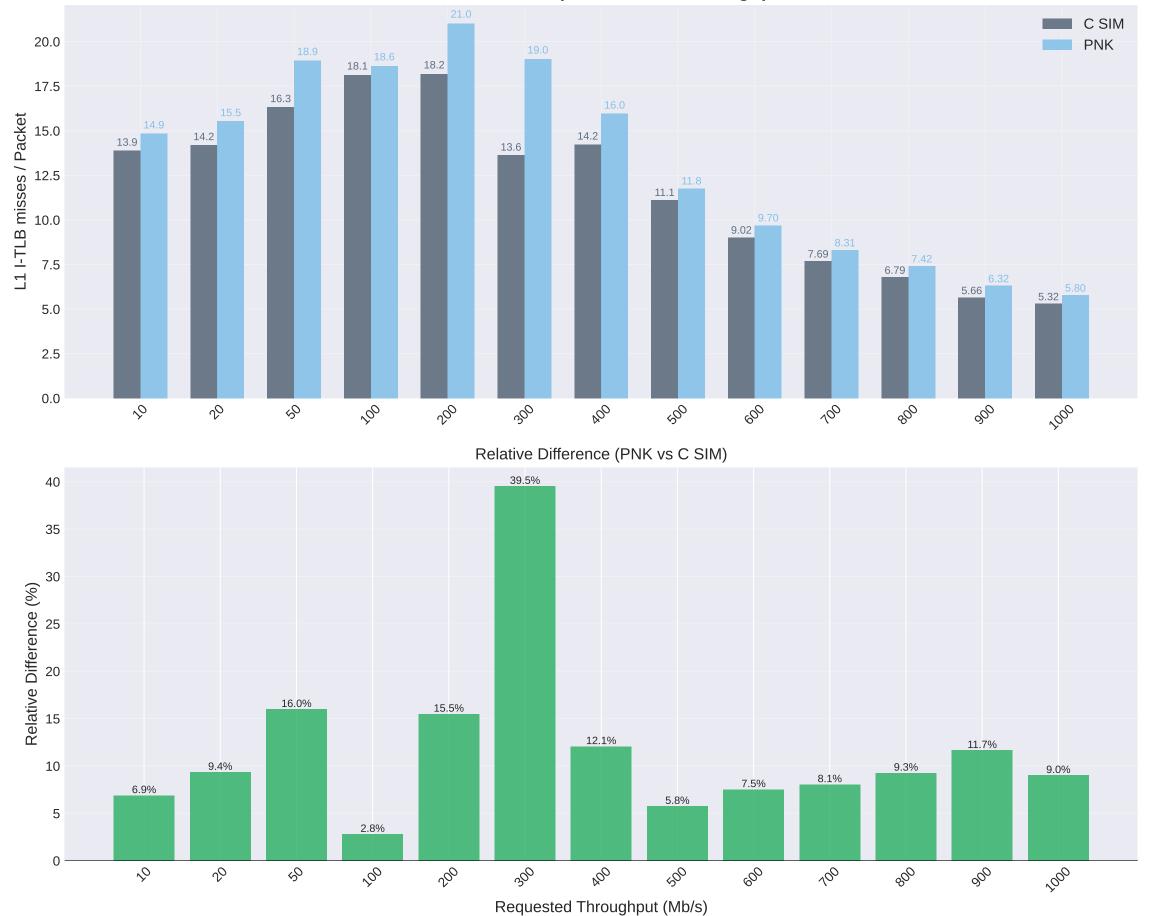




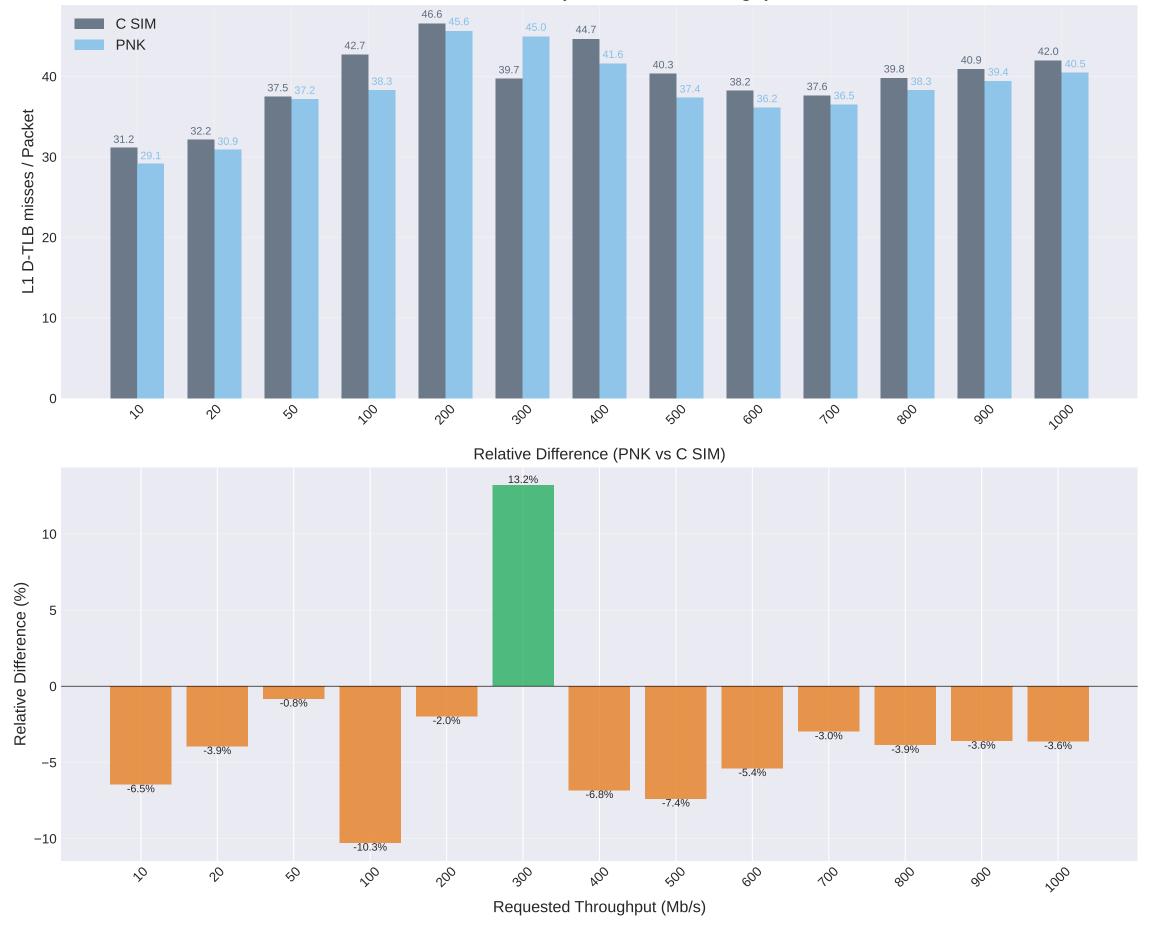
L1 D-cache Misses per Packet vs Throughput



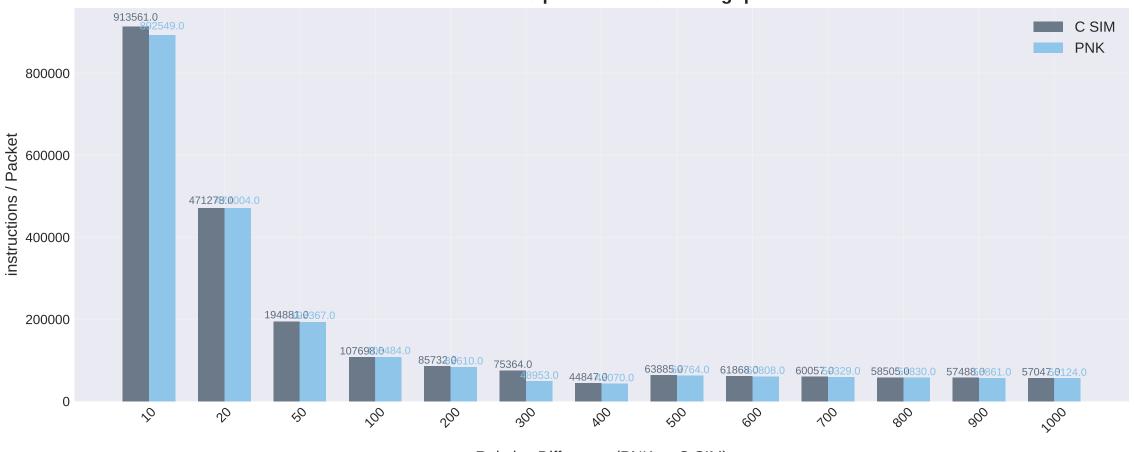
L1 I-TLB Misses per Packet vs Throughput



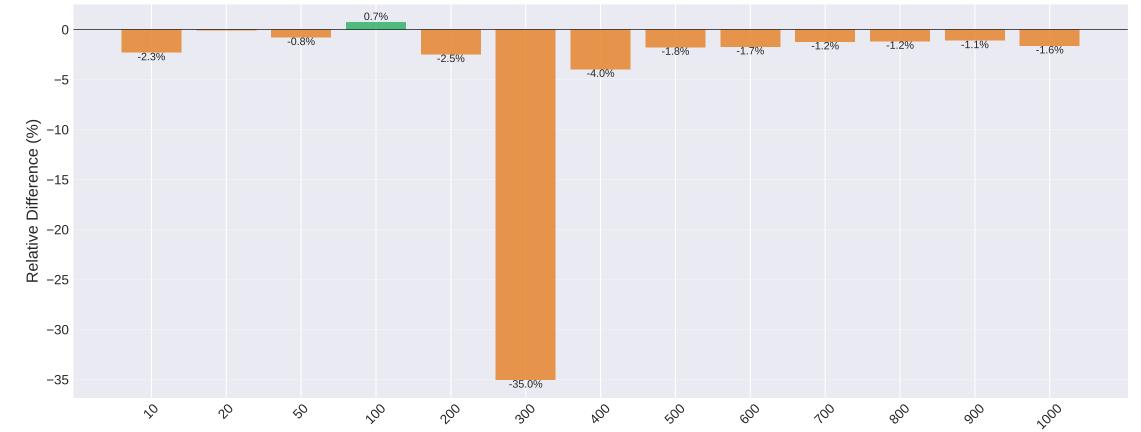
L1 D-TLB Misses per Packet vs Throughput





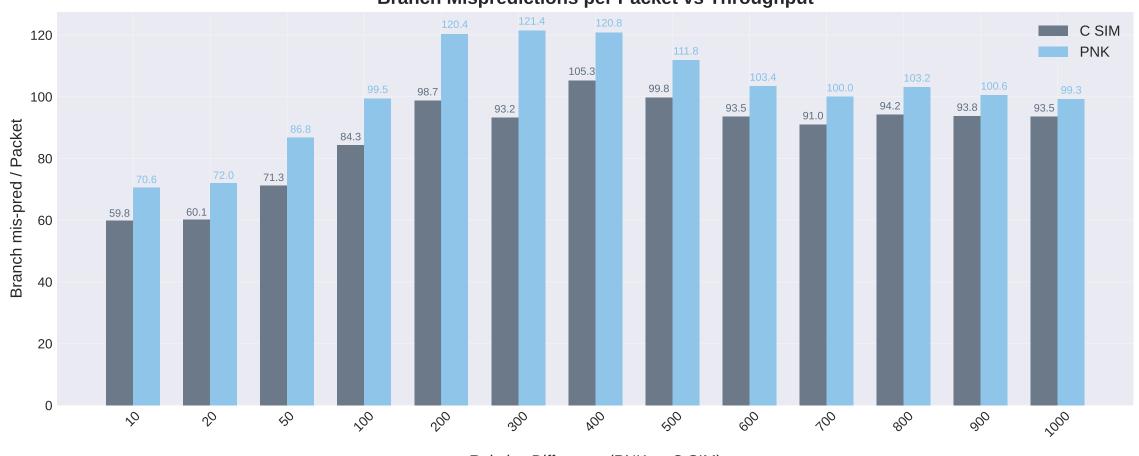


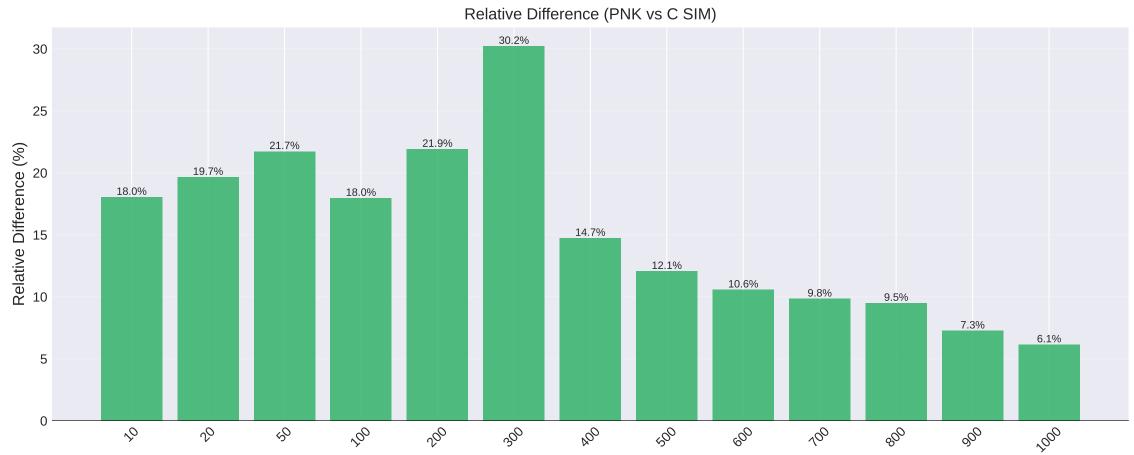


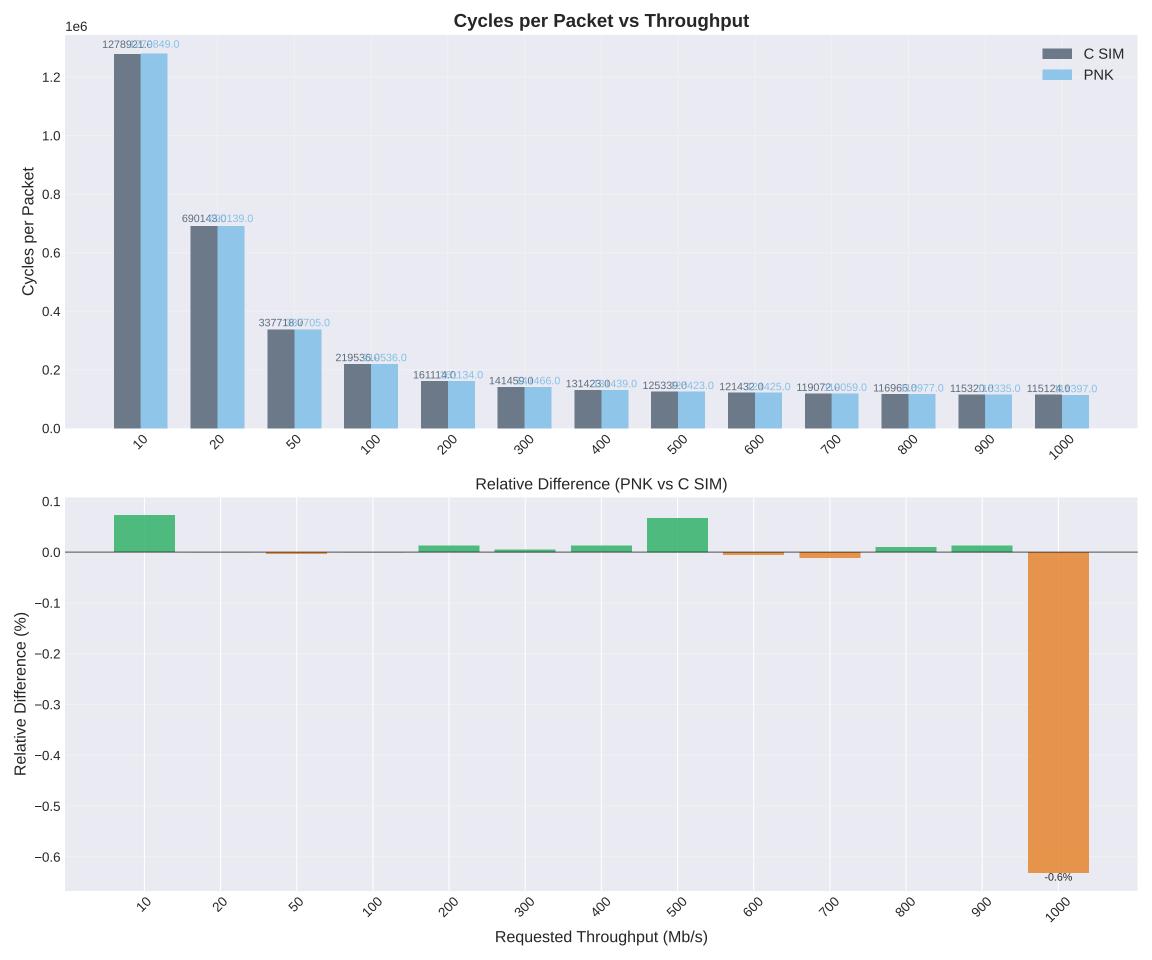


Requested Throughput (Mb/s)

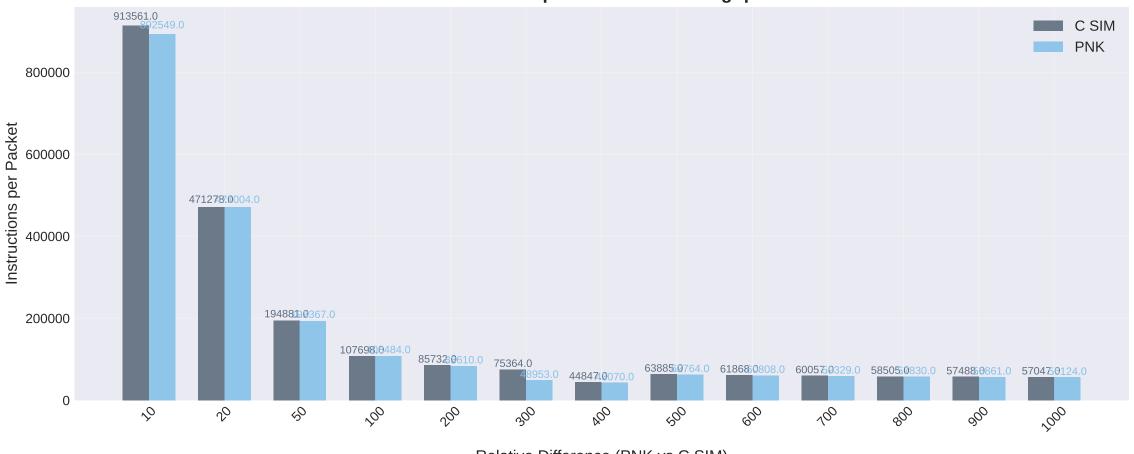
Branch Mispredictions per Packet vs Throughput

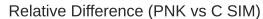


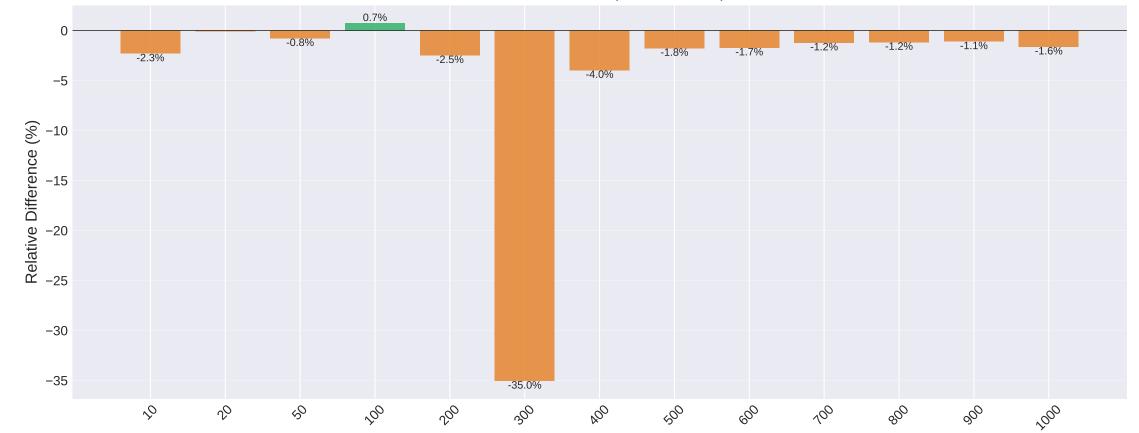






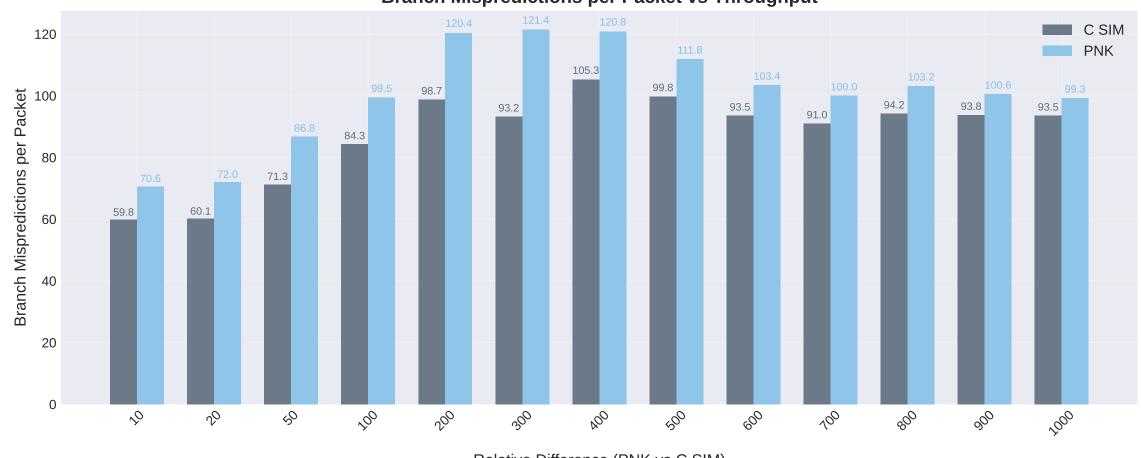


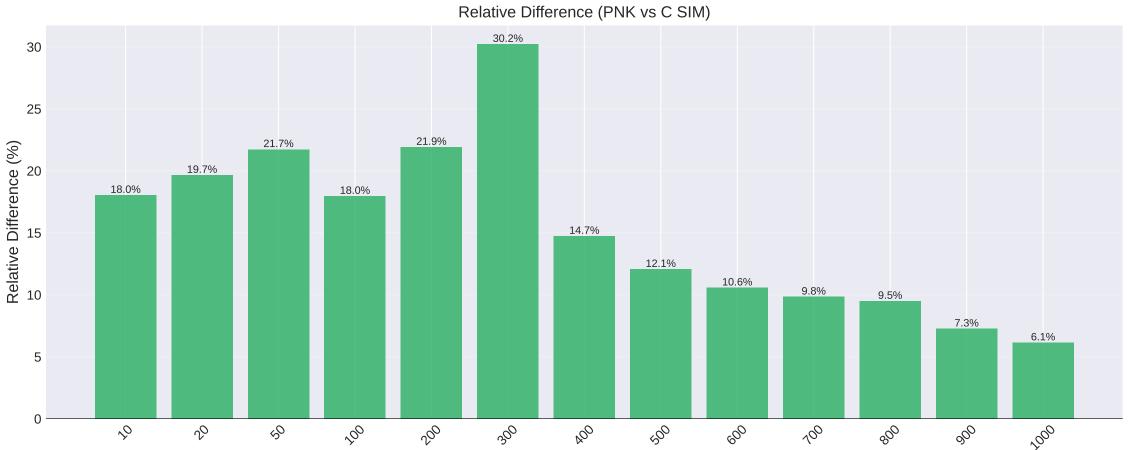




Requested Throughput (Mb/s)

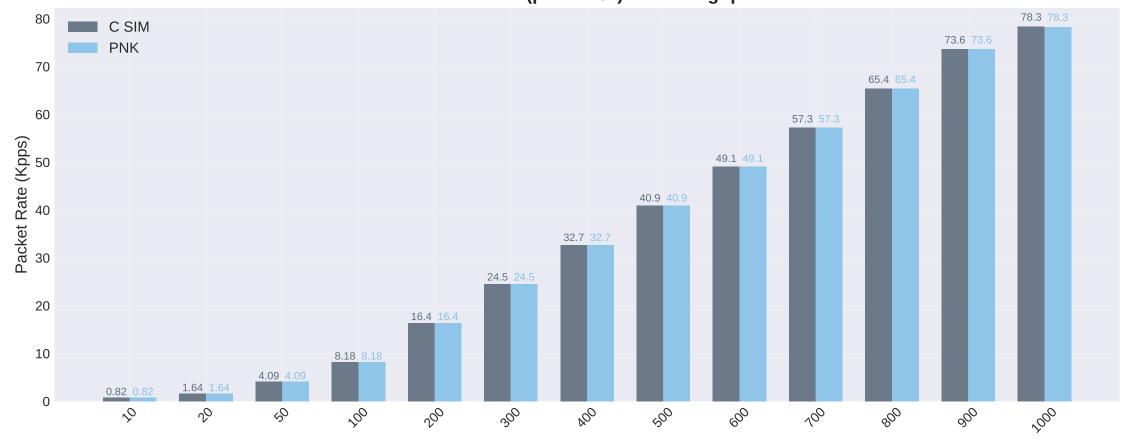
Branch Mispredictions per Packet vs Throughput

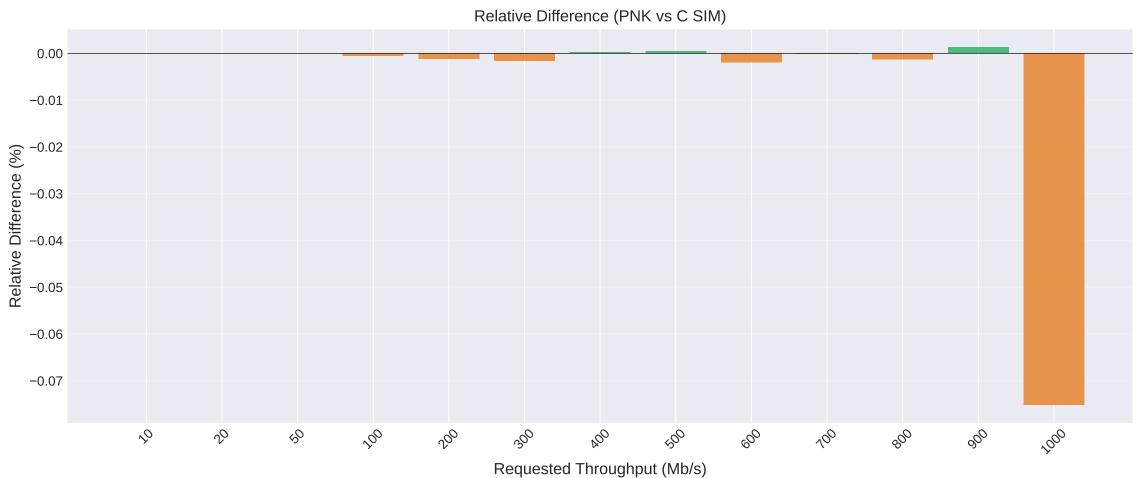


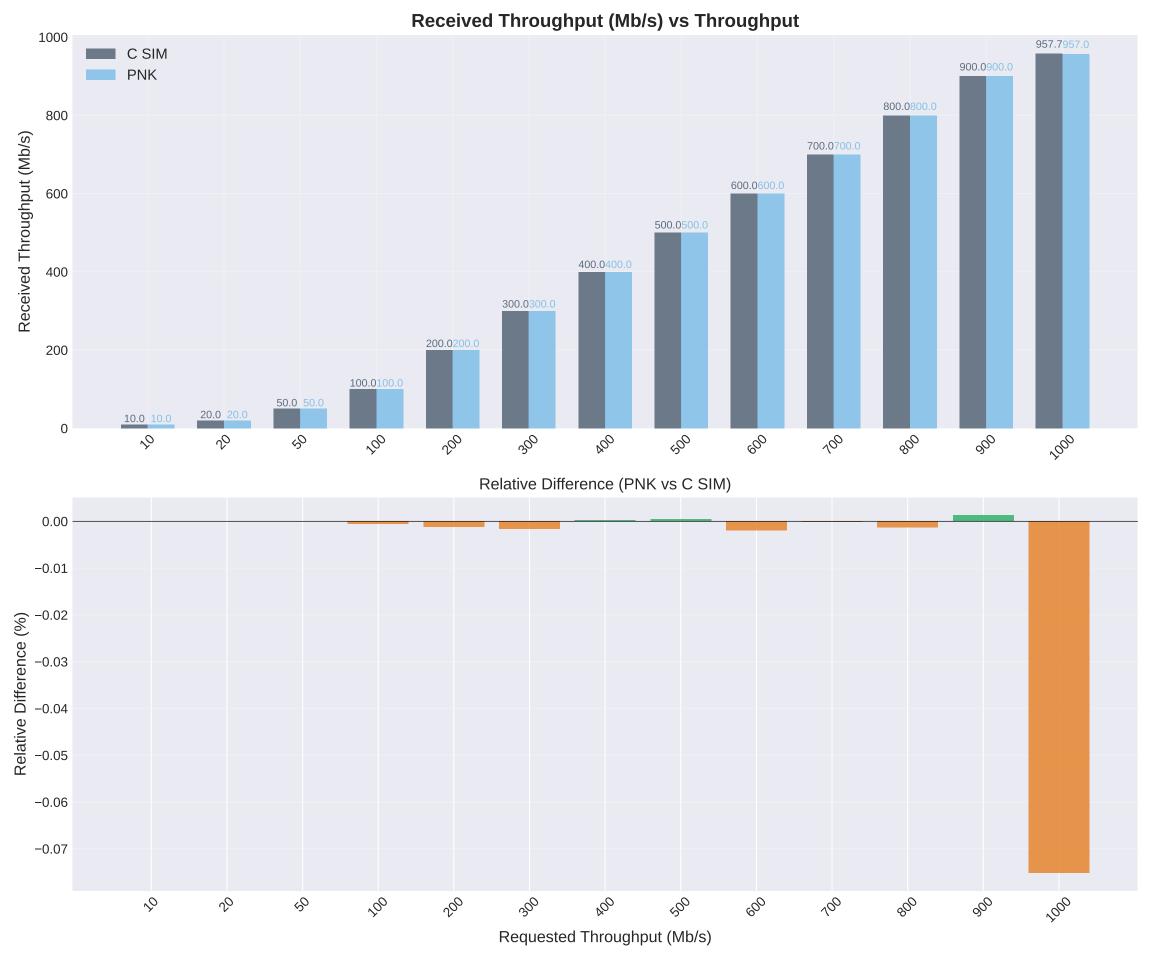












Sent Throughput (Mb/s) vs Throughput 1000.0000.0 C SIM 1000 PNK 900.0900.0 800.0800.0 800 Sent Throughput (Mb/s) 700.0700.0 600.0600.0 600 500.0500.0 400.0400.0 400 300.0300.0 200.0200.0 200 100.0100.0 50.0 50.0 20.0 20.0 10.0 10.0 0 200 200 300 NOO 400 600 901 900 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 0.00075 0.00050 0.00025 Relative Difference (%) 0.00000 -0.00025 -0.00050 -0.00075 -0.00100 -0.00125 \$ 200 200 300 NOO 400 600 900 20 SO 900 100