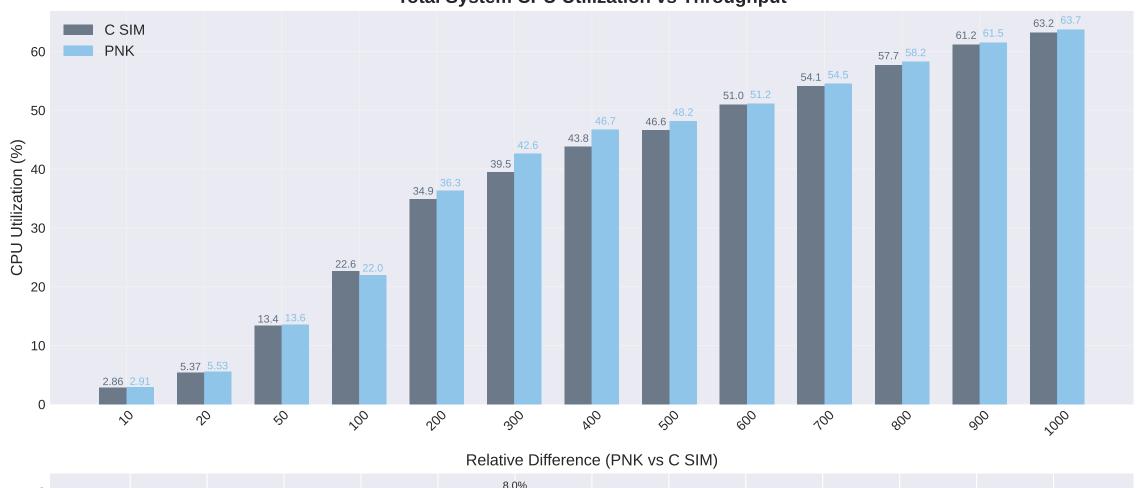


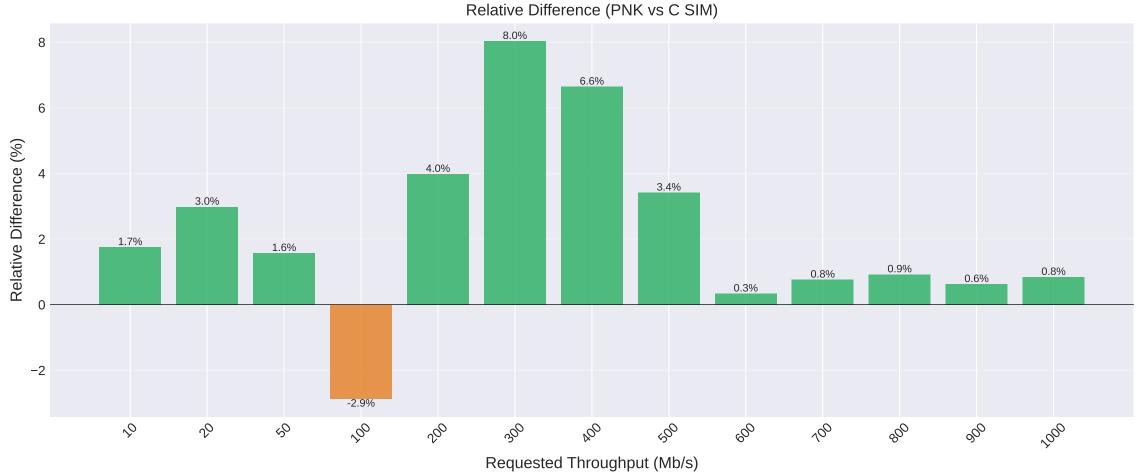
Requested Throughput (Mb/s)

Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3957.0 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.2% 61.5% 600.0600.0 600 CPU Utilization (%) 58.2% 54.5% 51.2% 500.0500.0 46.7% 42.6% 400 300.0300.0 200.0200.0 200 20 100.0100.0 50.0 50.0 2.9% 20.0 20 0 900 200 go NOO 400 2000 700 600 100 900 30 20 50

Requested Throughput (Mb/s)

Total System CPU Utilization vs Throughput

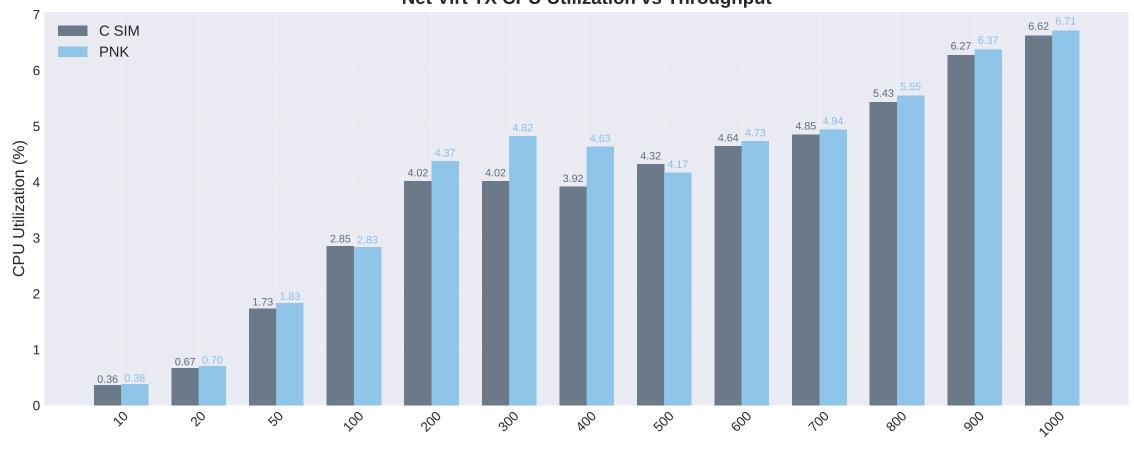


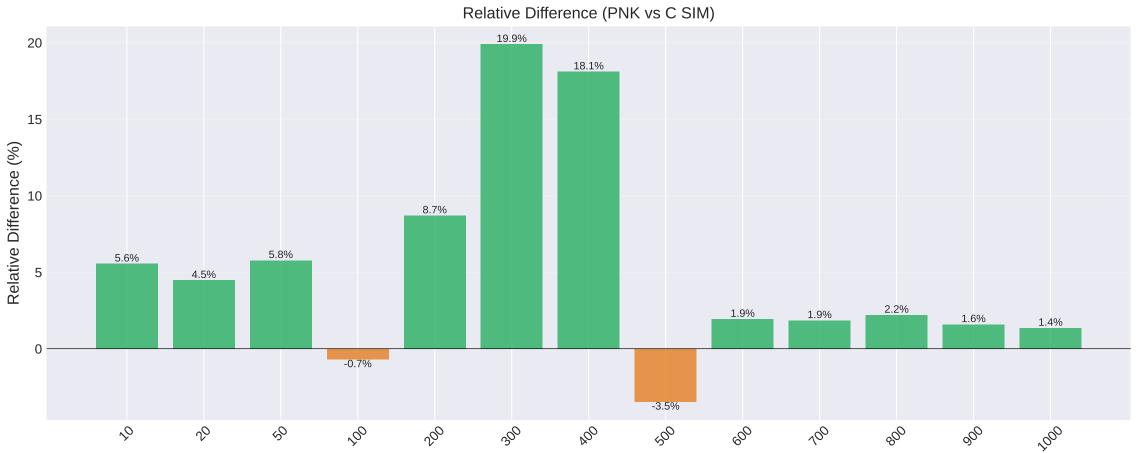


Ethernet Driver CPU Utilization vs Throughput

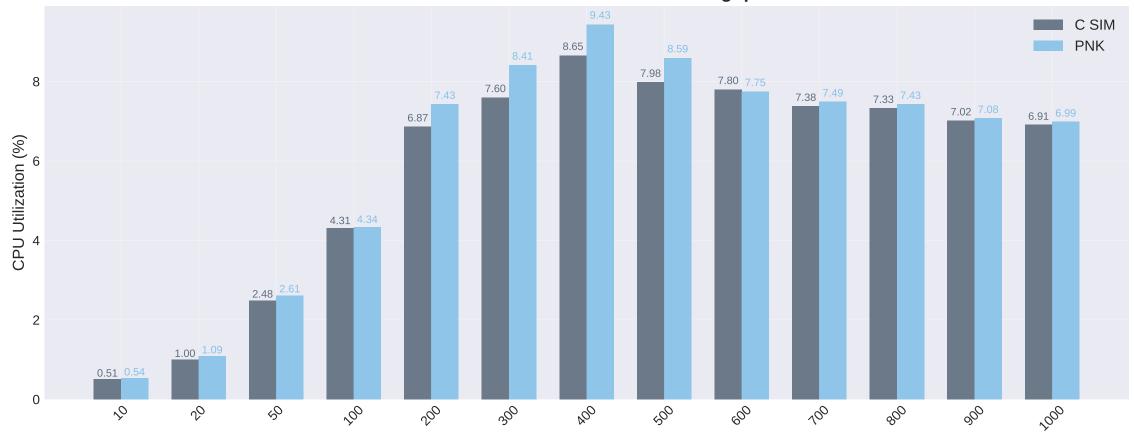


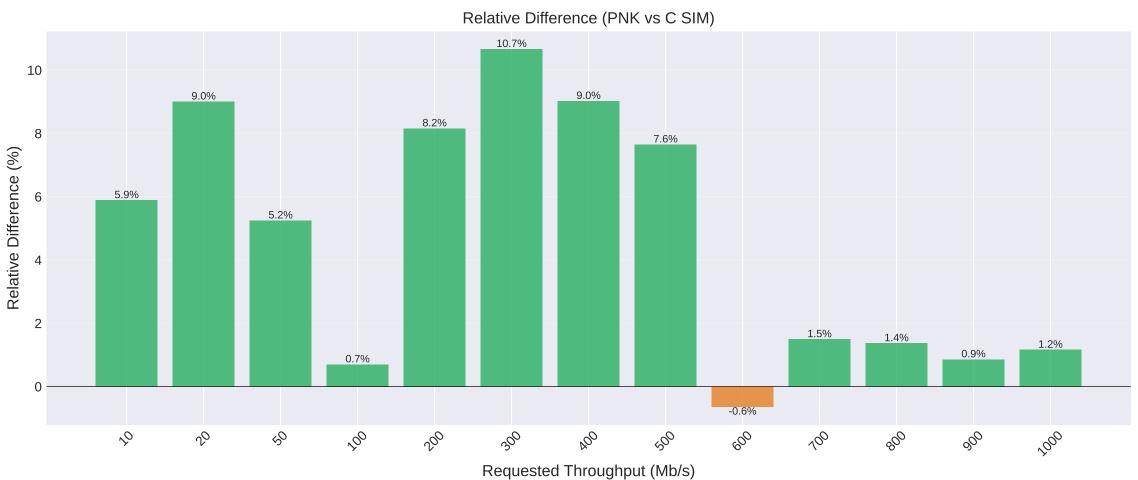




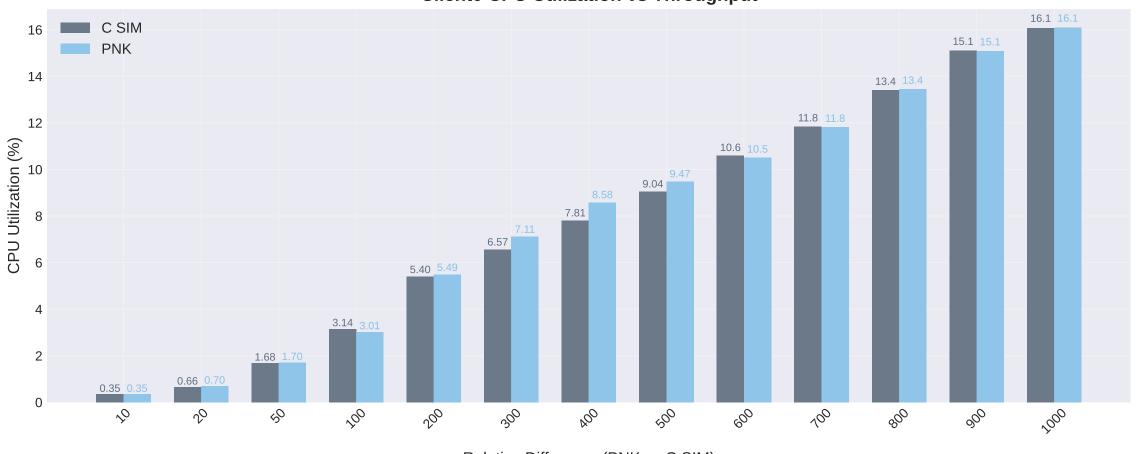


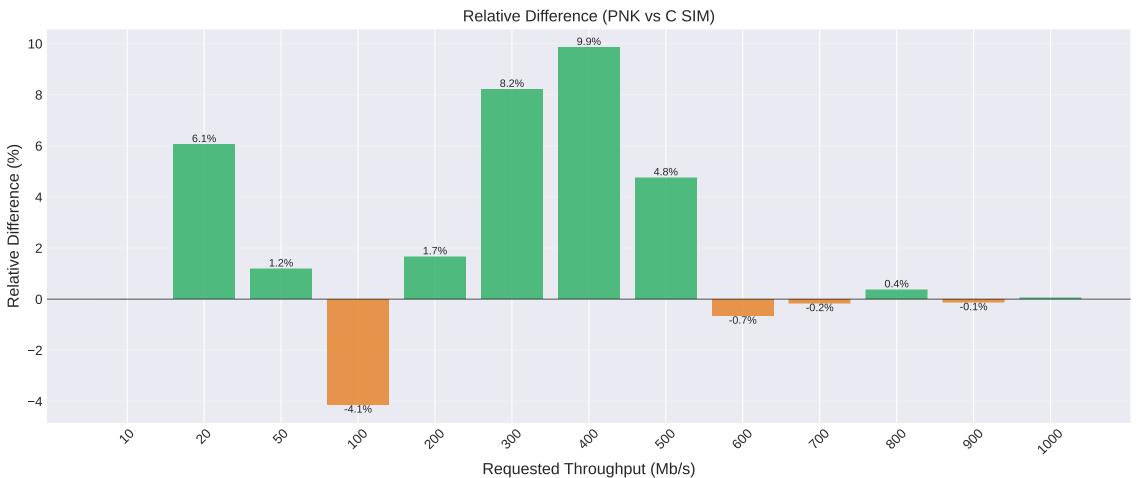
Net Virt RX CPU Utilization vs Throughput



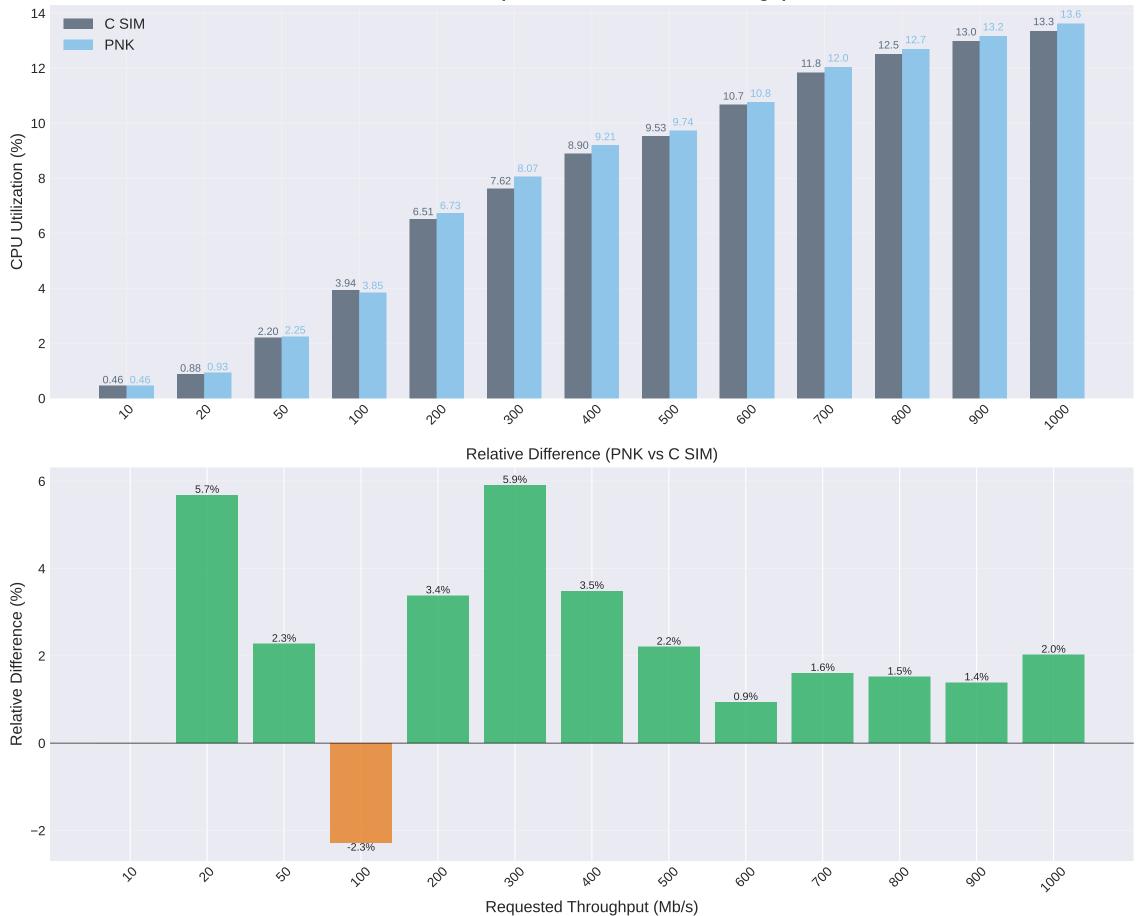


Client0 CPU Utilization vs Throughput

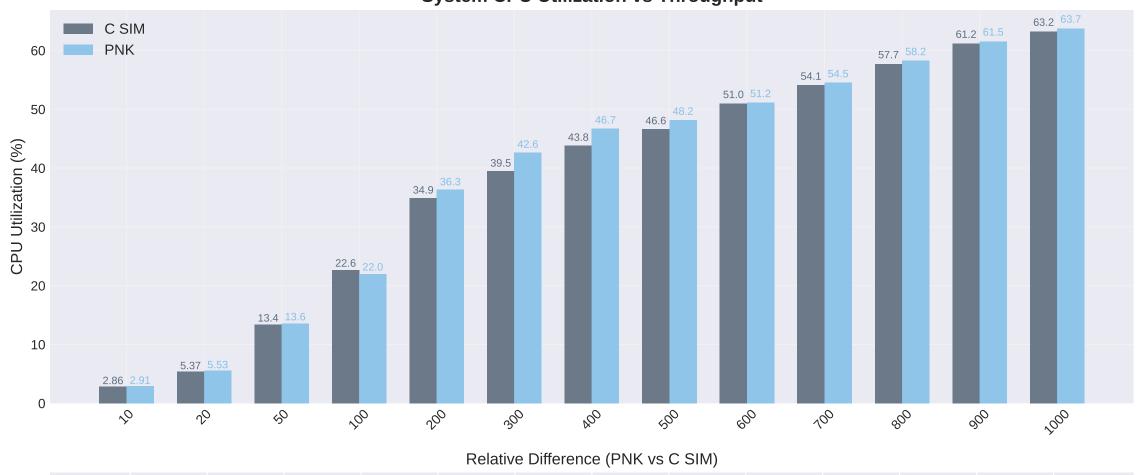


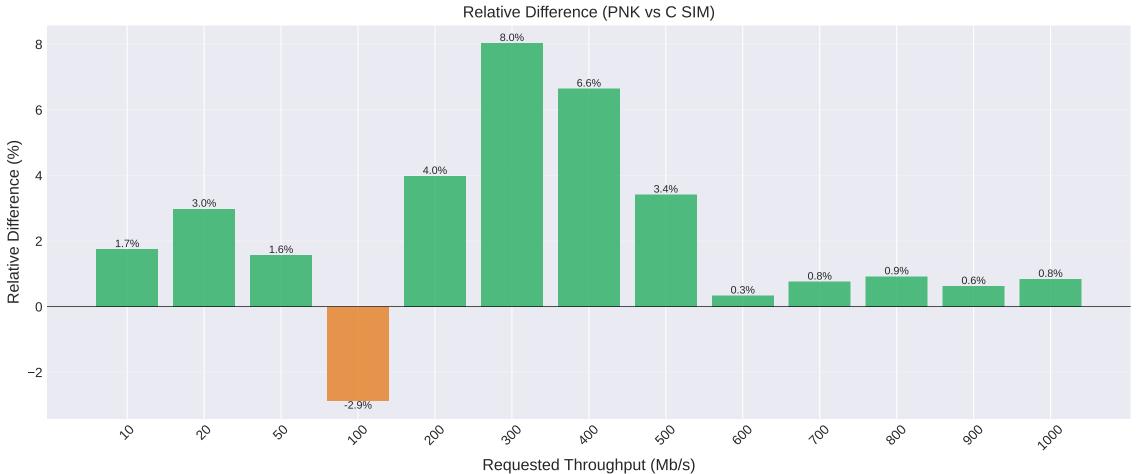


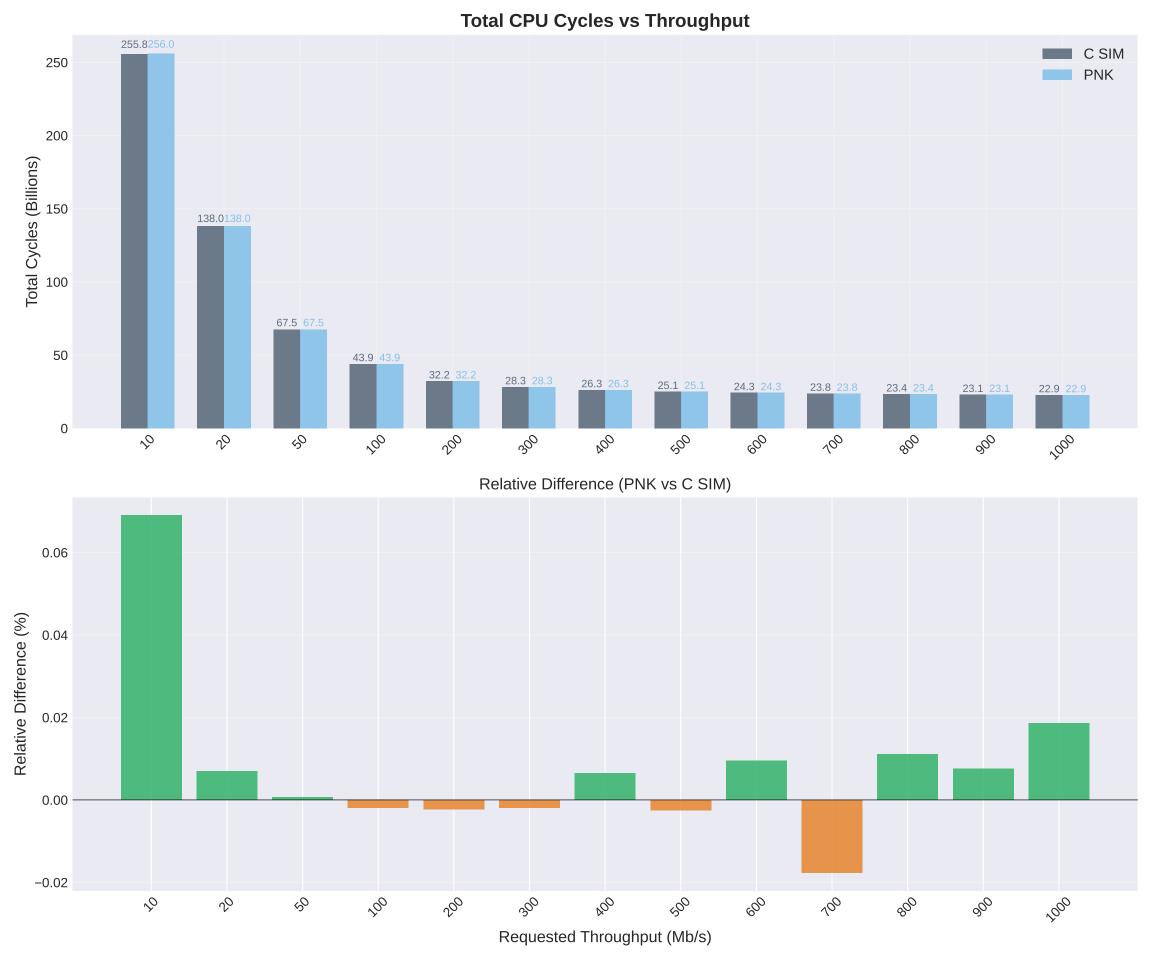
Client0 Net Copier CPU Utilization vs Throughput



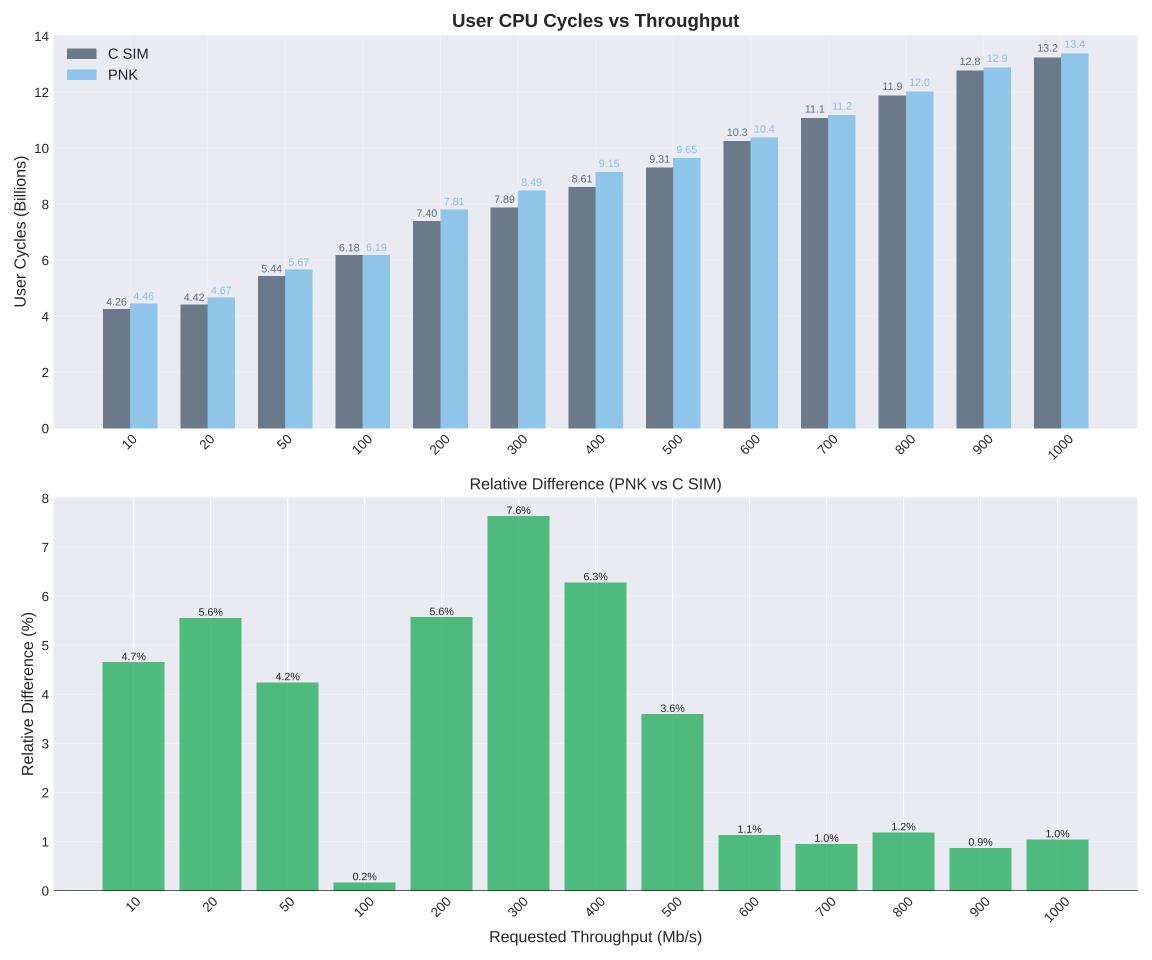
System CPU Utilization vs Throughput





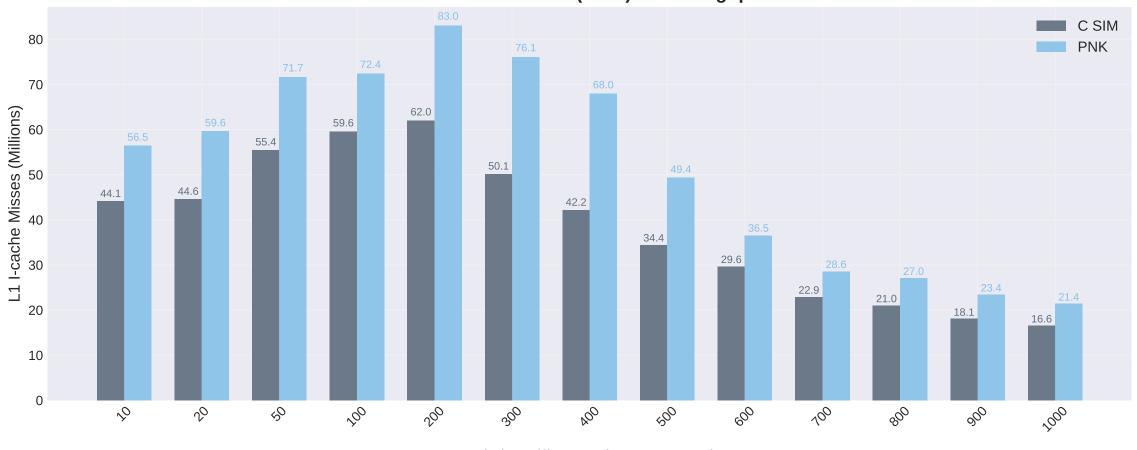


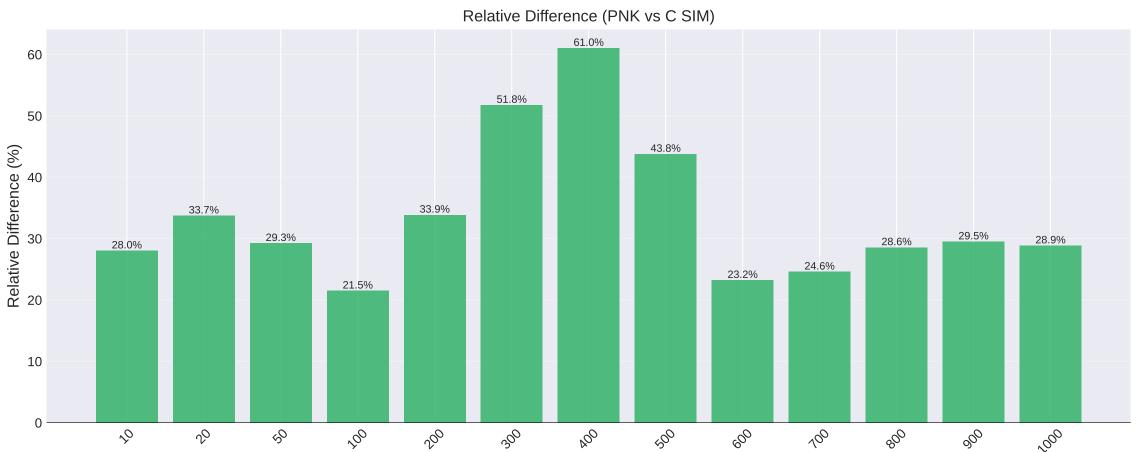
Kernel CPU Cycles vs Throughput 3.5 3.34 C SIM PNK 3.16 2.95 2.96 3.0 2.93 2.71 Kernel Cycles (Billions) 2.40 2.39 2.34 2.30 2.28 2.34 2.03 1.70 1.69 1.51 1.50 1.24 1.21 1.11 1.09 0.5 0.0 700 200 300 NOO 400 600 700 900 900 2000 30 20 60 Relative Difference (PNK vs C SIM) 9.2% 9.1% 8 6 Relative Difference (%) 2.9% 2.8% -0.4% -0.6% -1.2% -1.5% -1.8% -2.1% -3.8% -4 -6 -6.9% \$0 20 60 200 200 300 NOO 400 600 100 900 Requested Throughput (Mb/s)



Idle CPU Cycles vs Throughput 248.5248.5 250 C SIM PNK 200 Idle Cycles (Billions) 130.6130.4 58.5 58.4 50 34.0 34.3 21.0 20.5 17.1 16.2 14.8 14.0 13.4 13.0 11.9 11.9 10.9 10.8 9.89 9.77 8.42 8.30 0 \$0 200 200 300 NOO 400 600 700 800 900 2000 20 50 Relative Difference (PNK vs C SIM) 1 0.8% 0 -0.2% -0.2% -0.3% Relative Difference (%) -0.9% -1.0% -1.2% -1.4% -2.1% -3.0% -4 -5 -5.2% -5.2% 200 30 20 SO 100 200 300 400 600 100 900 Requested Throughput (Mb/s)

L1 I-cache Misses (Total) vs Throughput





L1 D-cache Misses (Total) vs Throughput C SIM 67.3 70 PNK 65.2 63.5 63.0 62.6 62.5 62.8 62.6 61.6 62.0 61.3 61.7 60.8 59.9 60 55.2 L1 D-cache Misses (Millions) 44.9 43.9 10 0 200 200 300 400 400 600 700 800 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 4 3.5% 2.3% 2 0 Relative Difference (%) -0.9% -1.2% -1.5% -1.6% -2 -4 -4.6% -4.5% -5.3% -6 -6.9% -7.4%

-10.6%

100

60

-8

-10

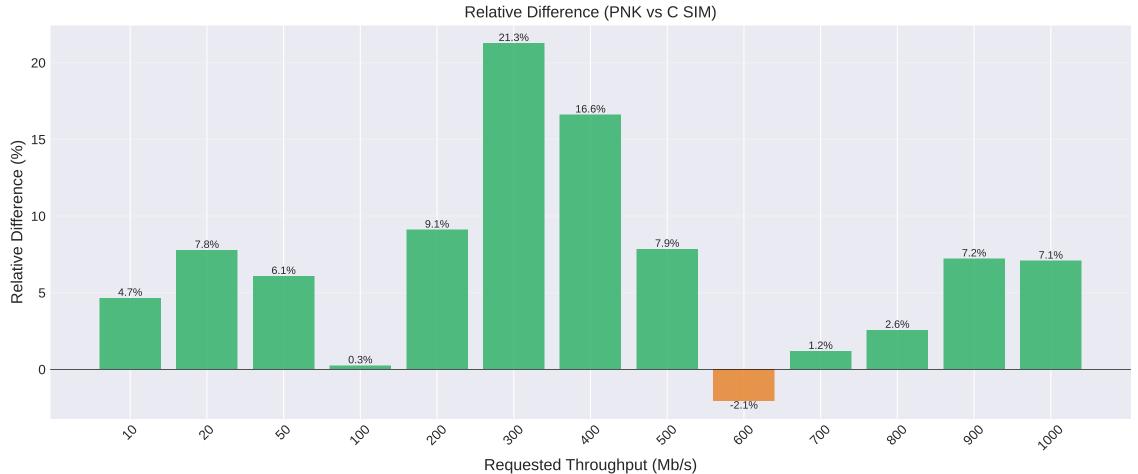
30

20

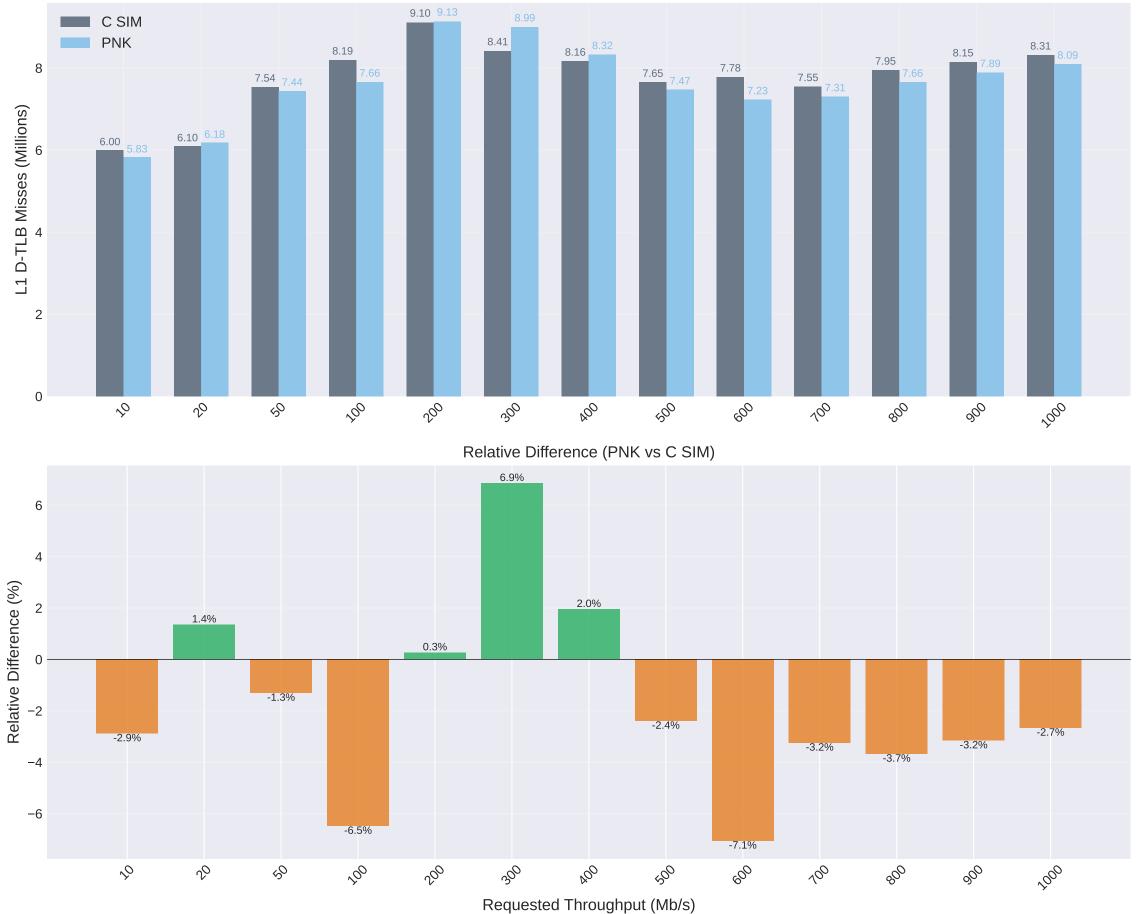
200 300 500 100 900 NO 600 Requested Throughput (Mb/s)

L1 I-TLB Misses (Total) vs Throughput

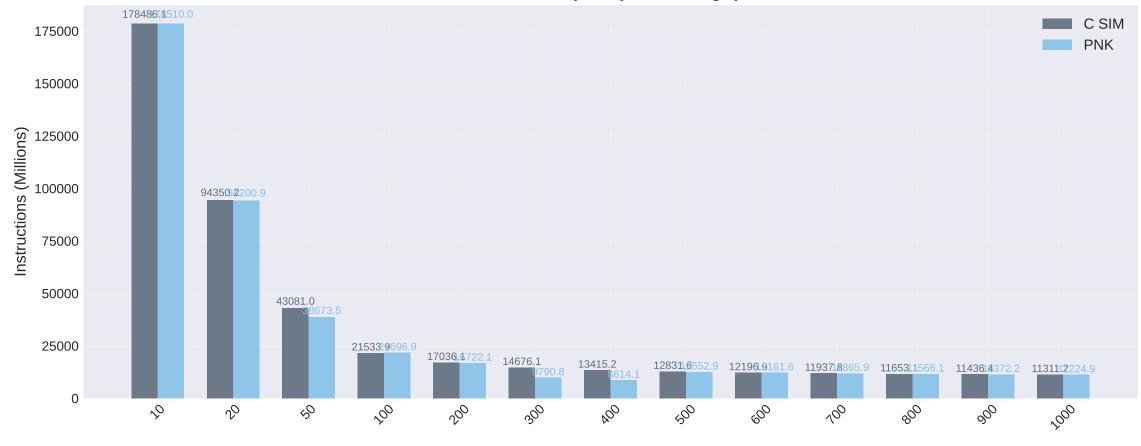




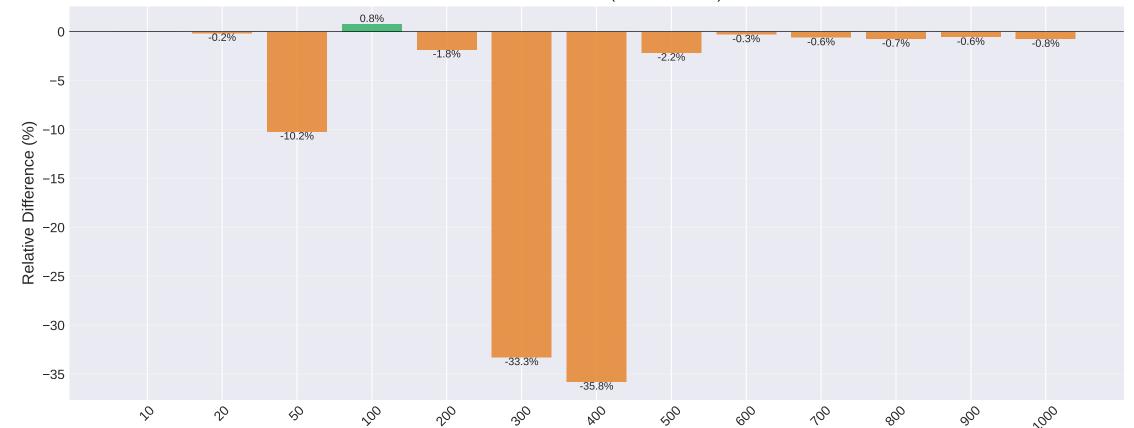
L1 D-TLB Misses (Total) vs Throughput





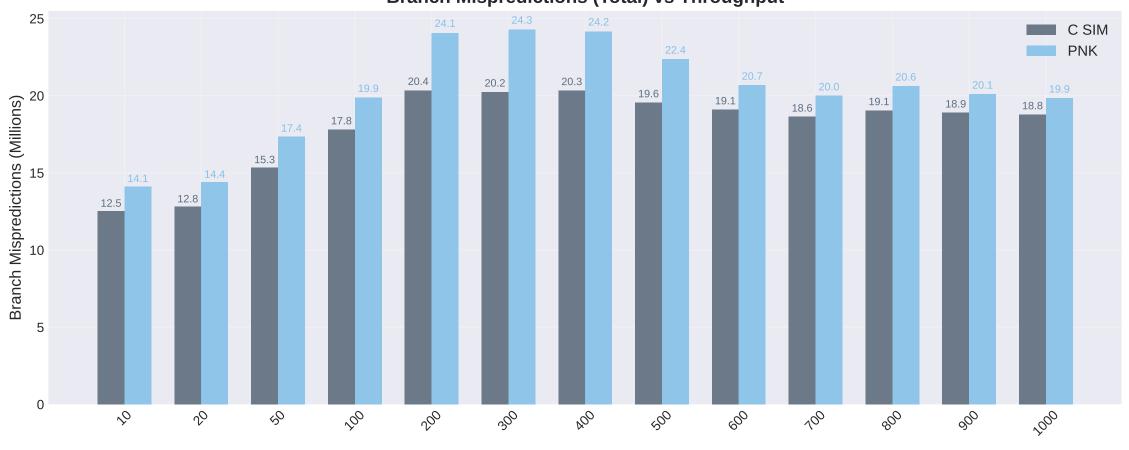


Relative Difference (PNK vs C SIM)

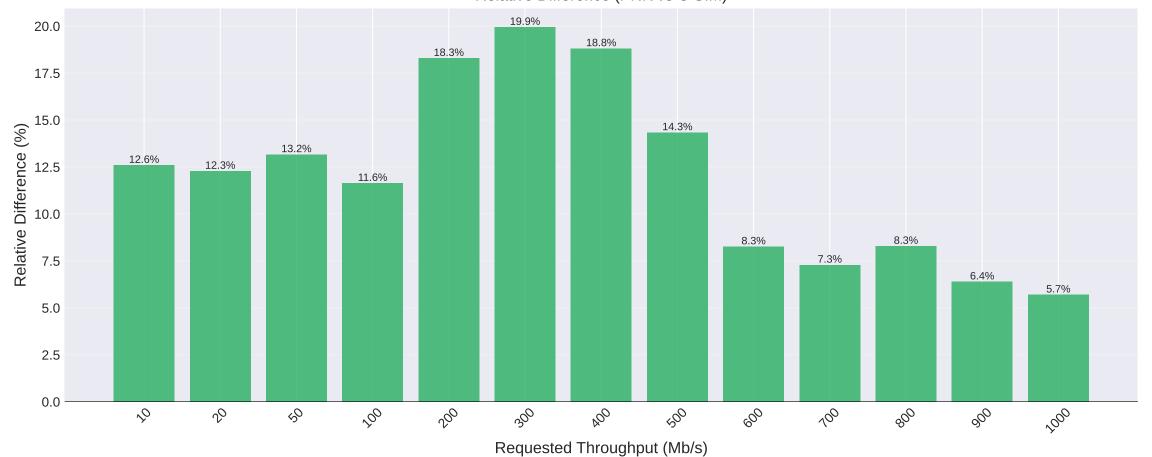


Requested Throughput (Mb/s)

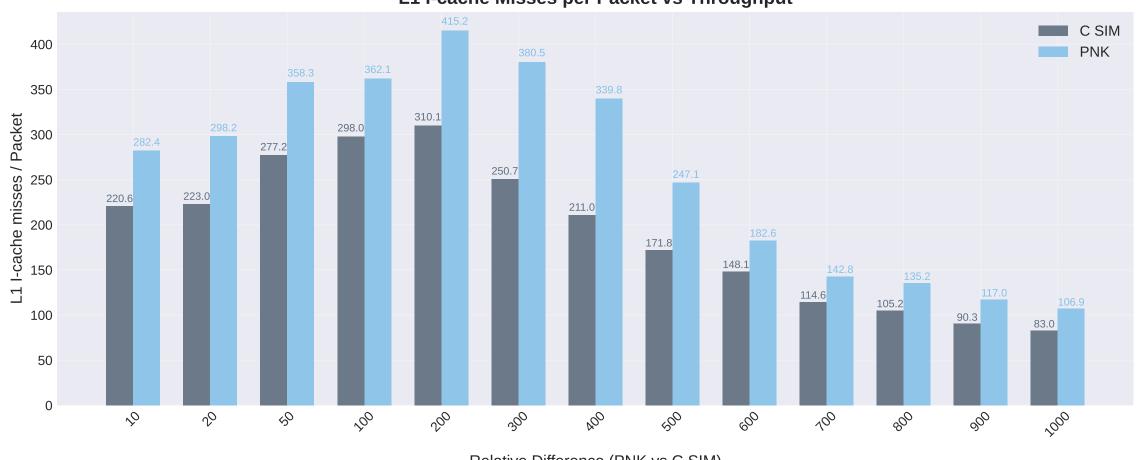
Branch Mispredictions (Total) vs Throughput

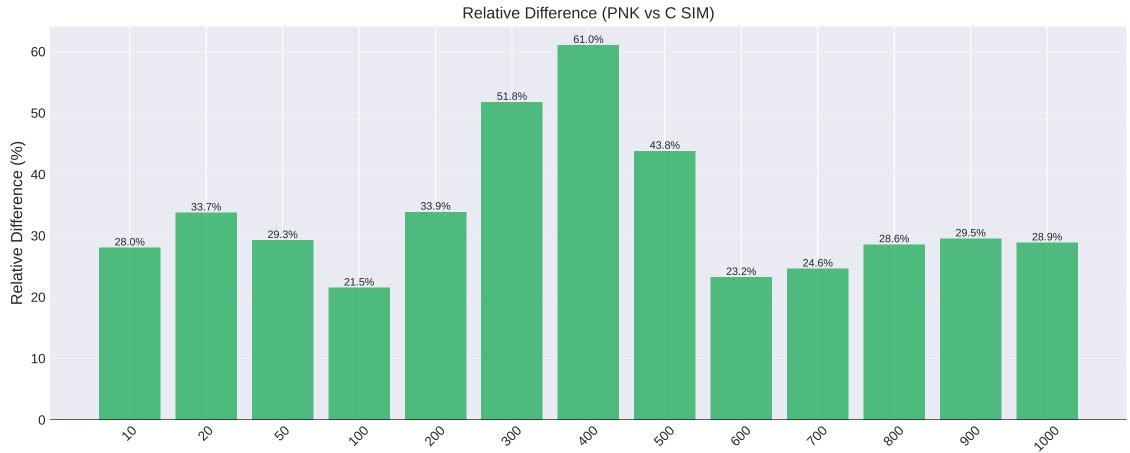




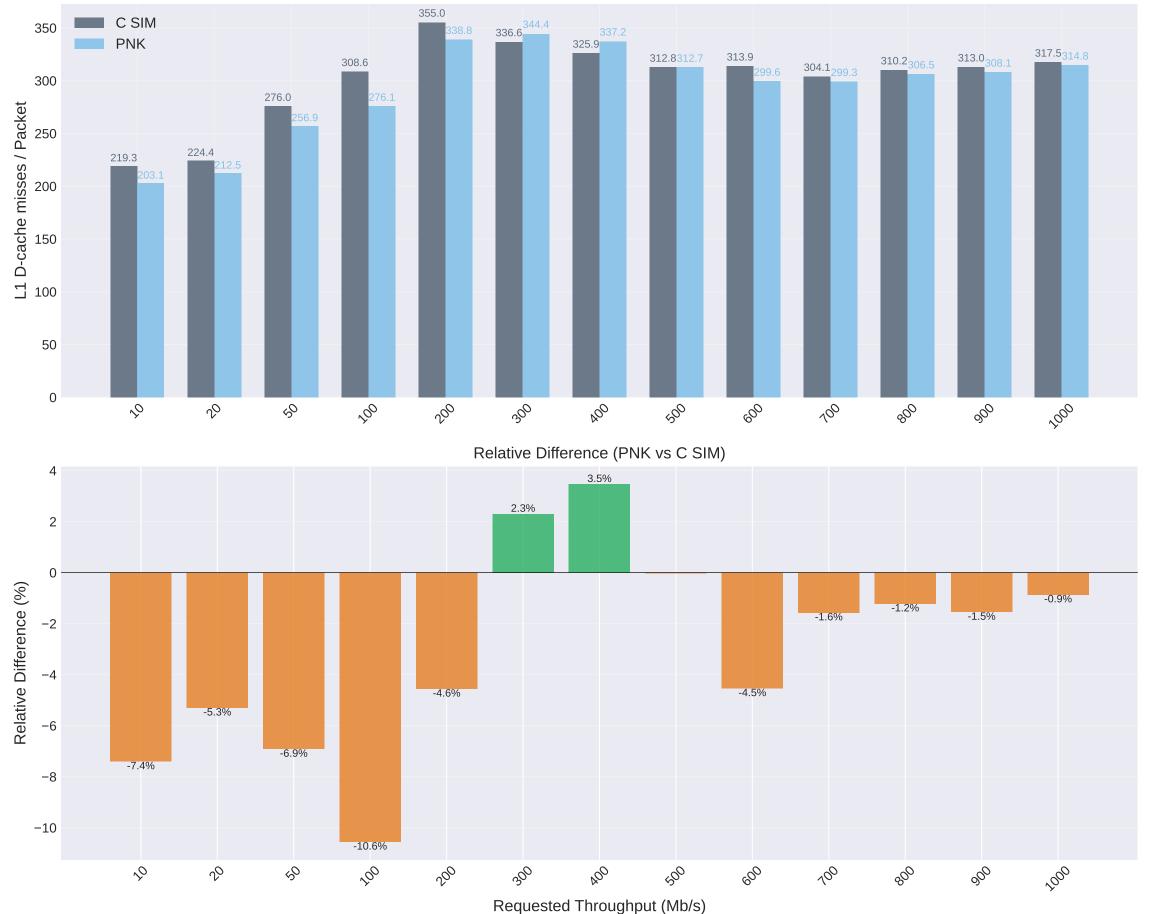


L1 I-cache Misses per Packet vs Throughput

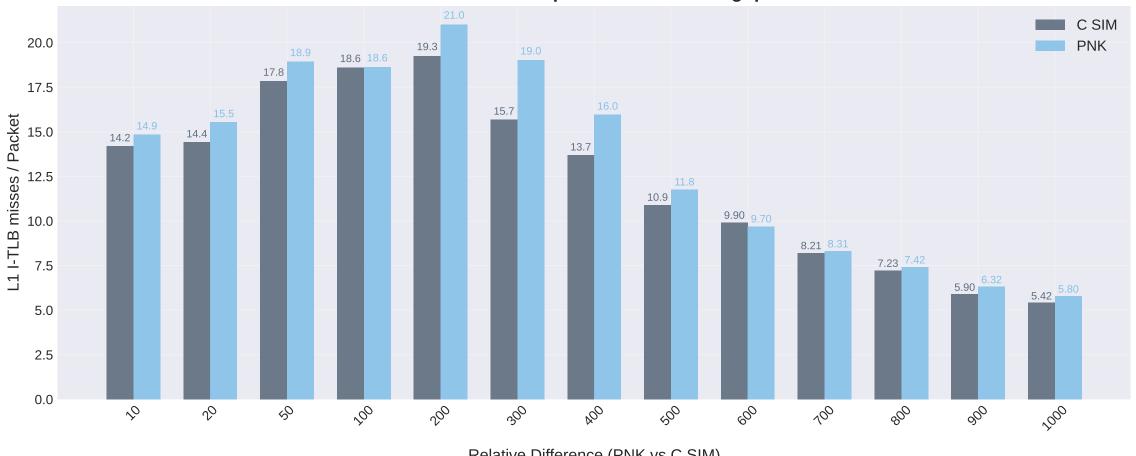


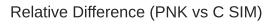


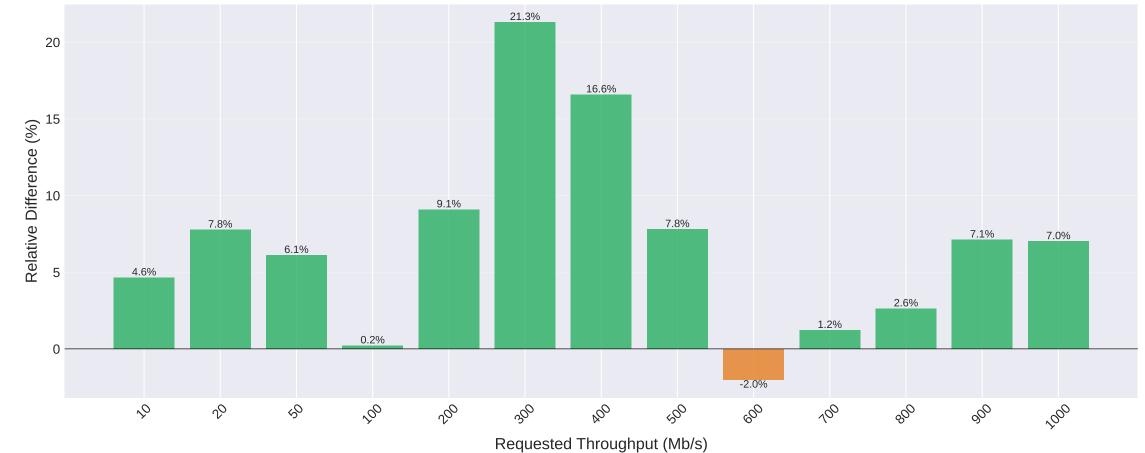
L1 D-cache Misses per Packet vs Throughput



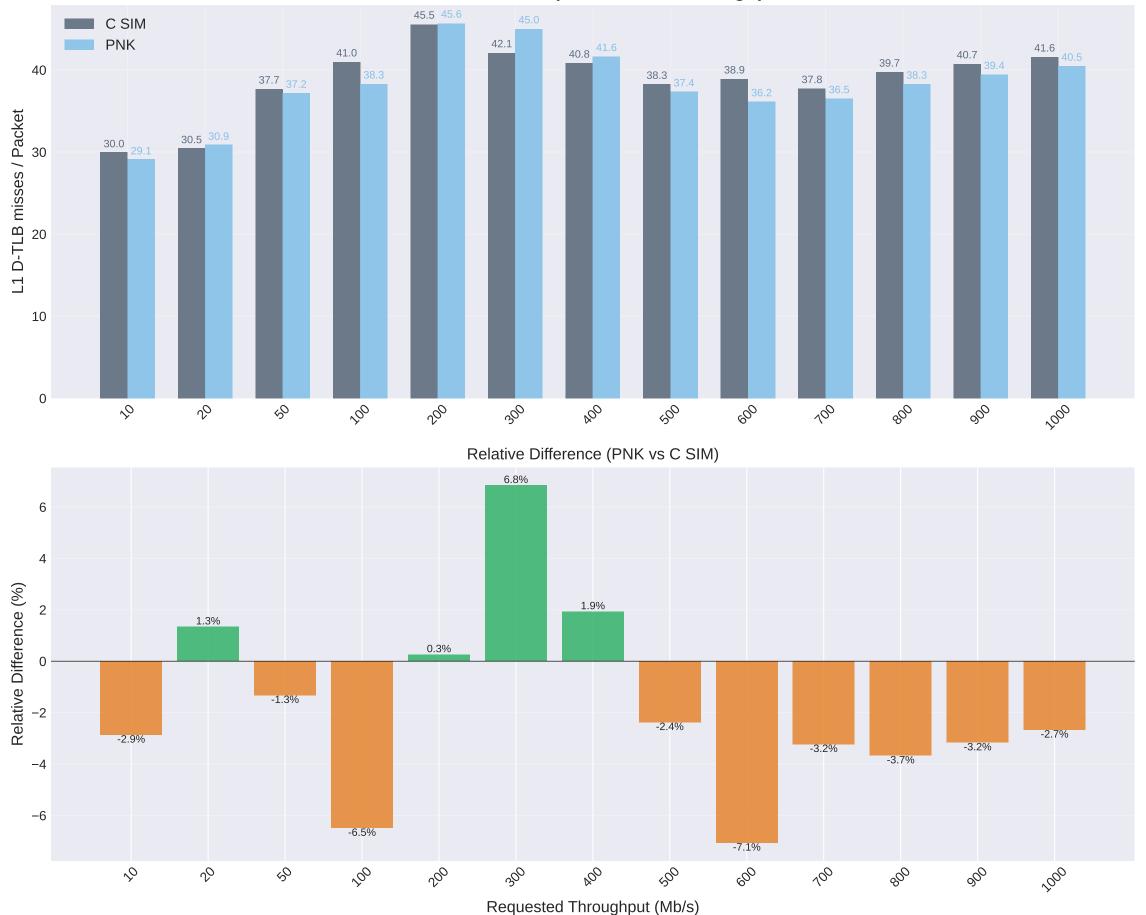




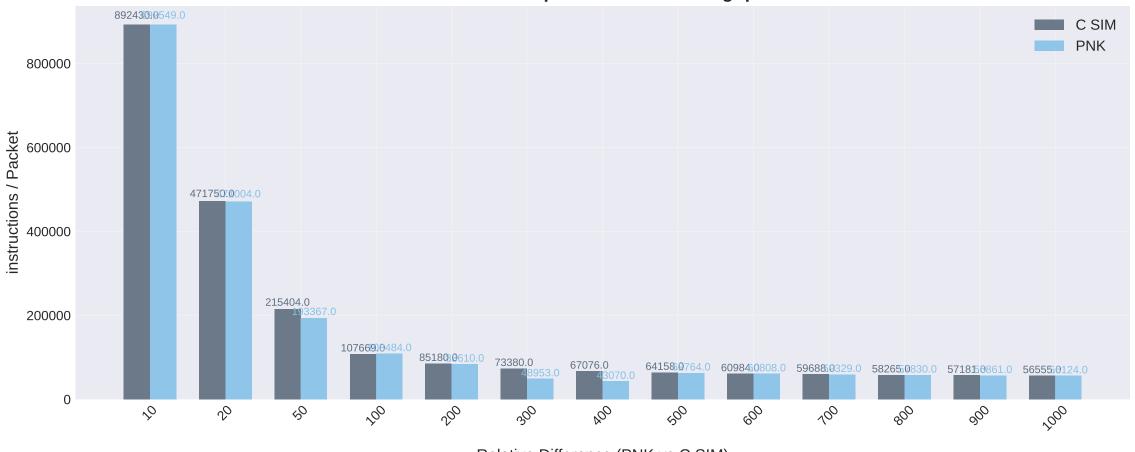


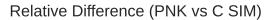


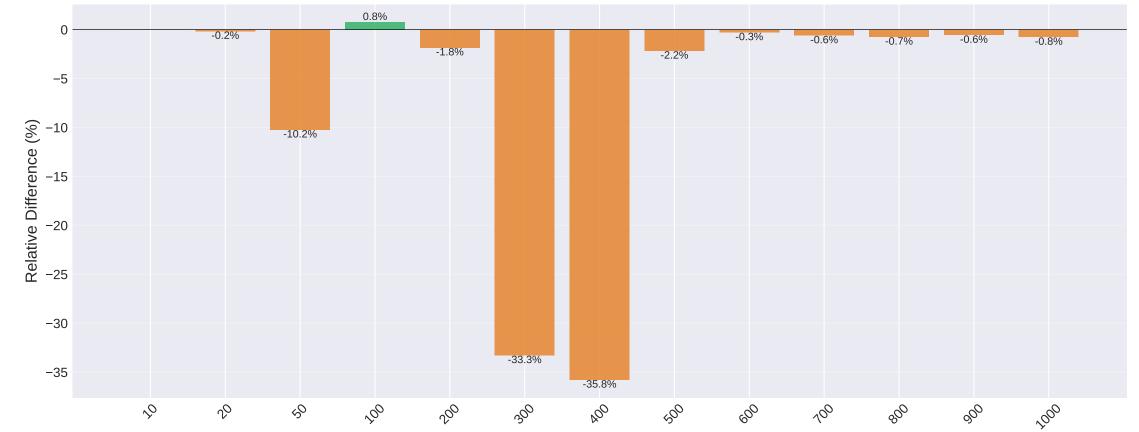
L1 D-TLB Misses per Packet vs Throughput



Instructions per Packet vs Throughput

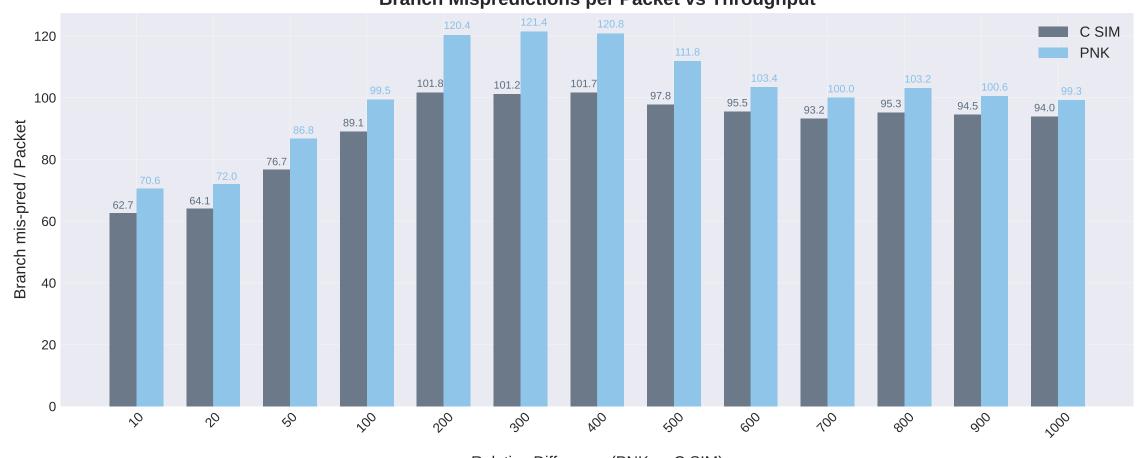


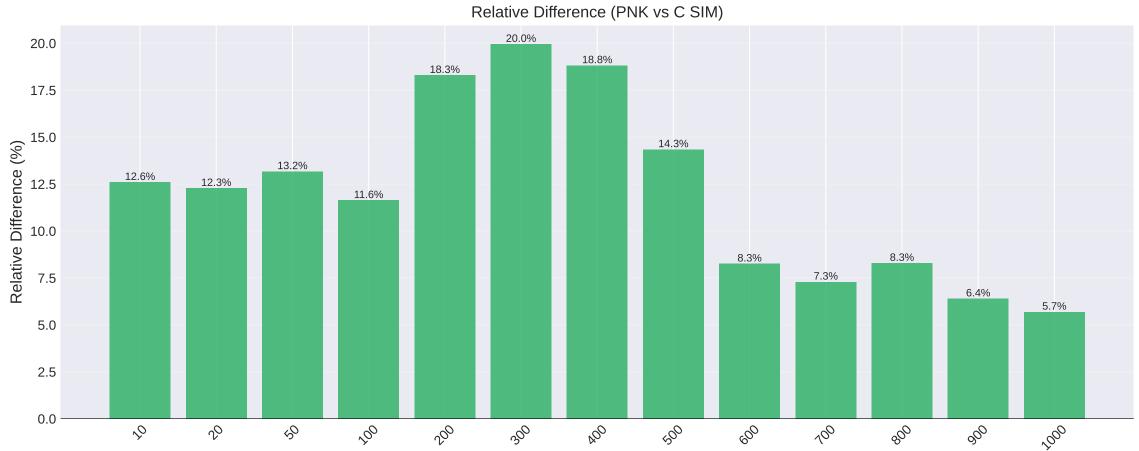


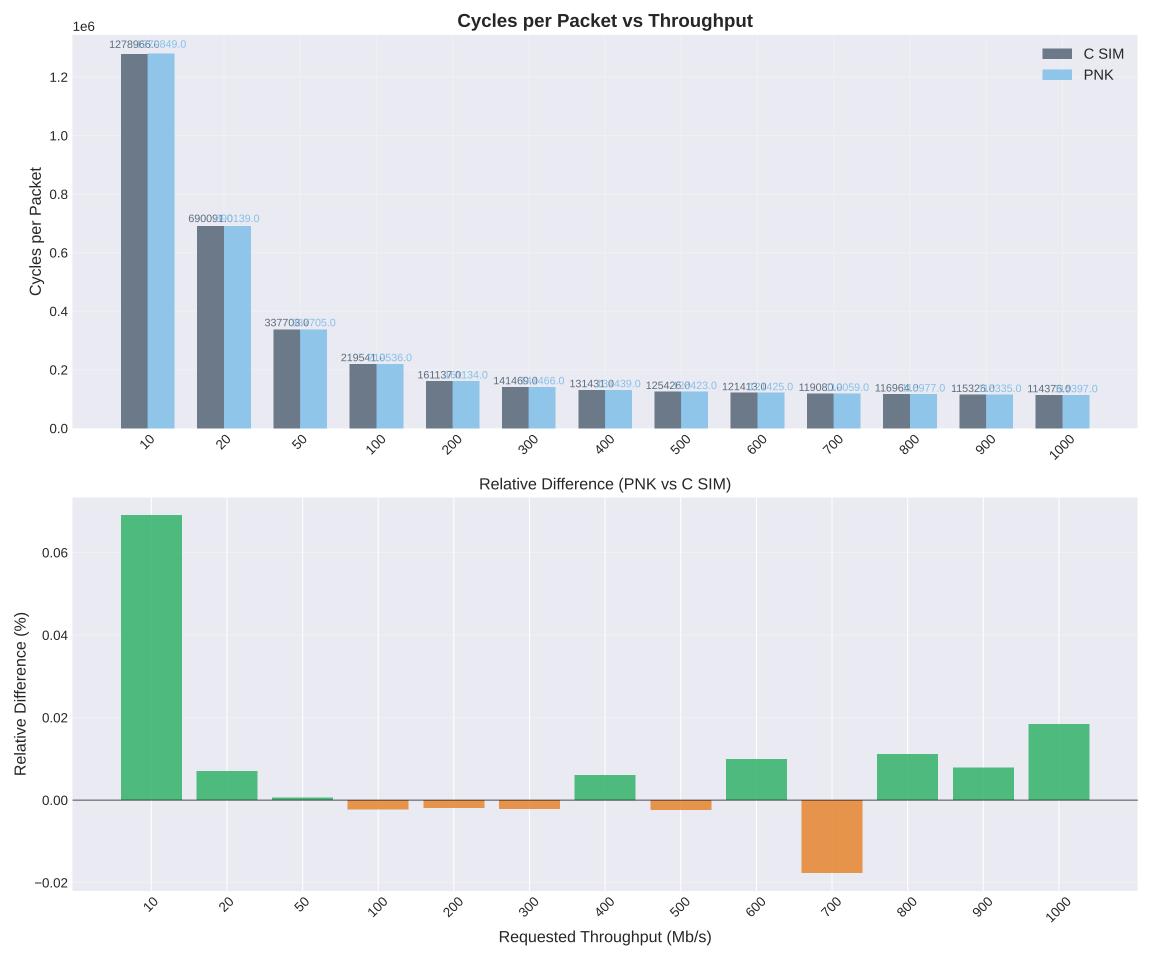


Requested Throughput (Mb/s)

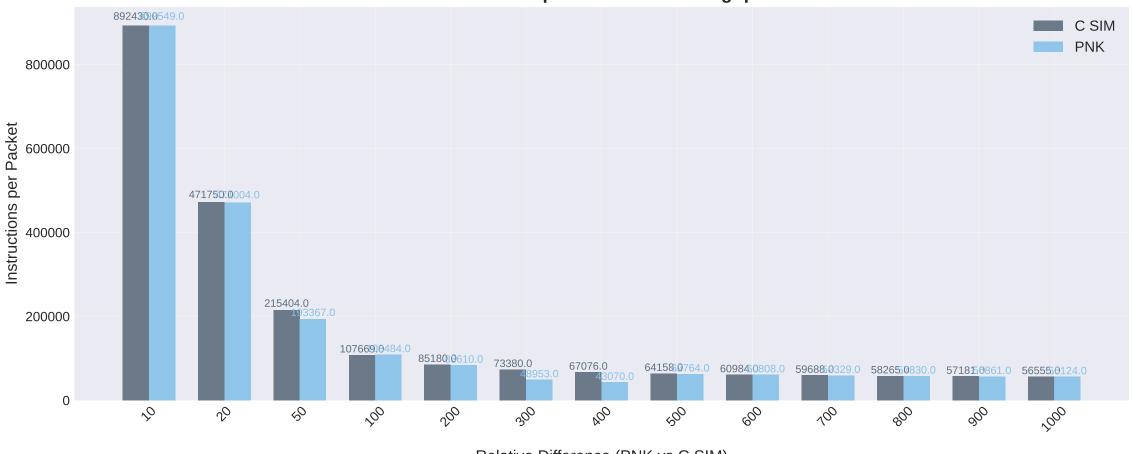
Branch Mispredictions per Packet vs Throughput

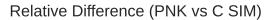


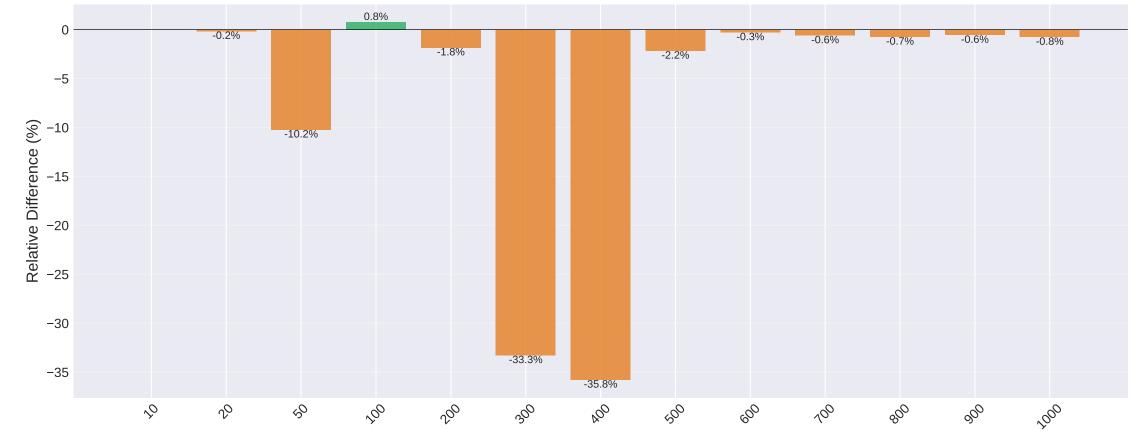




Instructions per Packet vs Throughput

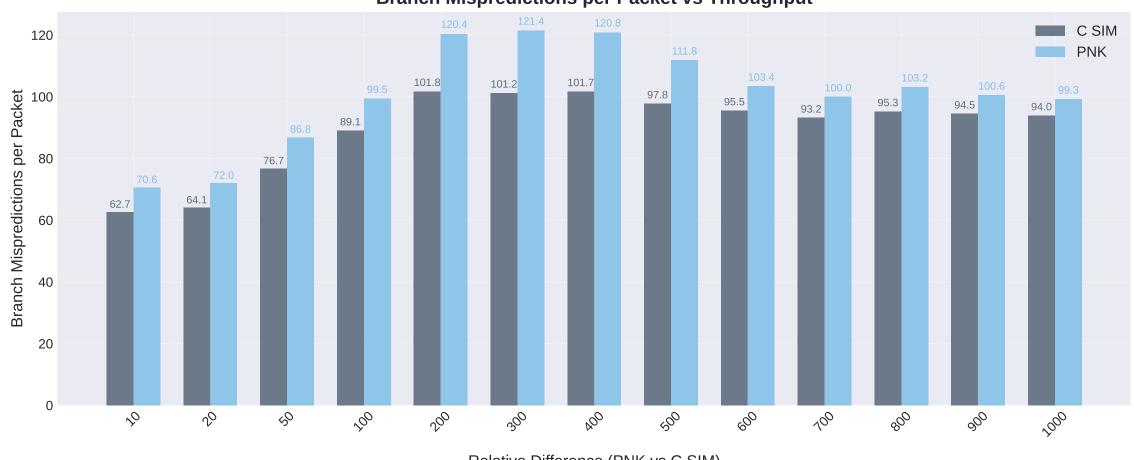


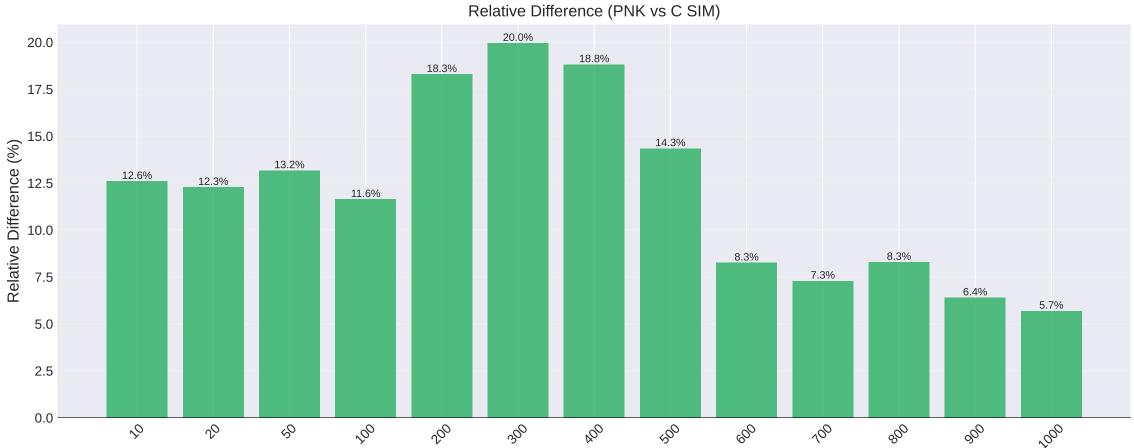


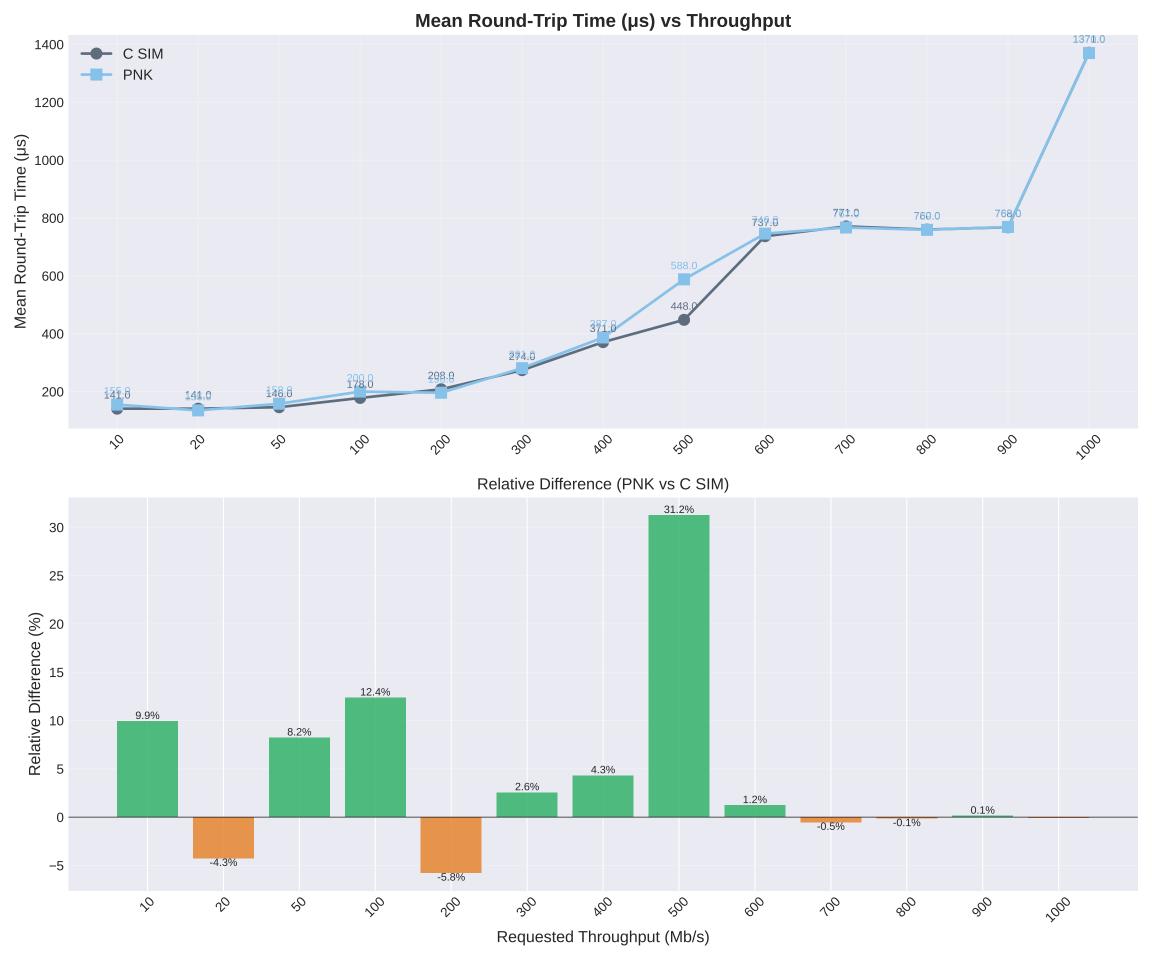


Requested Throughput (Mb/s)

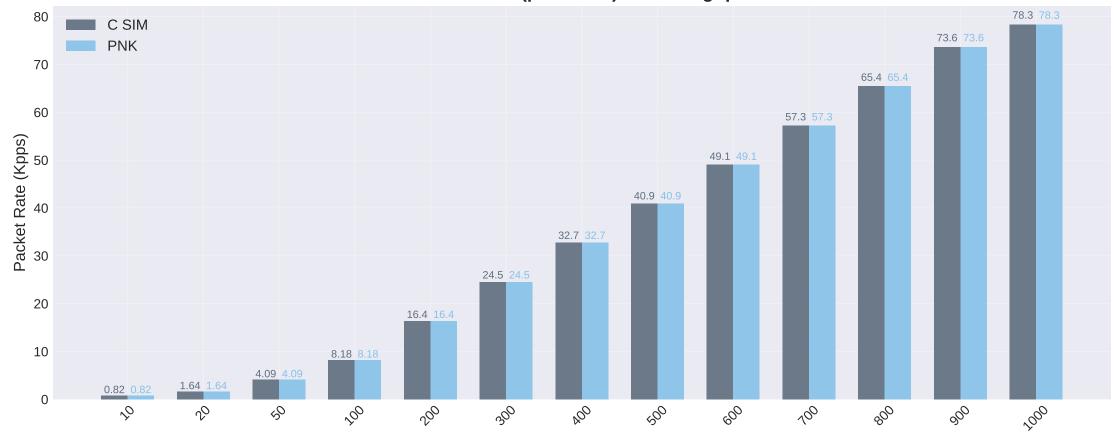
Branch Mispredictions per Packet vs Throughput

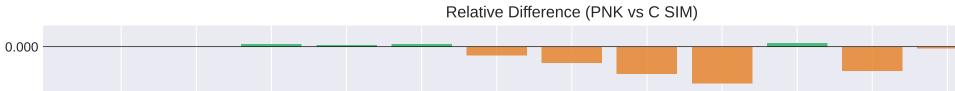


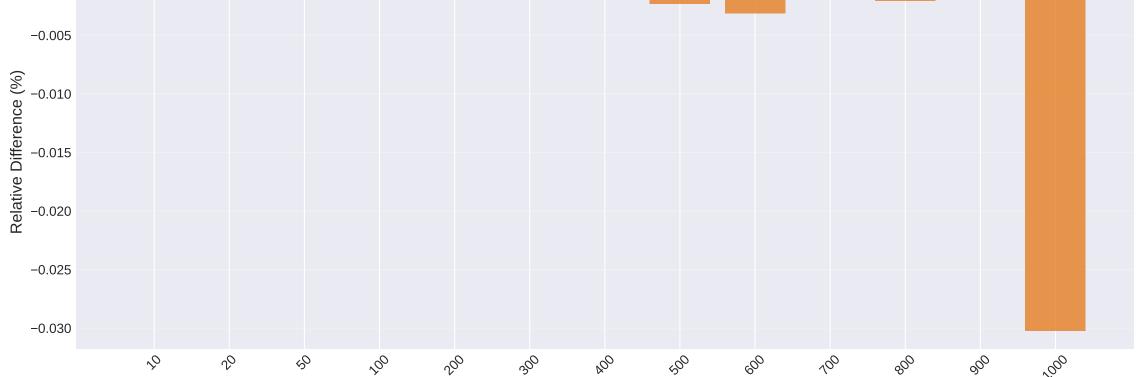


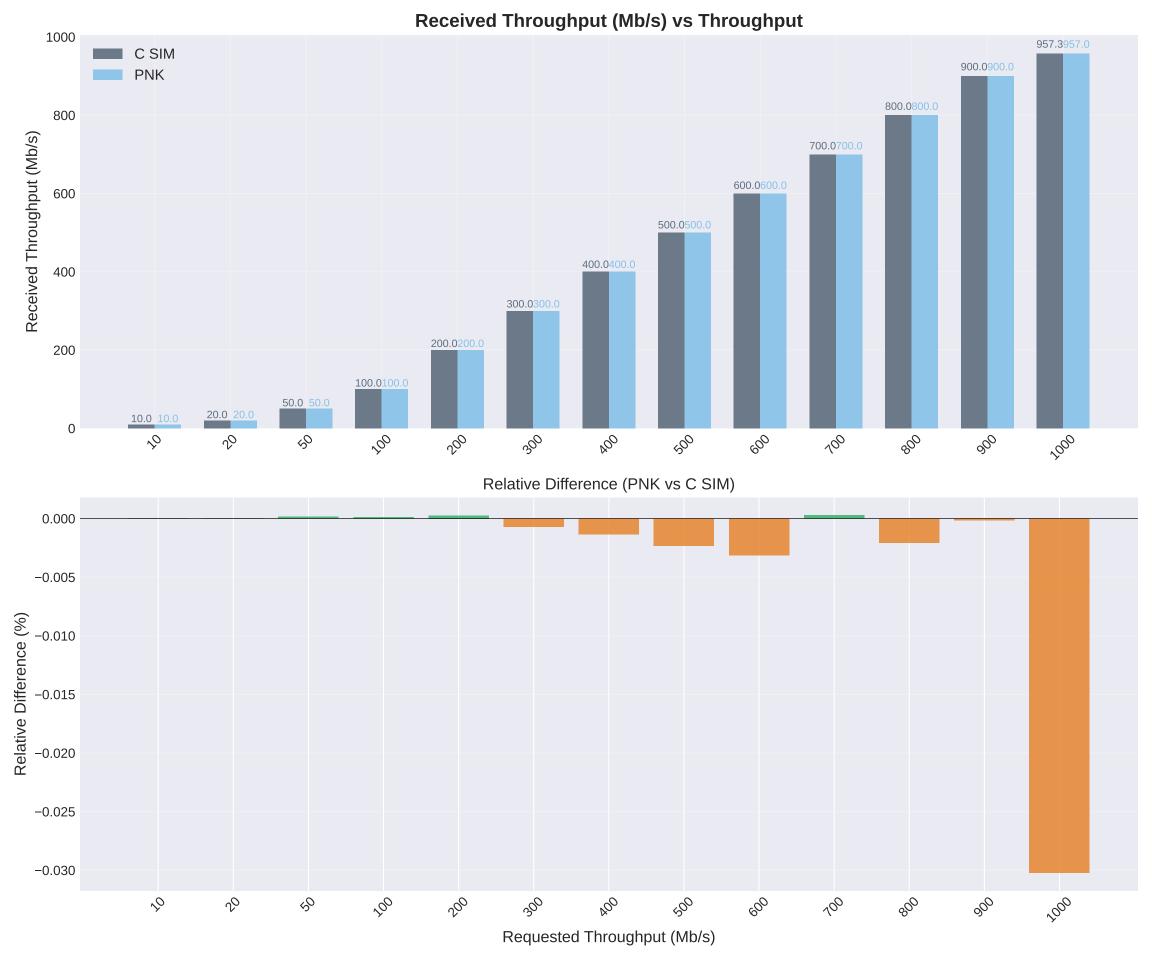












Sent Throughput (Mb/s) vs Throughput 1000.0000.0 C SIM 1000 PNK 900.0900.0 800.0800.0 800 Sent Throughput (Mb/s) 700.0700.0 600.0600.0 600 500.0500.0 400.0400.0 400 300.0300.0 200.0200.0 200 100.0100.0 50.0 50.0 20.0 20.0 10.0 10.0 0 2000 \$ 200 200 300 NOO 400 600 700 900 900 20 50 Relative Difference (PNK vs C SIM) 0.0008 0.0006 0.0004 0.0004

Relative Difference (%)

0.0000

-0.0002 -0.0004 -0.0006 30 20 200 200 300 NOO 400 600 100 900 50