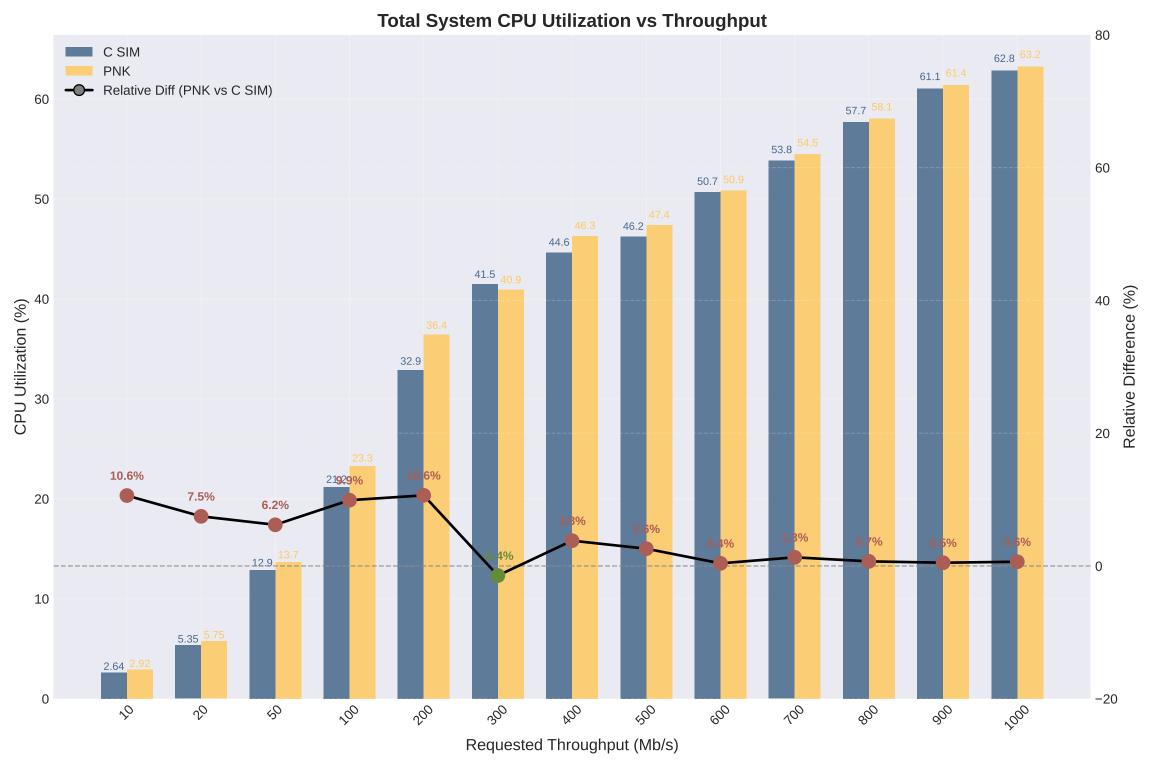
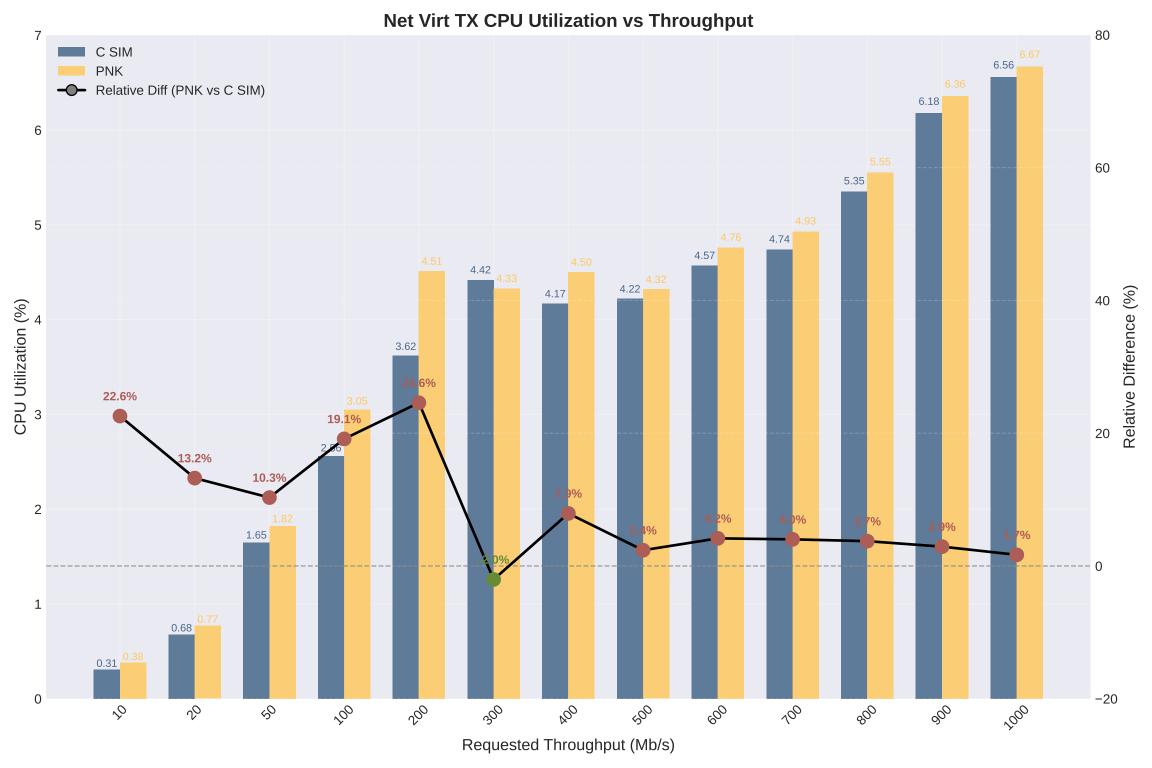
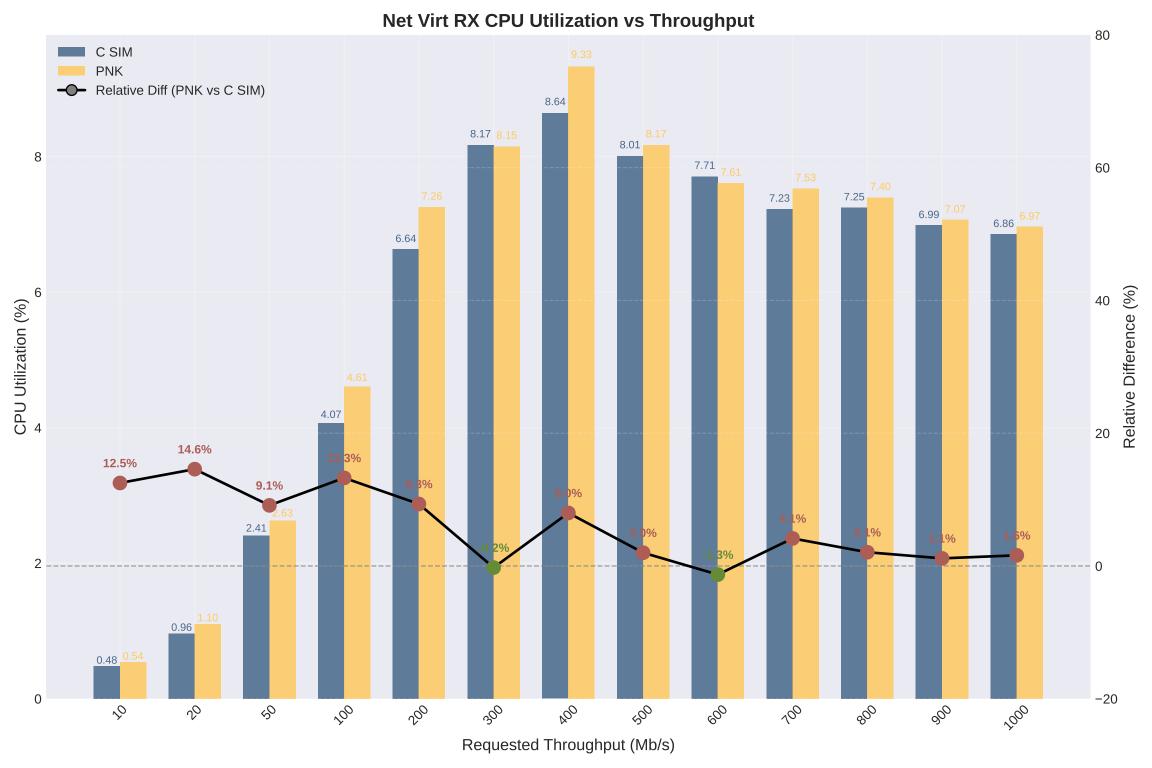


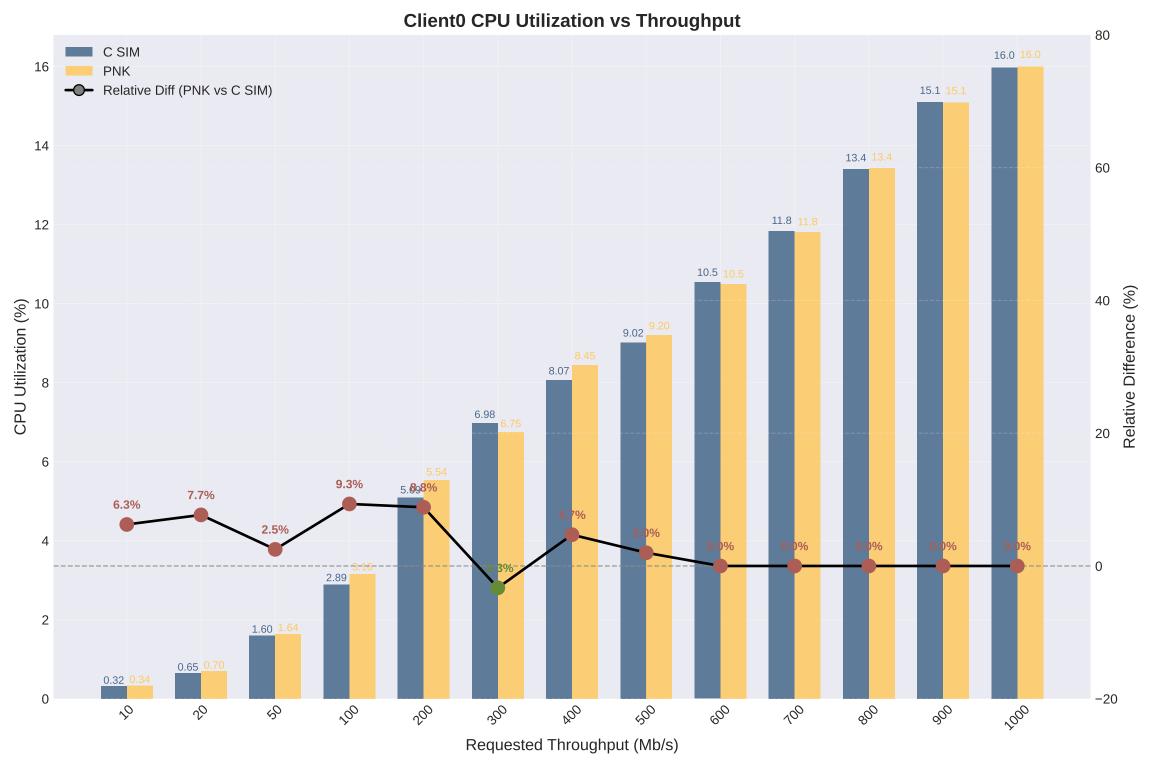
Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3956.4 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.gd 61._{1%} 600.0600.0 600 CPU Utilization (%) 500.0500.0 46.3% 44.6% 44.5% 400 300.0300.0 200.0200.0 200 20 100.0100.0 50.0 50.0 2.8% 20.0 0 800 900 2000 200 400 700 300 NOO 600 100 30 20 50 Requested Throughput (Mb/s)

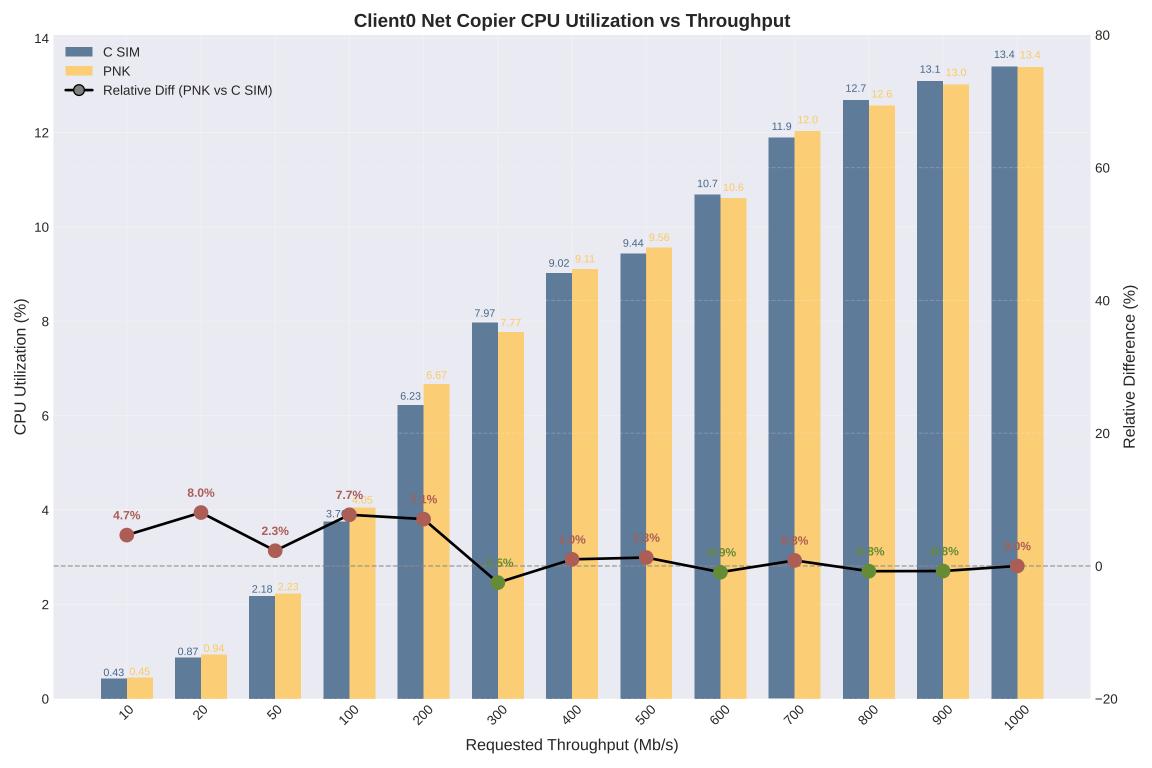


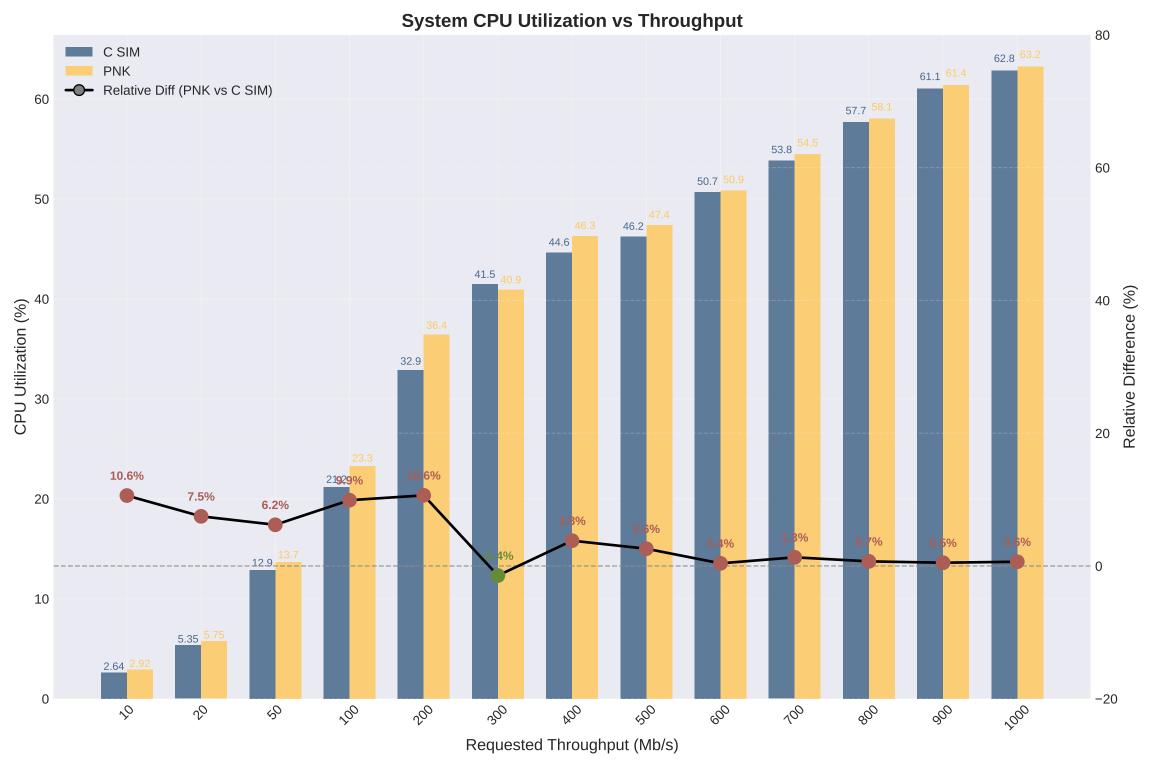


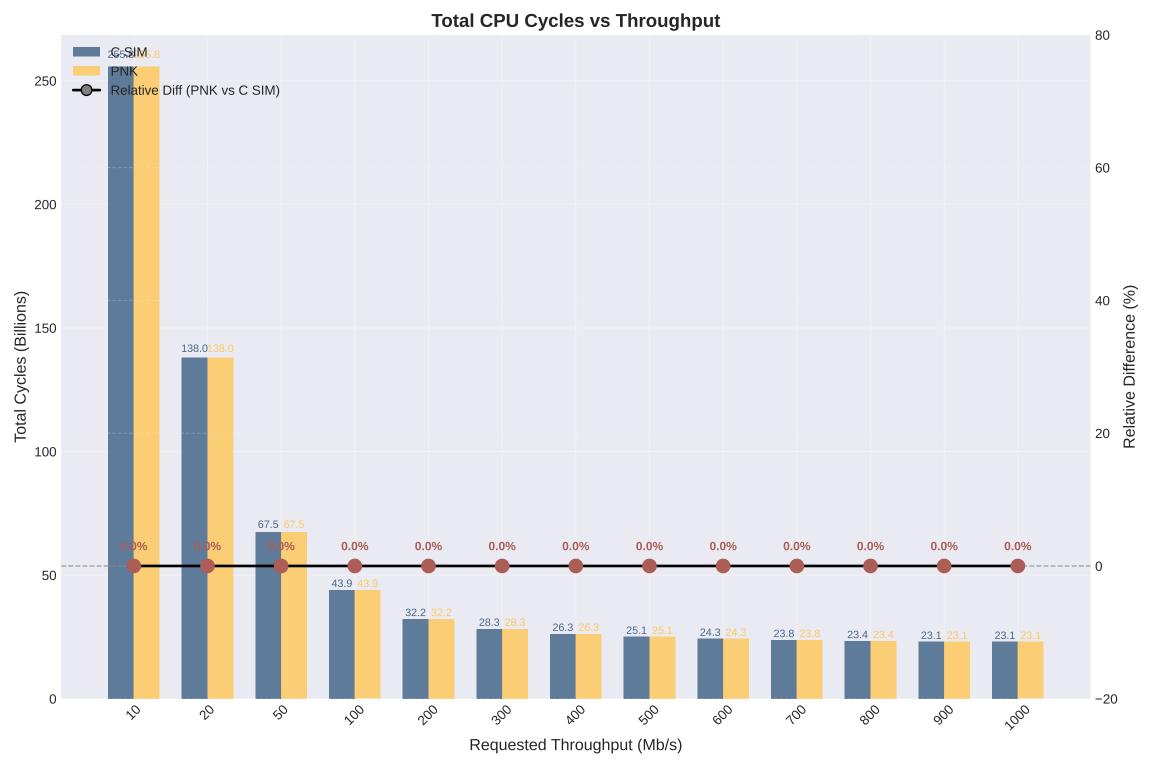


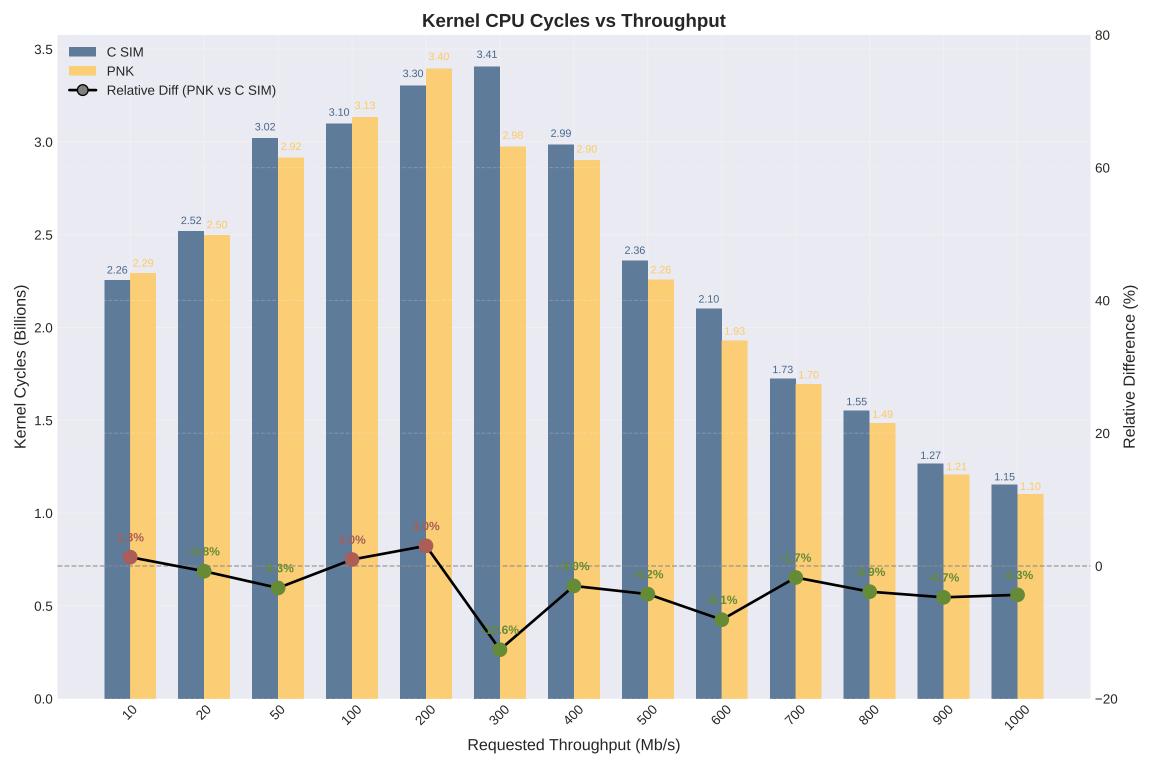


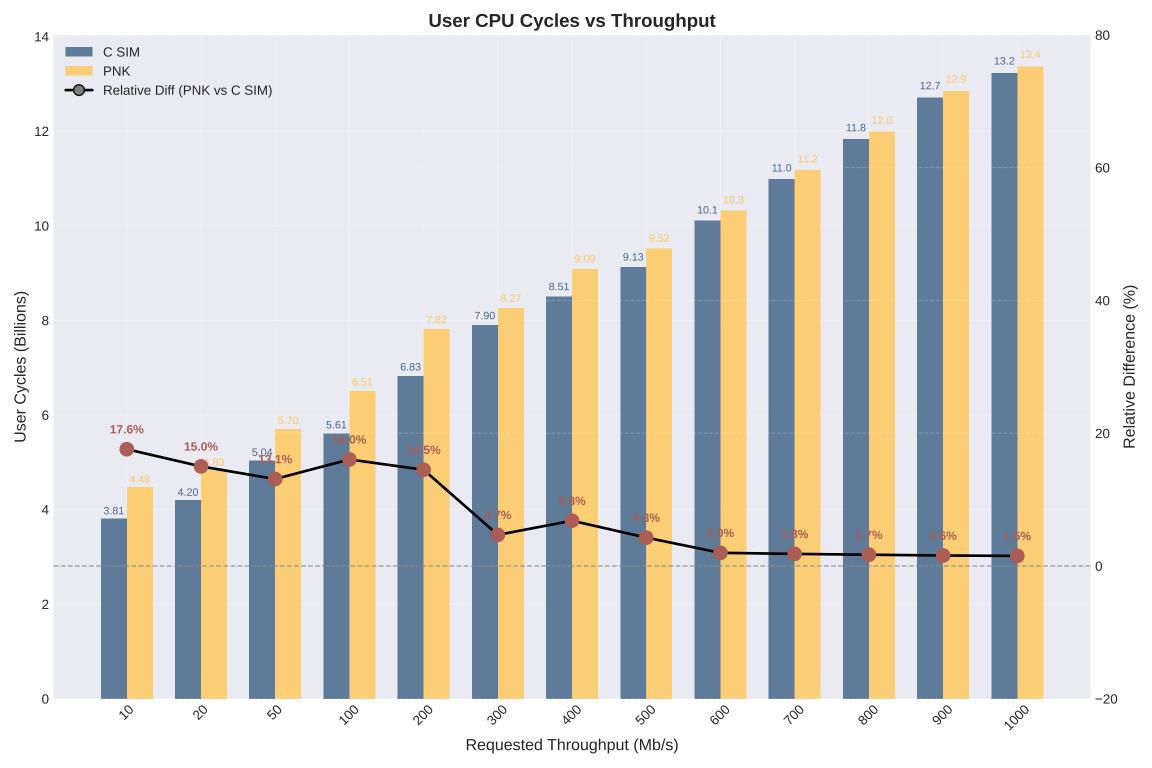


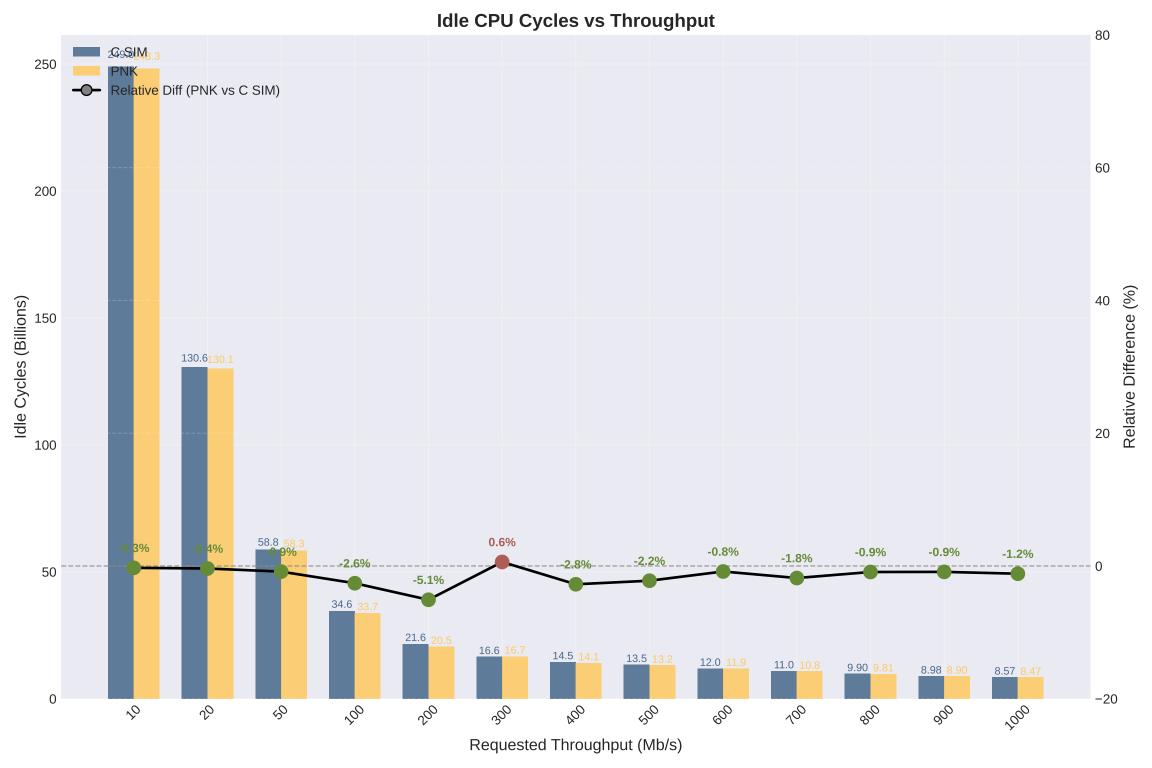


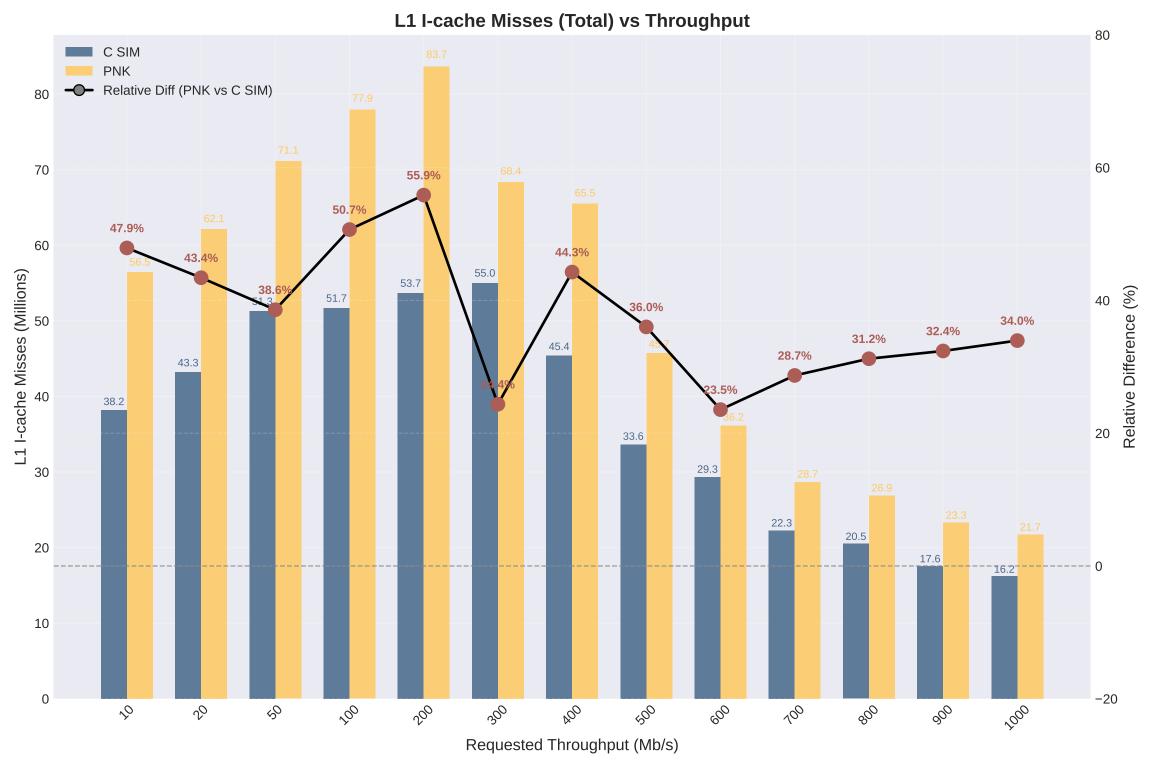


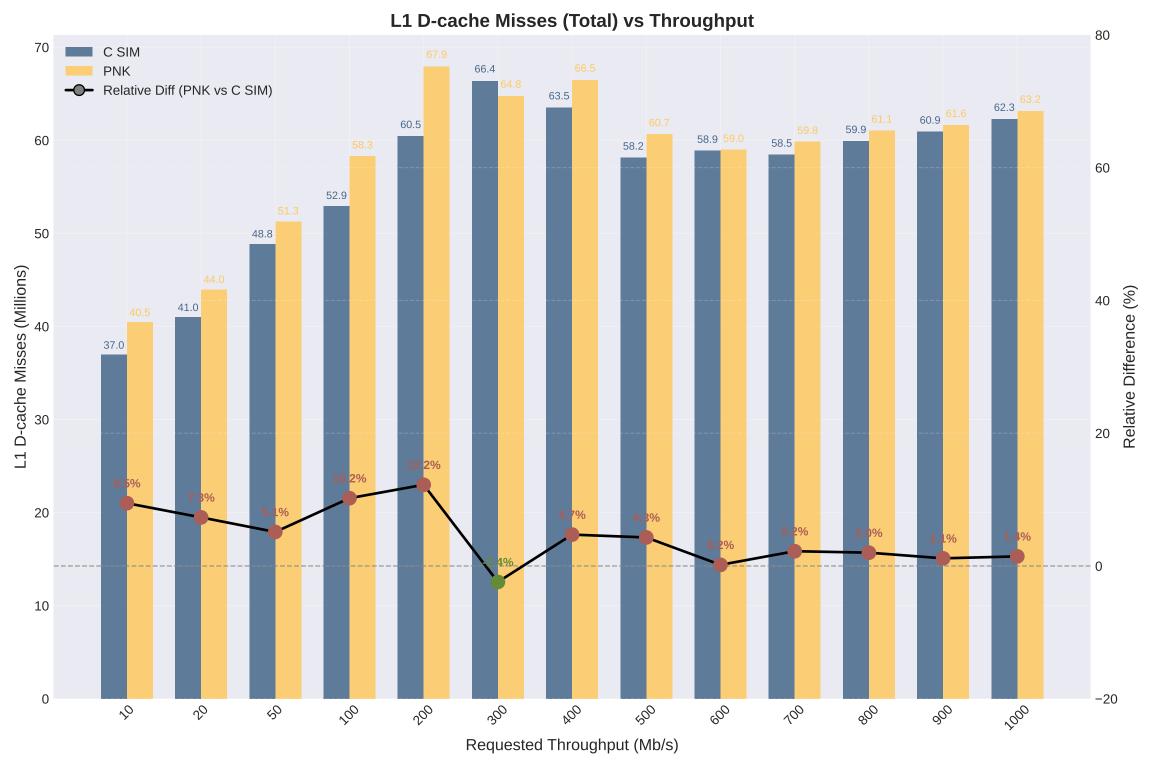












L1 I-TLB Misses (Total) vs Throughput 80 C SIM PNK Relative Diff (PNK vs C SIM) 4.0 60 3.5 3.11 3.11 3.06 3.06 3.0 L1 I-TLB Misses (Millions) Relative Difference (%) 2.62 2.47 2.31 27.5% 25.8% 23.8% 21.2% 18.5% 1.78 1.61 1.5 1.37 1.24 1.01 1.0 0.93 0.5 0.0 -20 Þ 700 200 300 NOO 400 600 100 900 900 20 50 Requested Throughput (Mb/s)

