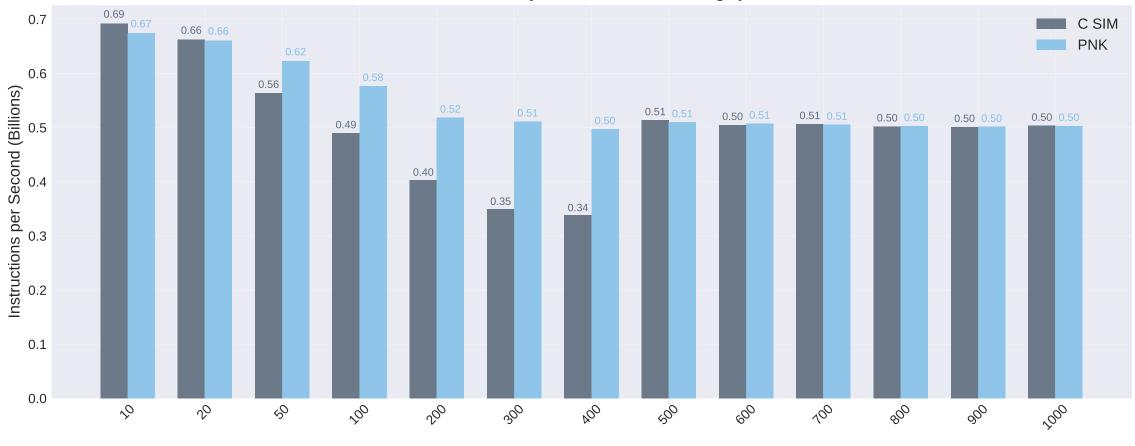
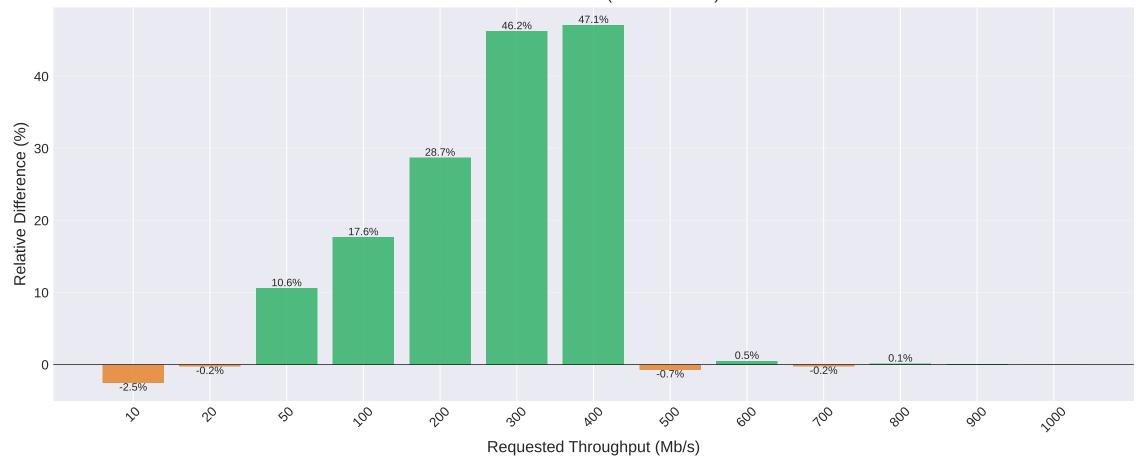
Instructions per Second vs Throughput



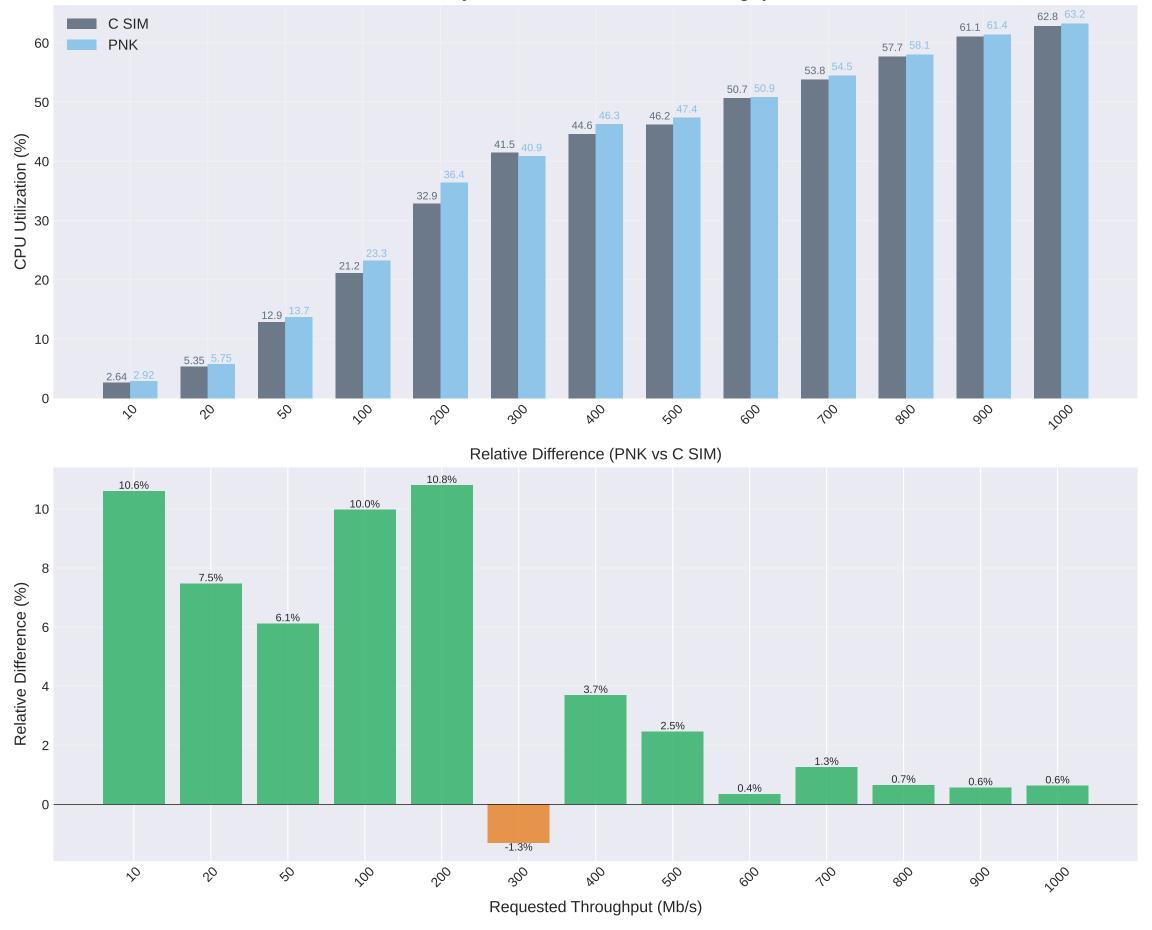




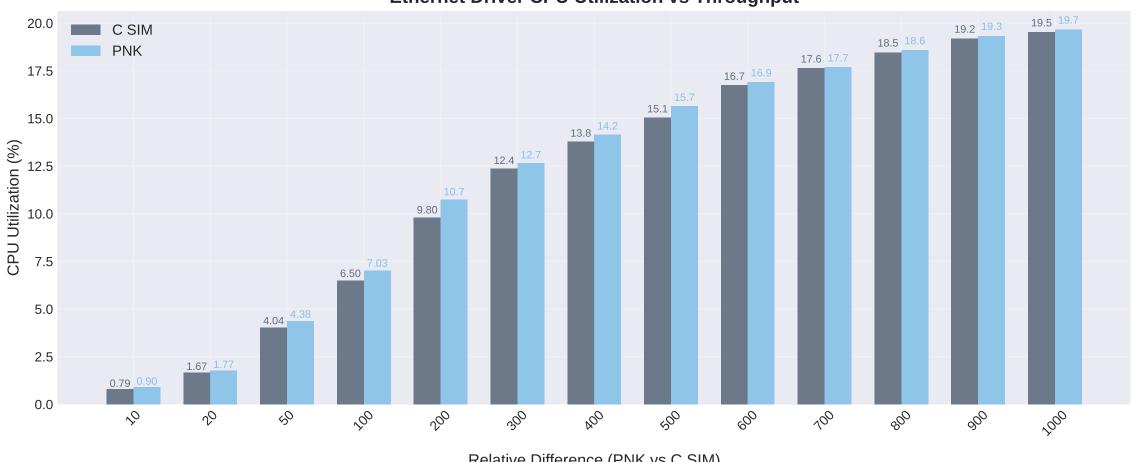
Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3956.4 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.2% 61.4% 600.0600.0 600 CPU Utilization (%) 58.1% 54.5% 500.0500.0 50.9% 400 300.0300.0 200.0200.0 200 20 100.0100.0 50.0 50.0 0 900 200 go NOO 400 2000 700 600 100 900 30 20 50

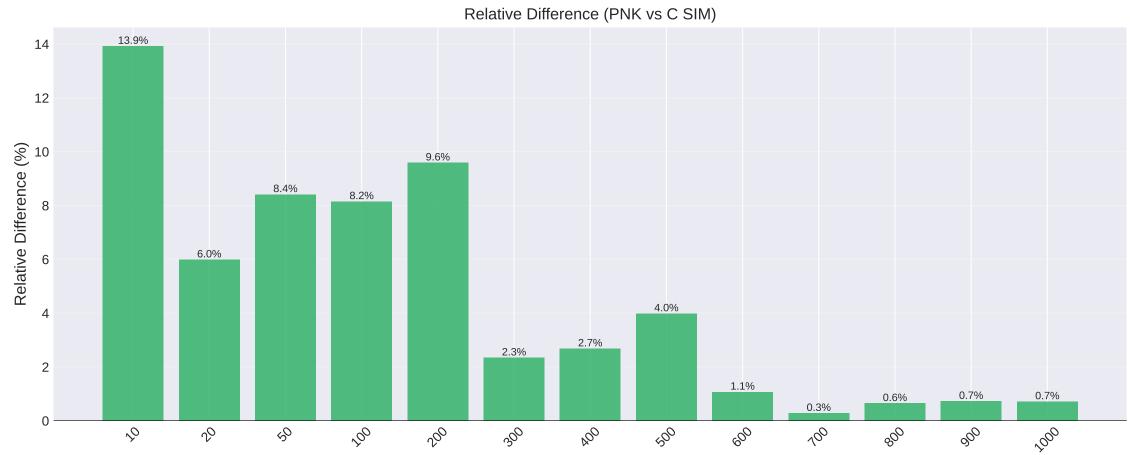
Requested Throughput (Mb/s)

Total System CPU Utilization vs Throughput



Ethernet Driver CPU Utilization vs Throughput

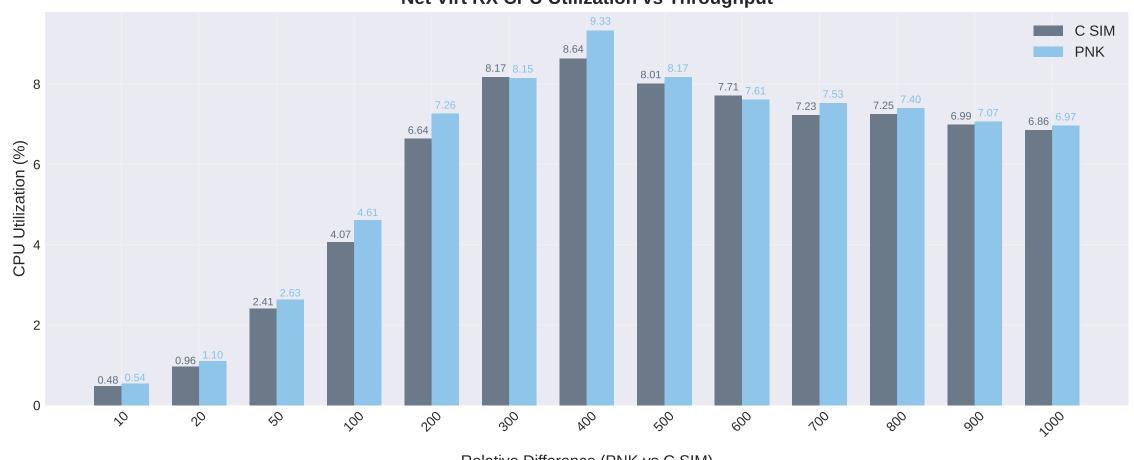


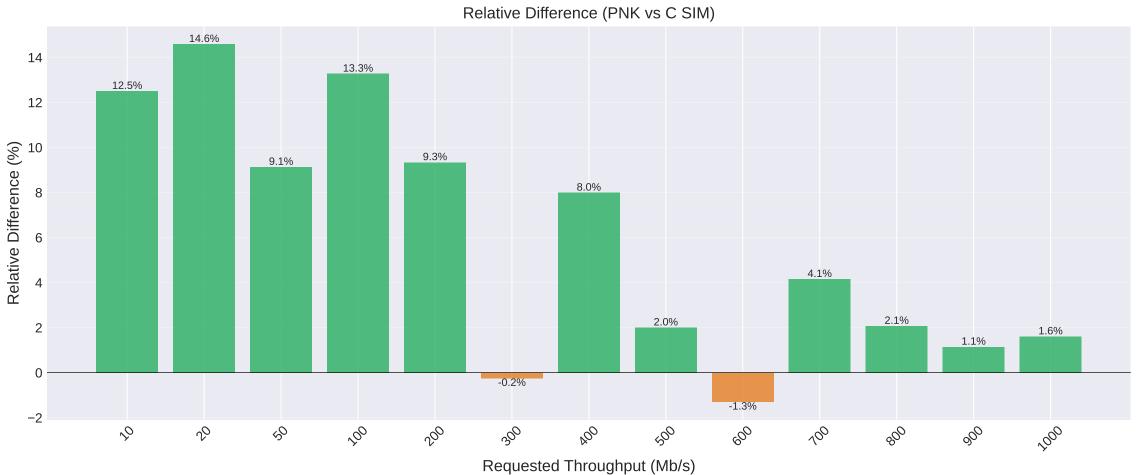


Requested Throughput (Mb/s)

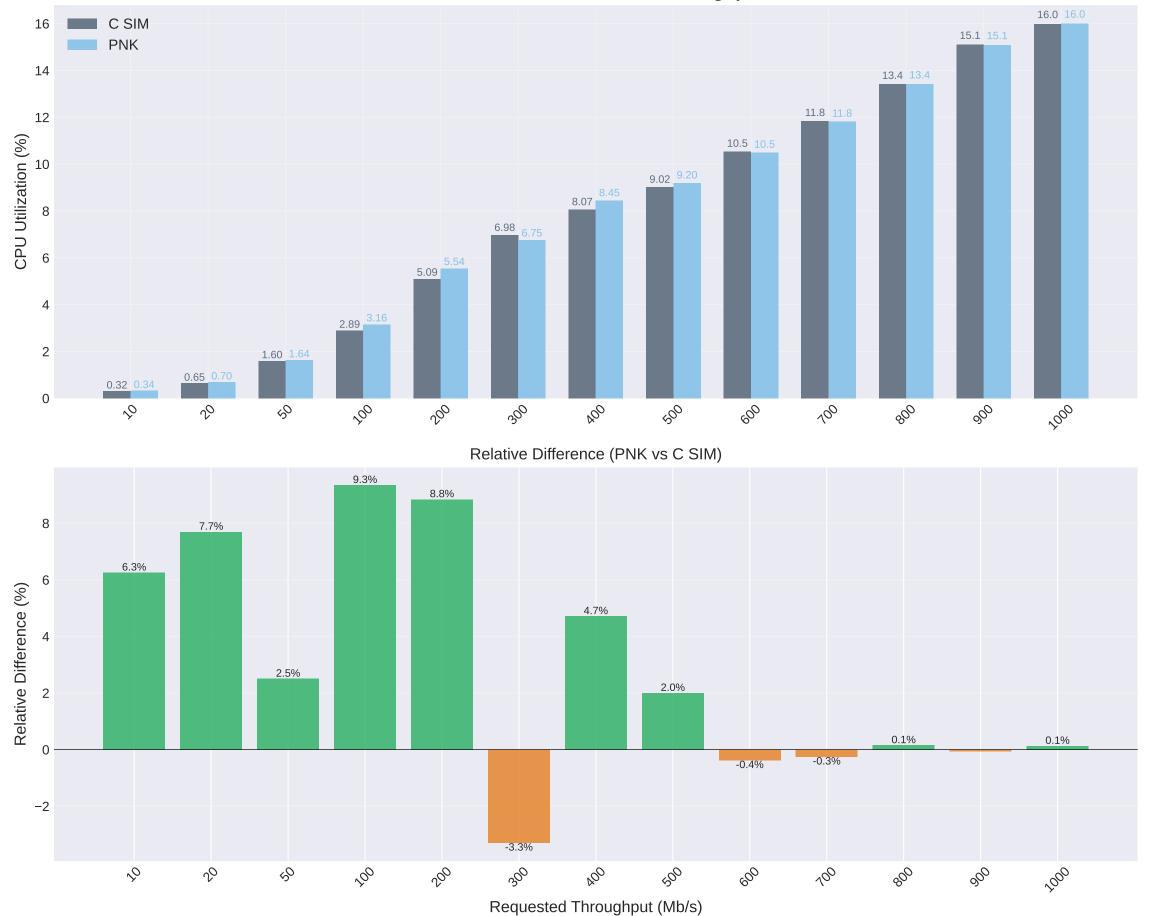


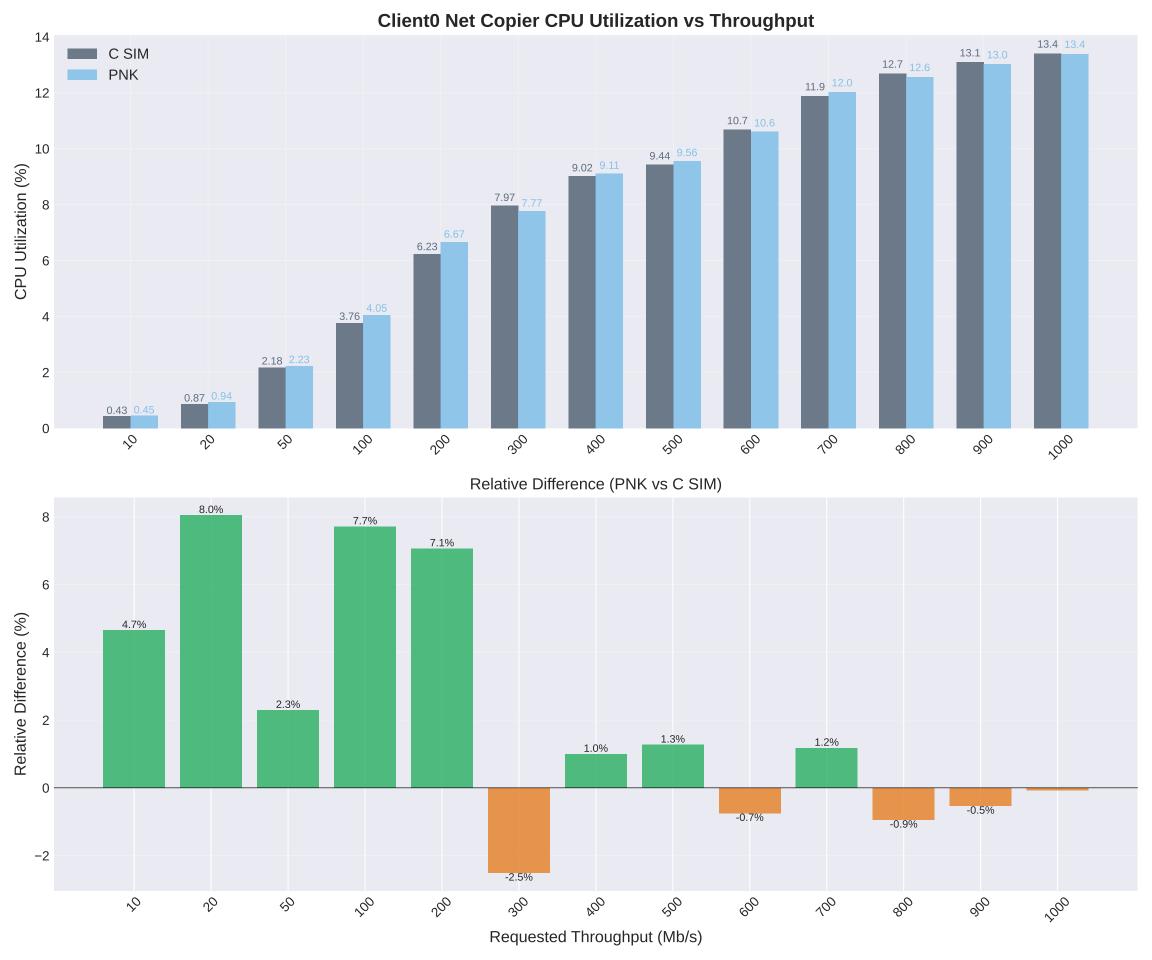
Net Virt RX CPU Utilization vs Throughput



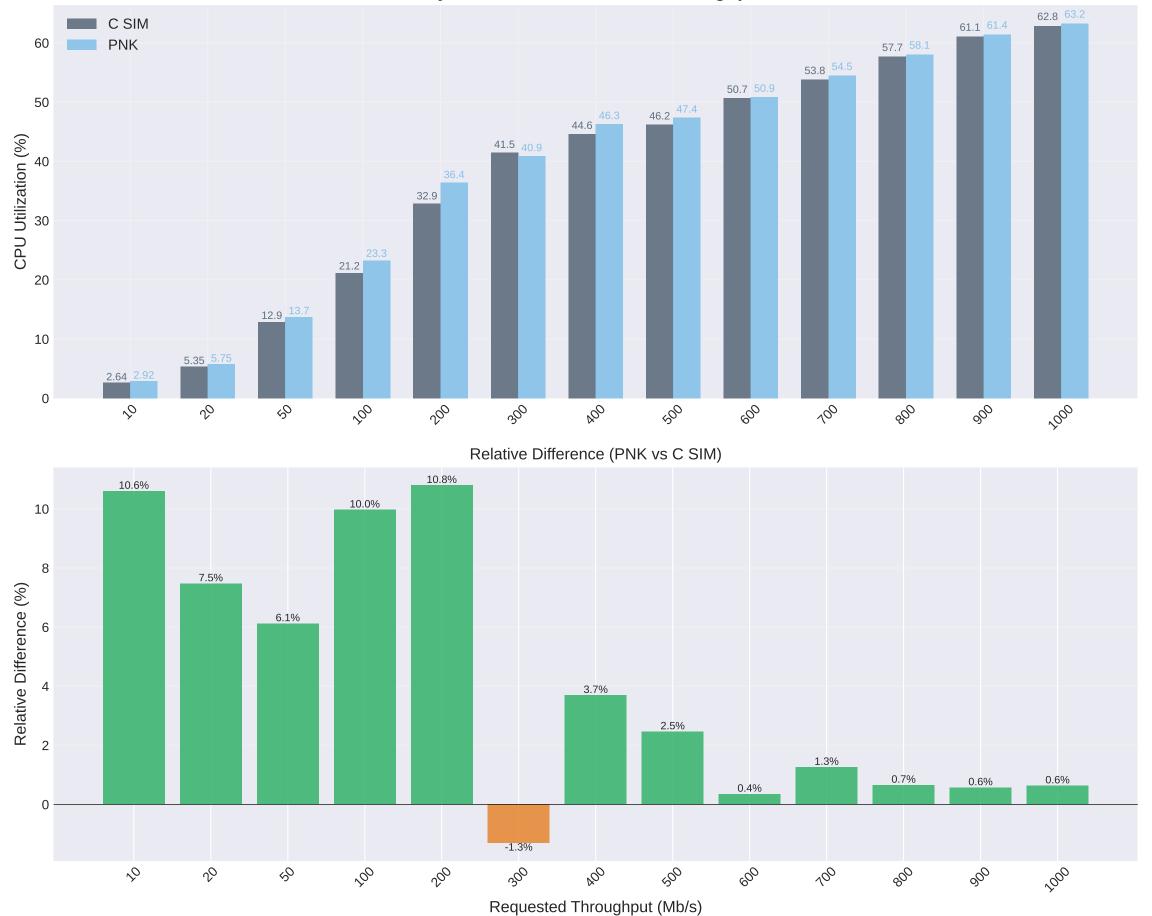


Client0 CPU Utilization vs Throughput



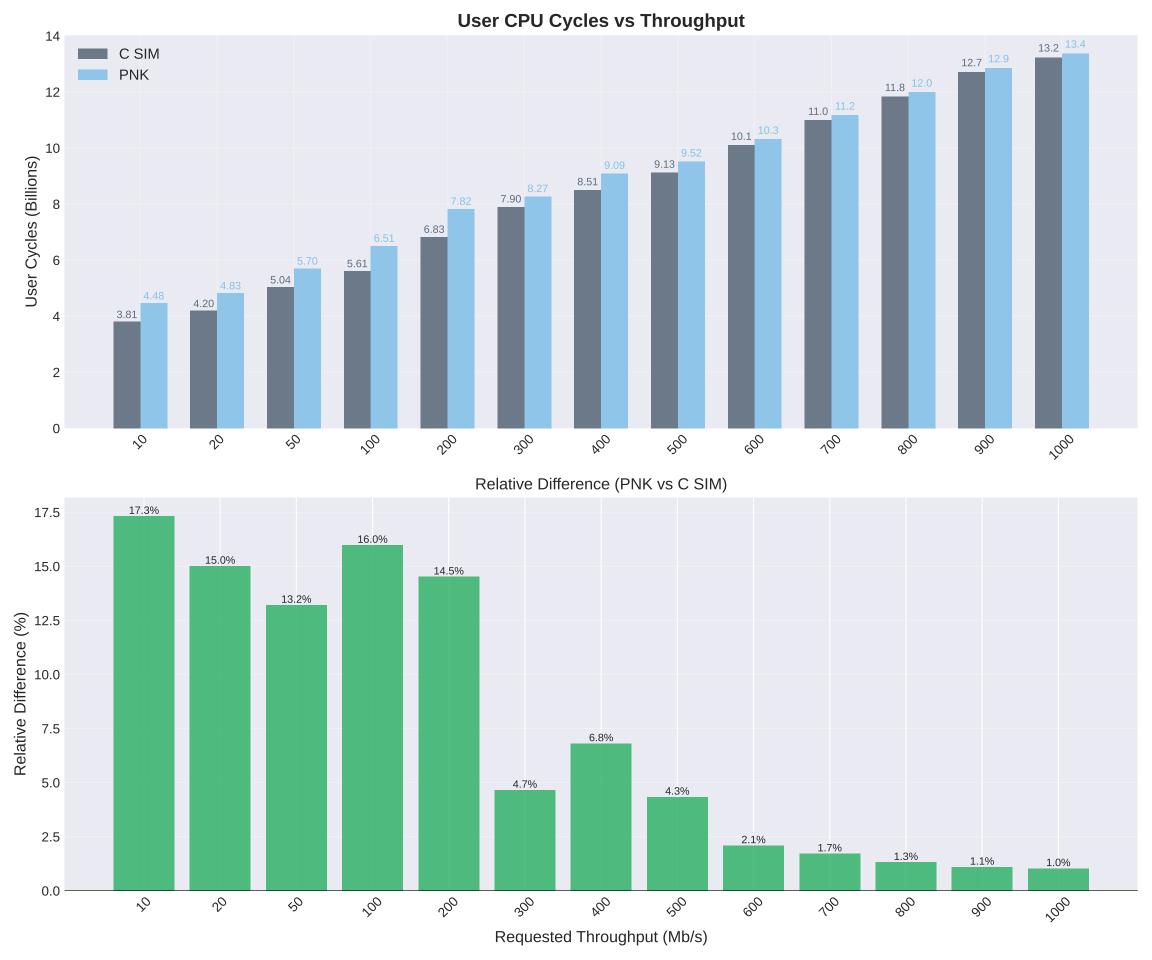


System CPU Utilization vs Throughput



Total CPU Cycles vs Throughput 255.8255.8 C SIM 250 PNK 200 Total Cycles (Billions) 138.0138.0 67.5 67.5 50 43.9 43.9 32.2 32.2 28.3 28.3 26.3 26.3 25.1 25.1 24.3 24.3 23.8 23.8 23.4 23.4 23.1 23.1 23.1 23.1 0 NOO Ď 100 200 300 400 600 100 900 900 1000 20 50 Relative Difference (PNK vs C SIM) 0.04 0.02 Relative Difference (%) -0.04 30 20 50 200 200 300 NOO 500 600 700 900 Requested Throughput (Mb/s)

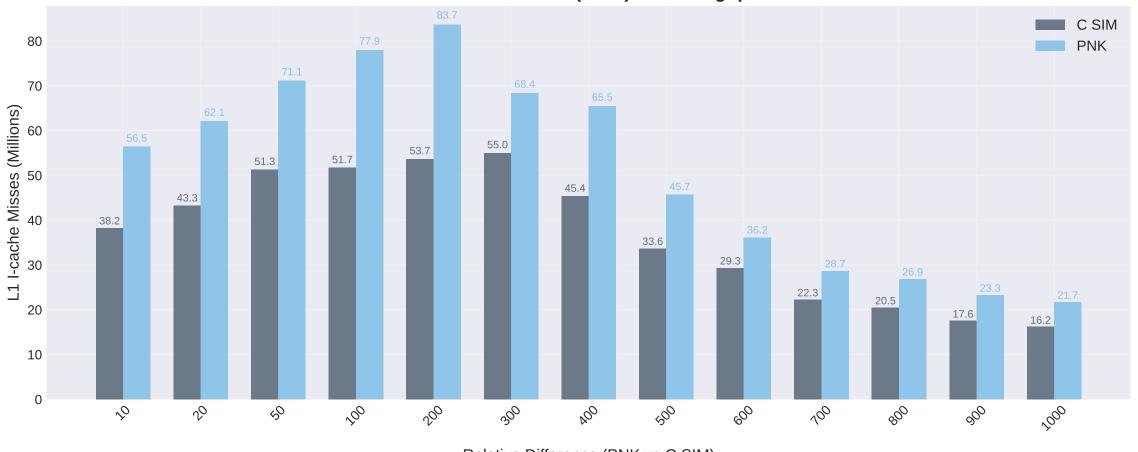
Kernel CPU Cycles vs Throughput 3.30 3.5 C SIM 3.10 3.13 PNK 3.02 2.99 3.0 2.52 2.50 Kernel Cycles (Billions) 1.5 1.0 2.36 2.26 2.29 2.10 1.73 1.70 1.55 1.27 1.15 0.5 0.0 200 200 300 400 400 600 700 800 900 2000 30 20 60 Relative Difference (PNK vs C SIM) 2.7% 2 1.6% 1.2% 0 --0.9% Relative Difference (%) -1.7% -2 -2.8% -3.5% -4 -4.3% -4.4% -4.4% -4.7% -6 -8 -8.1% -10 -12 -12.7% 30 200 200 300 500 600 100 900 20 SO NO Requested Throughput (Mb/s)



Idle CPU Cycles vs Throughput 249.0248.3 250 C SIM PNK 200 Idle Cycles (Billions) 130.6130.1 58.8 58.3 50 34.6 33.7 21.6 20.5 16.6 16.7 14.5 14.1 13.5 13.2 12.0 11.9 11.0 10.8 9.90 9.81 8.98 8.90 8.57 8.47 0 \$0 200 200 300 NOO 400 600 100 800 900 2000 20 50 Relative Difference (PNK vs C SIM) 0.9% 1 0 -0.3% -0.4% Relative Difference (%) -0.9% -0.9% -0.9% -1.1% -1.5% -2.1% -2.7% -2.9% -4 -5 -5.3% 30 20 SO 200 200 300 NOO 500 600 100 900

Requested Throughput (Mb/s)

L1 I-cache Misses (Total) vs Throughput



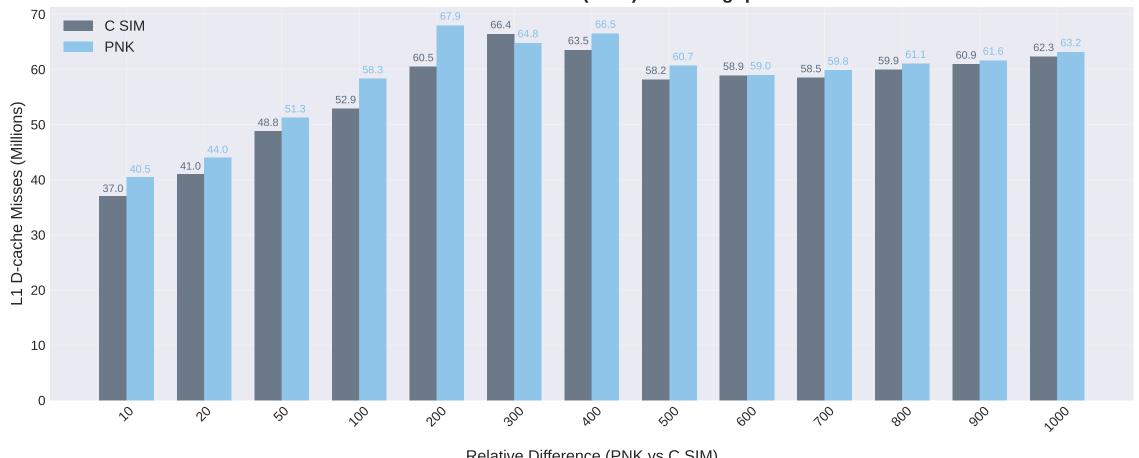


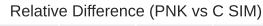
NOO

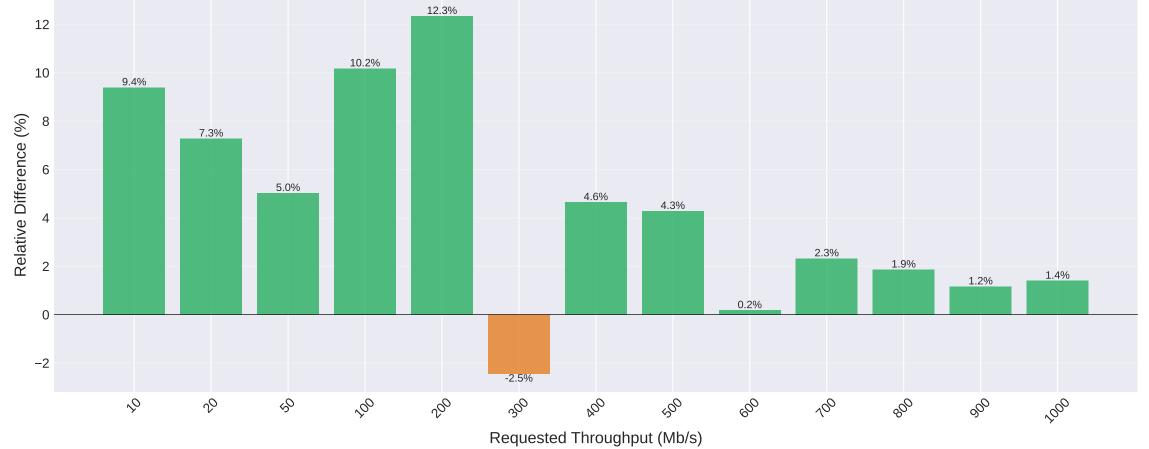
Requested Throughput (Mb/s)

SO

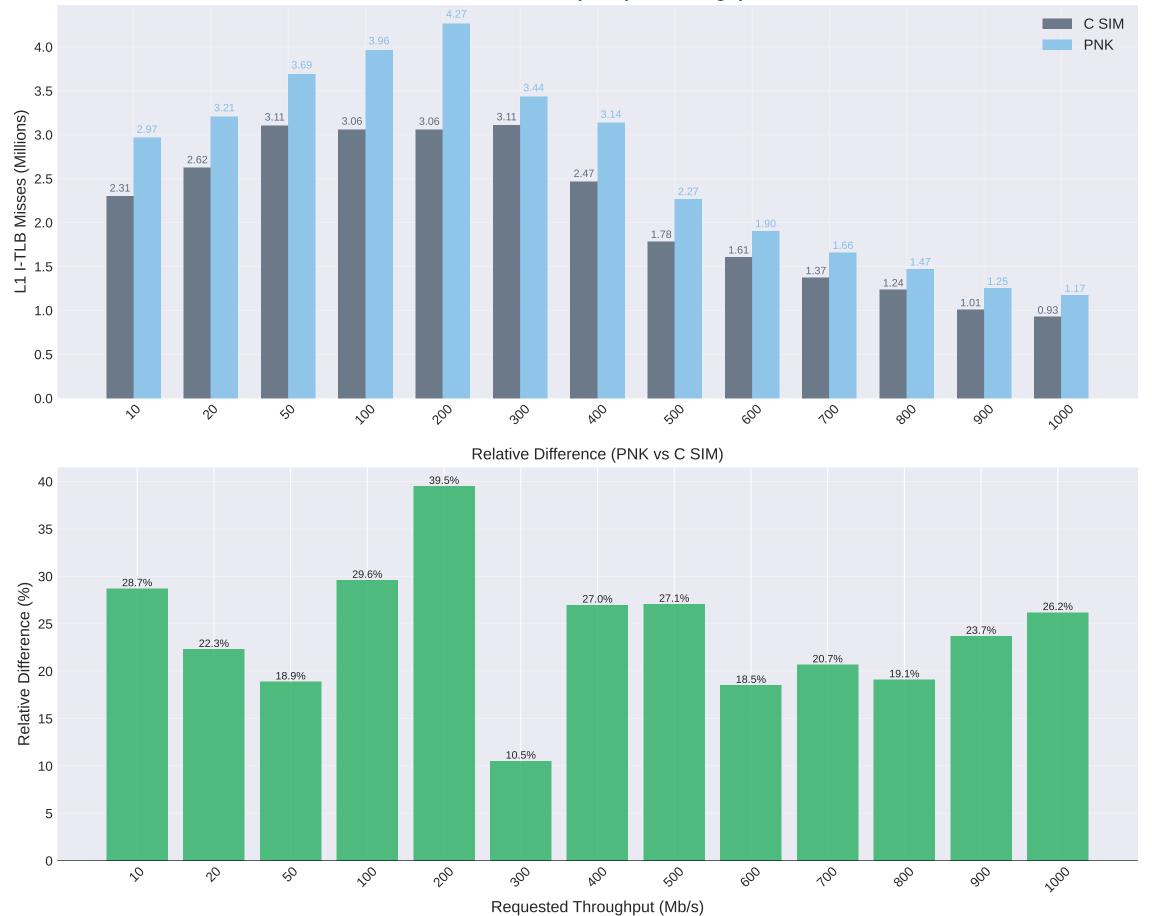




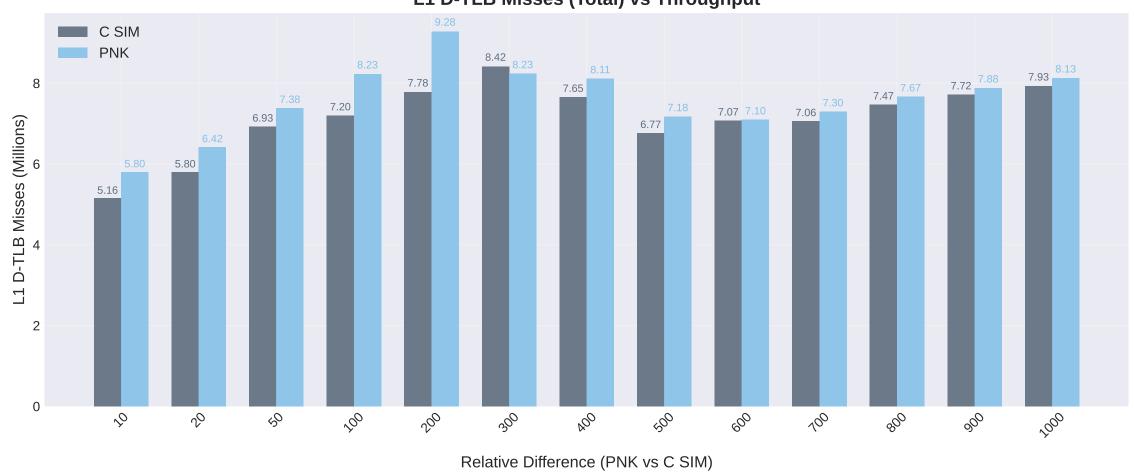


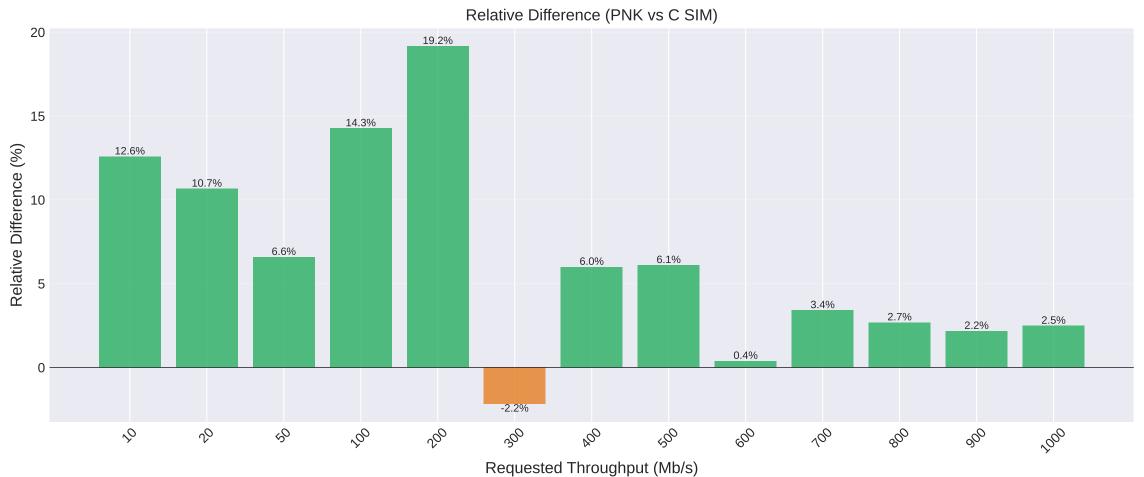


L1 I-TLB Misses (Total) vs Throughput

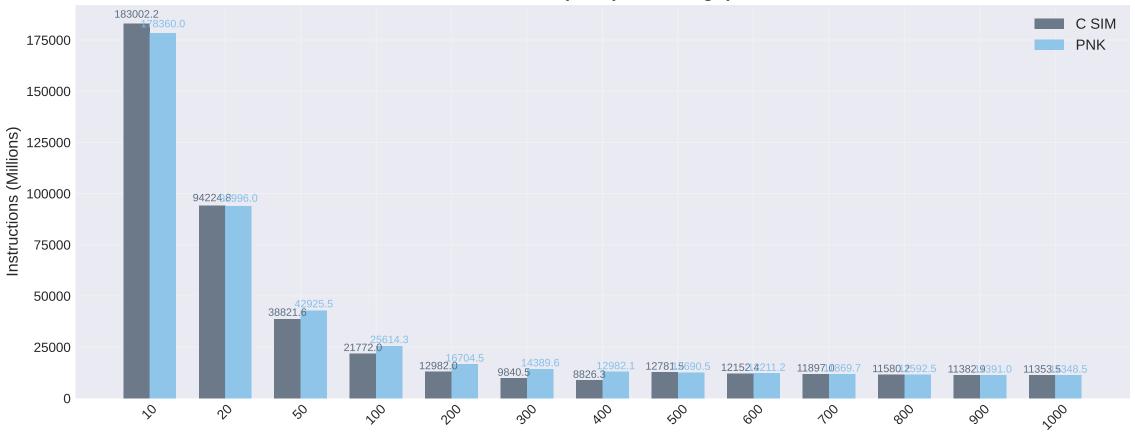


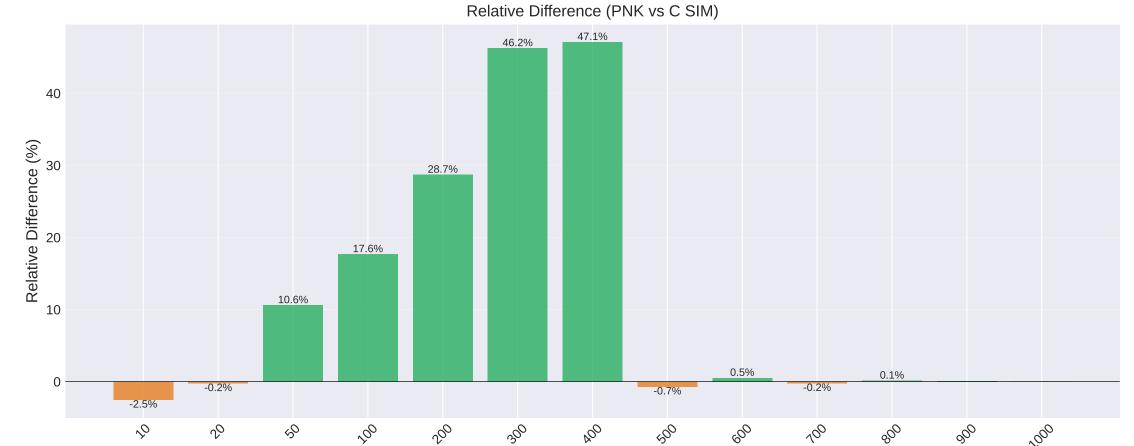
L1 D-TLB Misses (Total) vs Throughput



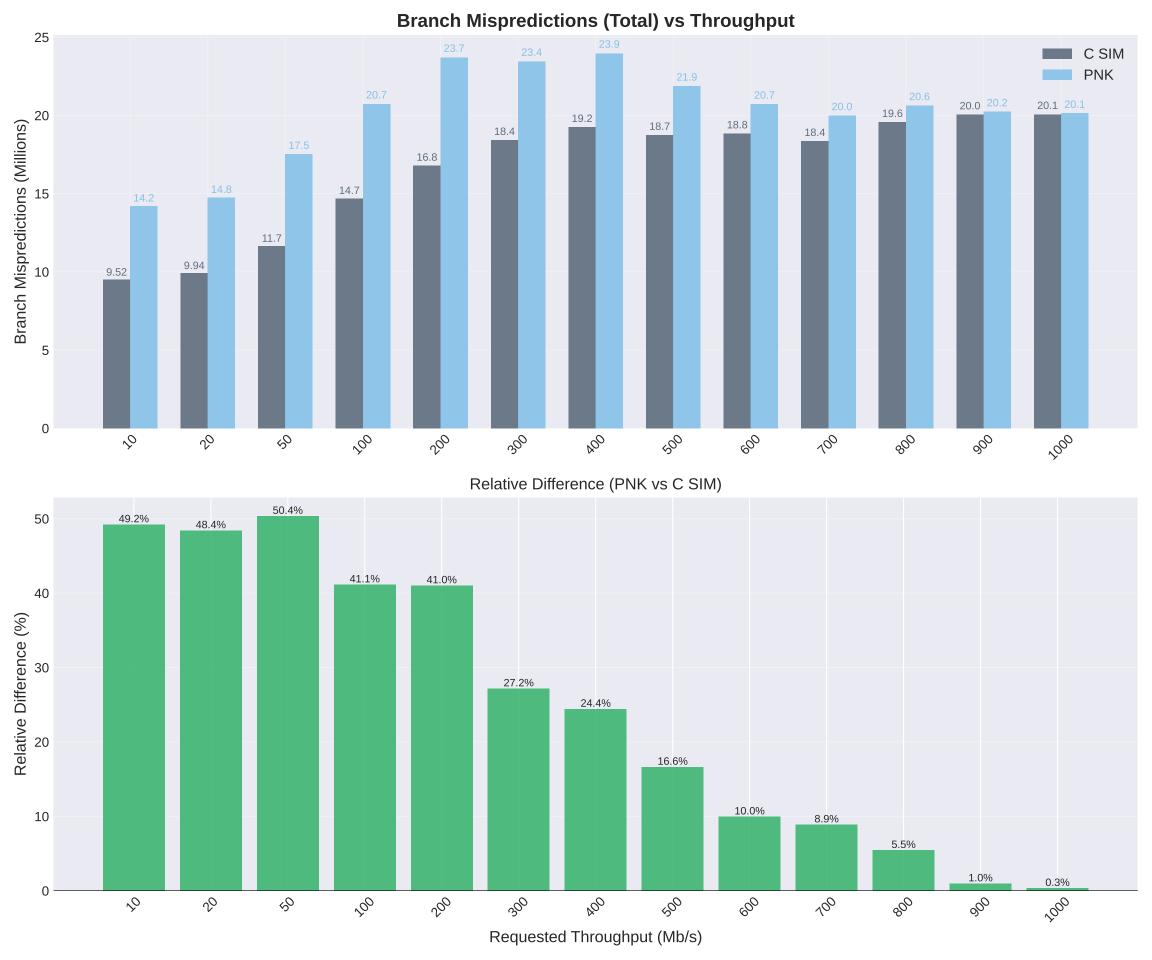


Instructions (Total) vs Throughput

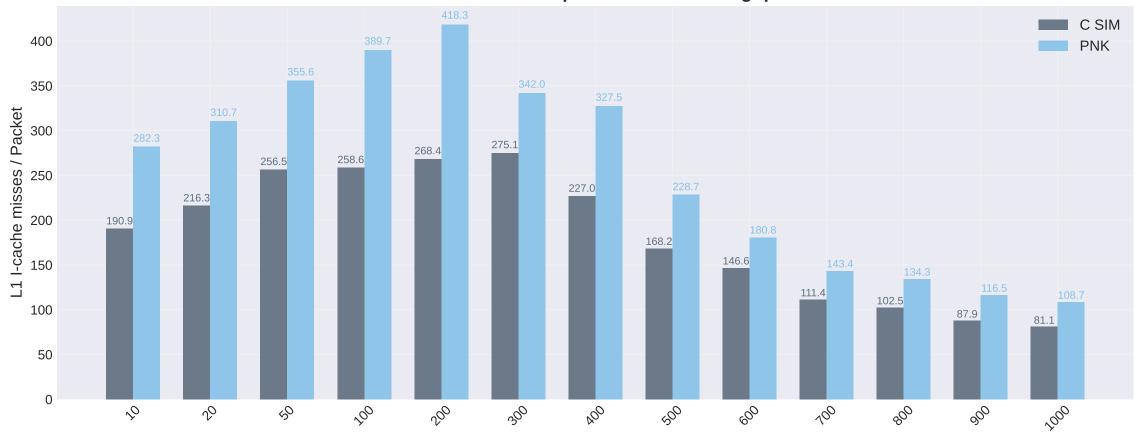


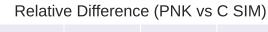


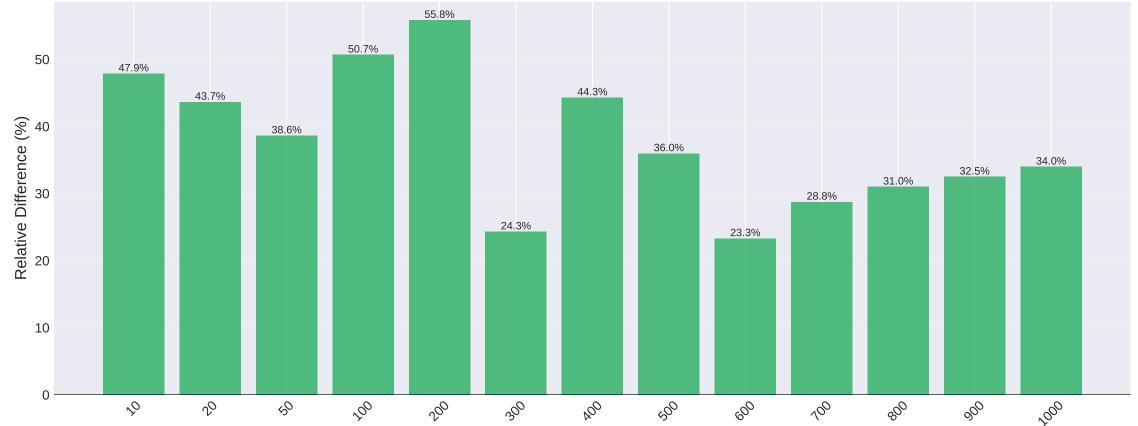
Requested Throughput (Mb/s)



L1 I-cache Misses per Packet vs Throughput

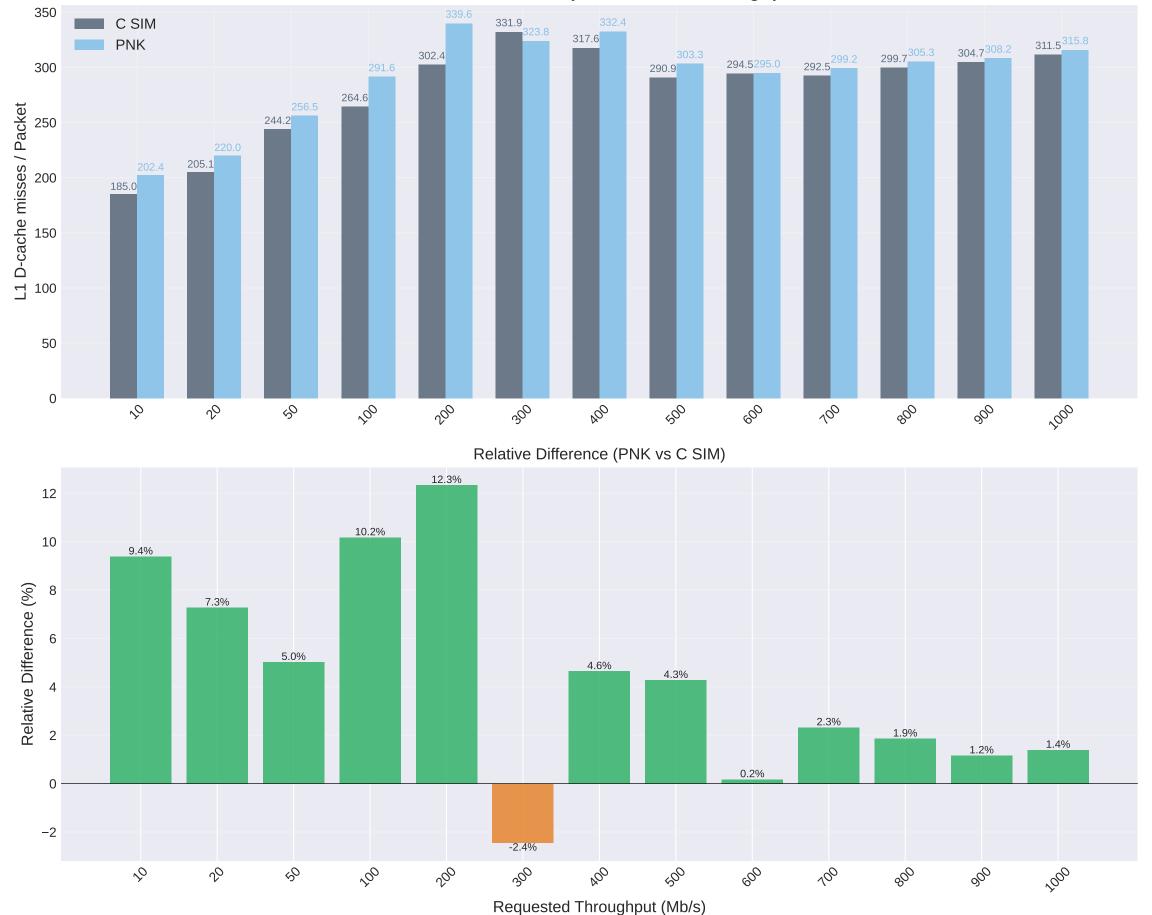




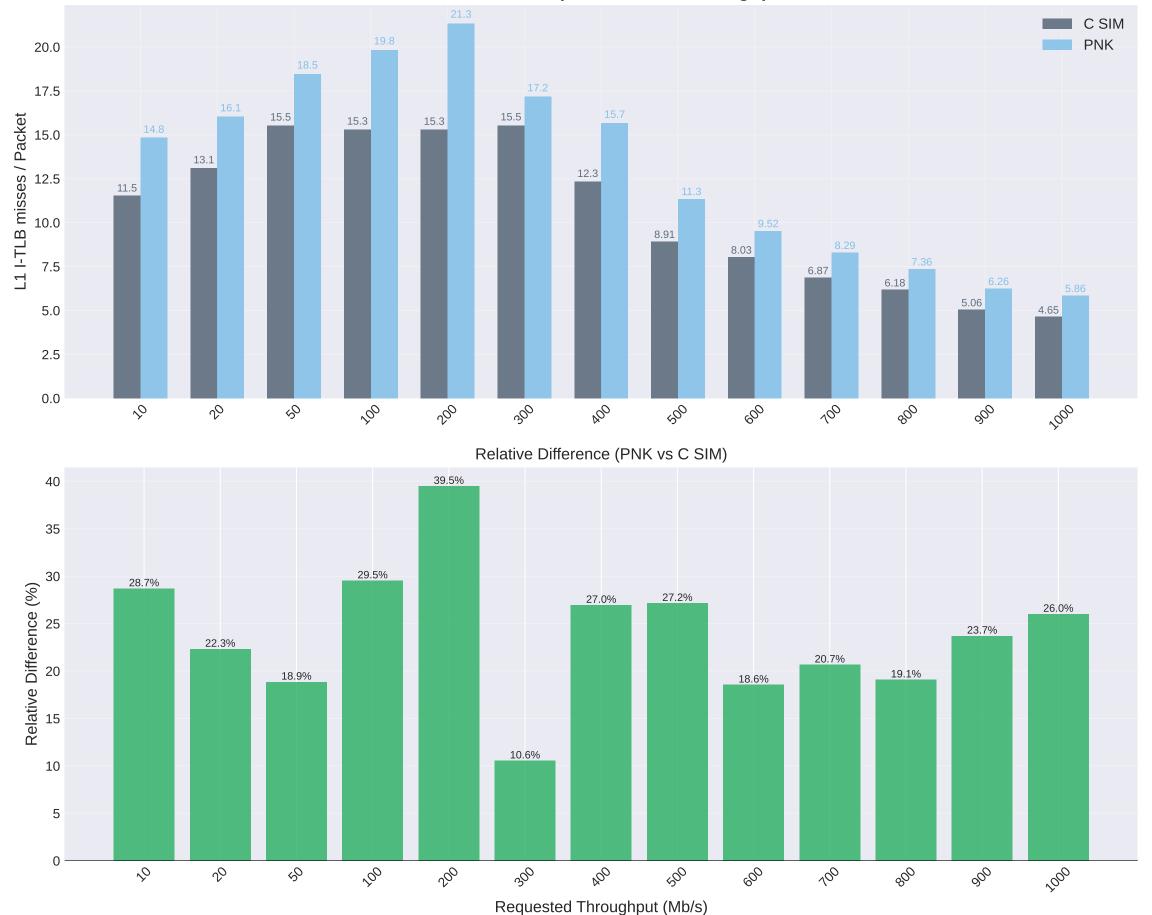


Requested Throughput (Mb/s)

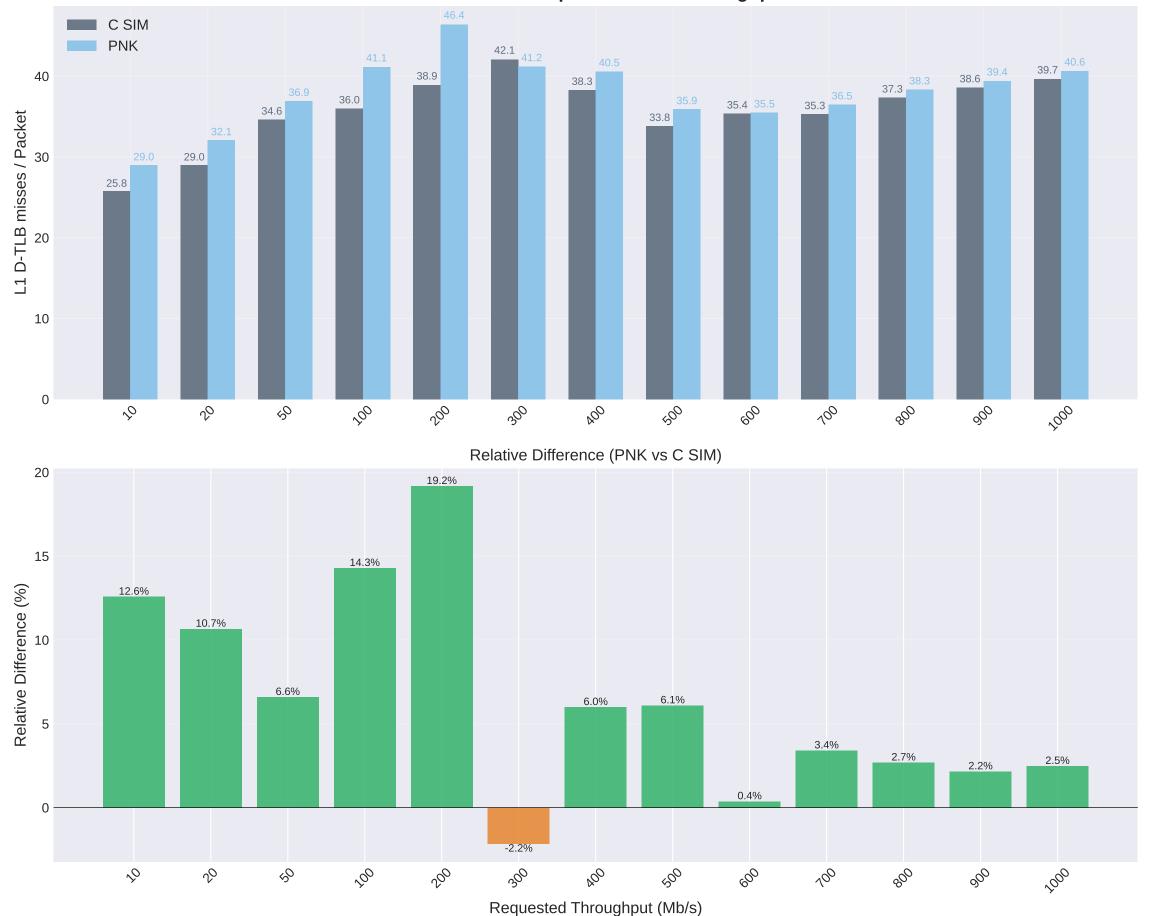
L1 D-cache Misses per Packet vs Throughput



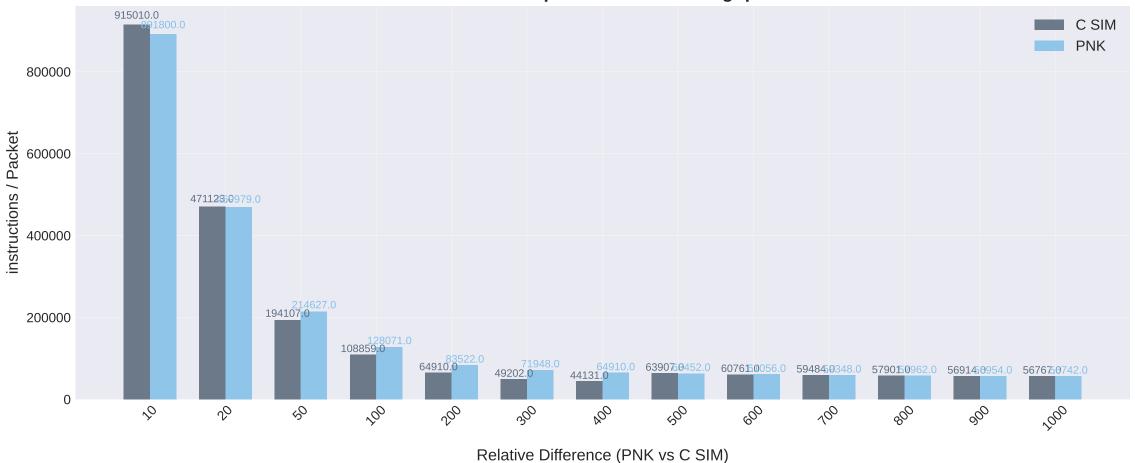
L1 I-TLB Misses per Packet vs Throughput



L1 D-TLB Misses per Packet vs Throughput



Instructions per Packet vs Throughput





10.6%

80

200

-0.2%

20

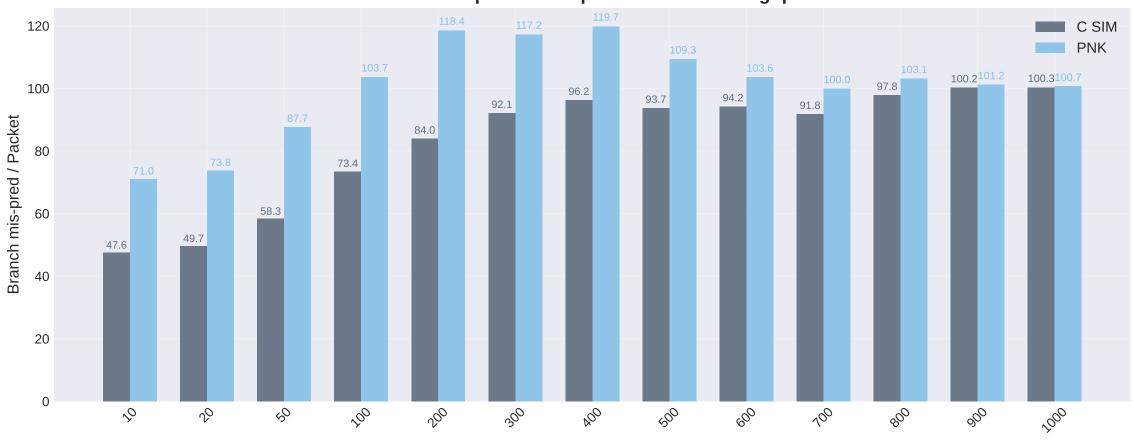
-2.5% \$

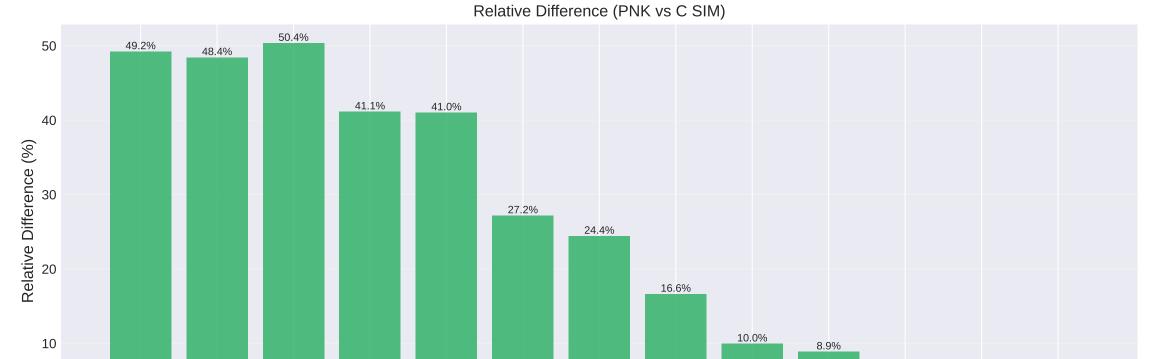
10

0

0.5% 0.1% -0.2% -0.7% 200 300 NOO 400 600 700 900 900 Requested Throughput (Mb/s)

Branch Mispredictions per Packet vs Throughput





NOO

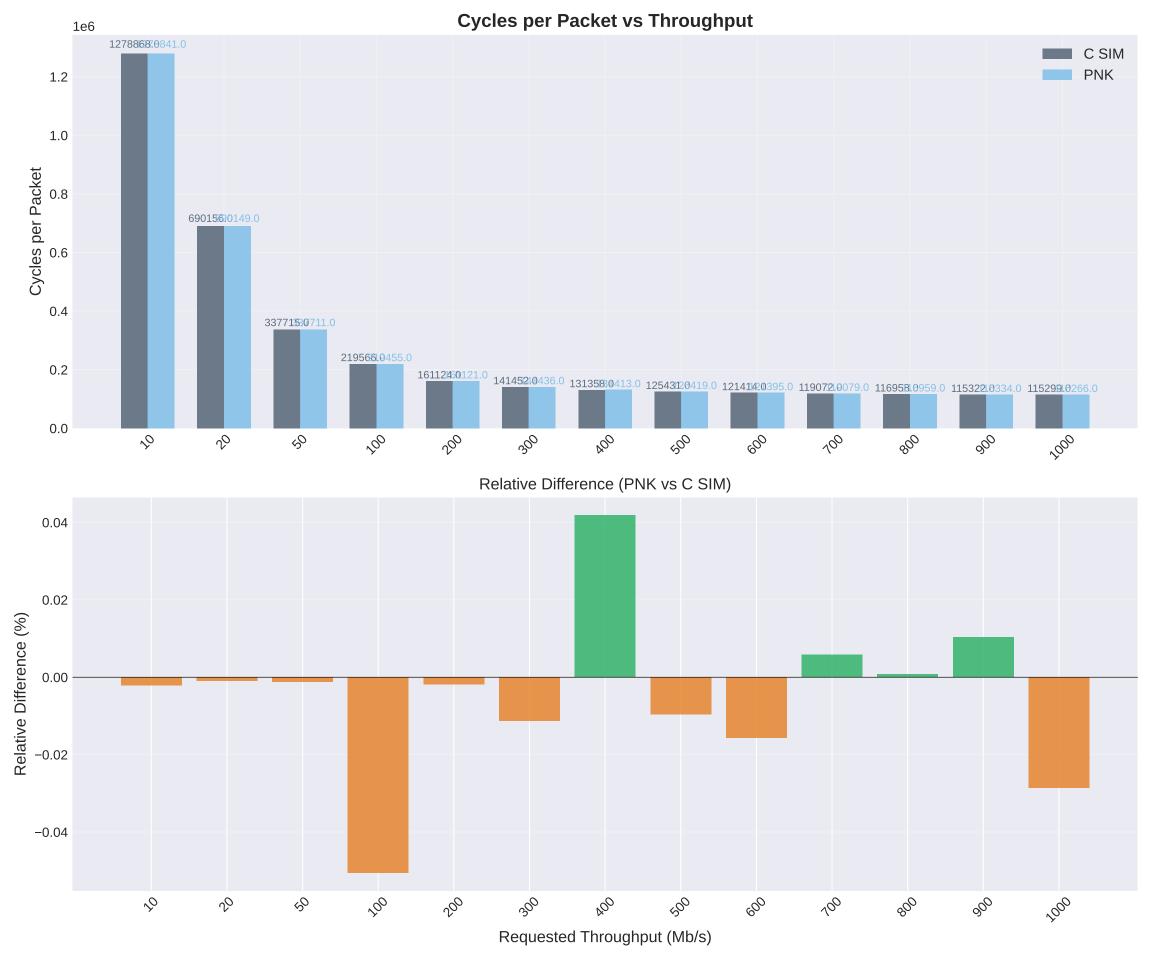
Requested Throughput (Mb/s)

\$

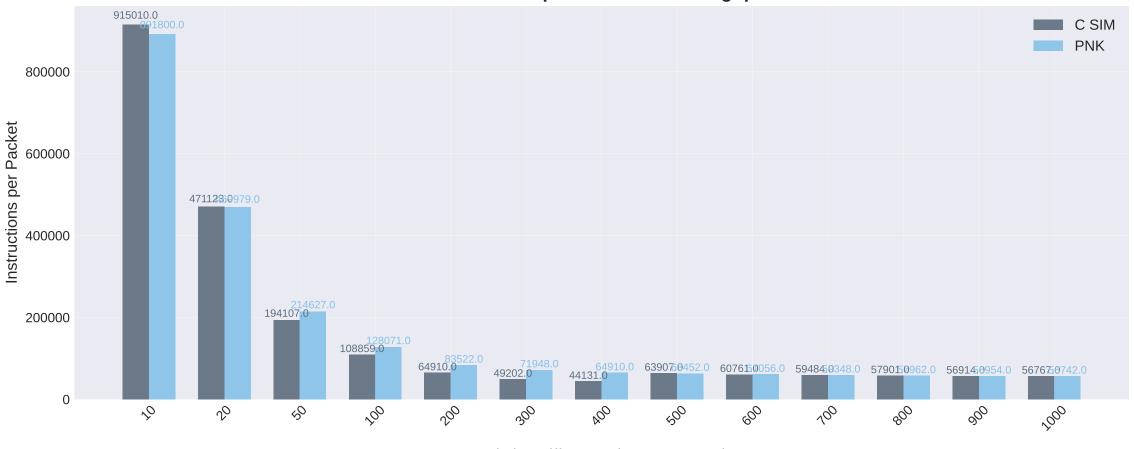
5.4%

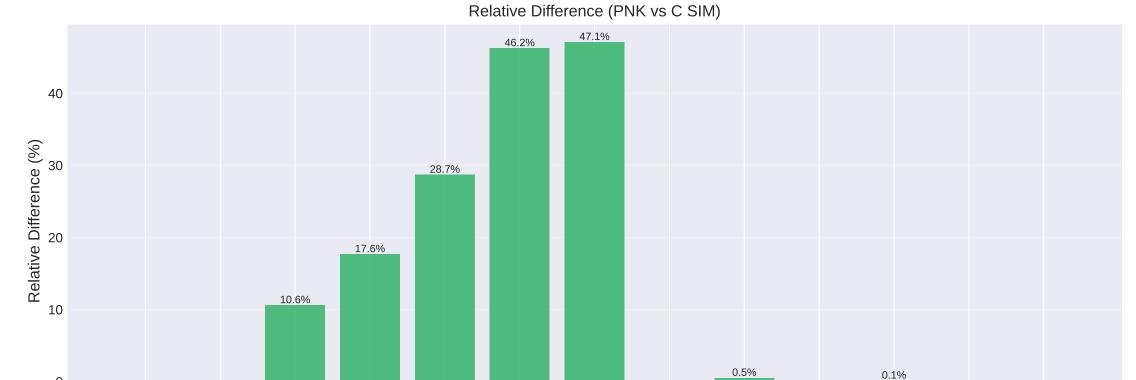
1.0%

0.3%



Instructions per Packet vs Throughput





NO

Requested Throughput (Mb/s)

-0.7%

400

600

-0.2%

700

900

900

0

-0.2%

20

200

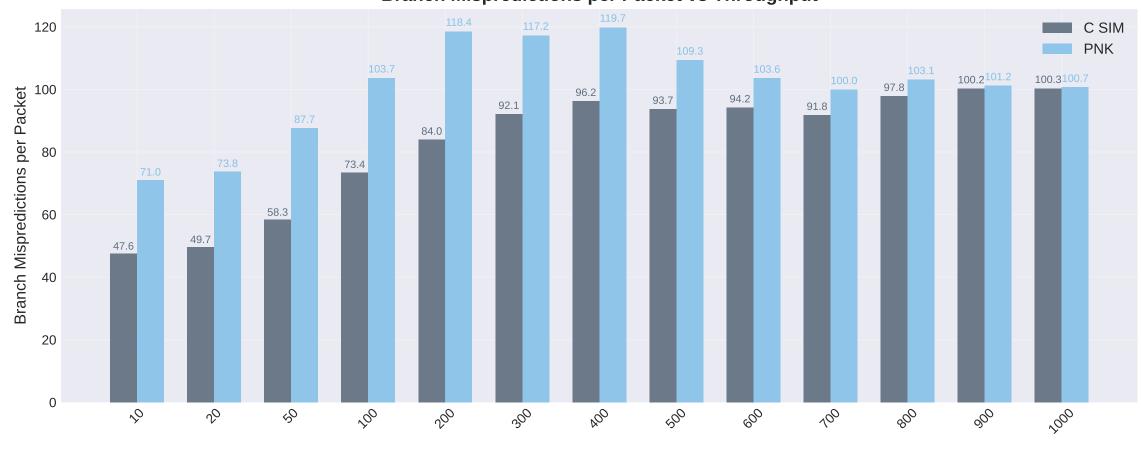
80

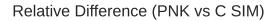
200

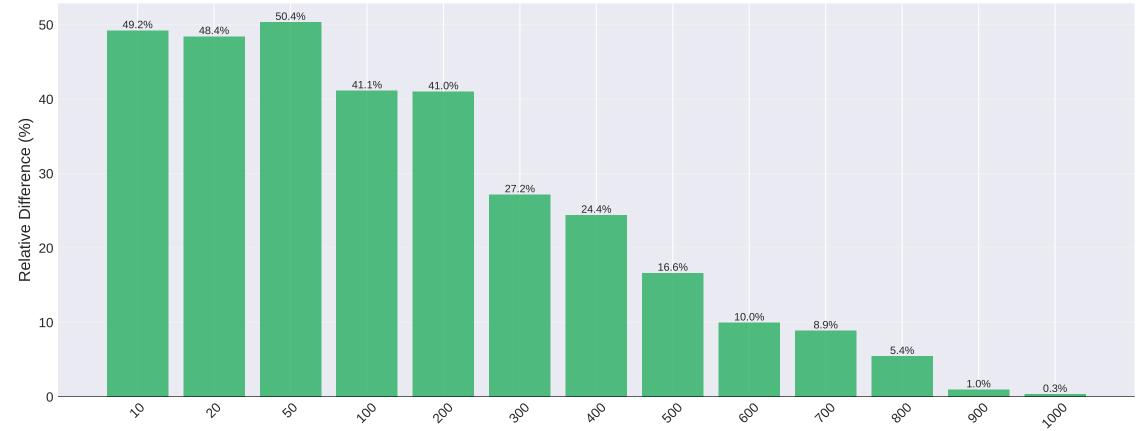
300

-2.5% ~

Branch Mispredictions per Packet vs Throughput







Requested Throughput (Mb/s)





