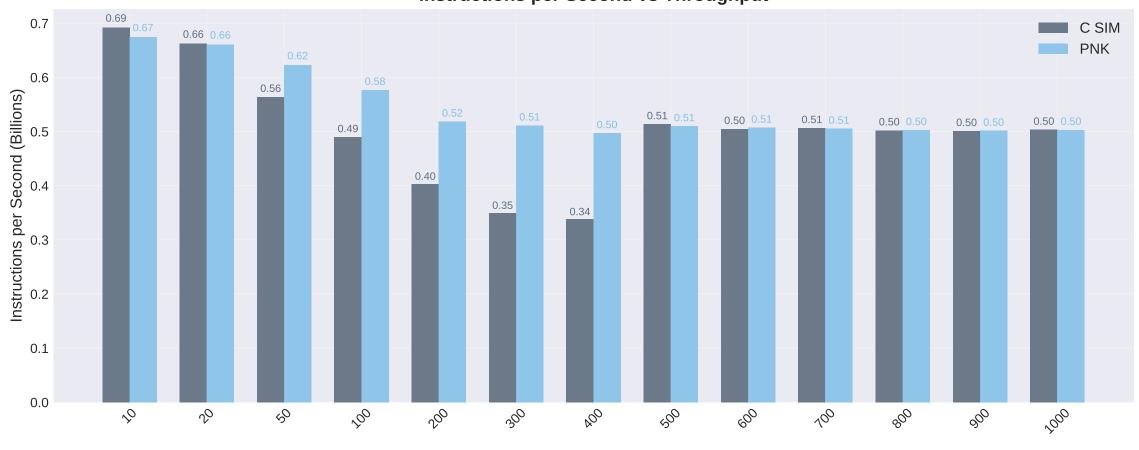
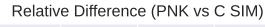
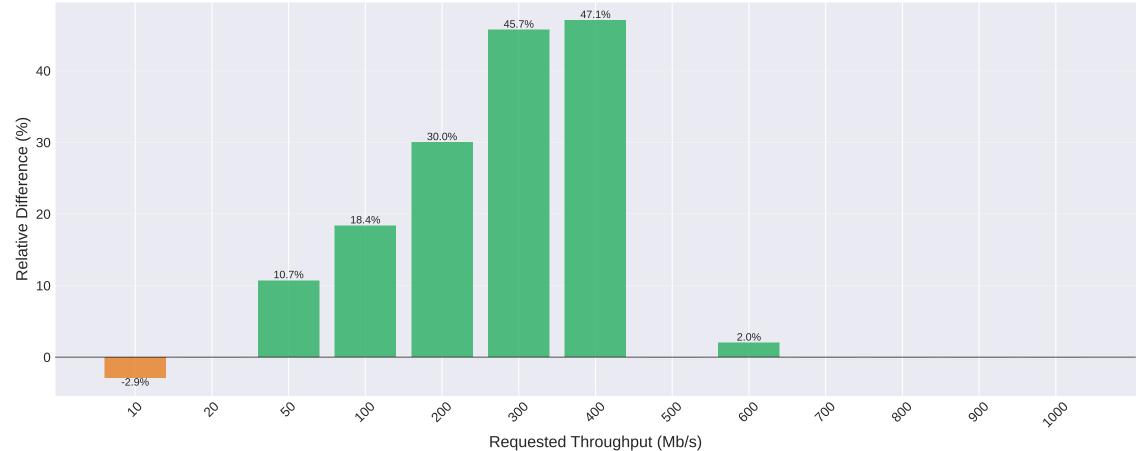
Instructions per Second vs Throughput



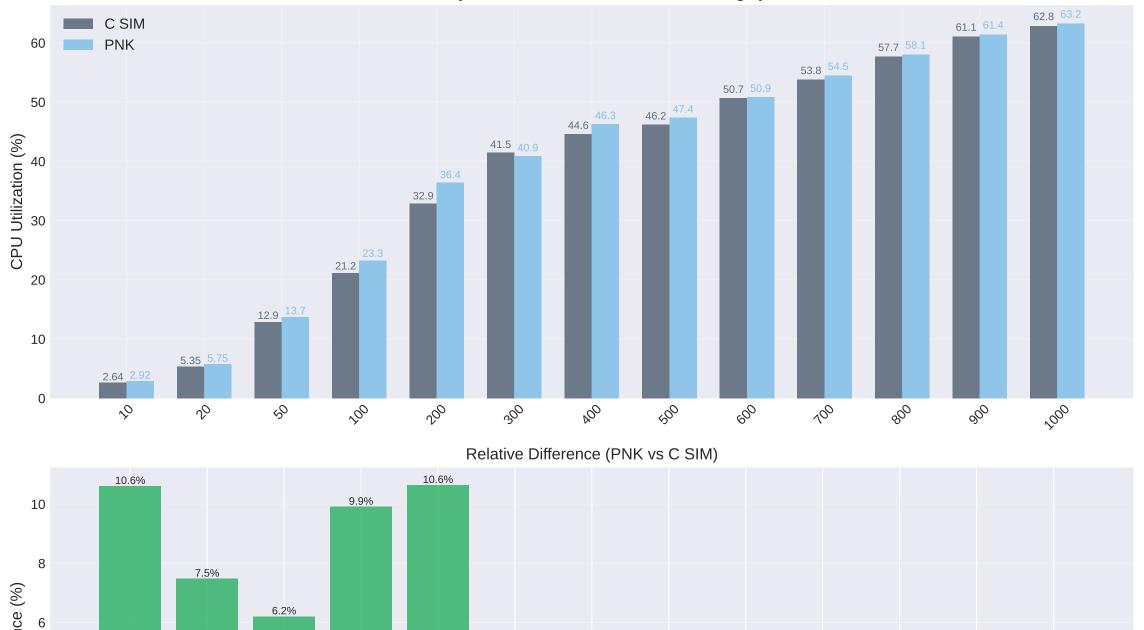




Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3956.4 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.2% 61.4% 600.0600.0 600 CPU Utilization (%) 58.1% 54.5% 500.0500.0 50.9% 400 300.0300.0 200.0200.0 200 20 100.0100.0 50.0 50.0 0 900 200 go NOO 400 2000 700 600 100 900 20 20 50

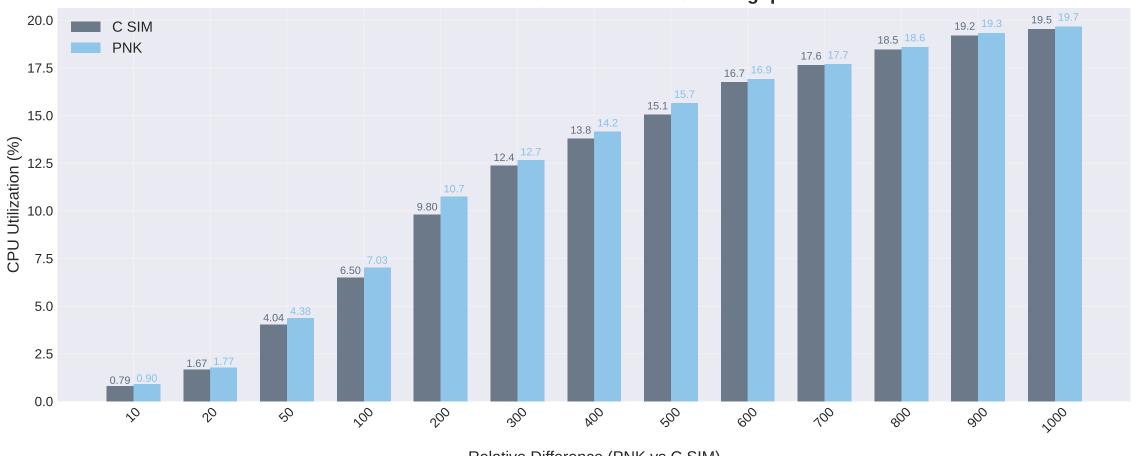
Requested Throughput (Mb/s)

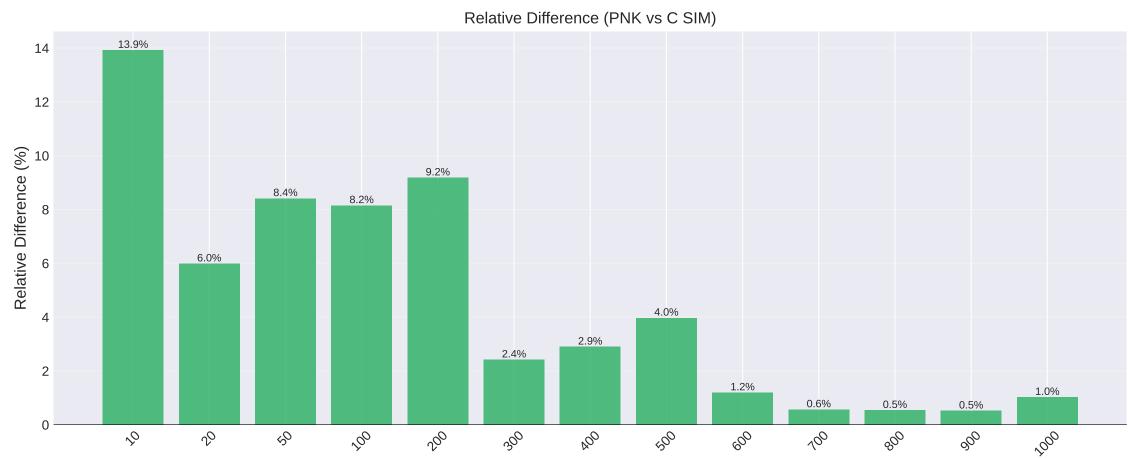
Total System CPU Utilization vs Throughput



Relative Difference (%) 3.8% 2.6% 1.3% 0.7% 0.6% 0.5% 0.4% 0 -1.4% -2 \$0 20 50 200 200 300 200 500 600 100 900 Requested Throughput (Mb/s)

Ethernet Driver CPU Utilization vs Throughput

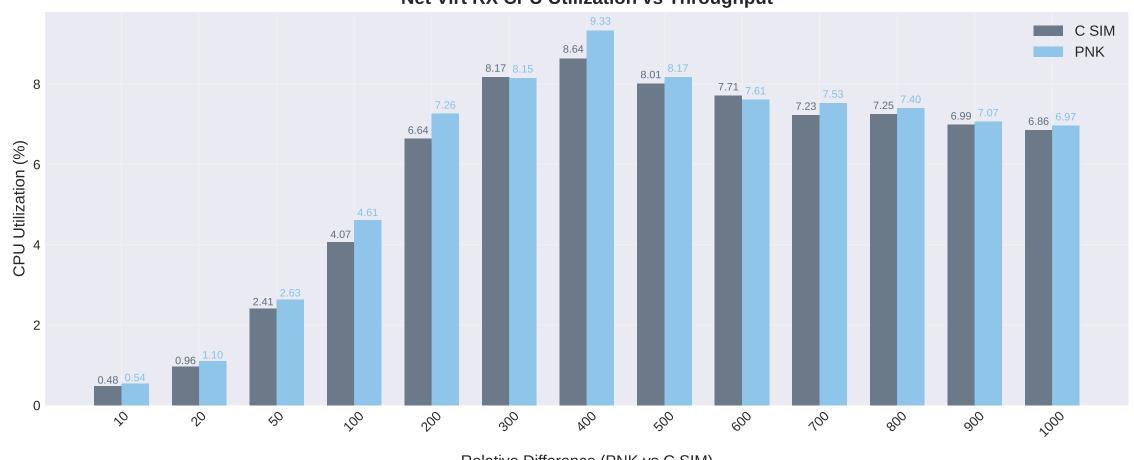


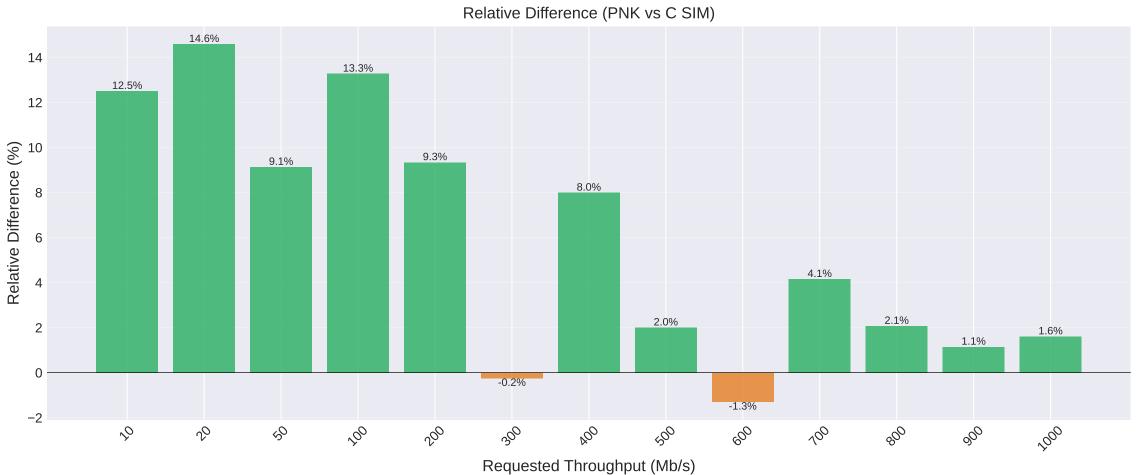


Requested Throughput (Mb/s)

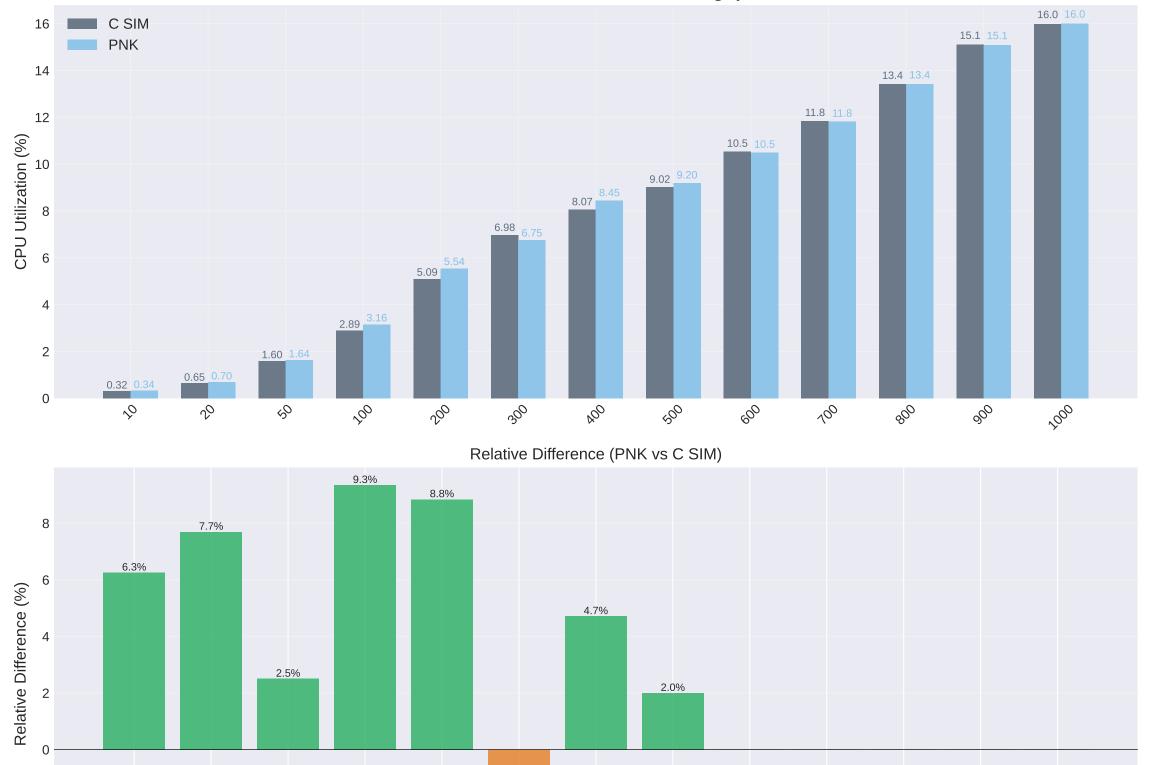


Net Virt RX CPU Utilization vs Throughput





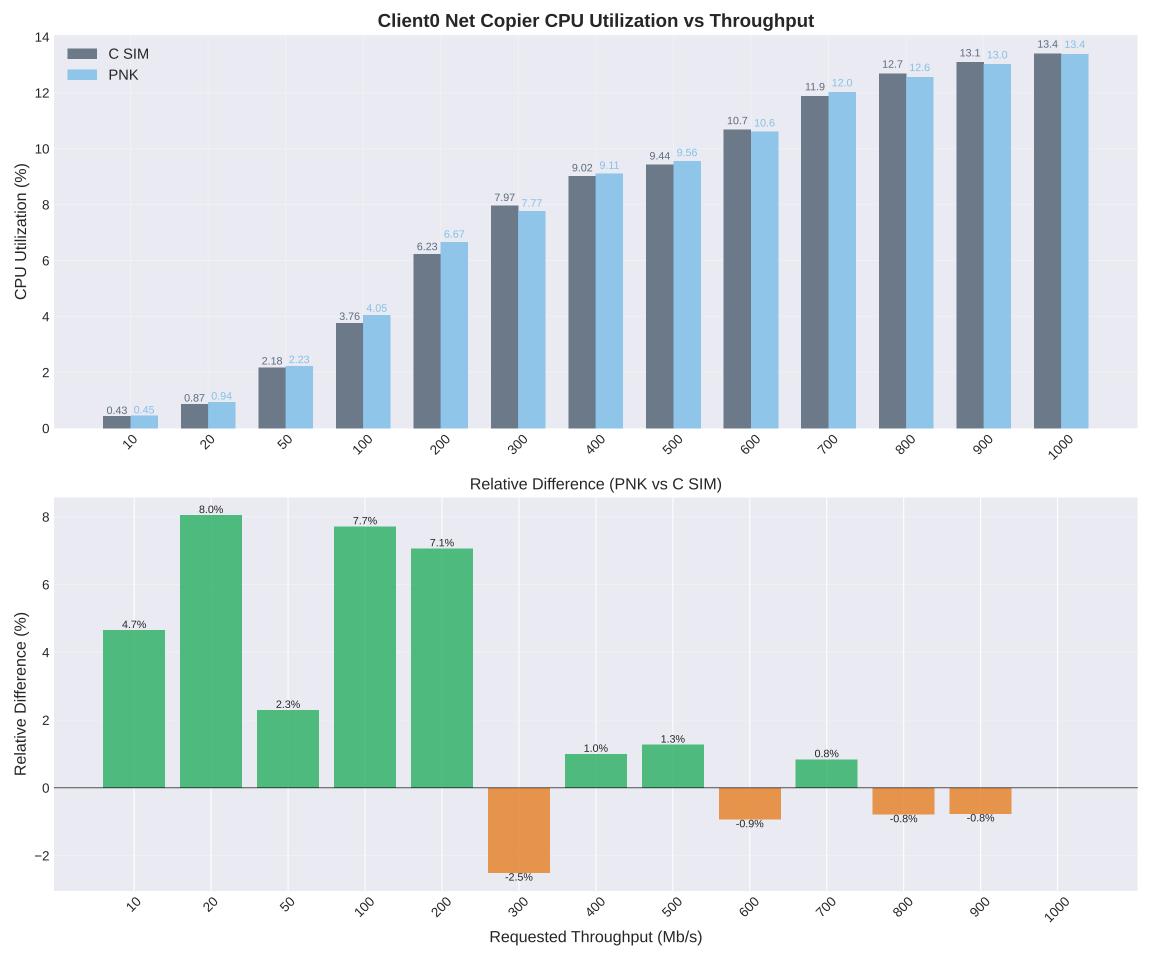
Client0 CPU Utilization vs Throughput



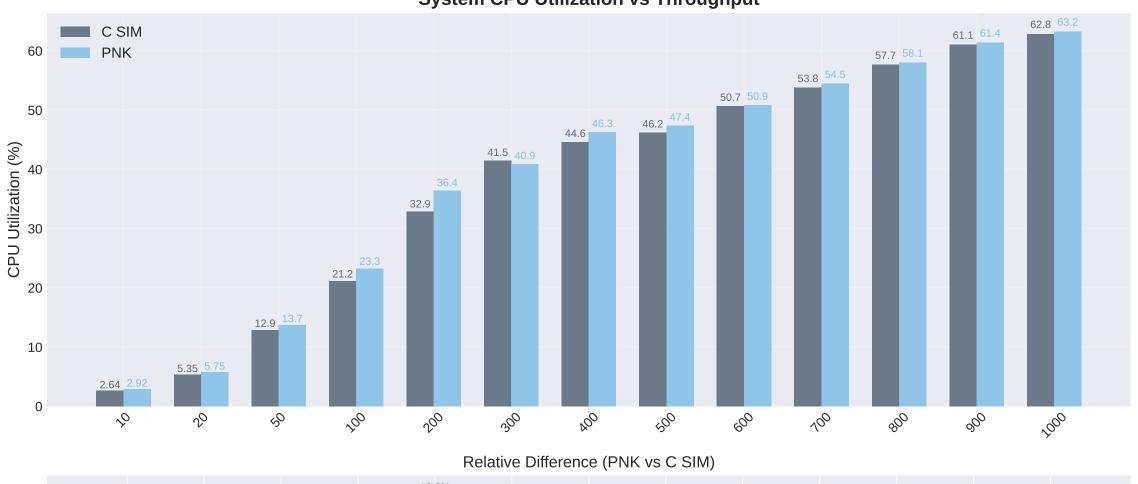
Requested Throughput (Mb/s)

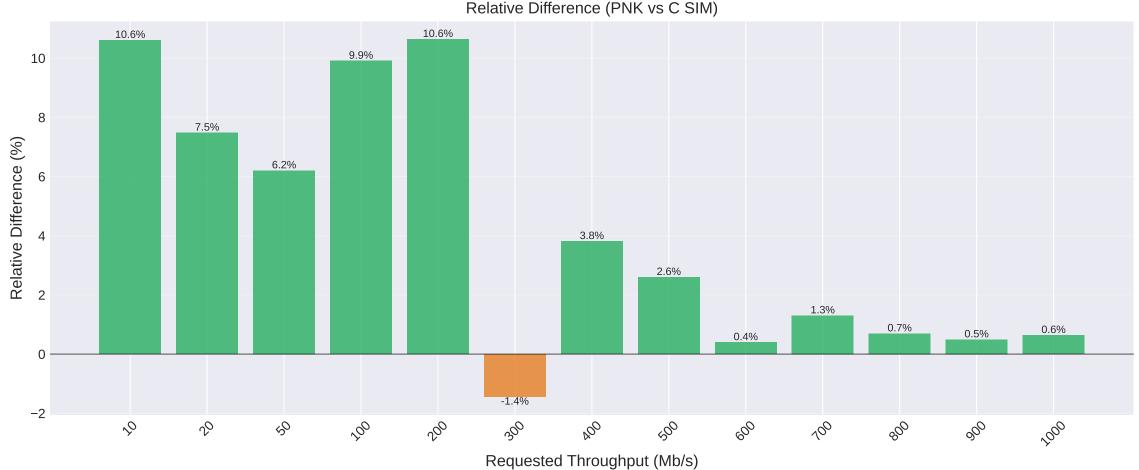
-3.3%

-2

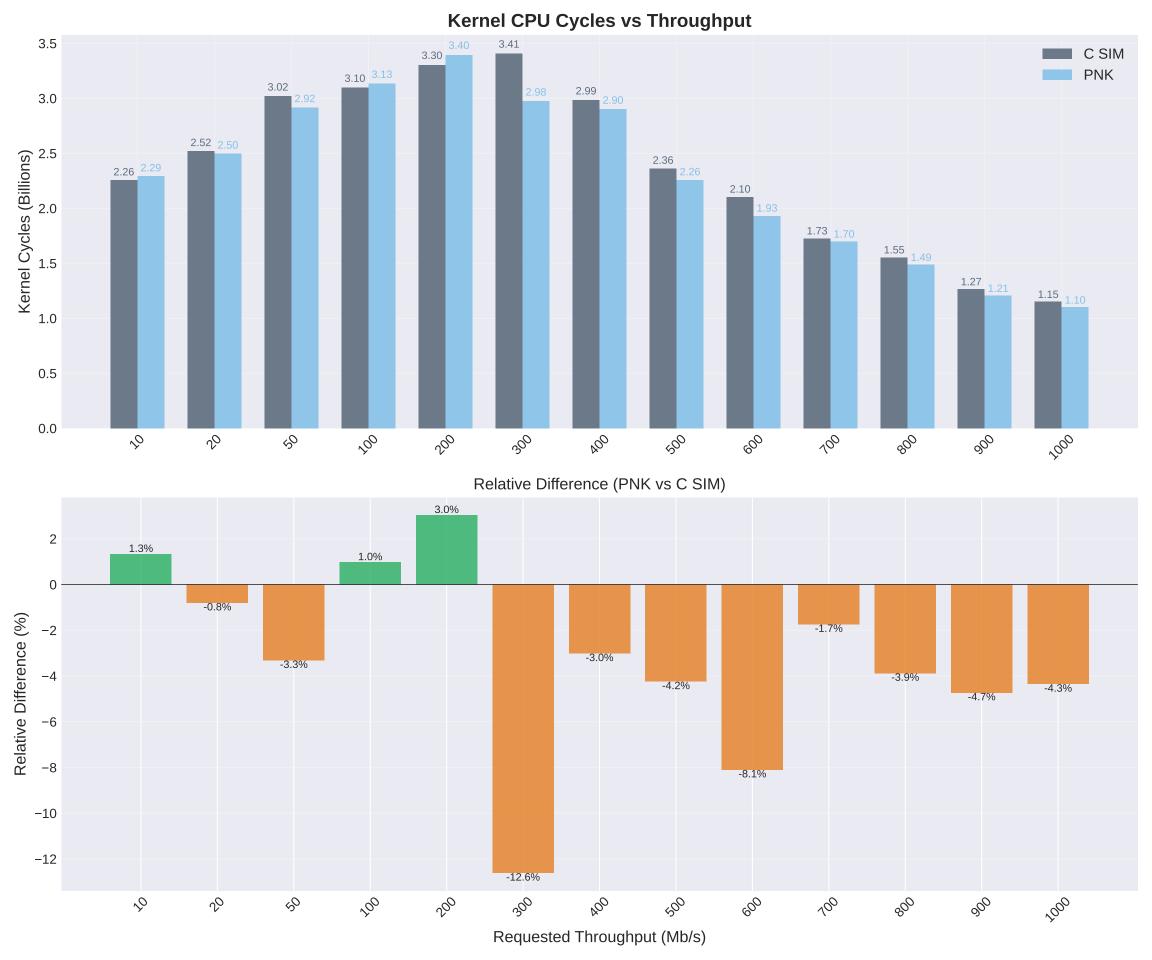


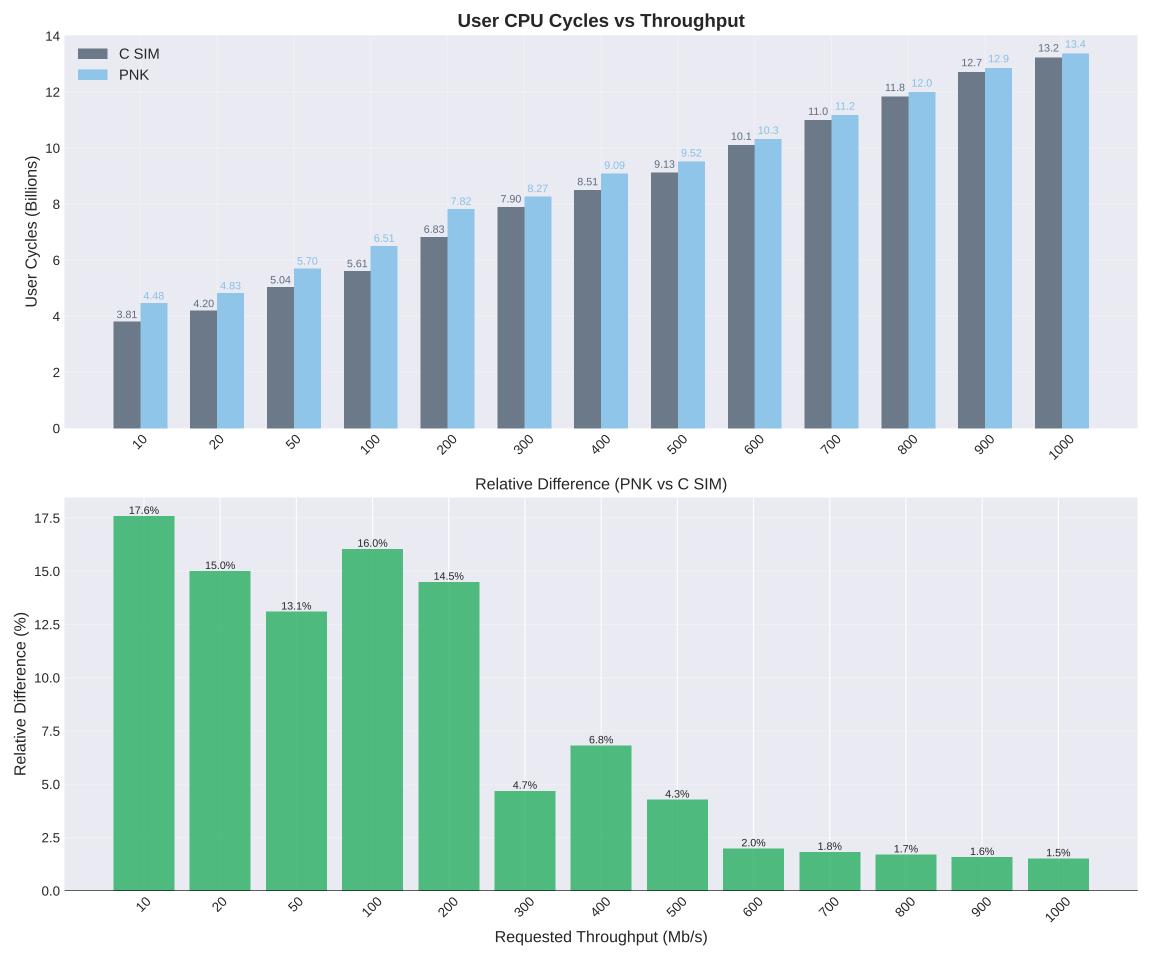
System CPU Utilization vs Throughput



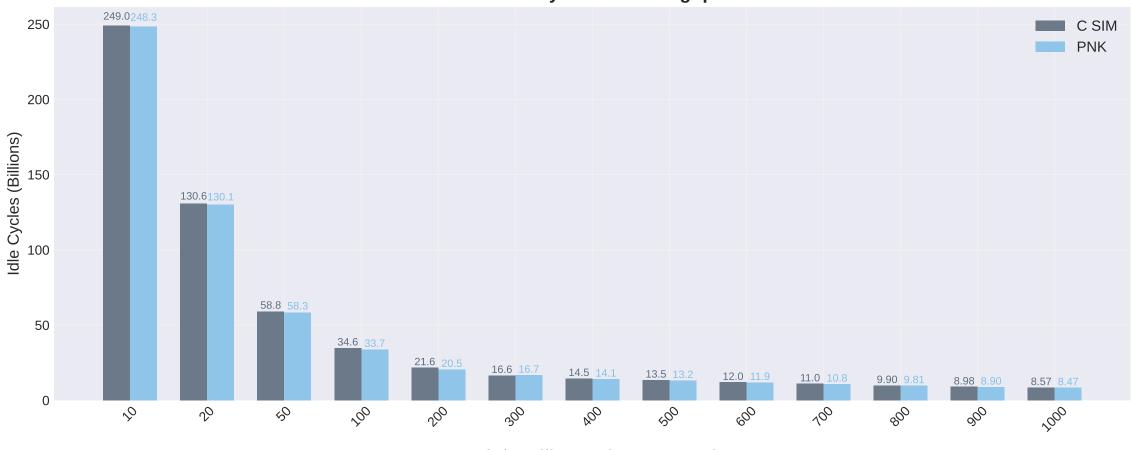


Total CPU Cycles vs Throughput 255.8255.8 C SIM 250 PNK 200 Total Cycles (Billions) 138.0138.0 67.5 67.5 50 43.9 43.9 32.2 32.2 28.3 28.3 26.3 26.3 25.1 25.1 24.3 24.3 23.8 23.8 23.4 23.4 23.1 23.1 23.1 23.1 0 30 100 200 300 NOO 400 600 100 900 900 1000 20 50 Relative Difference (PNK vs C SIM) 0.04 Relative Difference (%) 0.02 0.00 -0.02 -0.0420 50 100 200 300 NOO 500 700 900 30 Requested Throughput (Mb/s)

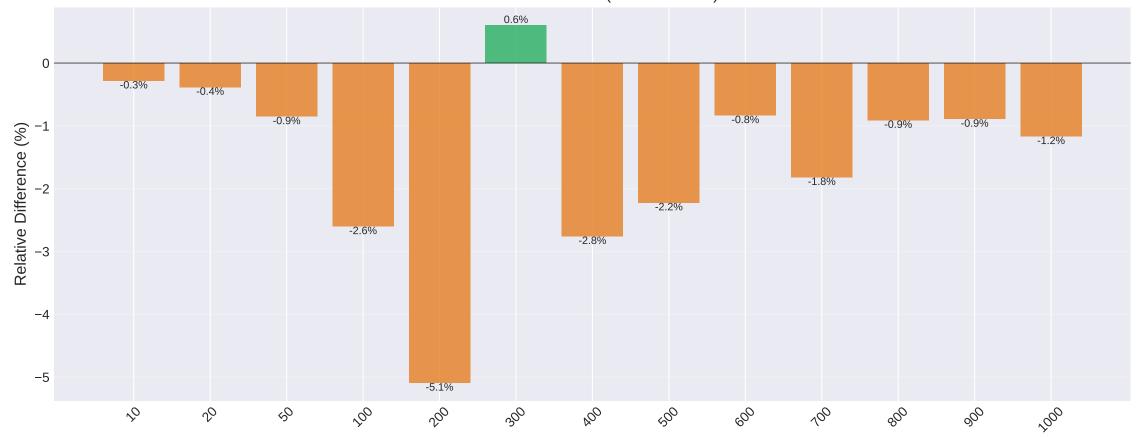






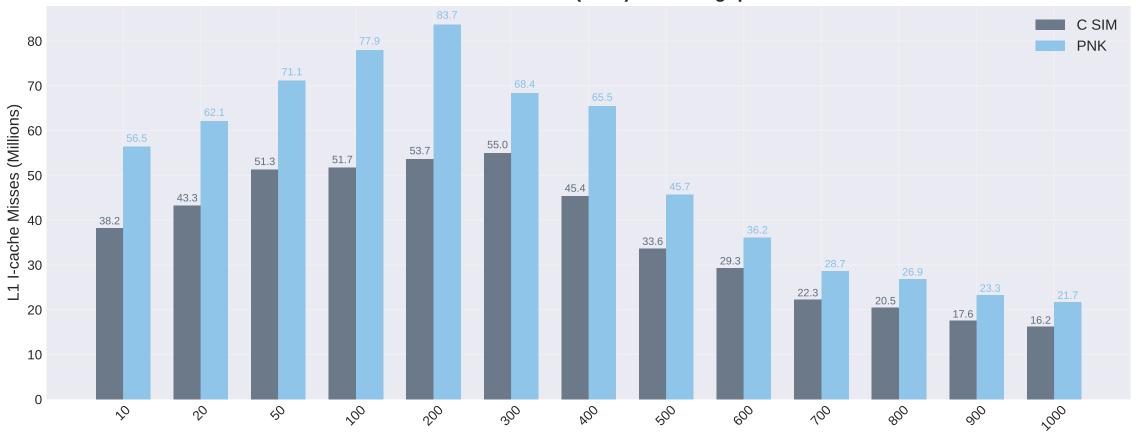


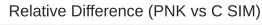


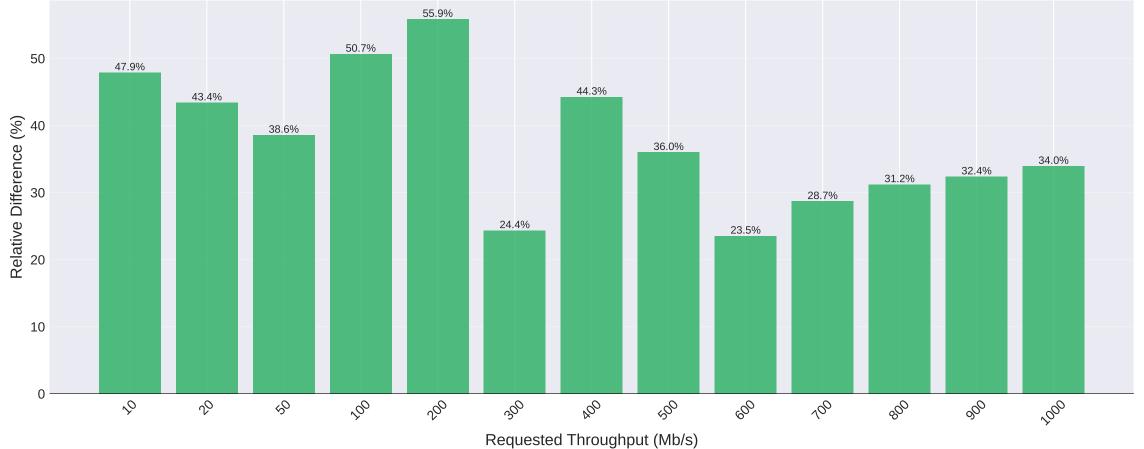


Requested Throughput (Mb/s)

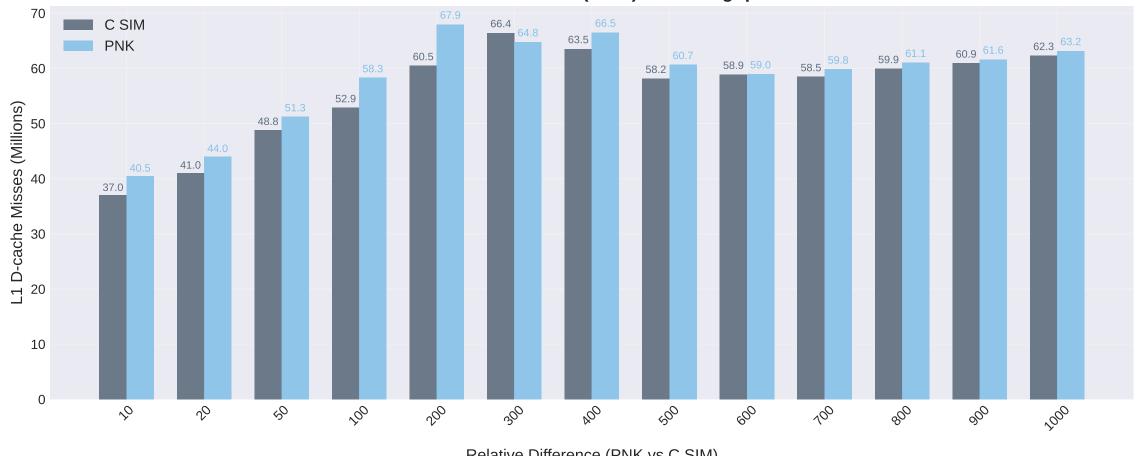
L1 I-cache Misses (Total) vs Throughput

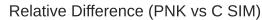


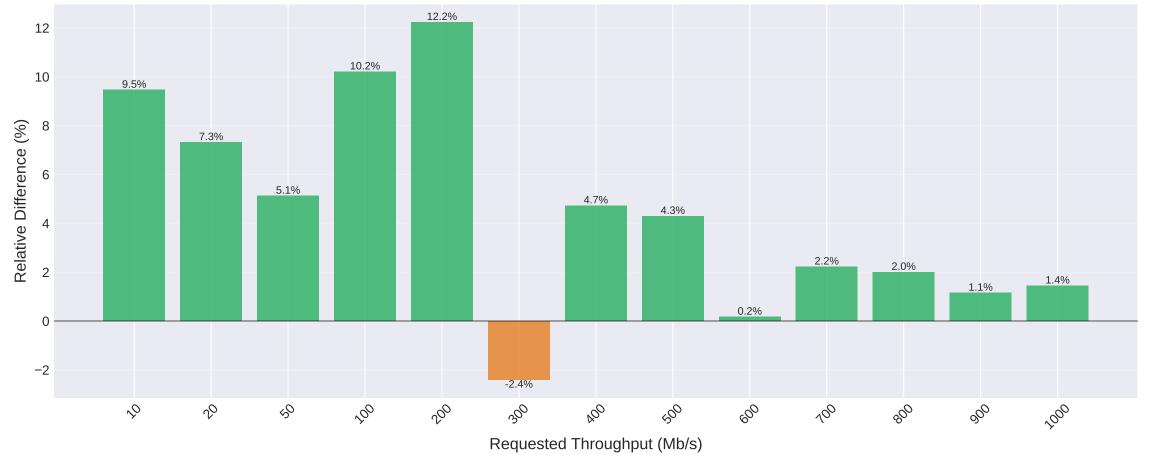




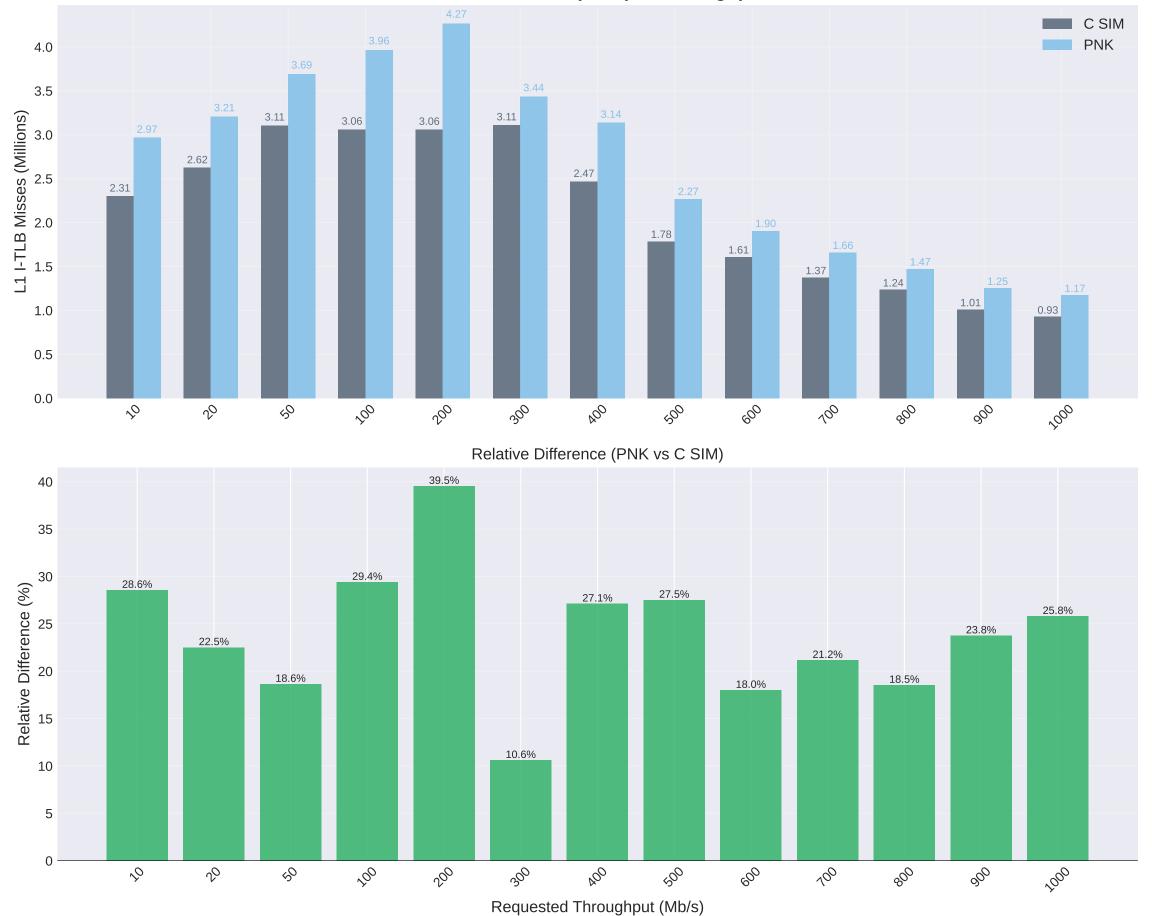
L1 D-cache Misses (Total) vs Throughput



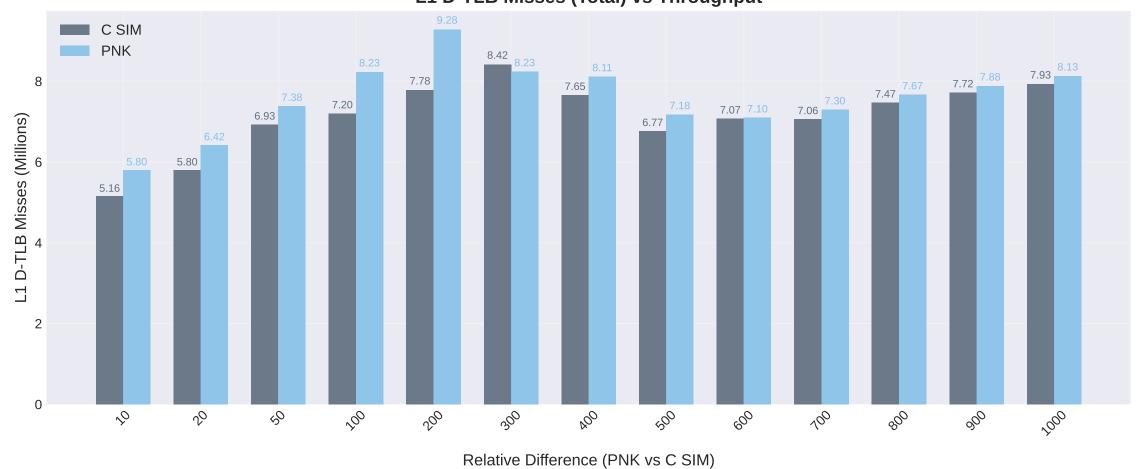


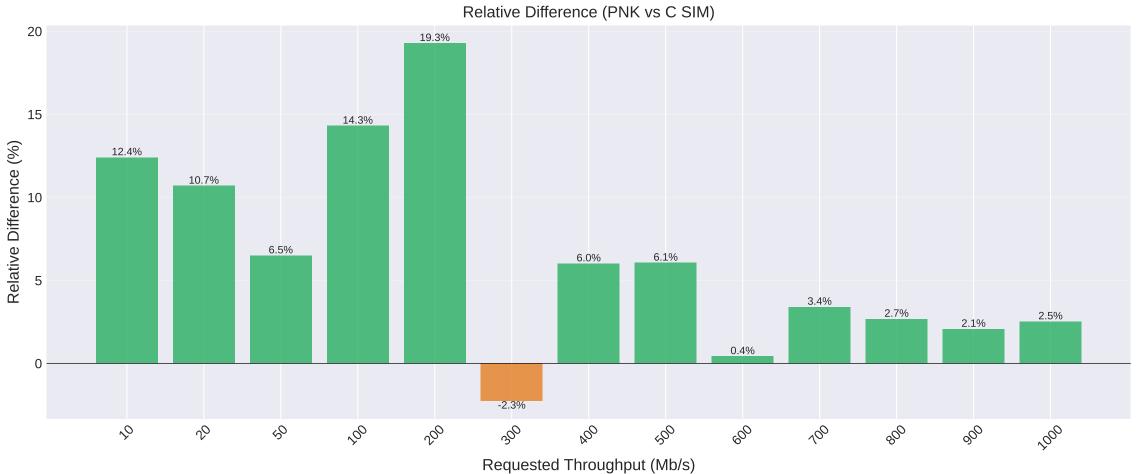


L1 I-TLB Misses (Total) vs Throughput

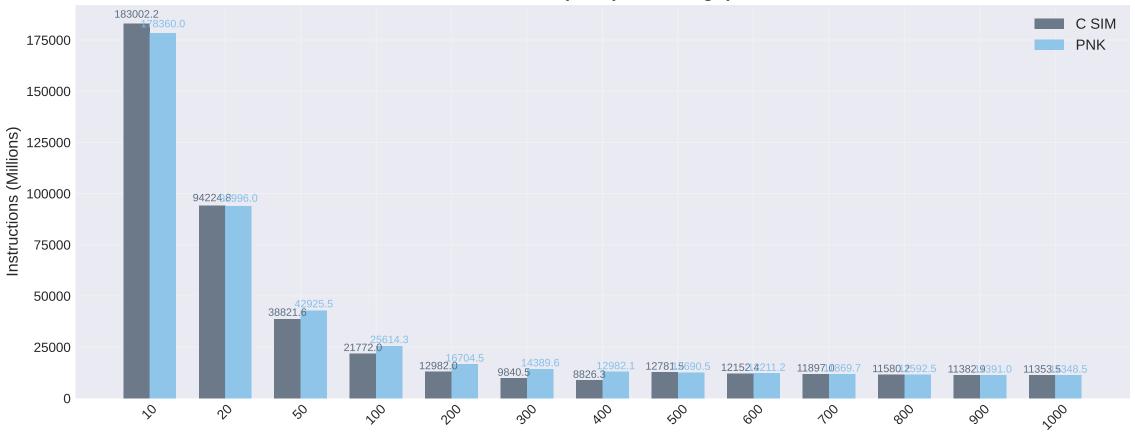


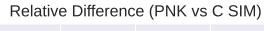
L1 D-TLB Misses (Total) vs Throughput

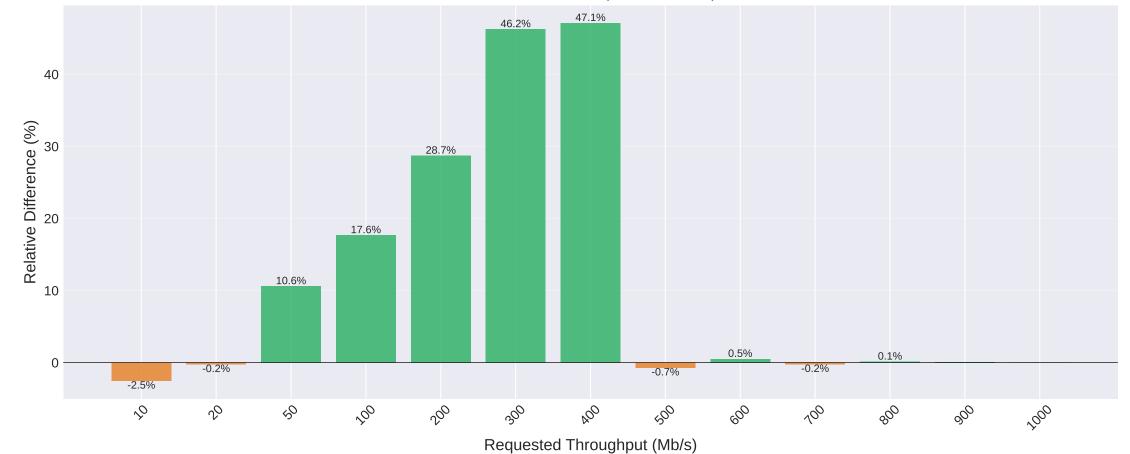


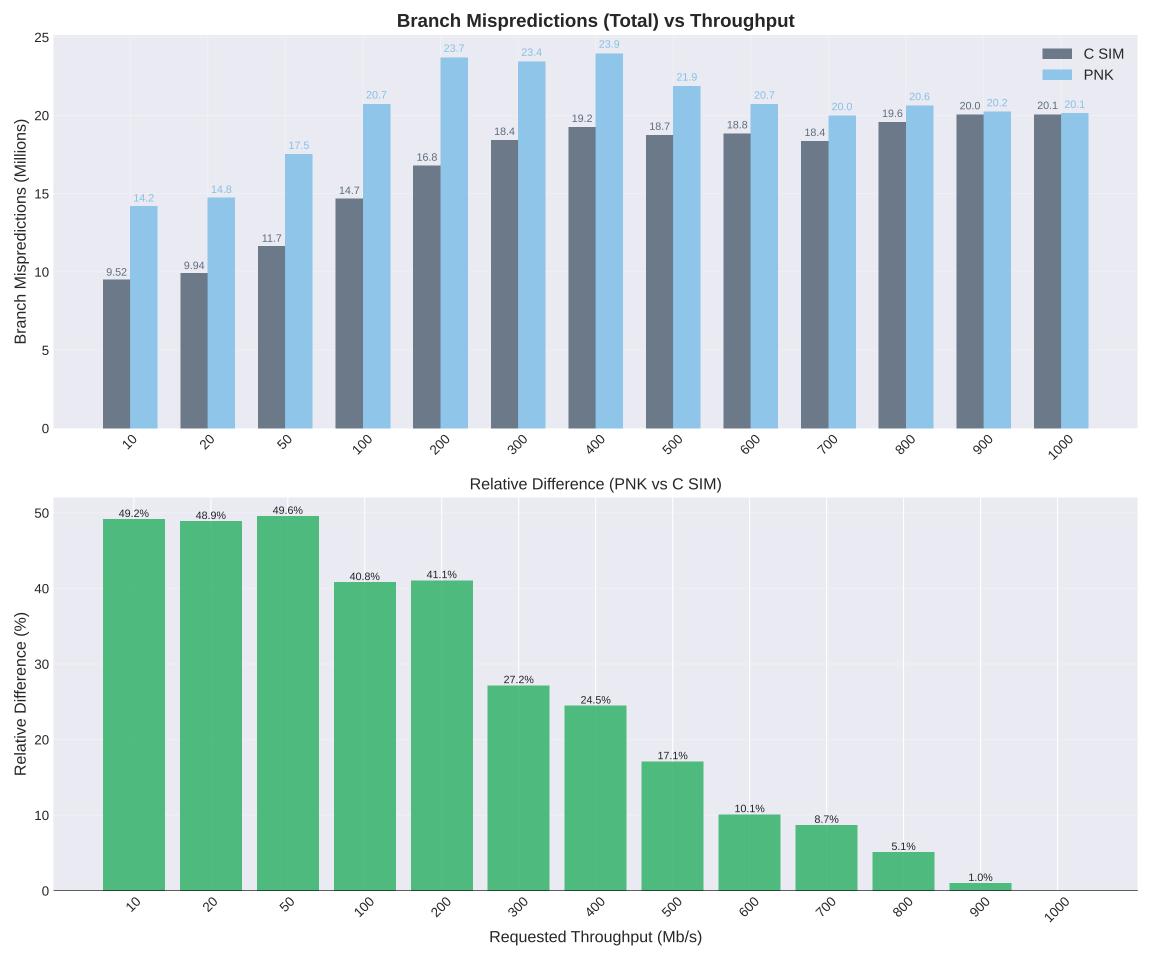




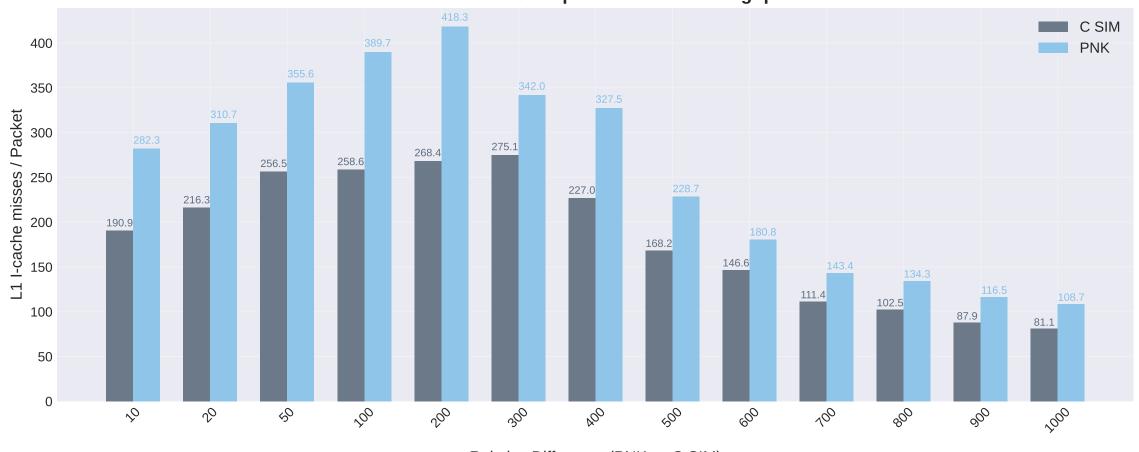


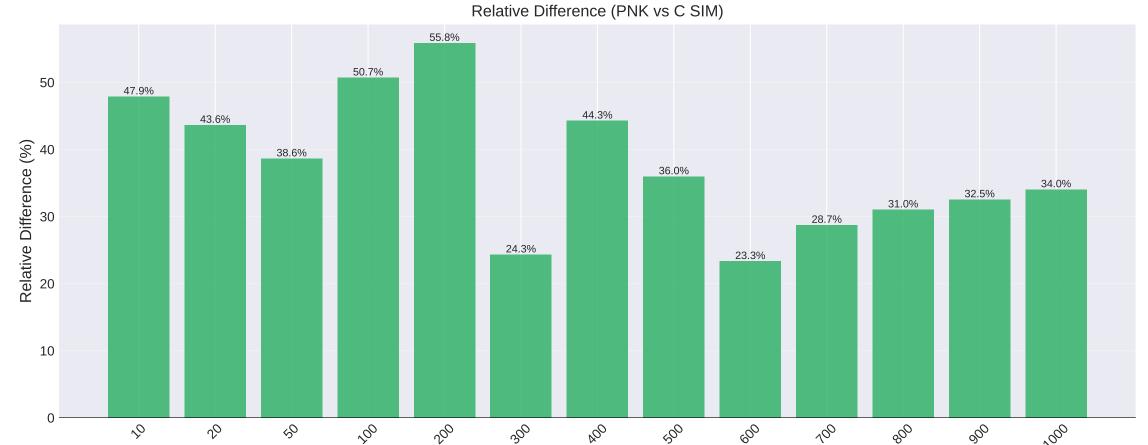






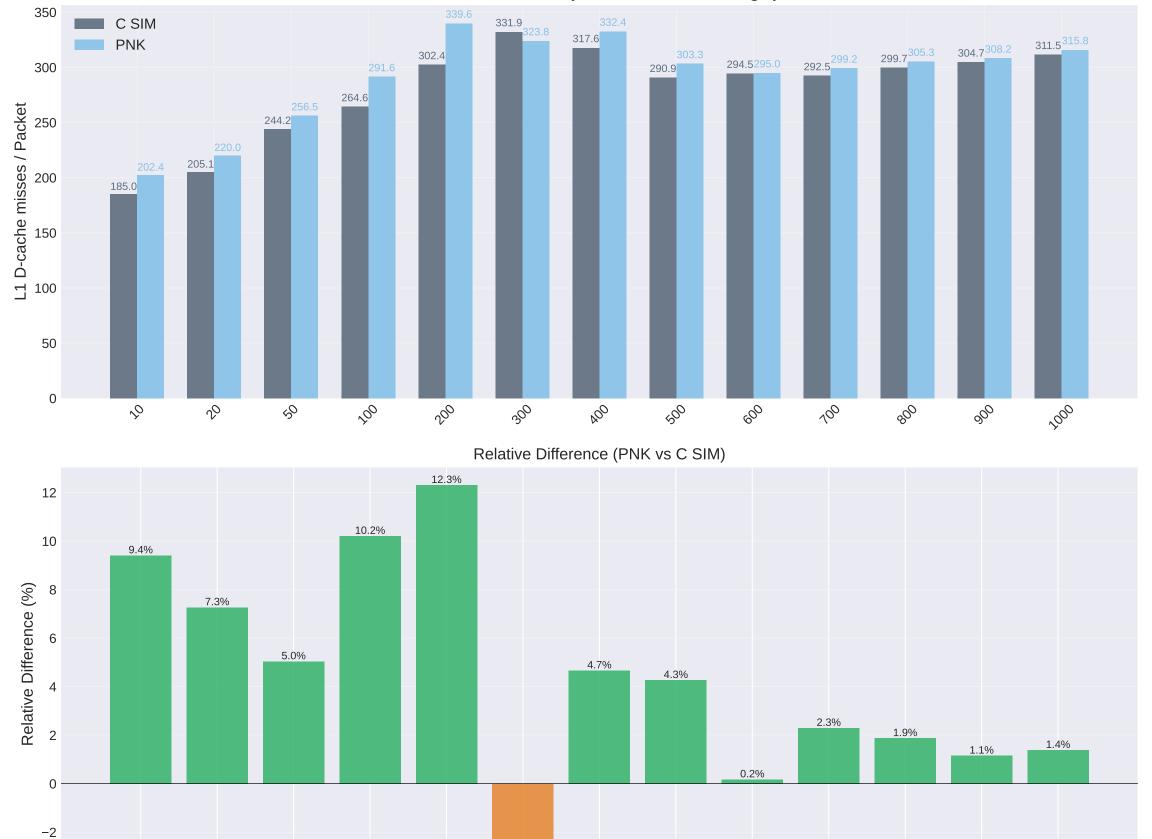
L1 I-cache Misses per Packet vs Throughput





Requested Throughput (Mb/s)

L1 D-cache Misses per Packet vs Throughput



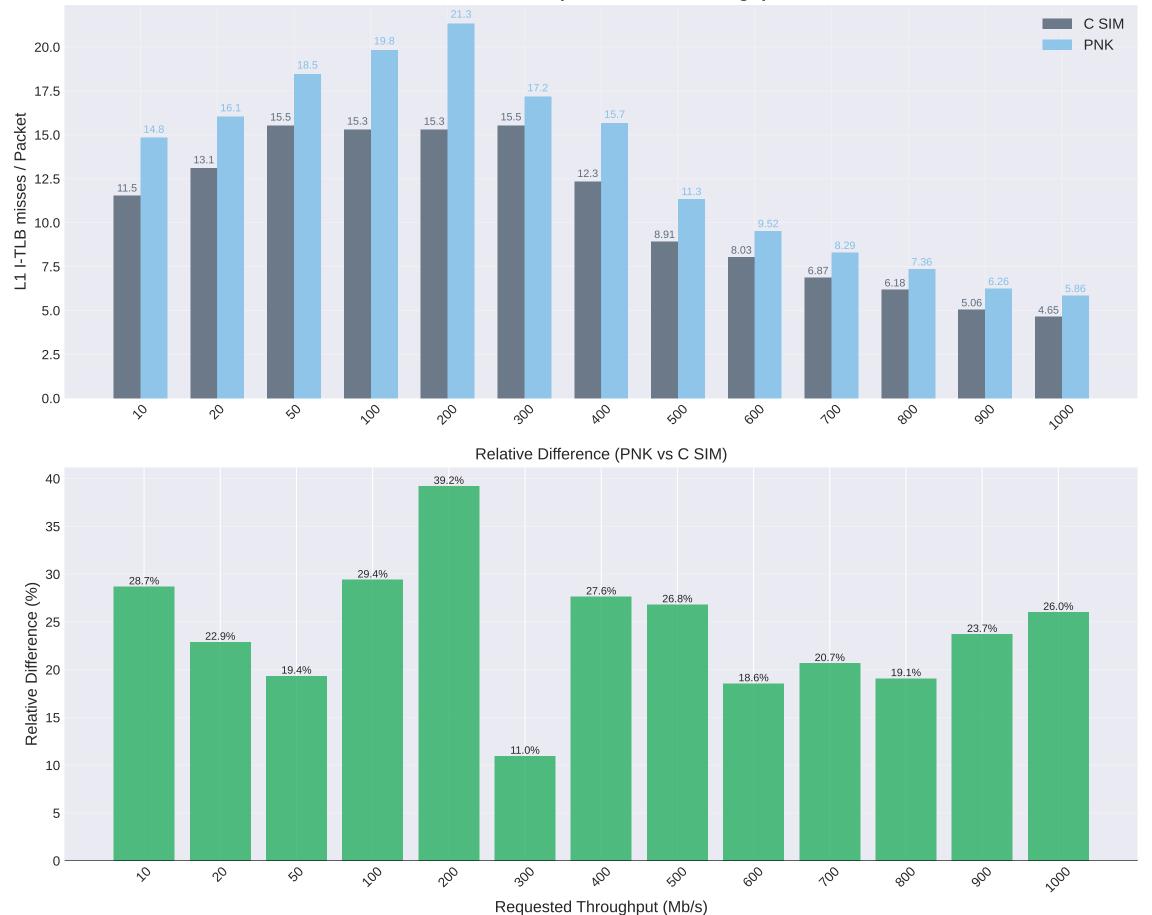
Requested Throughput (Mb/s)

NO

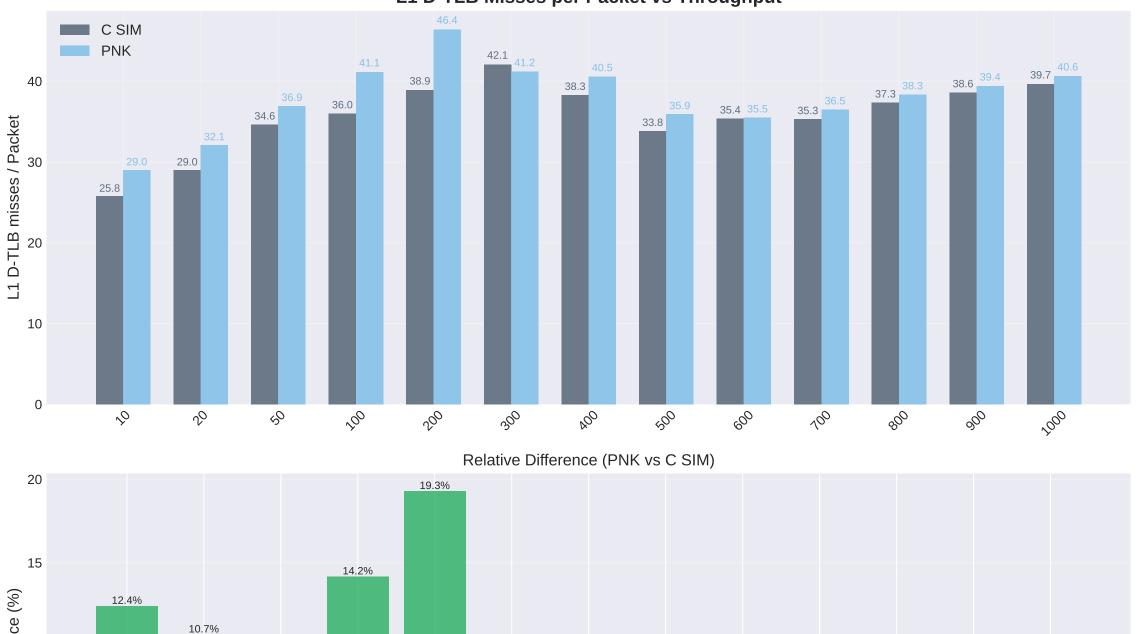
-2.4%

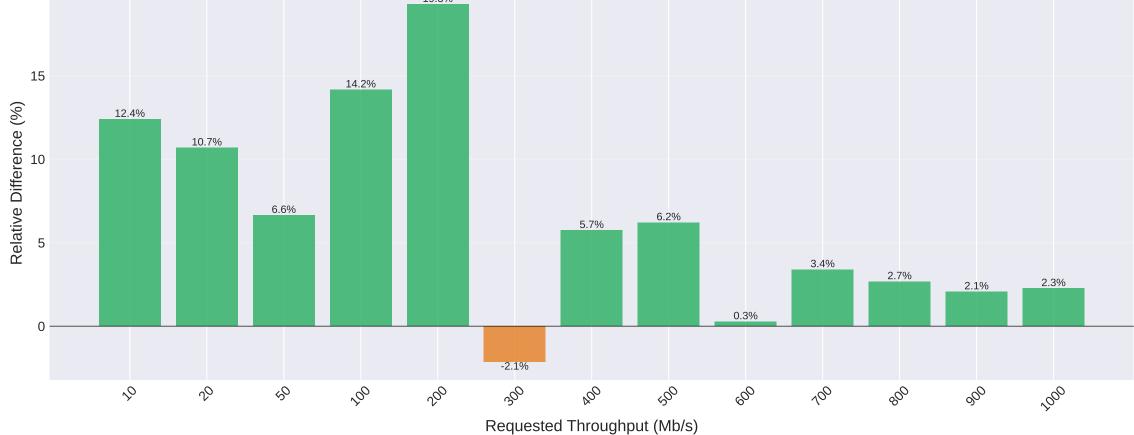
SO

L1 I-TLB Misses per Packet vs Throughput

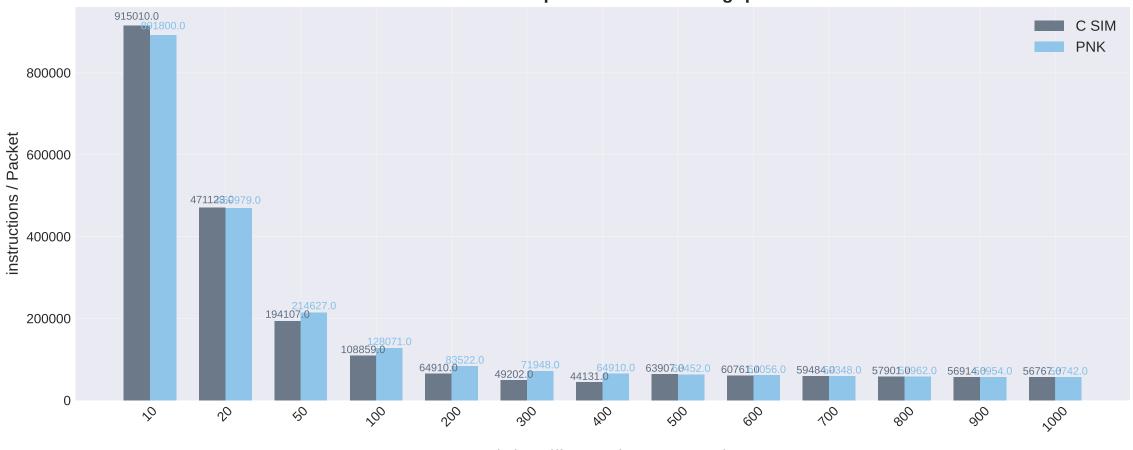


L1 D-TLB Misses per Packet vs Throughput

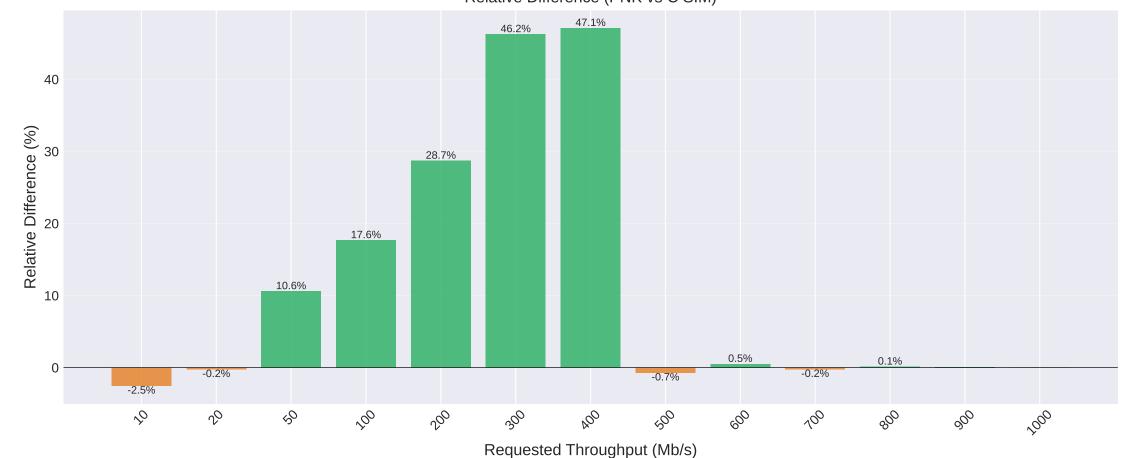




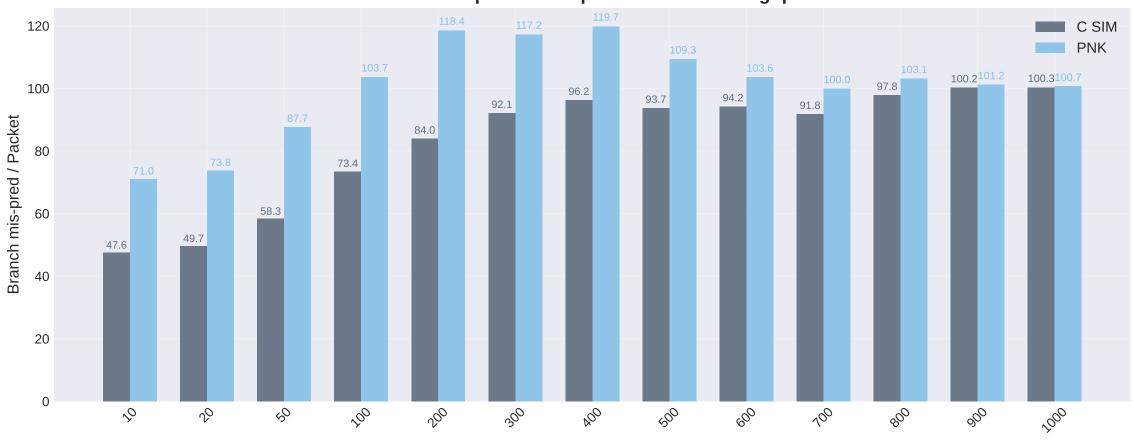
Instructions per Packet vs Throughput

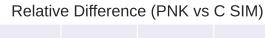


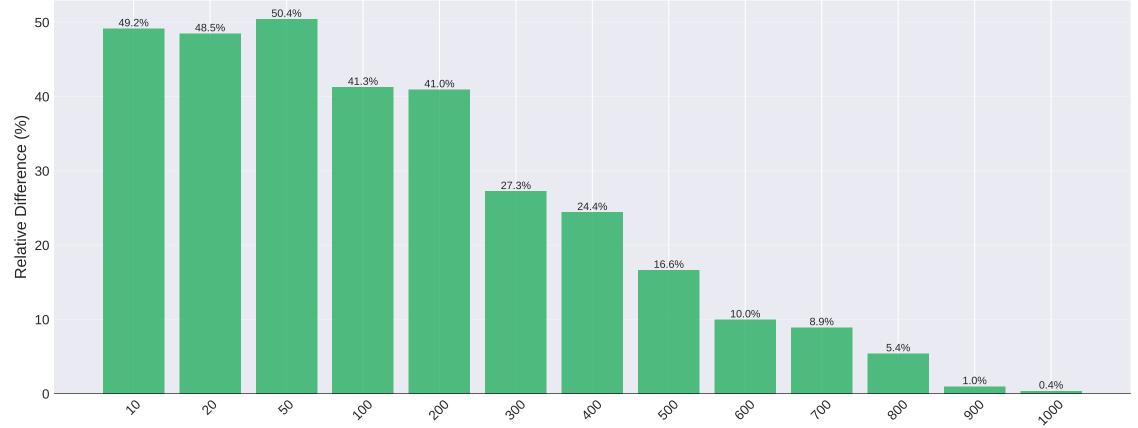




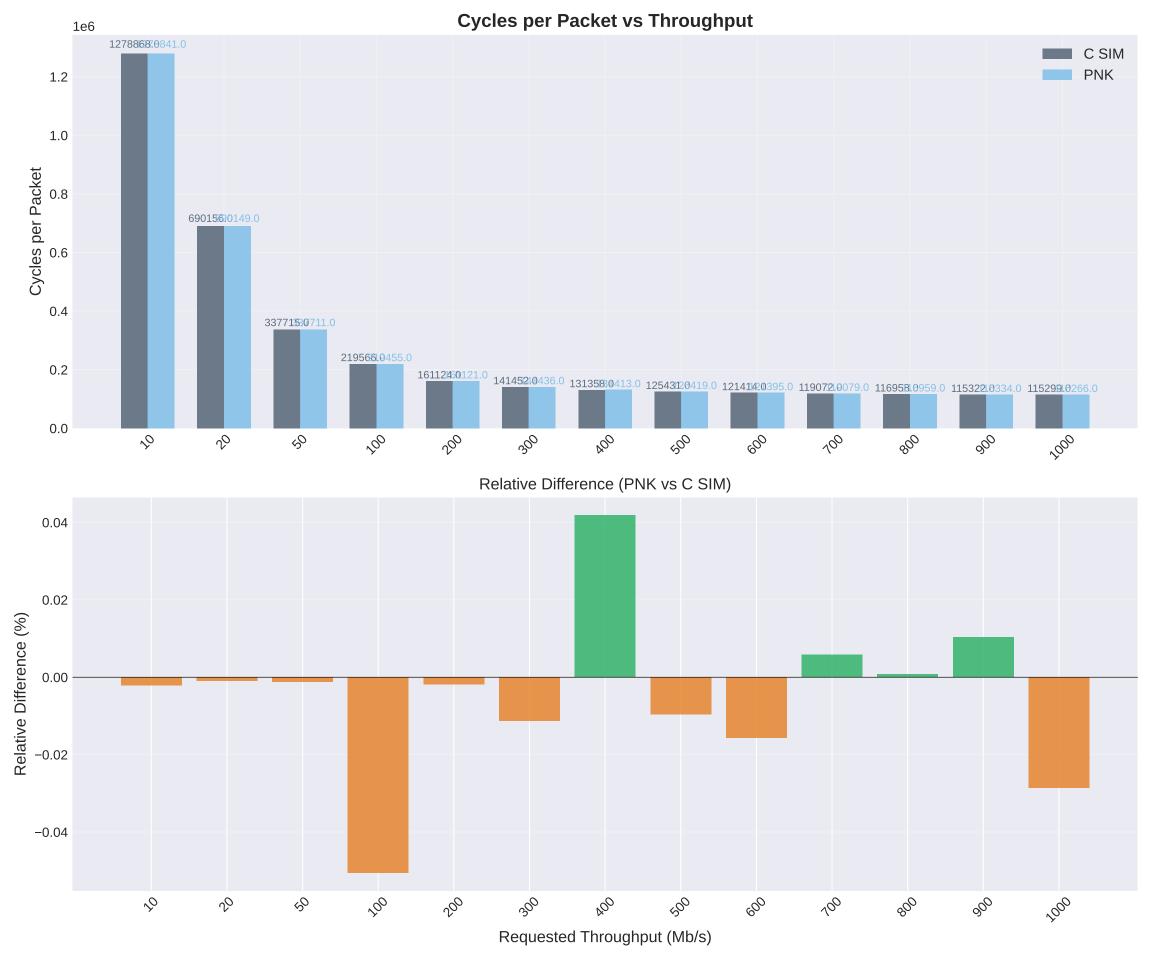
Branch Mispredictions per Packet vs Throughput



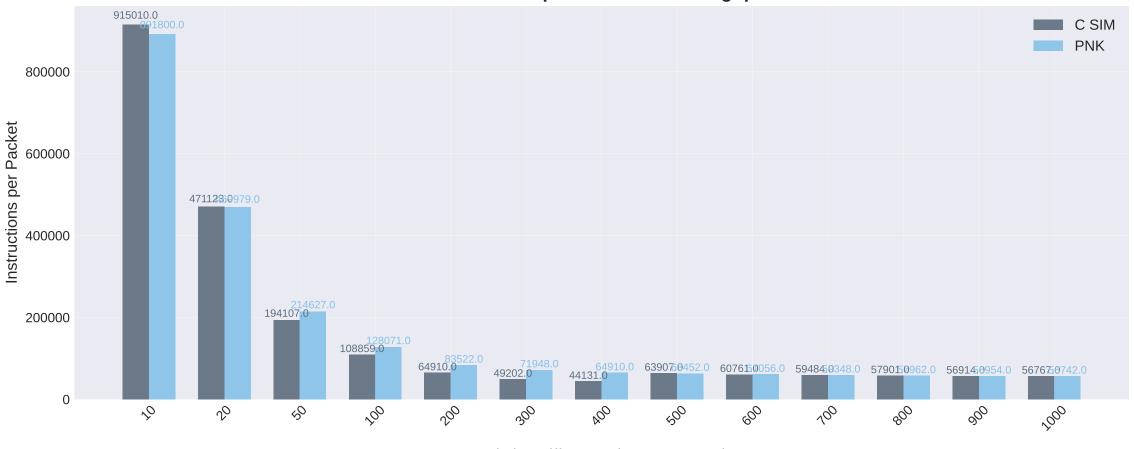


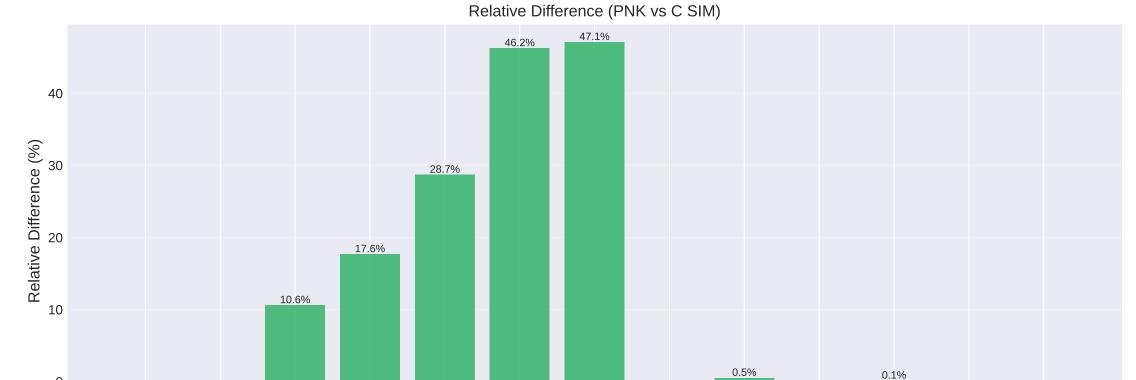


Requested Throughput (Mb/s)



Instructions per Packet vs Throughput





NO

Requested Throughput (Mb/s)

-0.7%

400

600

-0.2%

700

900

900

0

-0.2%

20

200

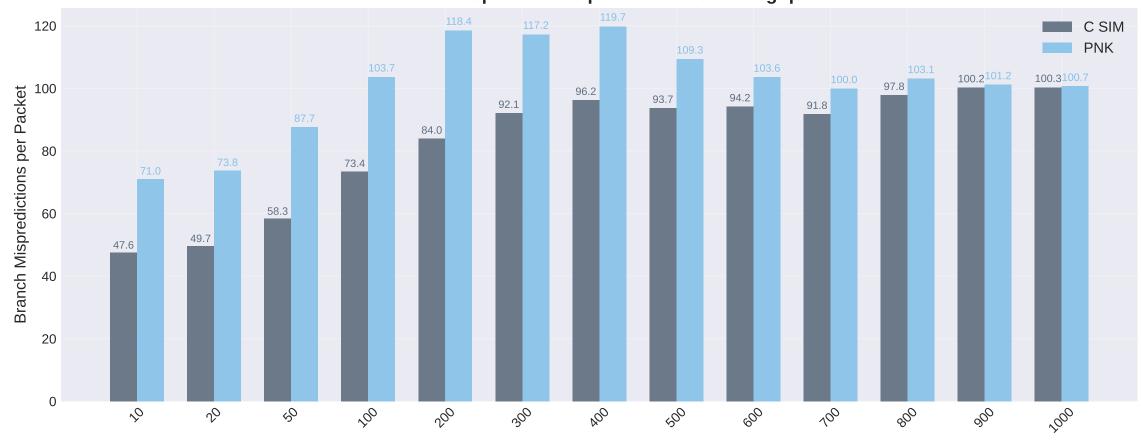
80

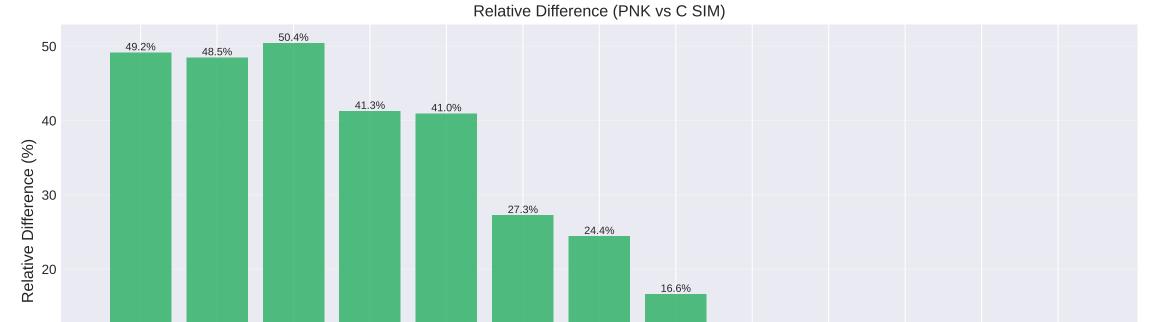
200

300

-2.5% ~

Branch Mispredictions per Packet vs Throughput





10

0

\$

20

60

200

200

300

Requested Throughput (Mb/s)

NOO

500

10.0%

600

8.9%

700

5.4%

900

1.0%

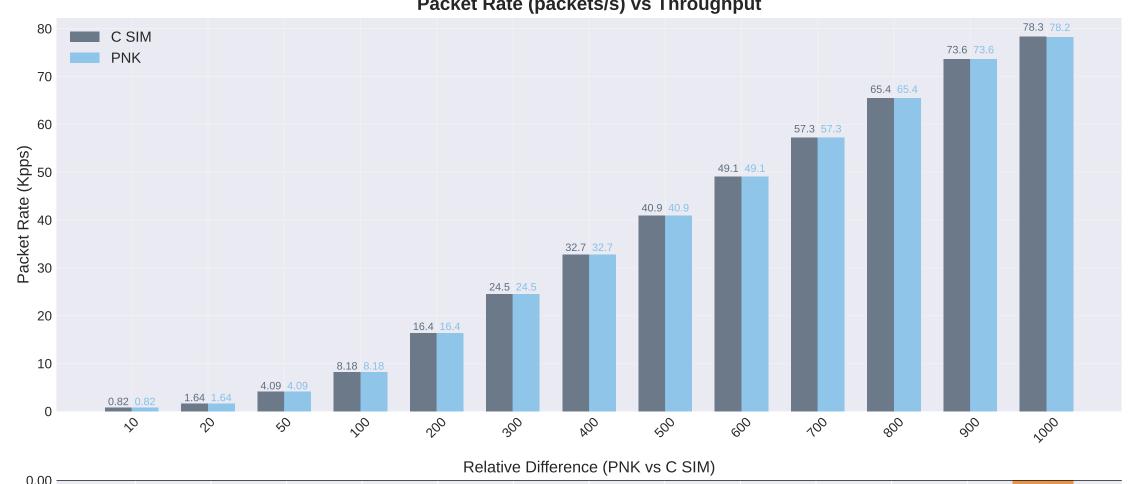
900

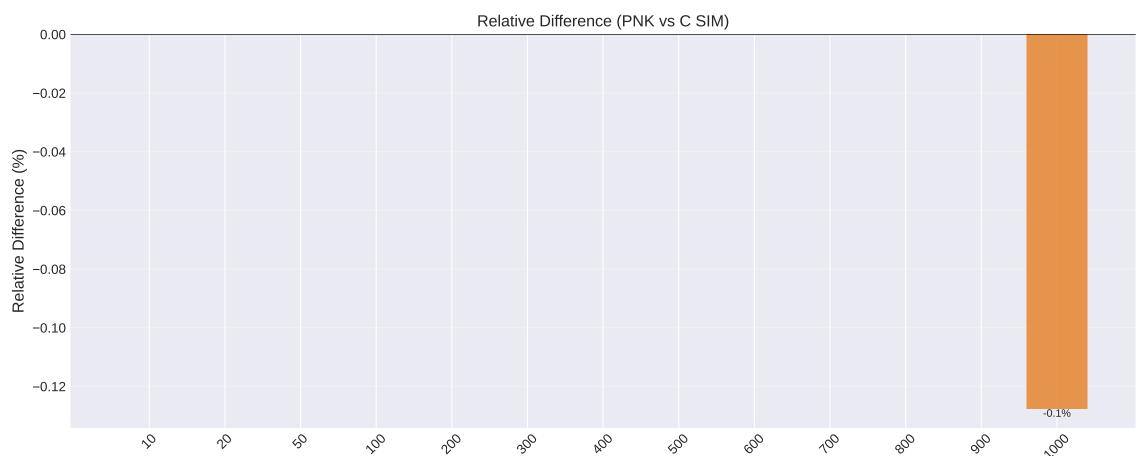
0.4%

2000

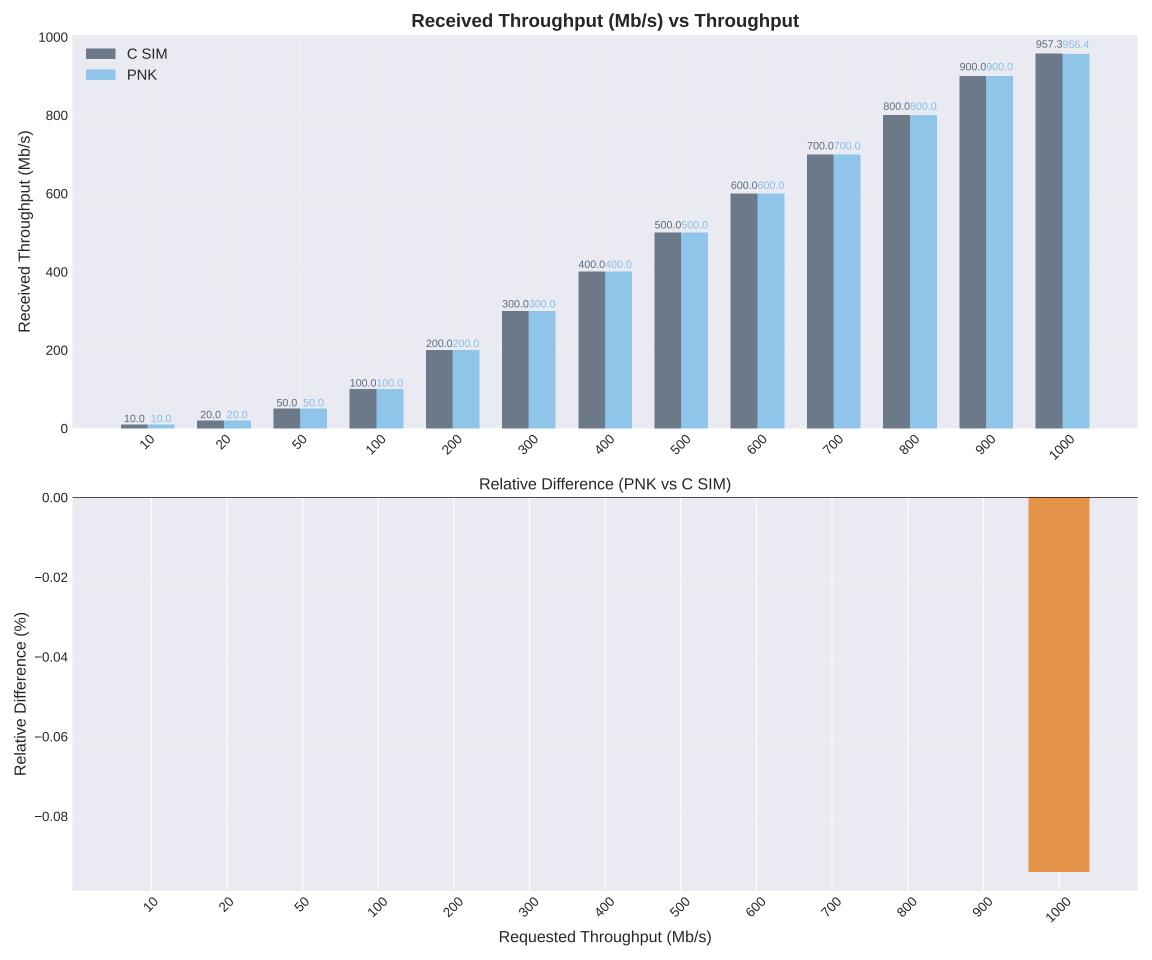


Packet Rate (packets/s) vs Throughput





Requested Throughput (Mb/s)



Sent Throughput (Mb/s) vs Throughput 1000.0000.0 C SIM 1000 PNK 900.0900.0 800.0800.0 800 Sent Throughput (Mb/s) 700.0700.0 600.0600.0 600 500.0500.0 400.0400.0 400 300.0300.0 200.0200.0 200 100.0100.0 50.0 50.0 20.0 20.0 10.0 10.0 0 200 NOO 100 300 400 600 100 900 900 2000 \$ 20 80 Relative Difference (PNK vs C SIM) 0.04 Relative Difference (%) 0.02 0.00 -0.02 -0.0420 200 200 300 NOO 500 900 30 50 100 Requested Throughput (Mb/s)