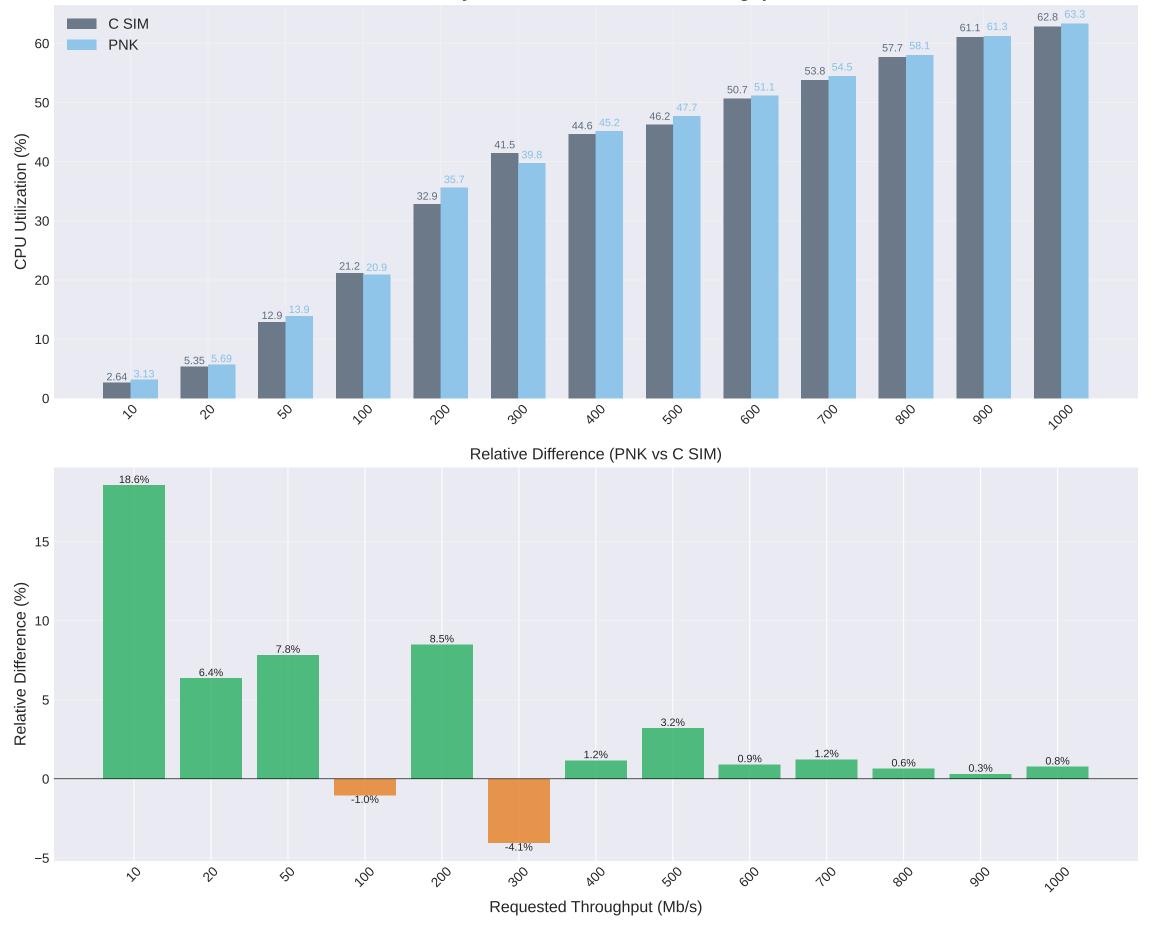
Instructions per Second vs Throughput 0.69 0.7 C SIM 0.66 0.66 PNK 0.6 0.56 Instructions per Second (Billions) 0.51 0.51 0.50 0.51 0.51 0.51 0.50 0.50 0.50 0.50 0.50 0.50 0.49 0.49 0.40 0.35 0.34 0.34 0.1 0.0 700 200 300 NOO 400 600 700 900 900 2000 30 20 60 Relative Difference (PNK vs C SIM) 50 48.3% 40 Relative Difference (%) 8 0 0 29.9% 10.3% 1.0% 0.5% 0.3% 0.1% 0.1% 0 -0.2% -0.6% -1.2% -5.0% \$0 20 60 700 200 300 NOO 500 600 700 900 Requested Throughput (Mb/s)

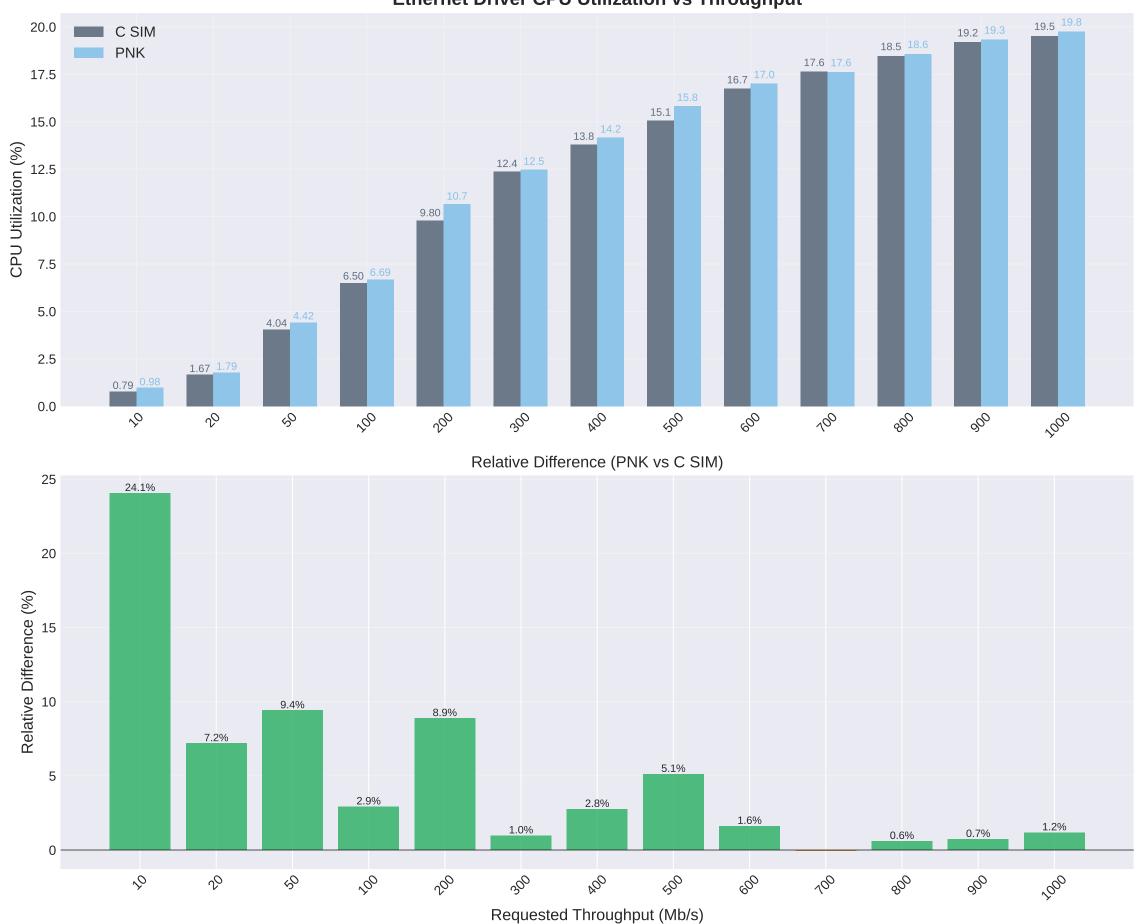
Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3957.1 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.3% 600.0600.0 61.3% 600 CPU Utilization (%) 58.1% 54.5% 54.1% 500.0500.0 45:3% 400 300.0300.0 200.0200.0 200 20 100.0100.0 3:4% 50.0 50.0 20.0 20.0 0 900 200 go NOO 400 2000 700 600 100 900 20 20 50

Requested Throughput (Mb/s)

Total System CPU Utilization vs Throughput

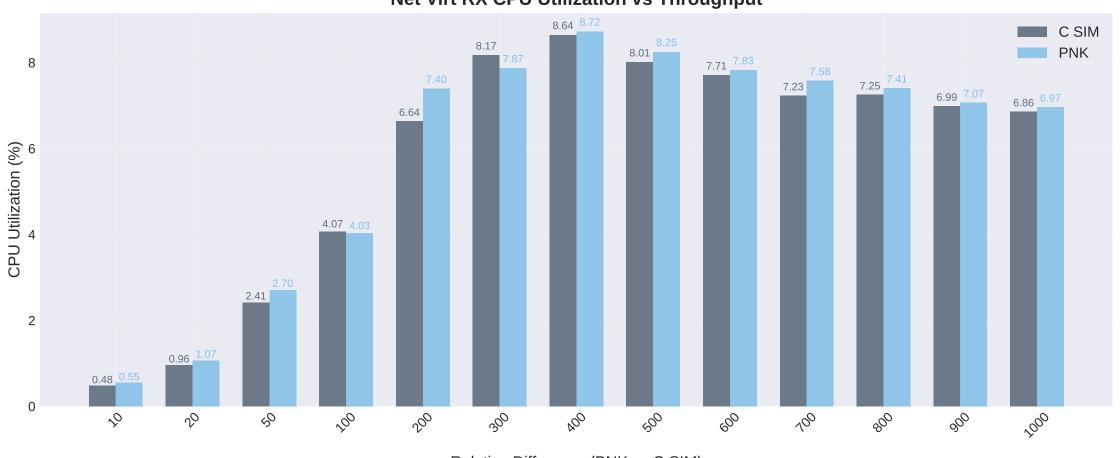


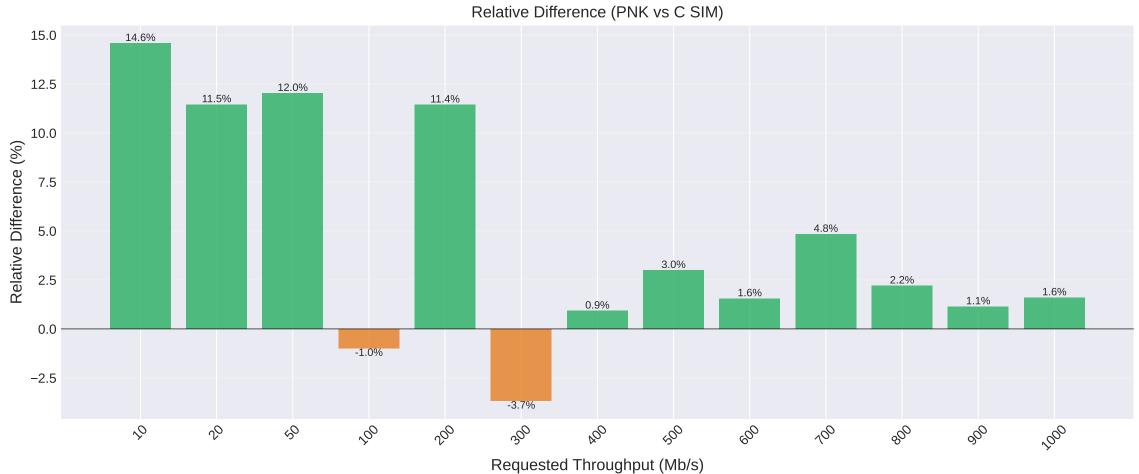
Ethernet Driver CPU Utilization vs Throughput



Net Virt TX CPU Utilization vs Throughput 7 6.56 6.70 C SIM 6.36 PNK 6.18 6 5.35 4.74 4.91 4.57 5 CPU Utilization (%) 4.22 4.35 4.42 4.41 4.30 4.17 3.62 2.56 2.56 2 1.65 1 0.68 0.76 0.31 0 200 200 300 NOO 400 600 700 800 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 40 38.7% 30 Relative Difference (%) 20 18.8% 13.9% 11.8% 10 5.8% 3.7% 3.9% 3.6% 3.1% 2.9% 2.1% 0 -7.7% -10 30 SO 200 200 300 NOO 500 600 100 900 20

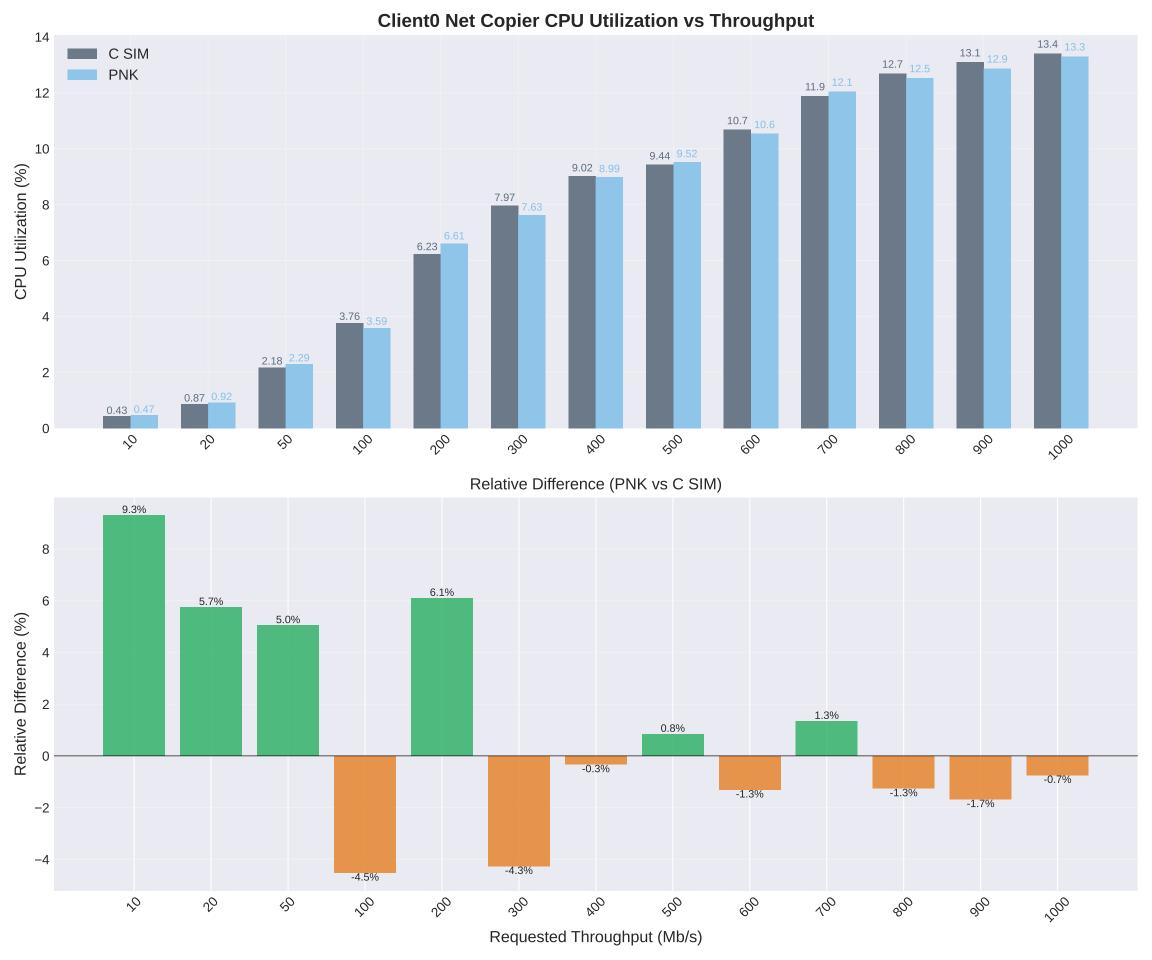
Net Virt RX CPU Utilization vs Throughput



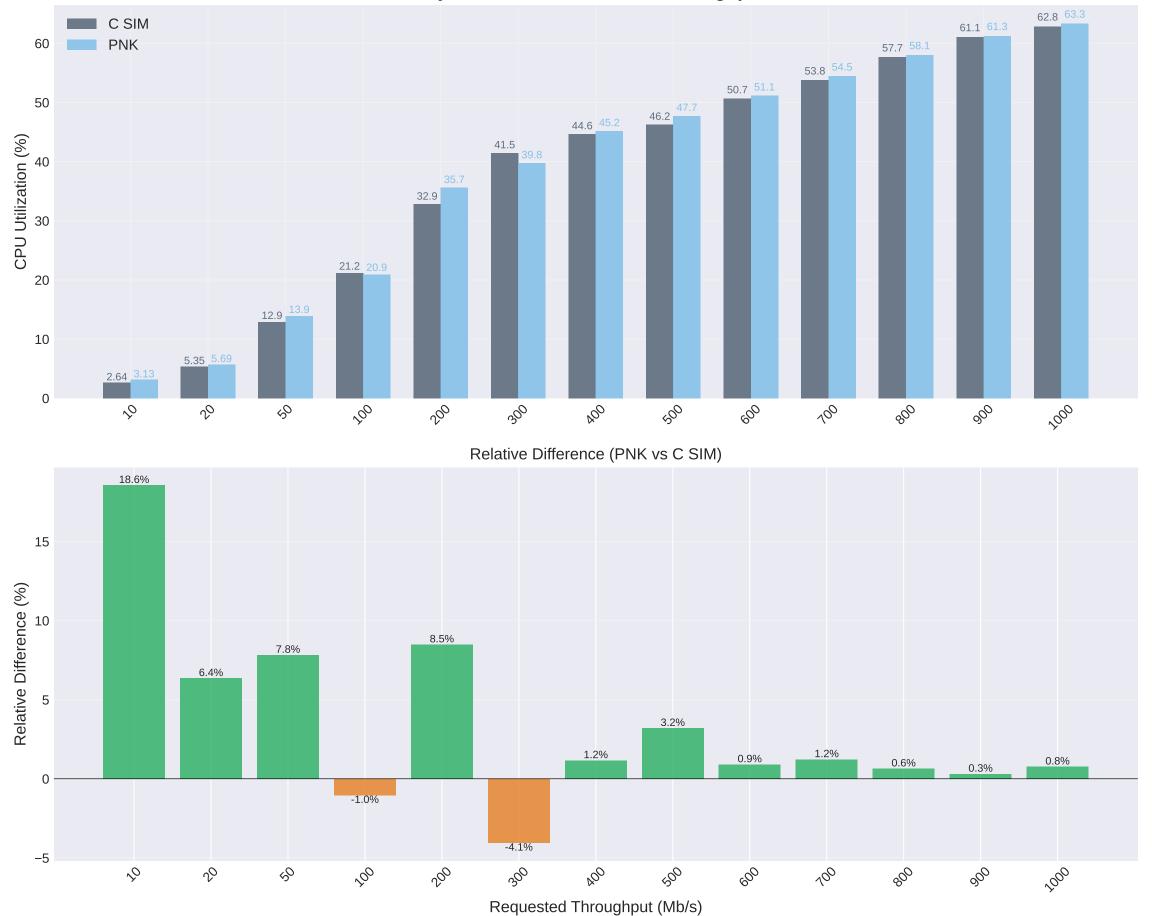


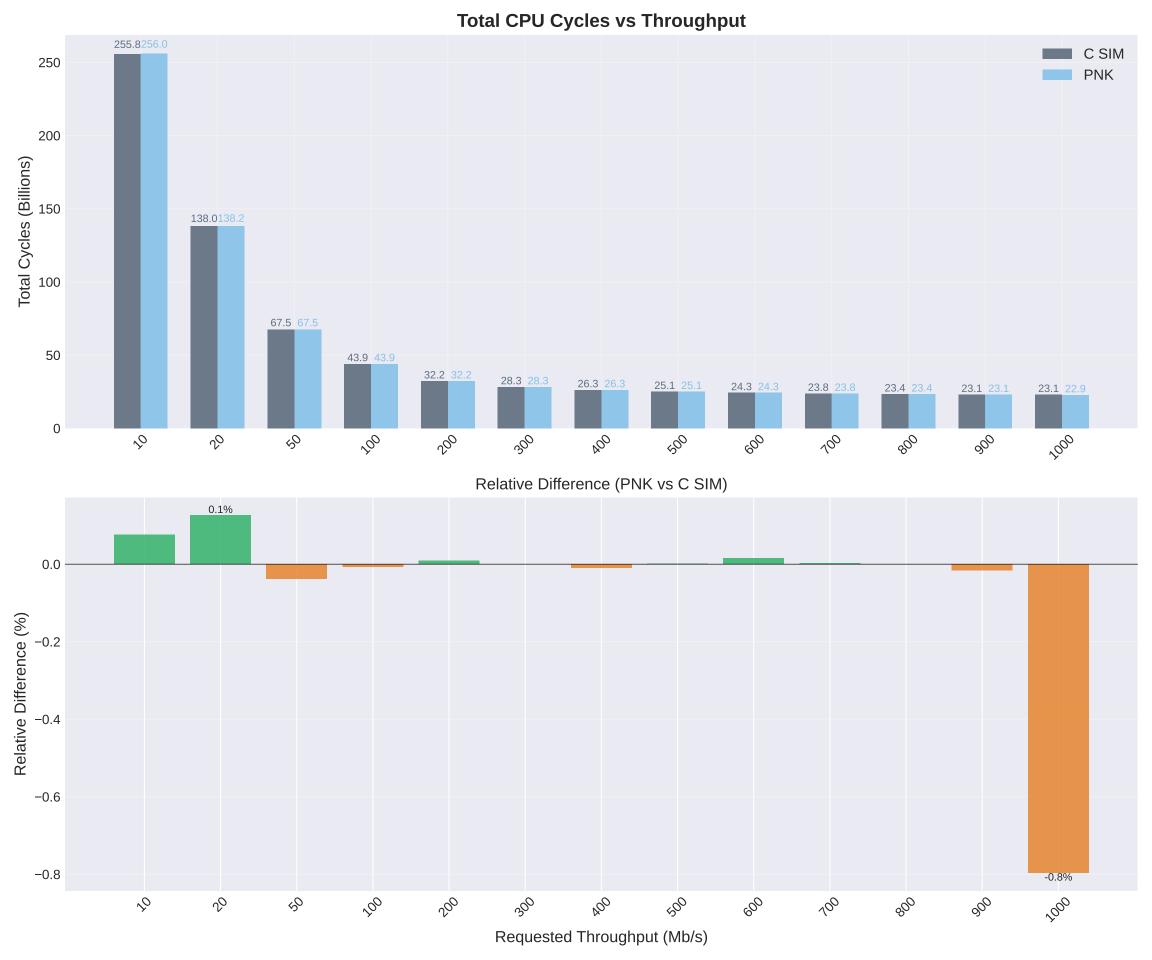
Client0 CPU Utilization vs Throughput





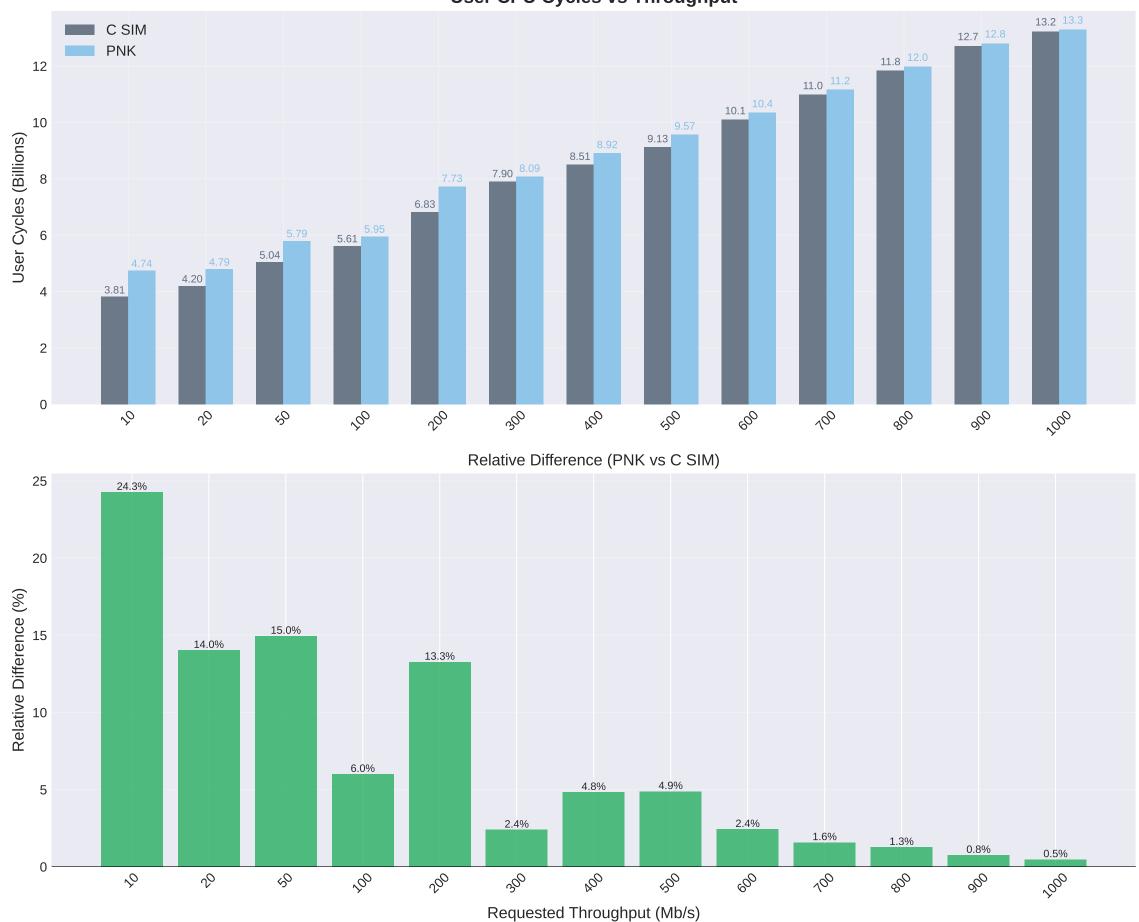
System CPU Utilization vs Throughput





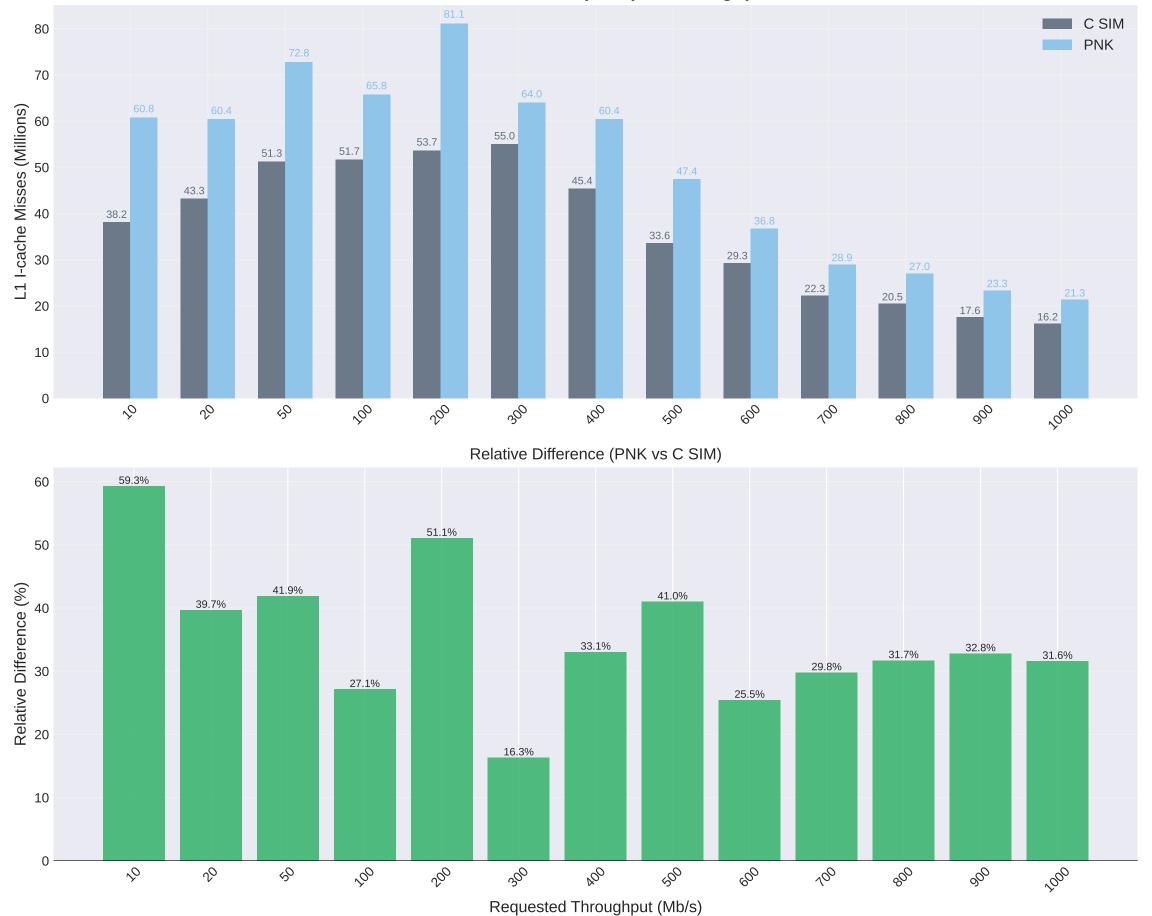
Kernel CPU Cycles vs Throughput 3.5 3.30 3.37 C SIM PNK 3.10 3.02 3.01 2.99 3.0 2.52 2.48 Kernel Cycles (Billions) 1.5 1.0 2.36 2.26 2.10 1.73 1.71 1.55 1.27 1.15 0.5 0.0 200 200 300 400 400 600 700 800 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 10.0% 10 5 1.9% Relative Difference (%) -0.3% -1.1% -1.8% -2.8% -4.1% -4.5% -5 -5.9% -6.3% -7.7% -10 -12.4% -15 -16.4% 30 SO 200 200 300 500 600 100 900 20 NO Requested Throughput (Mb/s)

User CPU Cycles vs Throughput

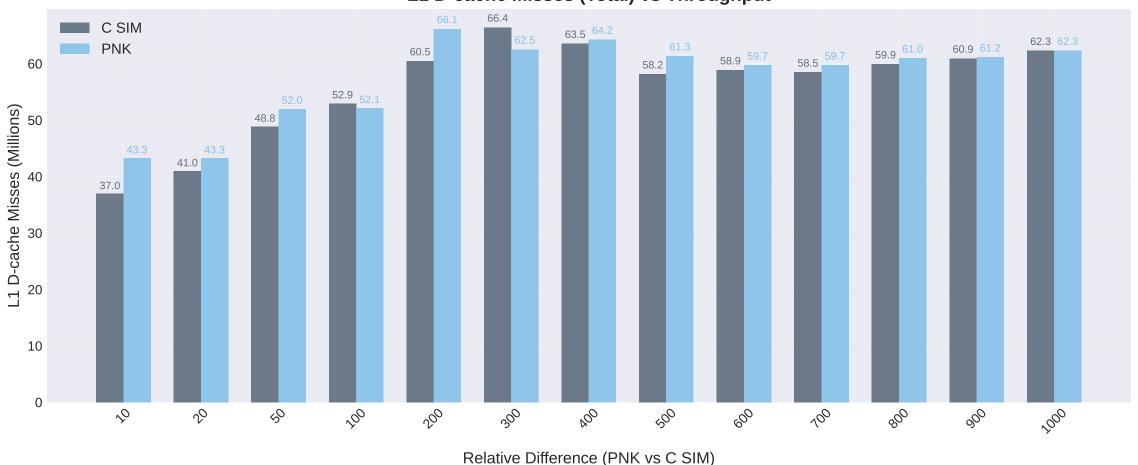


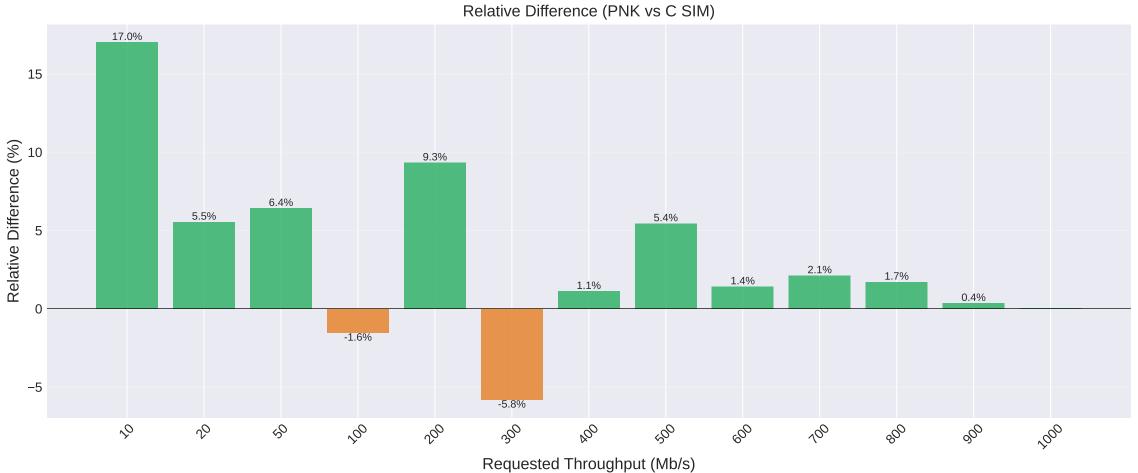
Idle CPU Cycles vs Throughput 249.0248.0 250 C SIM PNK 200 Idle Cycles (Billions) 130.6130.3 58.8 58.1 50 34.6 34.7 21.6 20.7 16.6 17.0 14.5 14.4 13.5 13.1 12.0 11.9 11.0 10.8 9.90 9.81 8.57 8.39 8.98 8.94 0 600 \$ 200 200 300 NOO 400 700 800 900 2000 20 50 Relative Difference (PNK vs C SIM) 2.9% 3 2 Relative Difference (%) 0.3% -0.2% -0.4% -0.5% -0.9% -1.0% -0.9% -1.2% -1.4% -2.1% -2.7% -3 -4 -4.2% \$ 20 SO 200 200 300 NOO 400 600 700 900

L1 I-cache Misses (Total) vs Throughput



L1 D-cache Misses (Total) vs Throughput





L1 I-TLB Misses (Total) vs Throughput



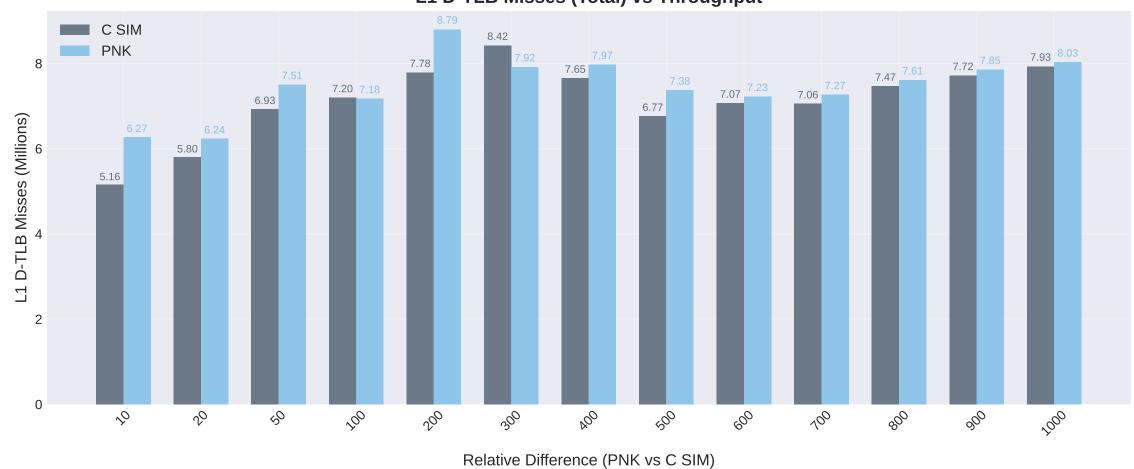
Requested Throughput (Mb/s)

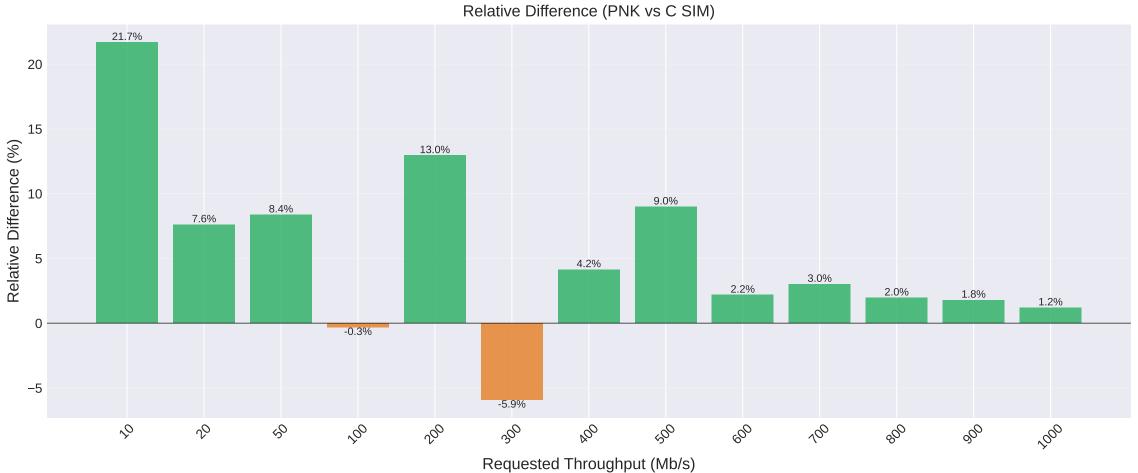
NOO

3.2%

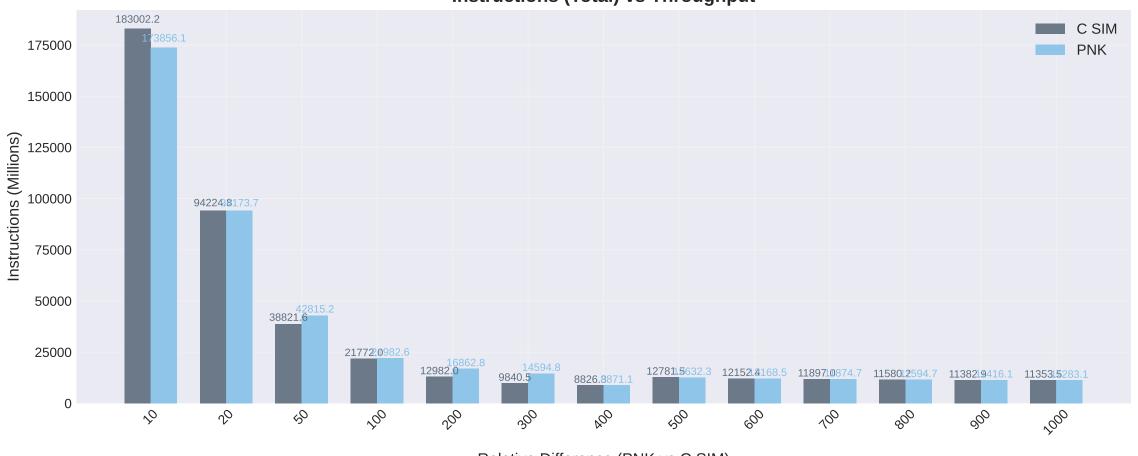
\$0

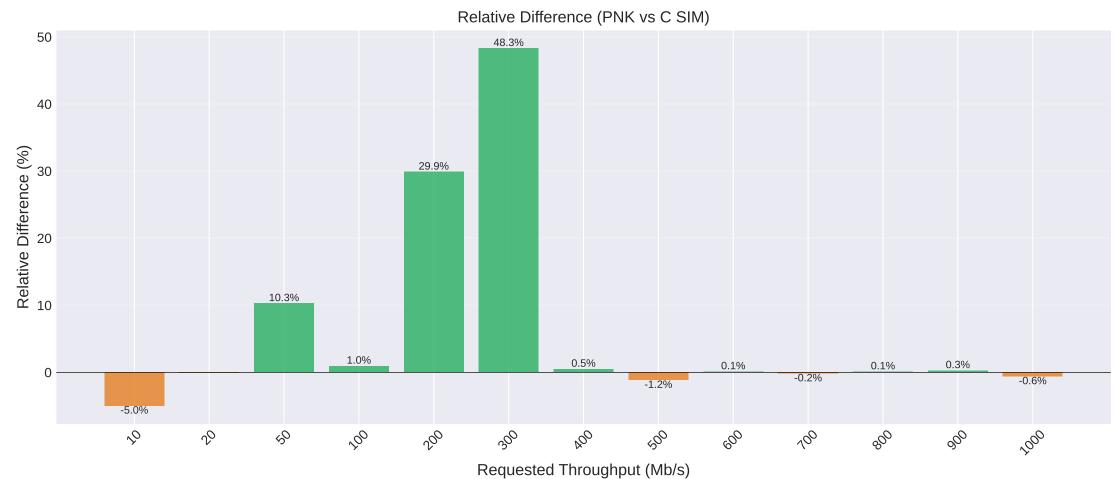
L1 D-TLB Misses (Total) vs Throughput



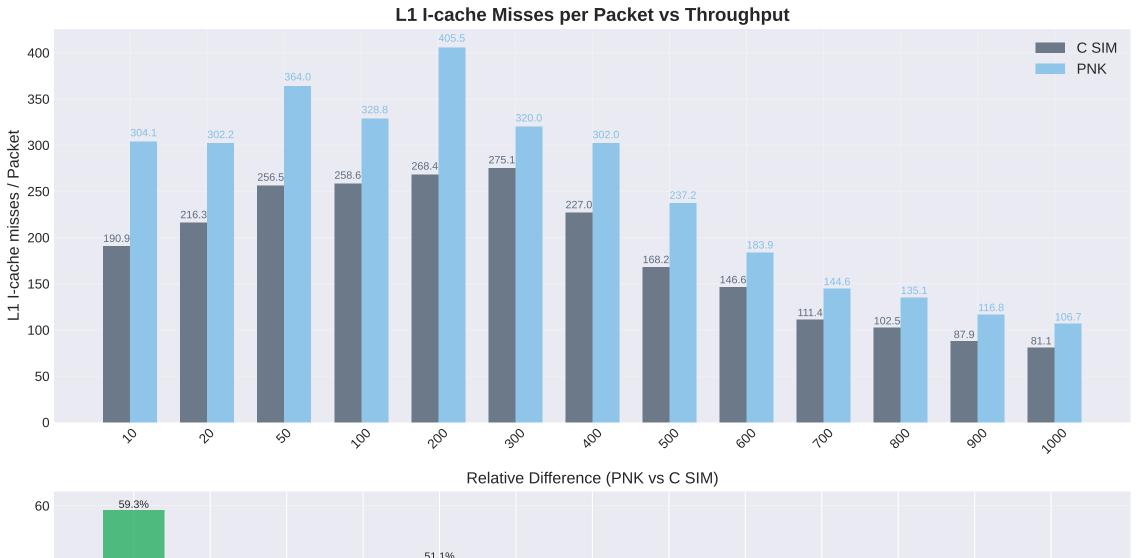


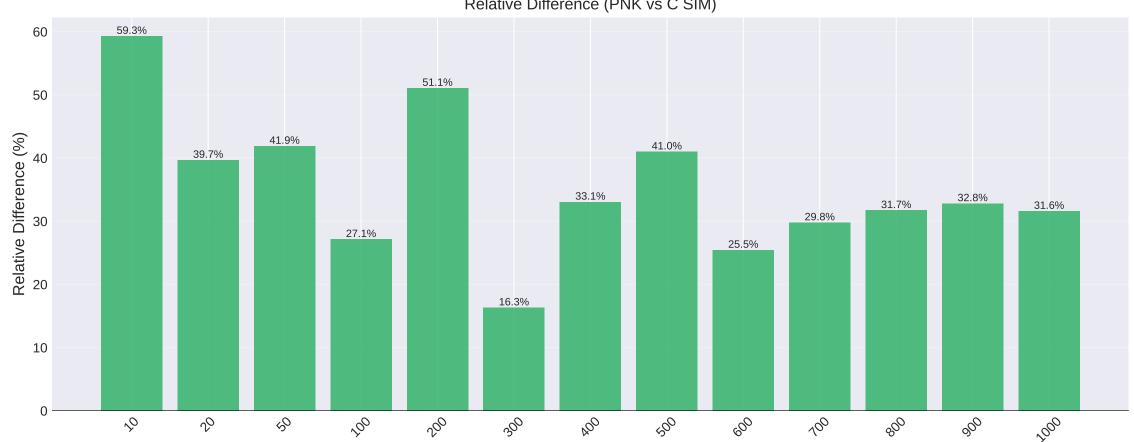




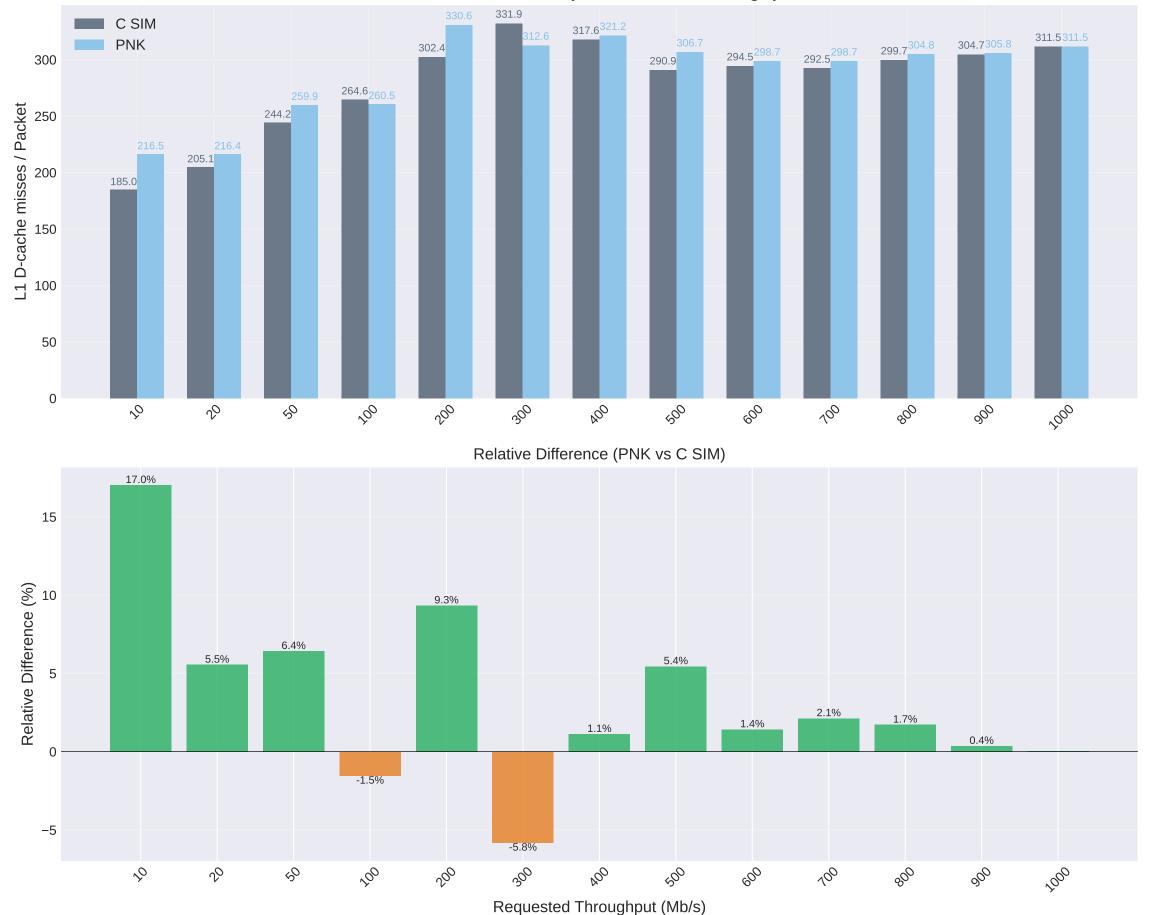




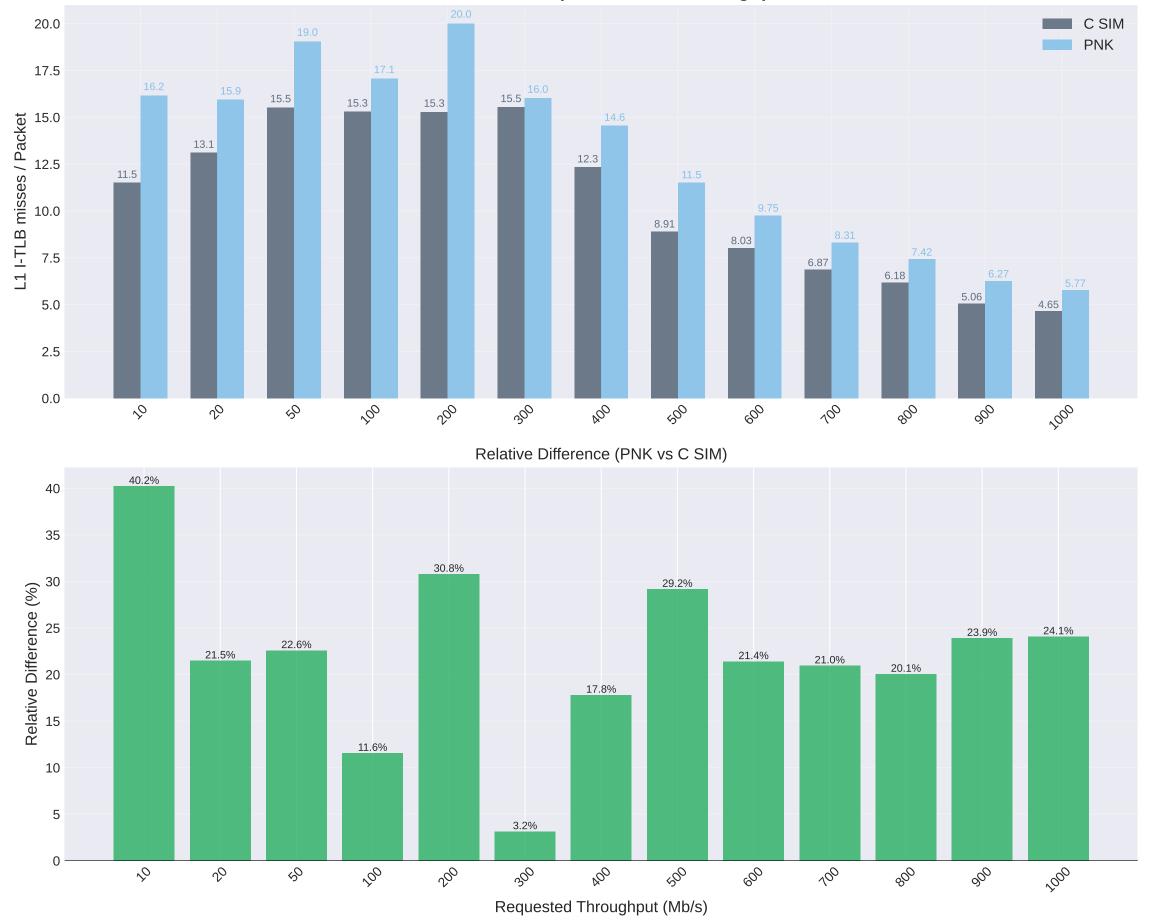




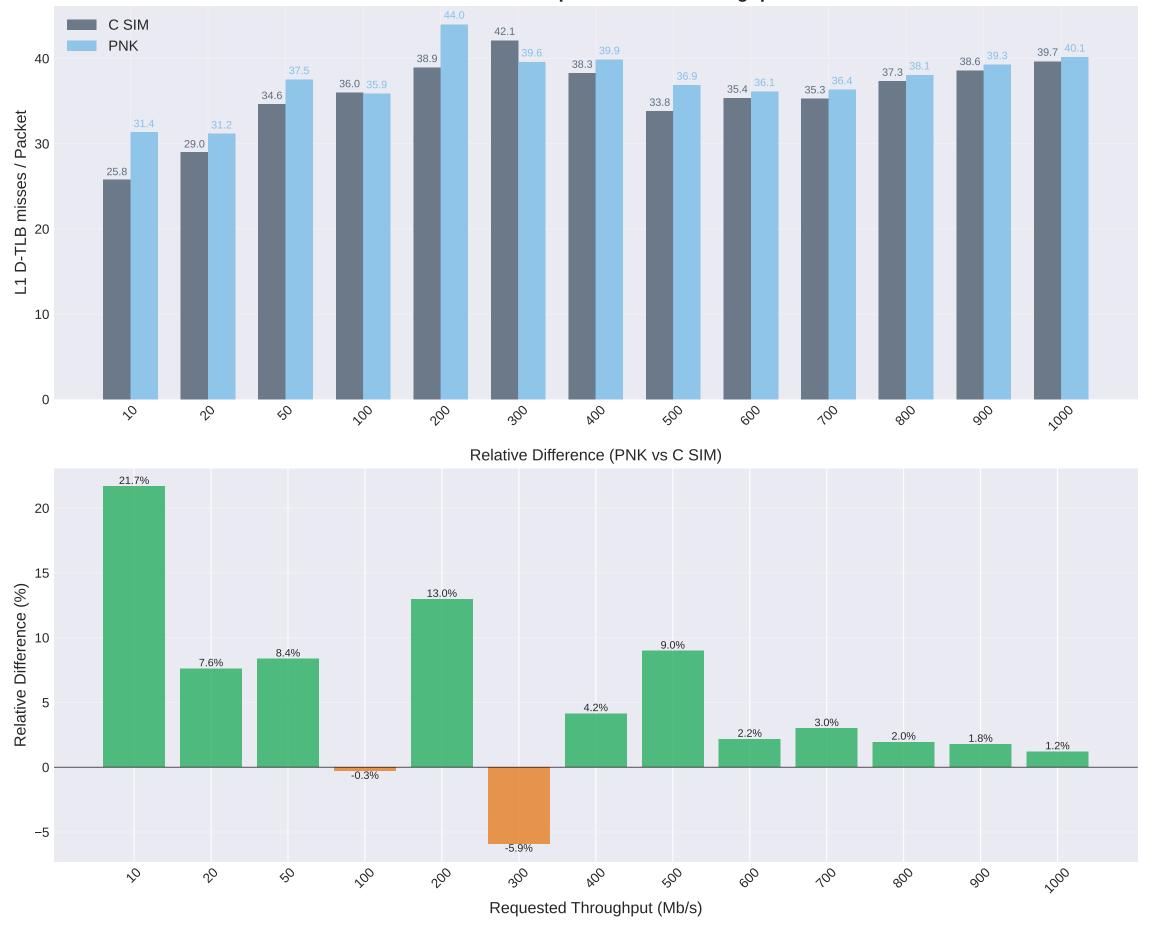
L1 D-cache Misses per Packet vs Throughput



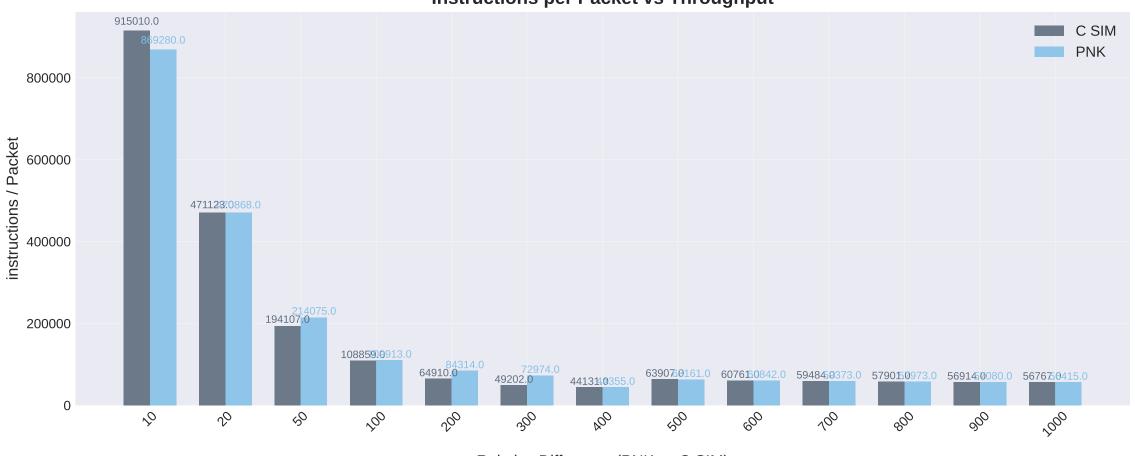
L1 I-TLB Misses per Packet vs Throughput

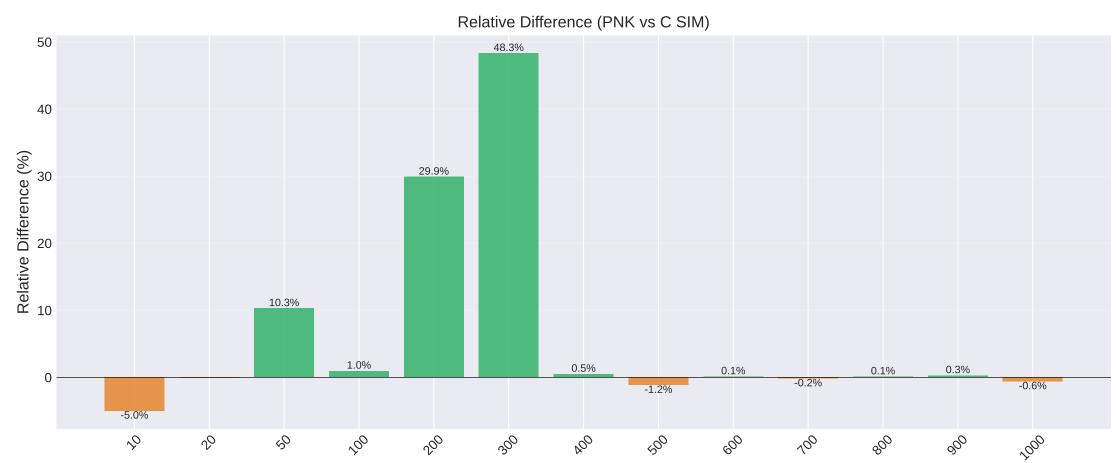


L1 D-TLB Misses per Packet vs Throughput

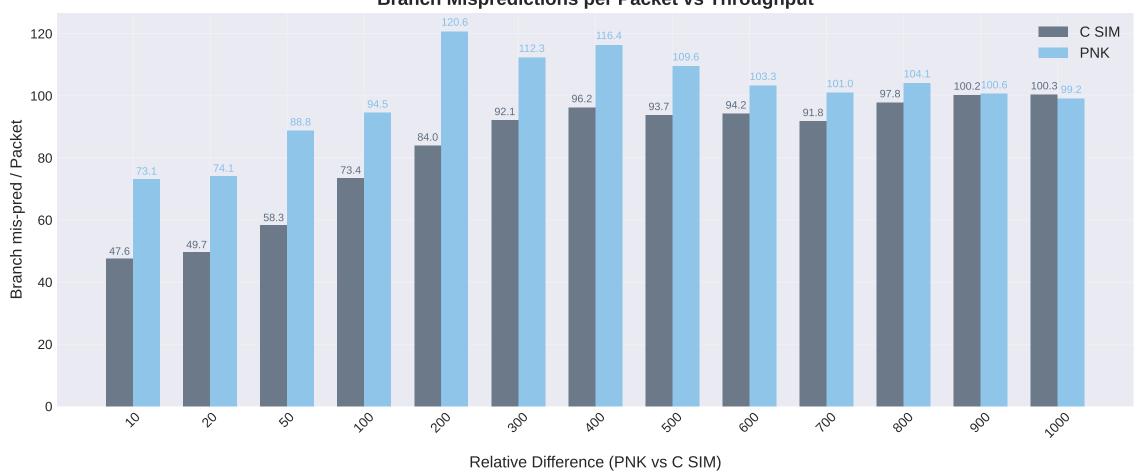


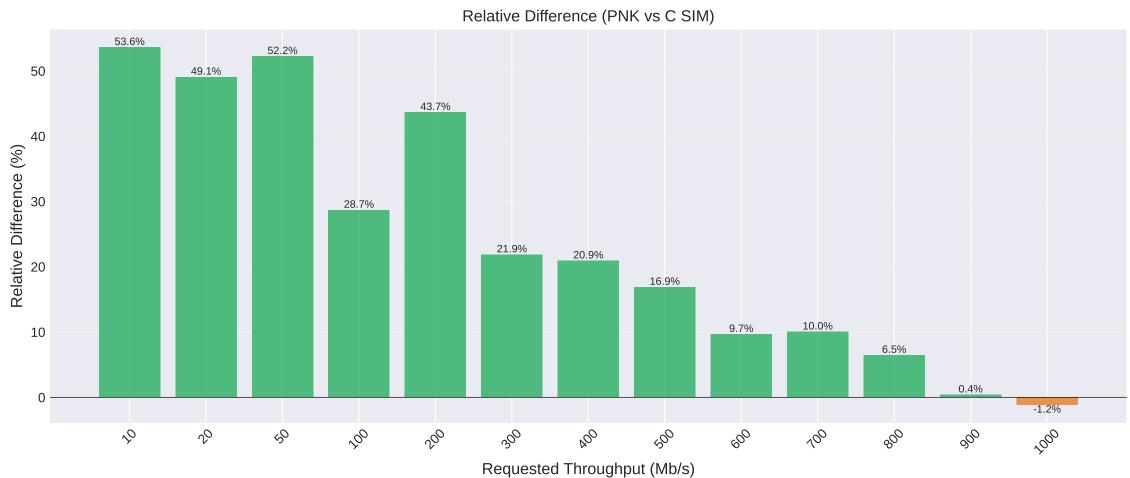


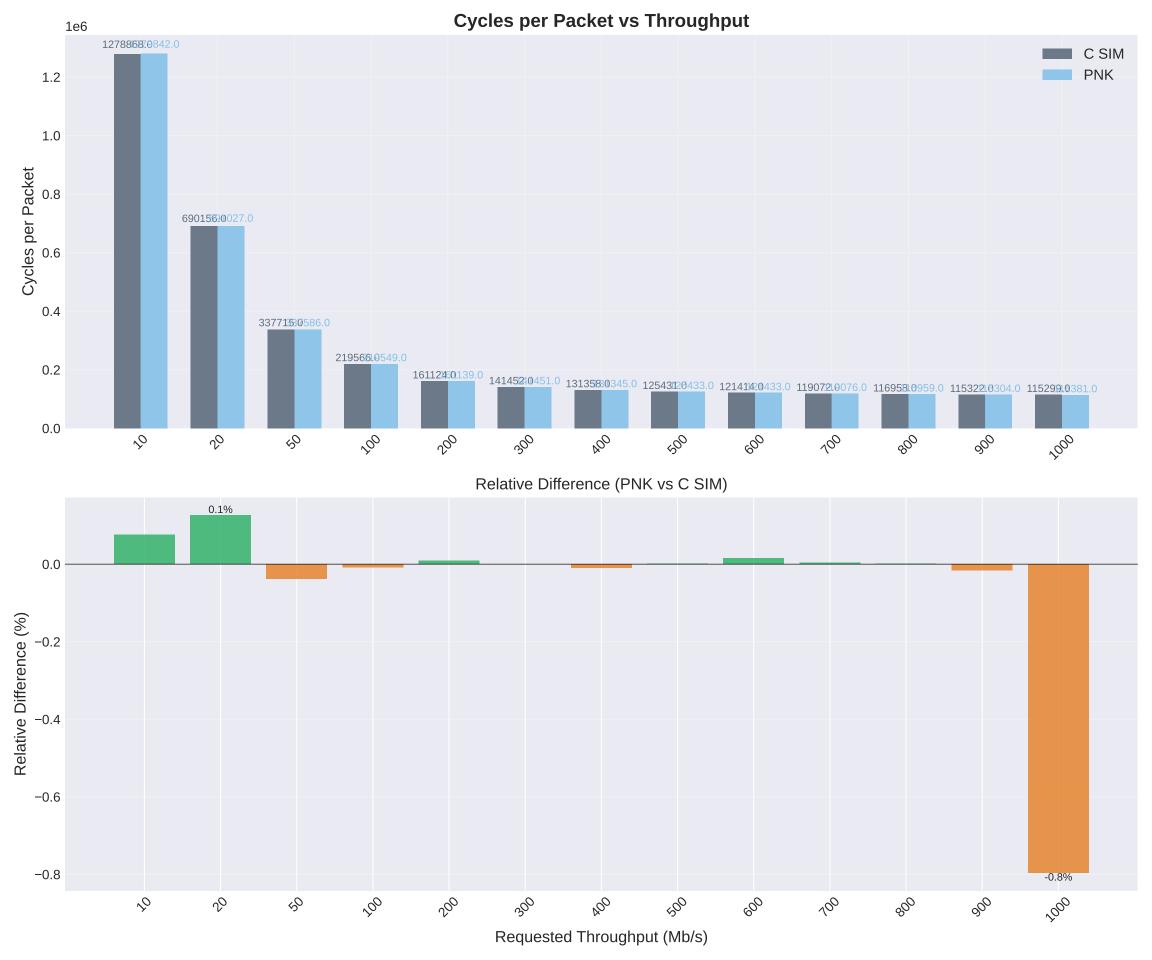




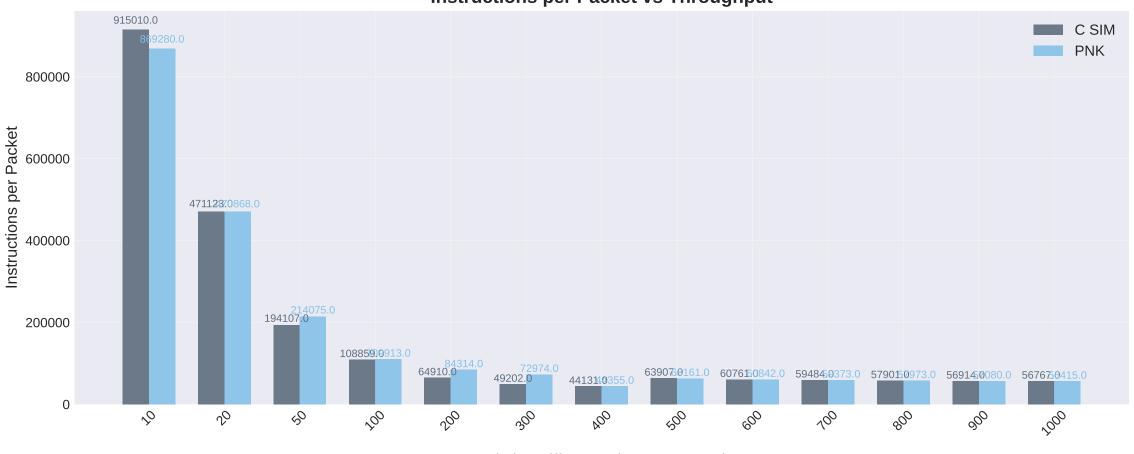
Branch Mispredictions per Packet vs Throughput

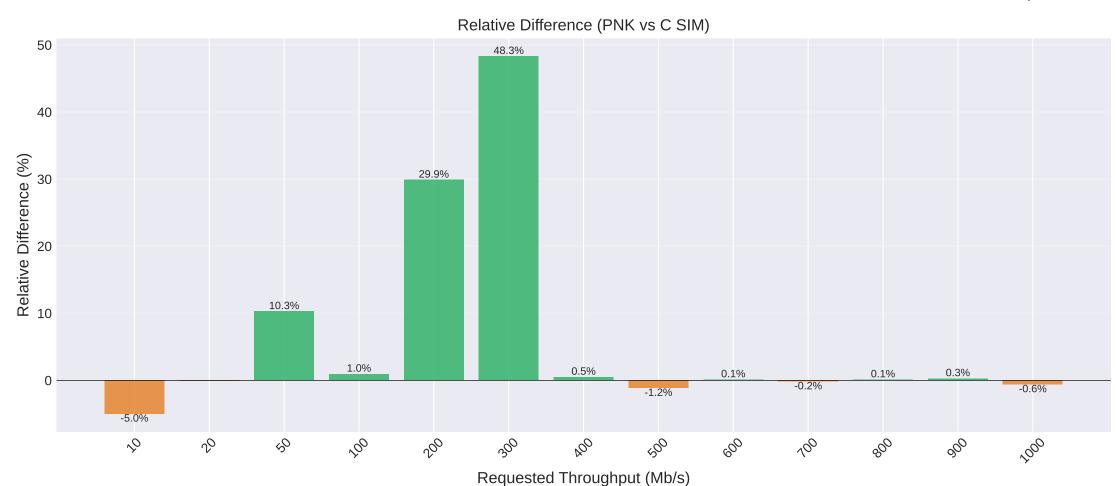












Branch Mispredictions per Packet vs Throughput

