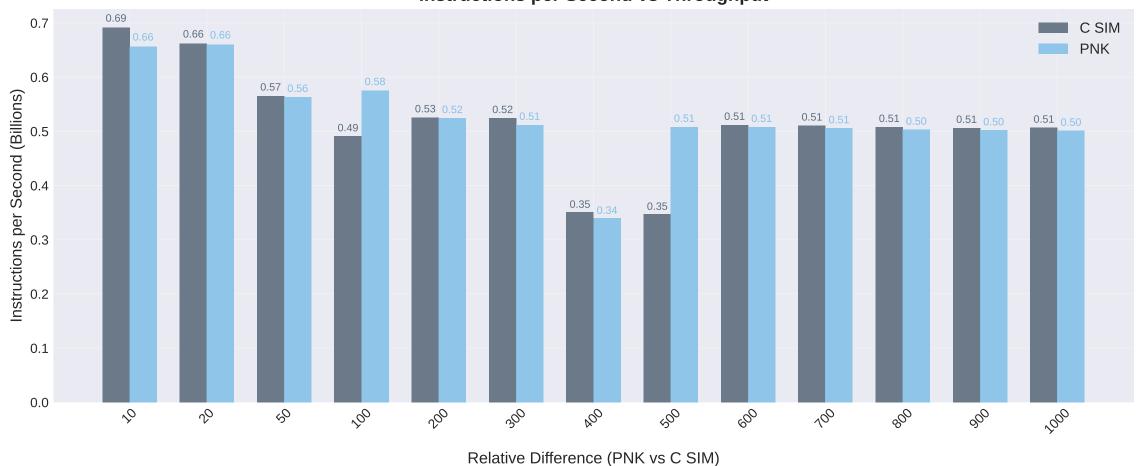
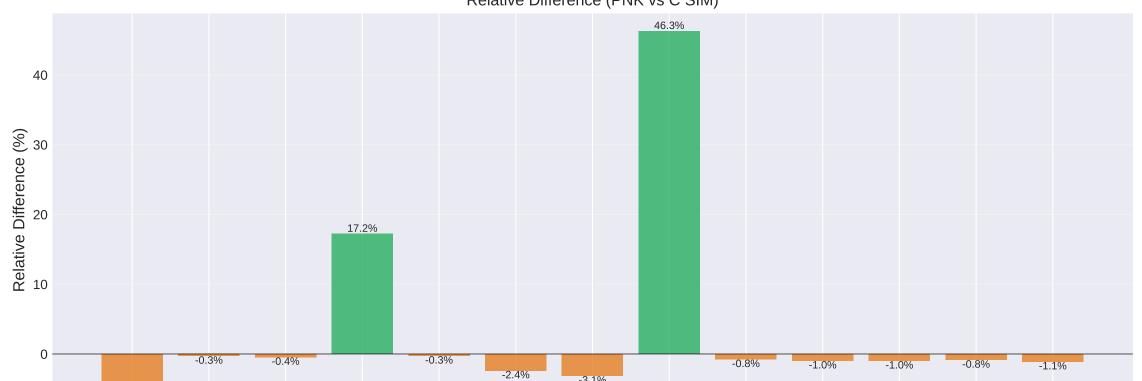
Instructions per Second vs Throughput





-3.1%

NOO

Requested Throughput (Mb/s)

400

600

700

900

-5.0% \$0

20

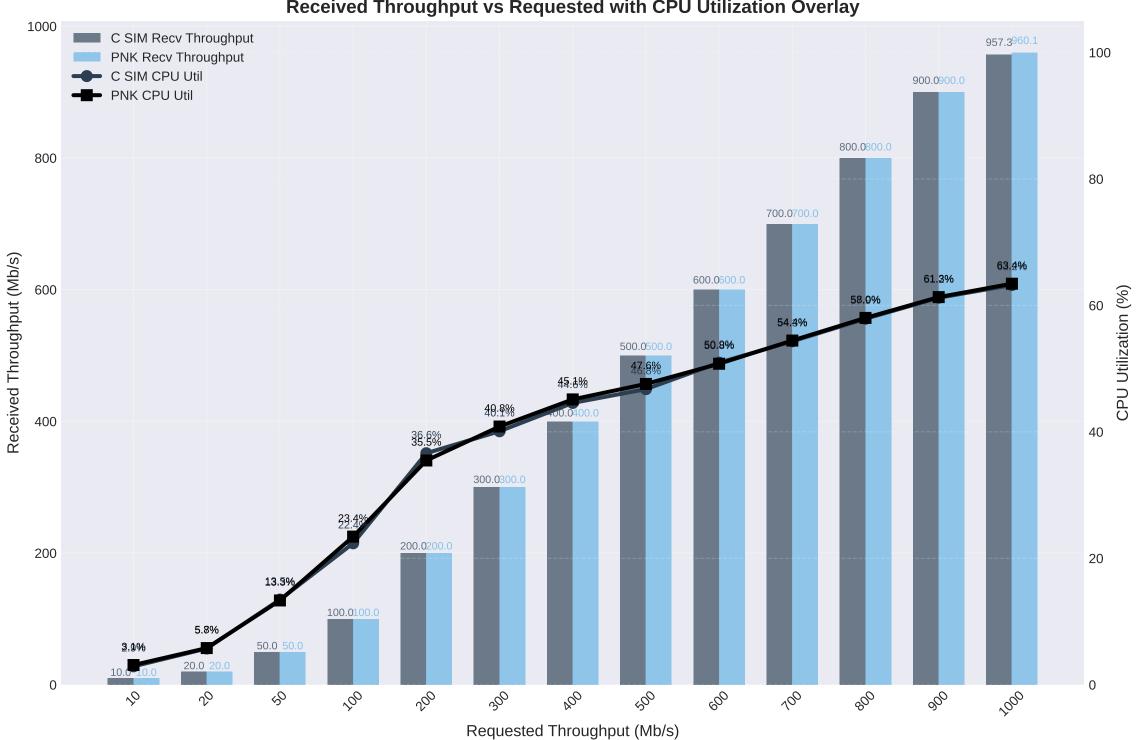
60

200

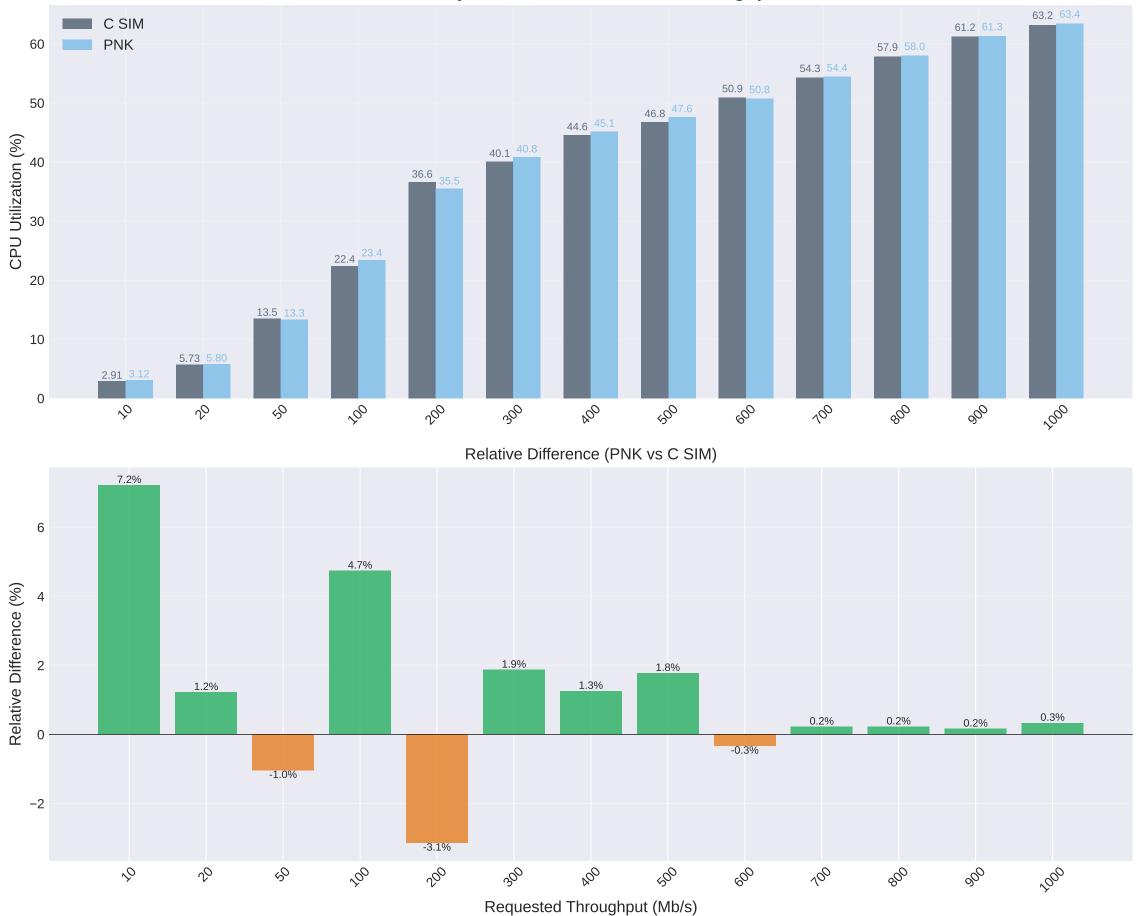
200

300

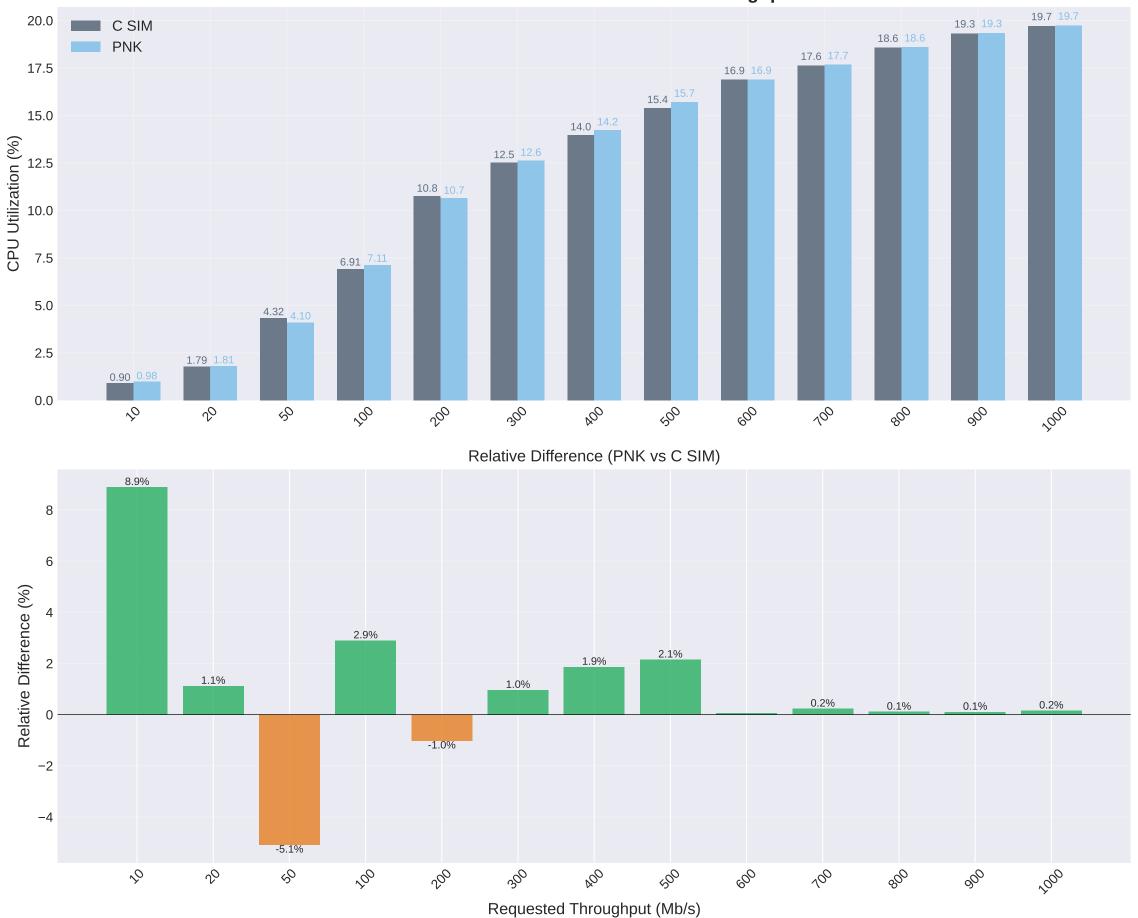
Received Throughput vs Requested with CPU Utilization Overlay



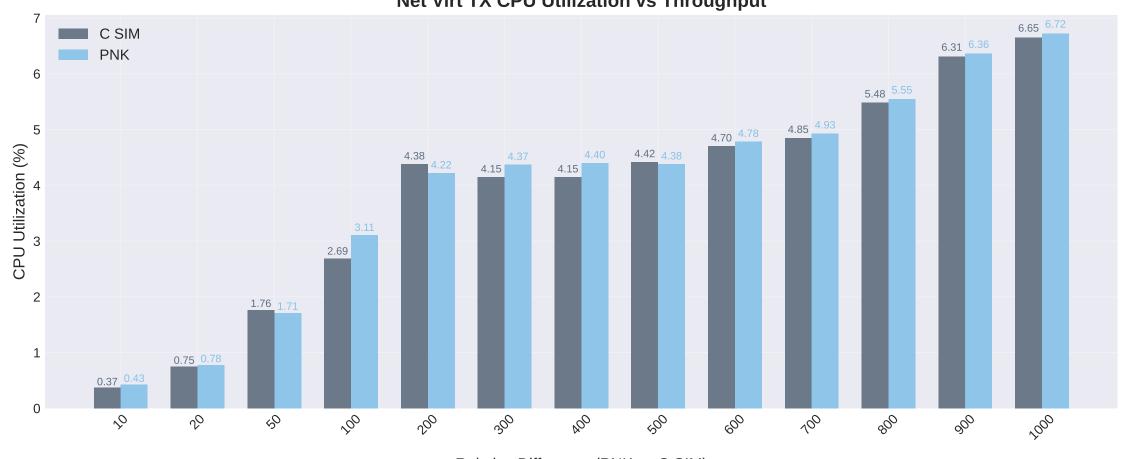
Total System CPU Utilization vs Throughput

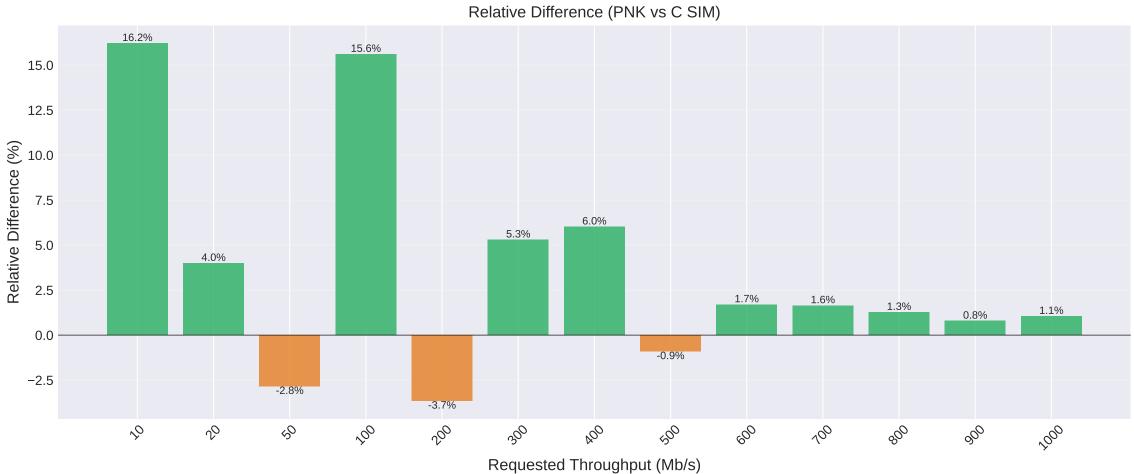


Ethernet Driver CPU Utilization vs Throughput

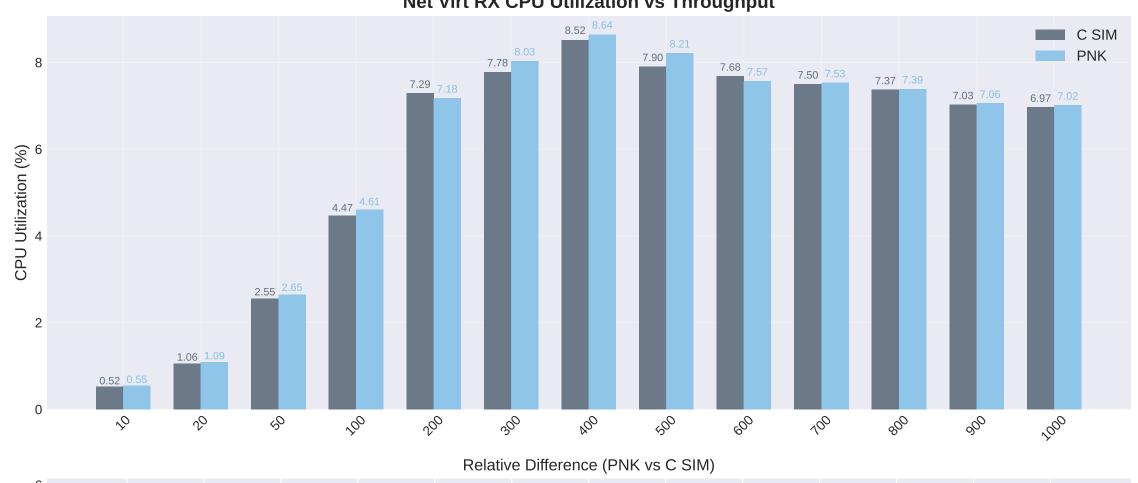


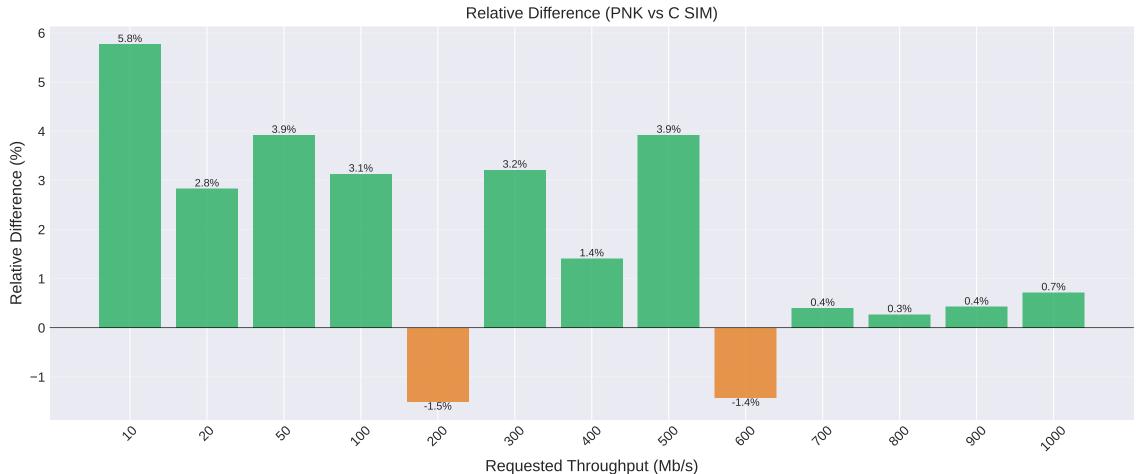




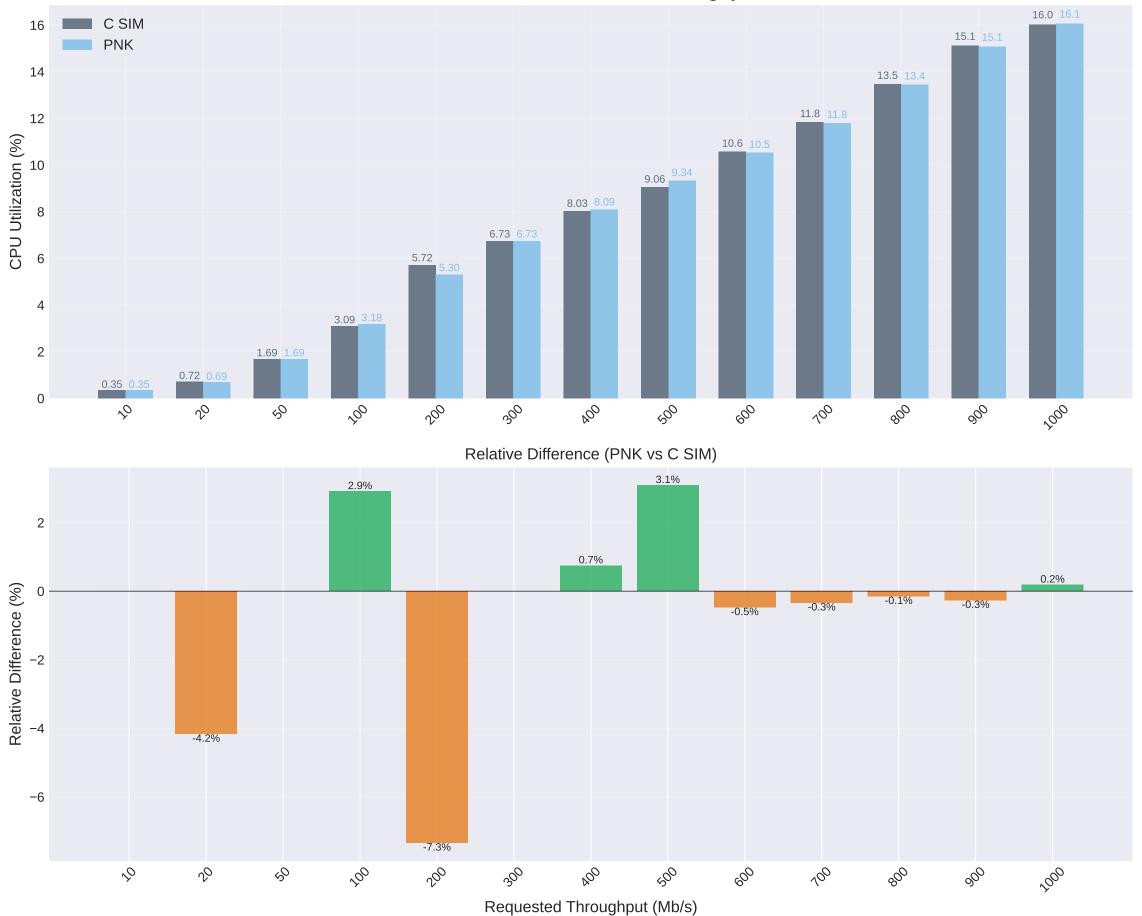


Net Virt RX CPU Utilization vs Throughput

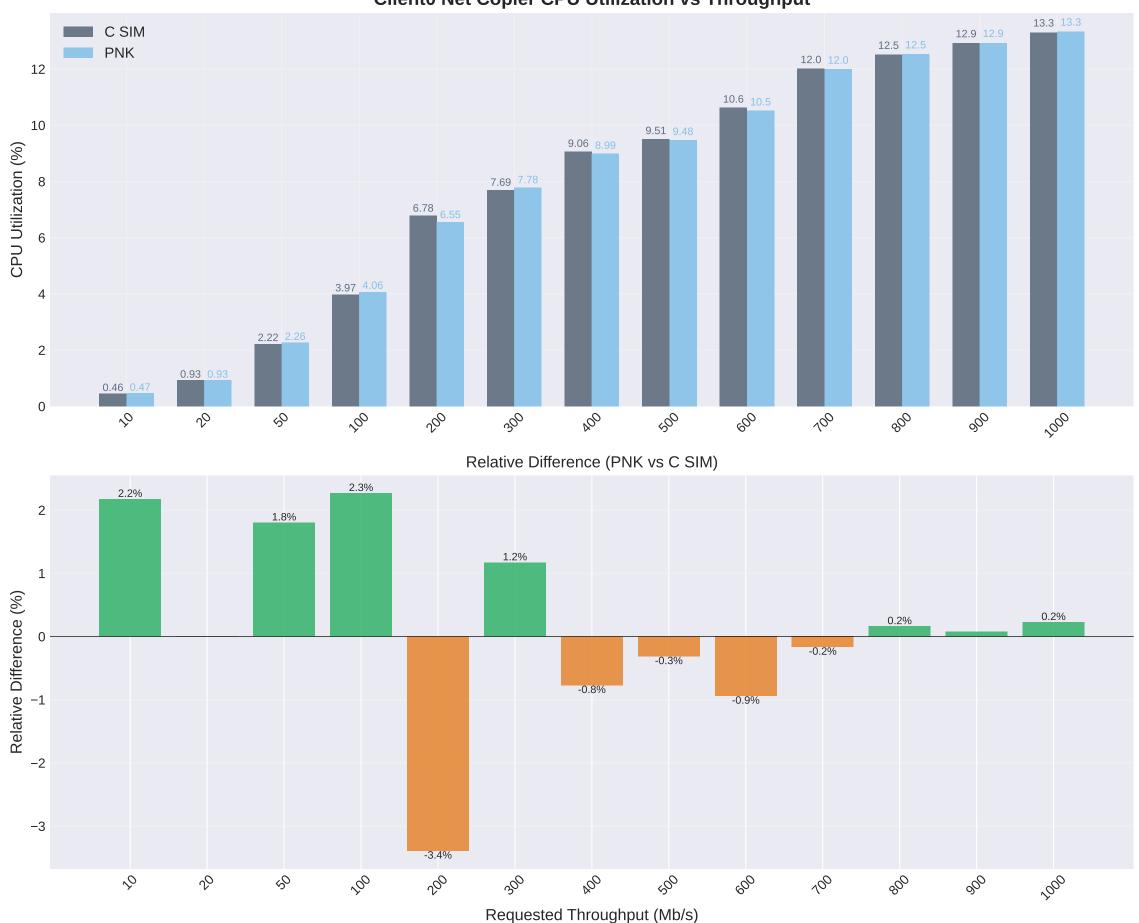




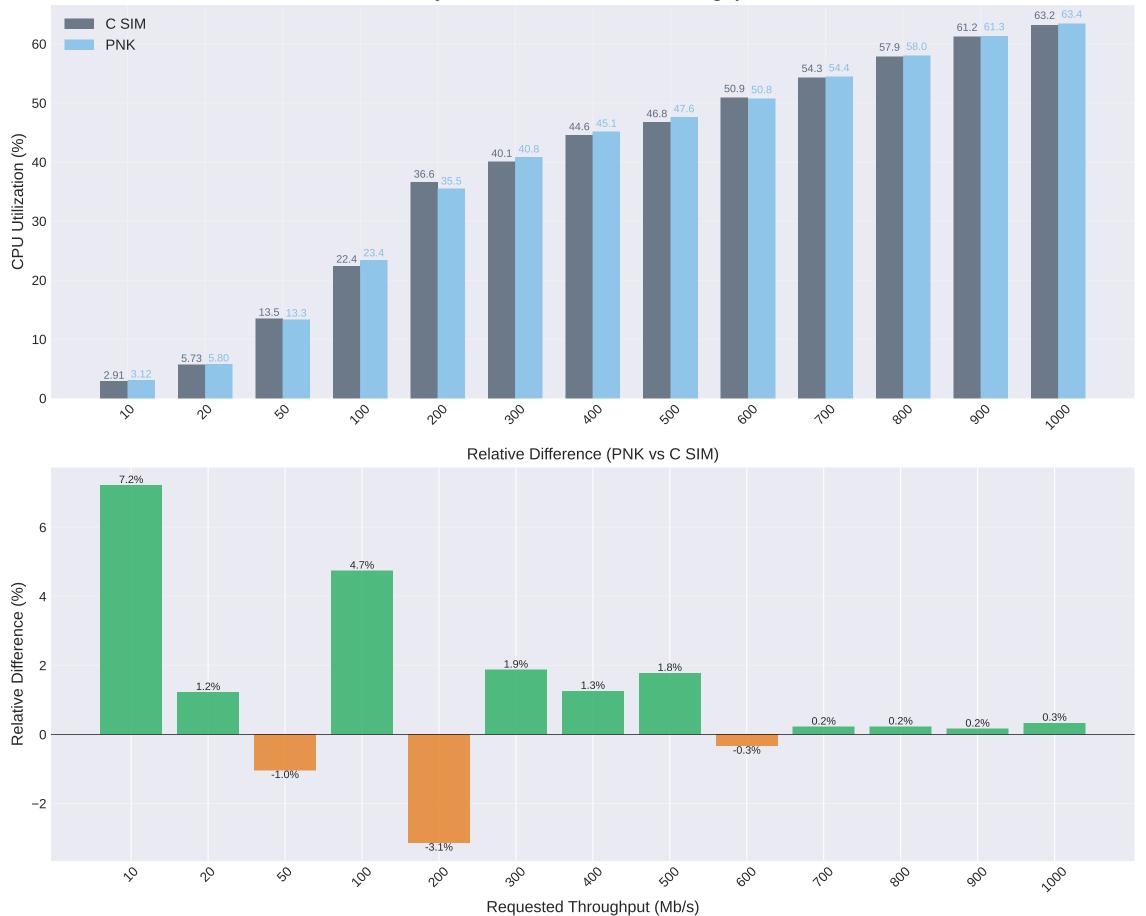
Client0 CPU Utilization vs Throughput

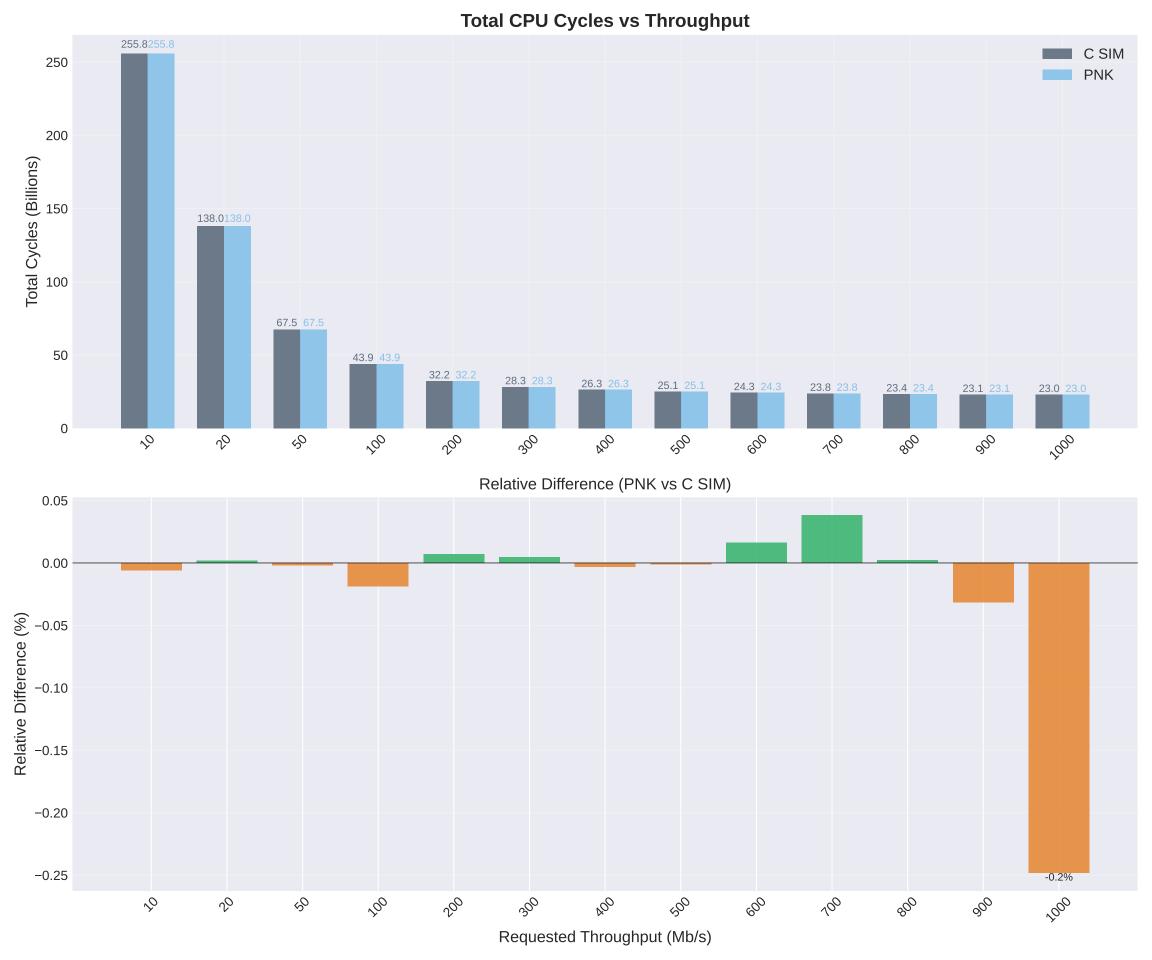


Client0 Net Copier CPU Utilization vs Throughput

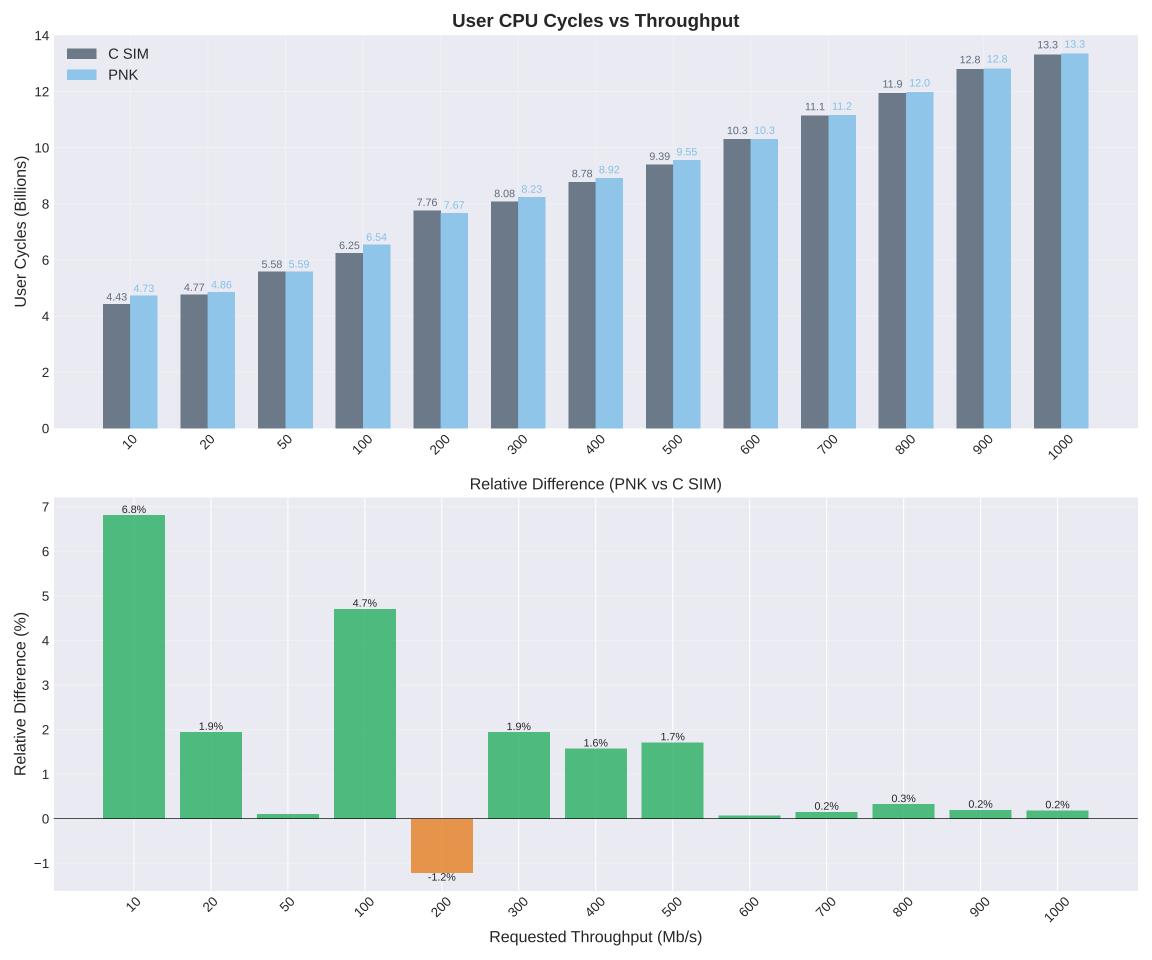


System CPU Utilization vs Throughput





Kernel CPU Cycles vs Throughput 3.51 C SIM 3.5 PNK 3.06 2.93 2.97 3.0 2.92 2.72 2.75 2.51 2.52 Kernel Cycles (Billions) 2.32 2.23 2.29 1.97 1.69 1.70 1.49 1.49 1.21 1.21 1.13 1.11 1.0 0.5 0.0 200 700 300 NOO 400 600 700 900 900 2000 30 20 60 Relative Difference (PNK vs C SIM) 6.9% 6 3.9% 4 Relative Difference (%) 2.3% 1.2% 1.0% 0.7% 0.1% -0.6% -0.6% -1.3% -2.4% -3.1% -4 -6 -6.7% \$0 20 60 200 200 300 NOO 400 600 100 900 Requested Throughput (Mb/s)



Idle CPU Cycles vs Throughput 248.3247.8 250 C SIM PNK 200 Idle Cycles (Billions) 130.1130.0 58.4 58.5 50 34.1 33.6 20.4 20.8 16.9 16.7 14.6 14.4 13.4 13.1 11.9 12.0 10.9 10.9 9.85 9.82 8.47 8.41 8.95 8.92 0 \$ 200 200 300 NOO 400 600 700 900 900 2000 20 80 Relative Difference (PNK vs C SIM) 1.8% 1.5 1.0 Relative Difference (%) 0.4% 0.2% -0.2% -0.2% -0.3% -0.3% -0.8% -1.0 -1.0% -1.2% -1.4% -1.5 -1.6%

Requested Throughput (Mb/s)

NOO

500

600

100

900

900

30

20

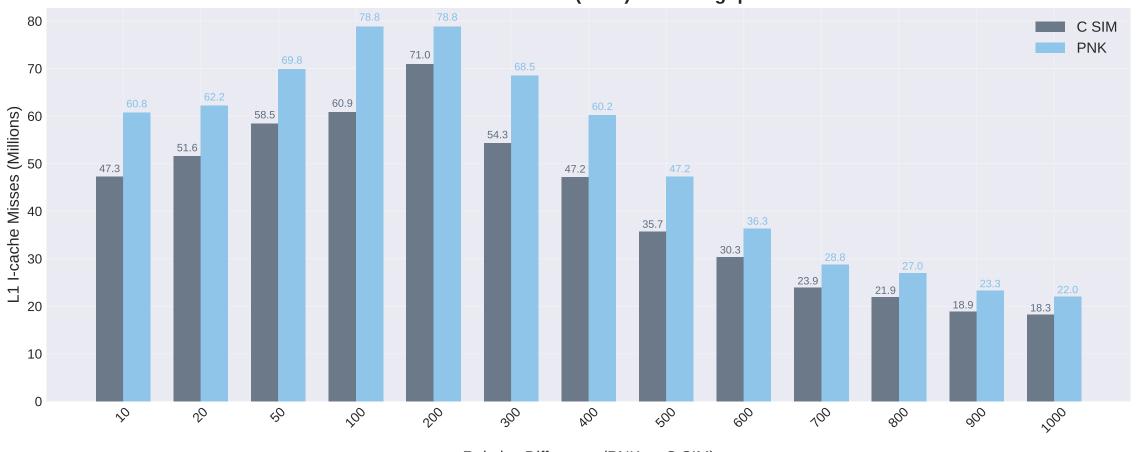
SO

200

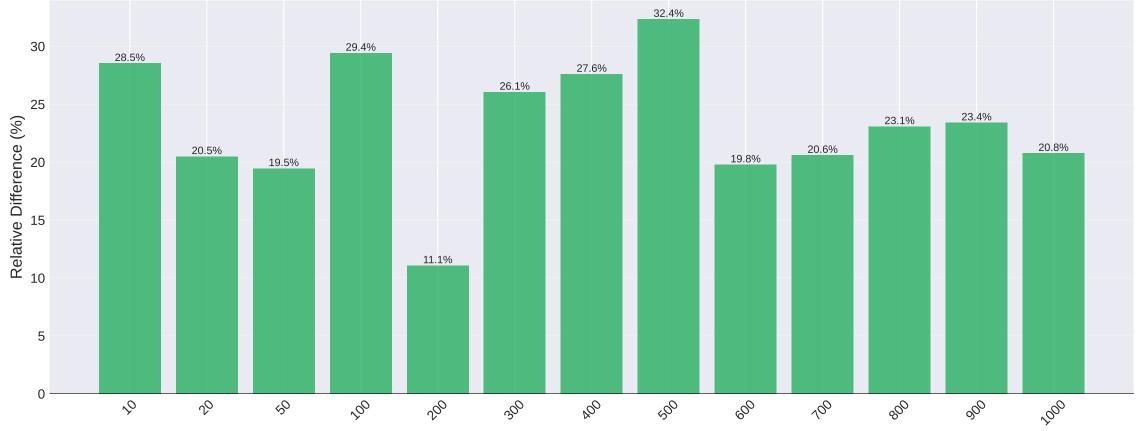
200

300

L1 I-cache Misses (Total) vs Throughput







Requested Throughput (Mb/s)

L1 D-cache Misses (Total) vs Throughput C SIM 70 PNK 67.6 66.6 63.8 63.1 62.3 61.1 61.8 60.6 61.8 61.5 61.7 60.7 59.7 59.6 _{58.7} L1 D-cache Misses (Millions)
0 0 0 0 0 0 54.2 47.0 43.5 43.2 10 0 200 200 300 400 400 600 700 800 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 0 -0.4% -0.6% -1.0% -1.6% -1.8% -2 -1.9% -2.0% Relative Difference (%) -3.7% -4 -4.1% -4.6% -6 -5.9% -7.1% -8 -10 -11.2%

Requested Throughput (Mb/s)

NOO

500

100

600

900

200

SO

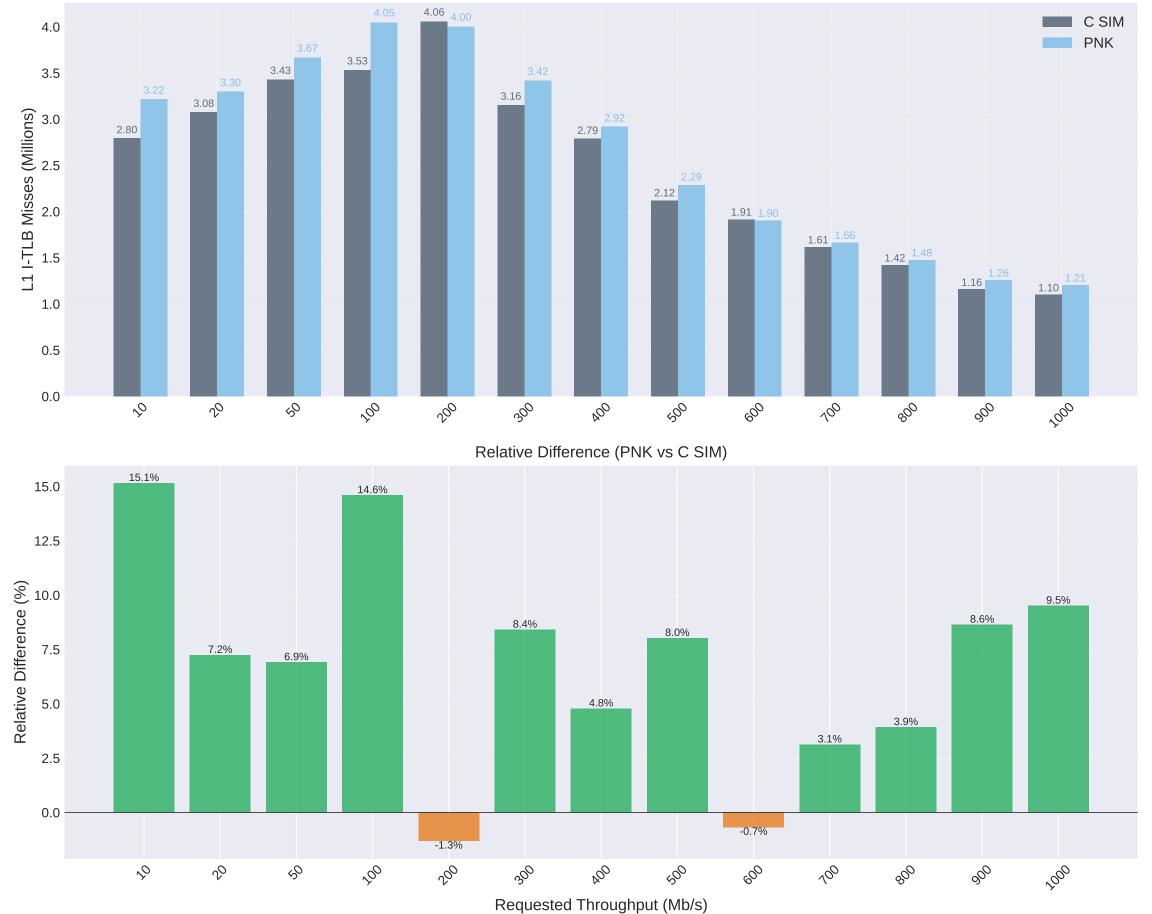
30

20

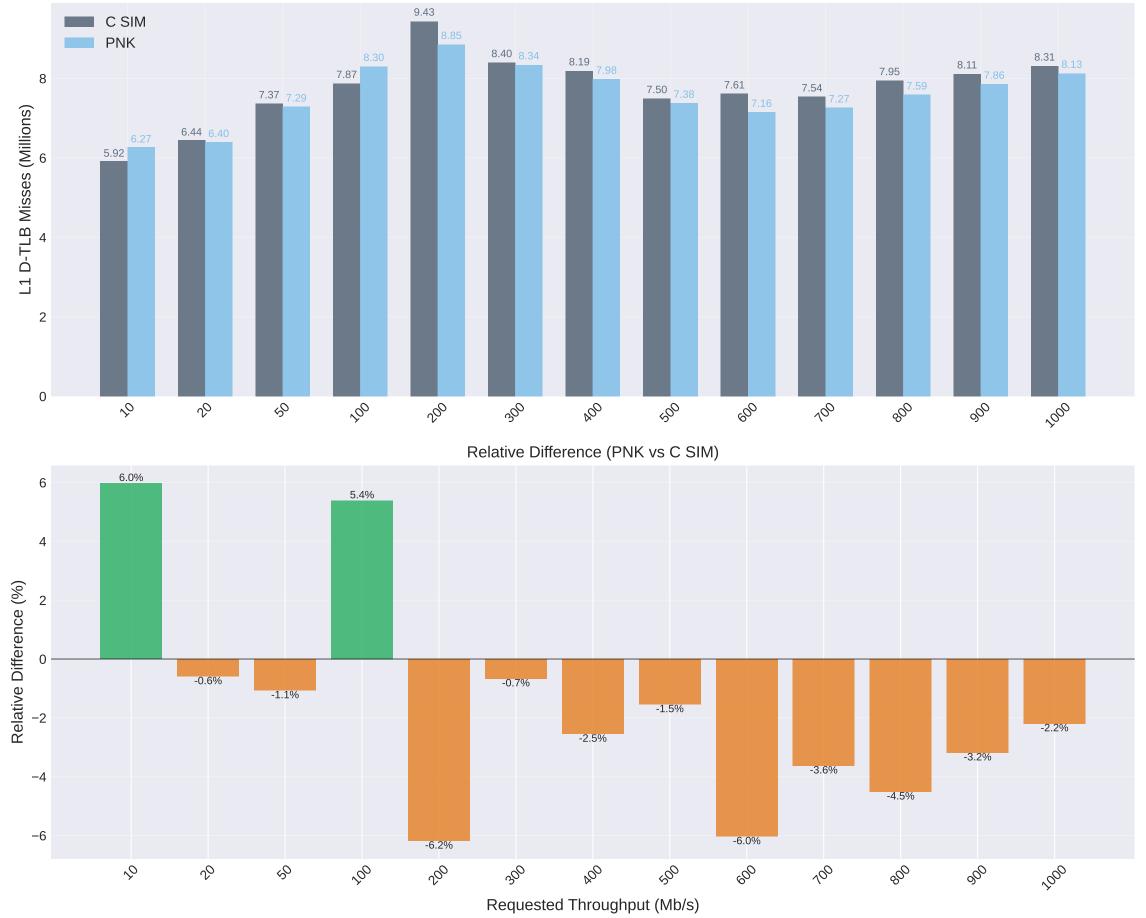
200

300

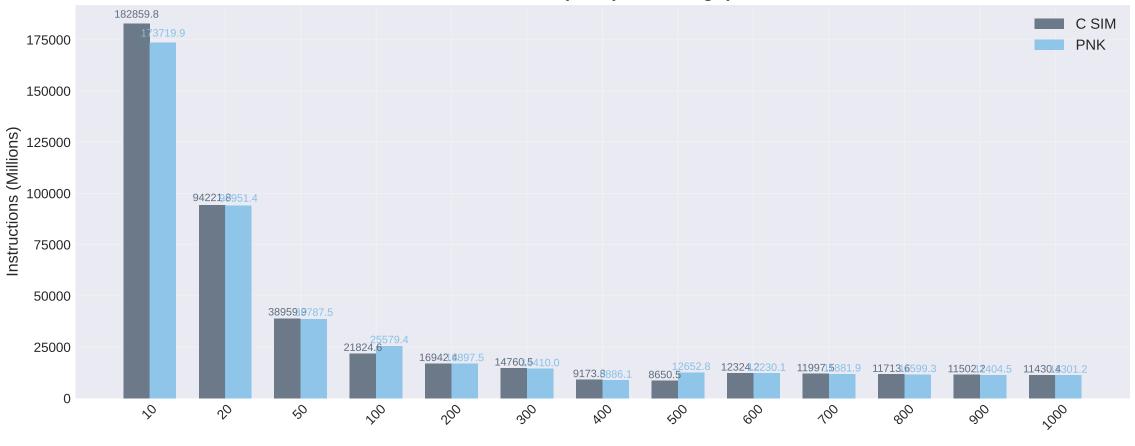
L1 I-TLB Misses (Total) vs Throughput

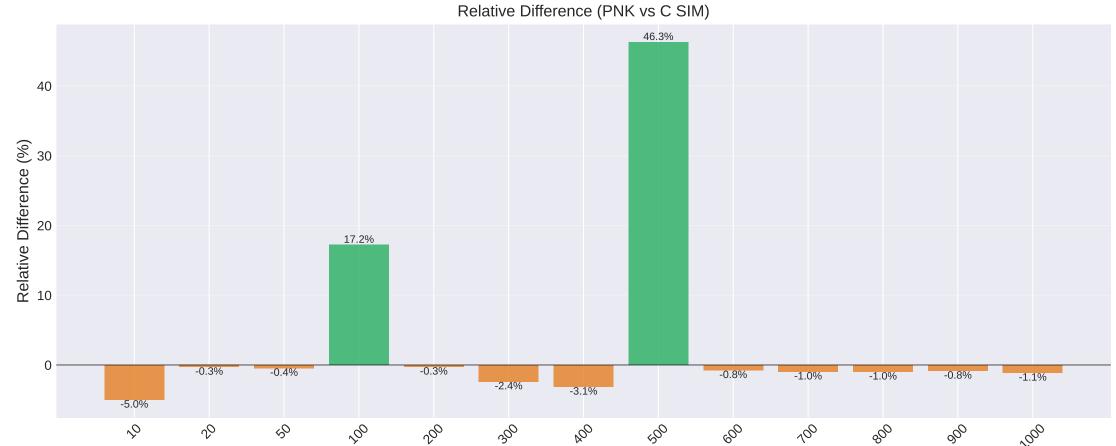


L1 D-TLB Misses (Total) vs Throughput



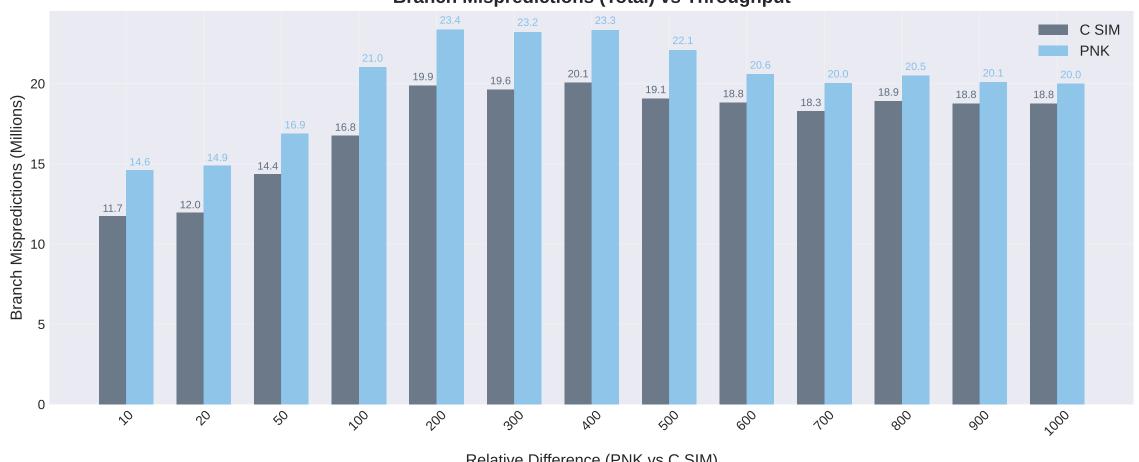
Instructions (Total) vs Throughput

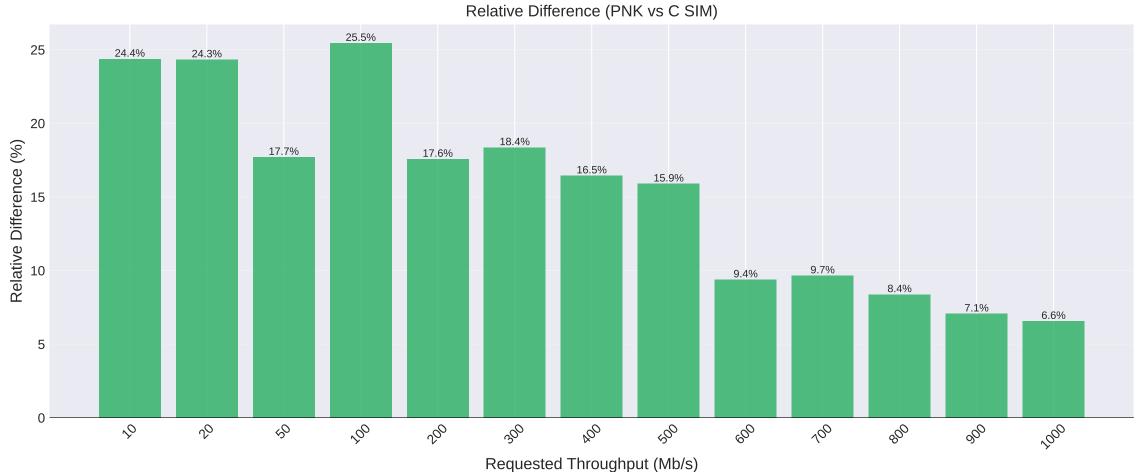




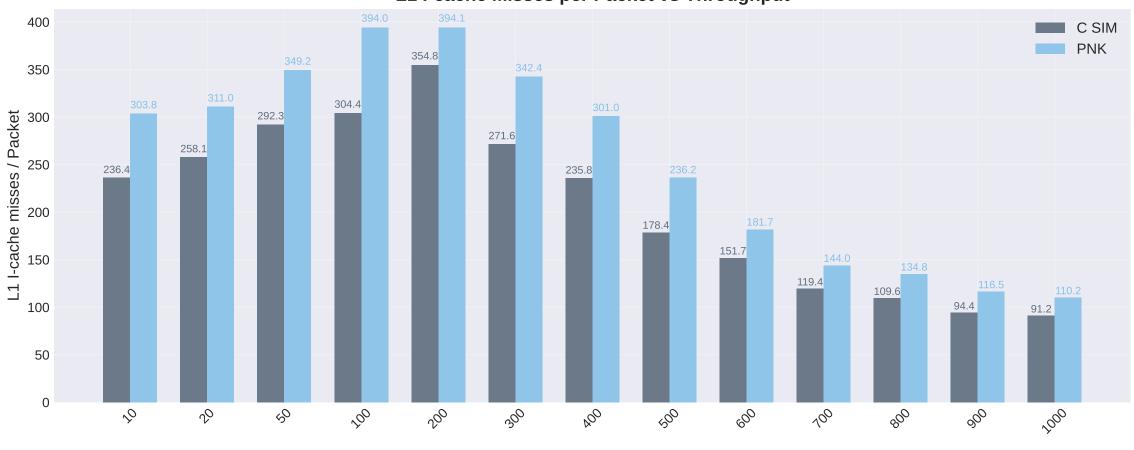
Requested Throughput (Mb/s)

Branch Mispredictions (Total) vs Throughput

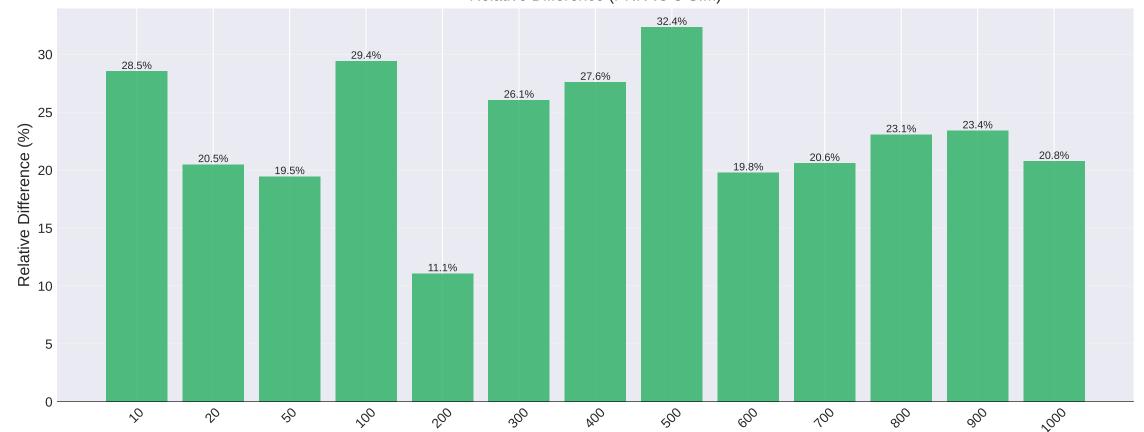




L1 I-cache Misses per Packet vs Throughput





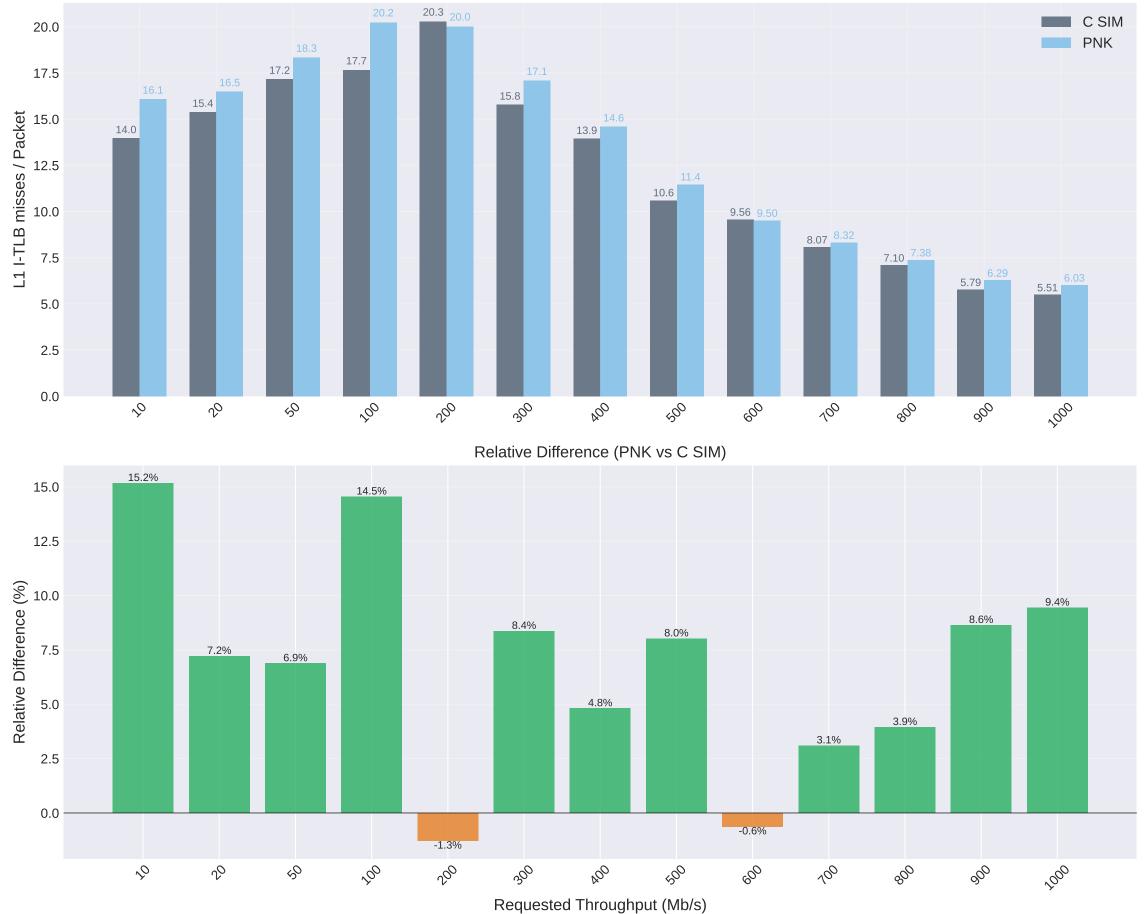


Requested Throughput (Mb/s)

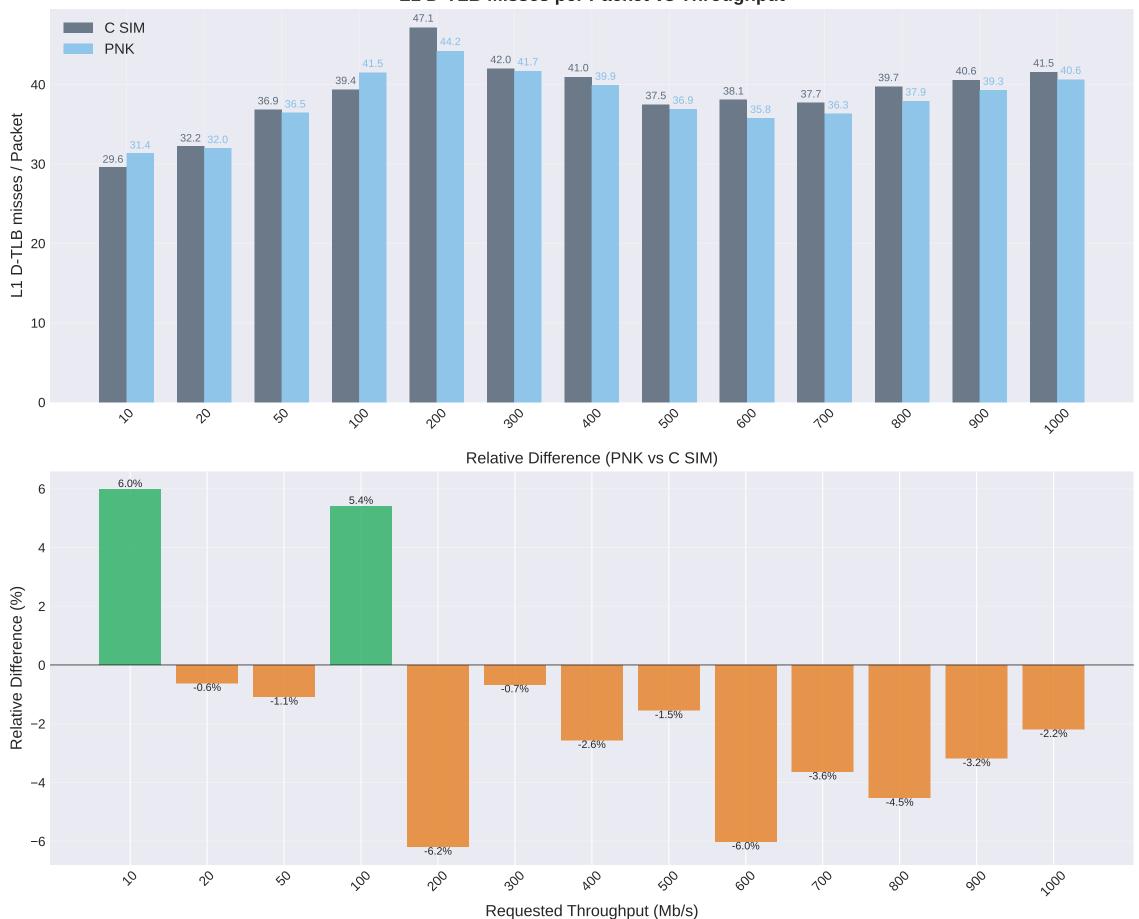
L1 D-cache Misses per Packet vs Throughput



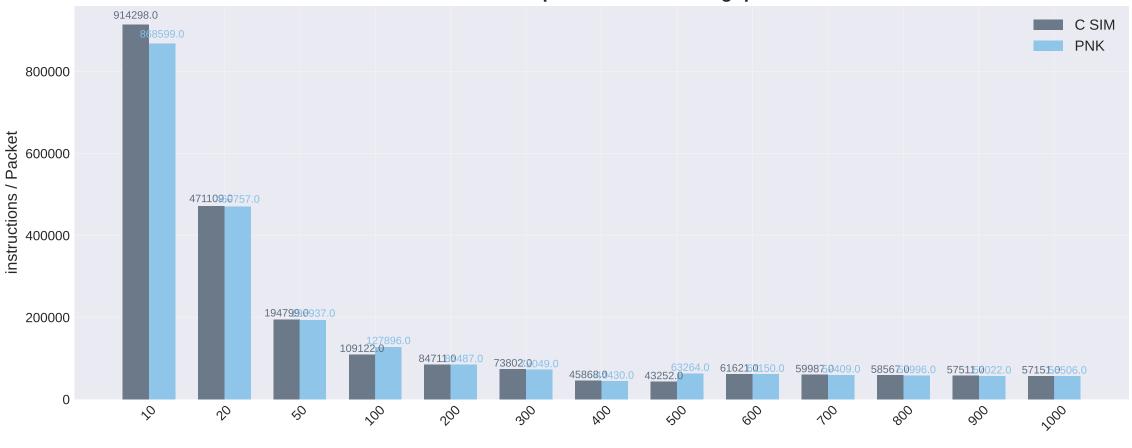
L1 I-TLB Misses per Packet vs Throughput

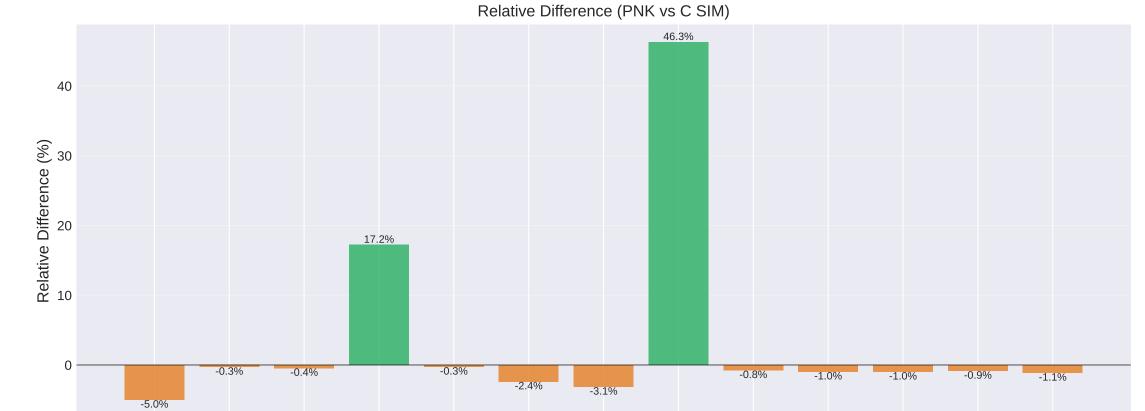


L1 D-TLB Misses per Packet vs Throughput



Instructions per Packet vs Throughput





NO

Requested Throughput (Mb/s)

400

600

700

\$

20

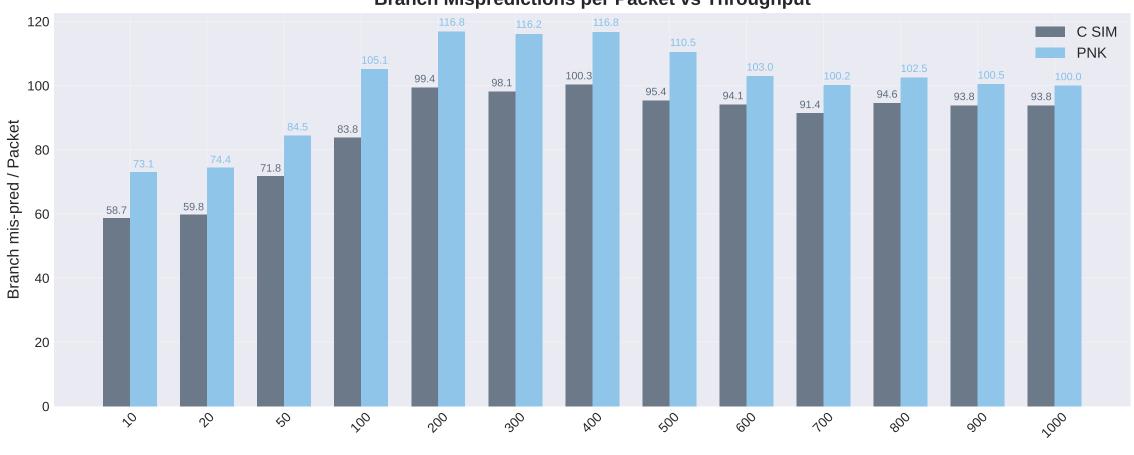
200

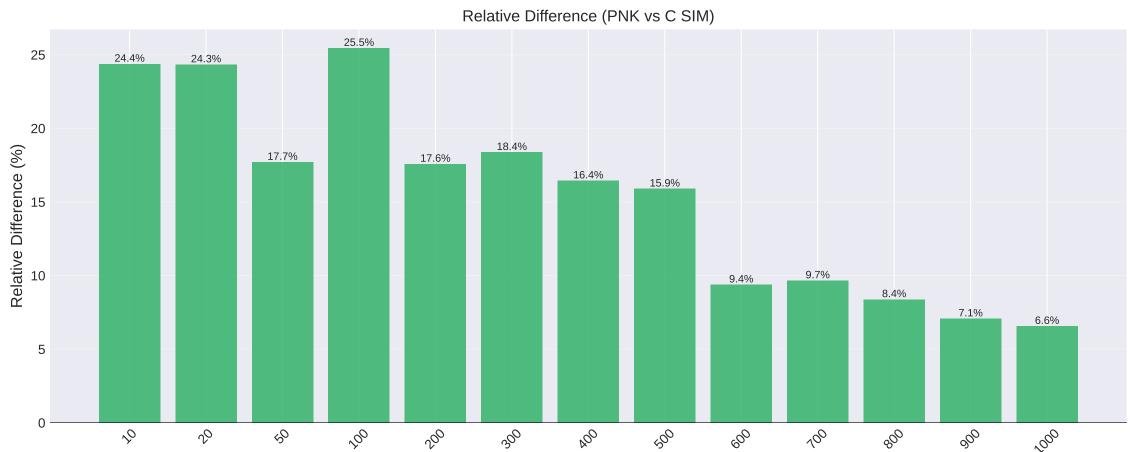
SO

200

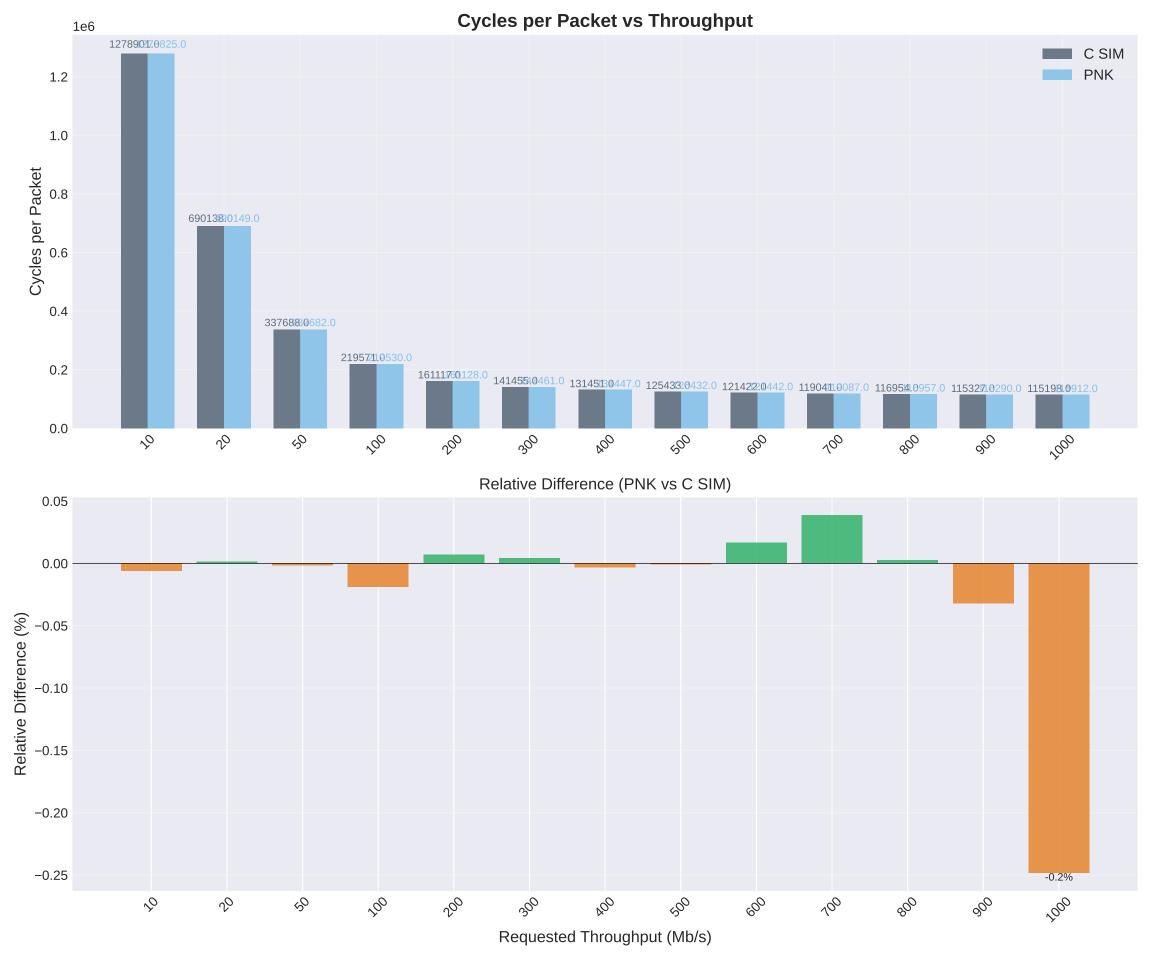
300

Branch Mispredictions per Packet vs Throughput

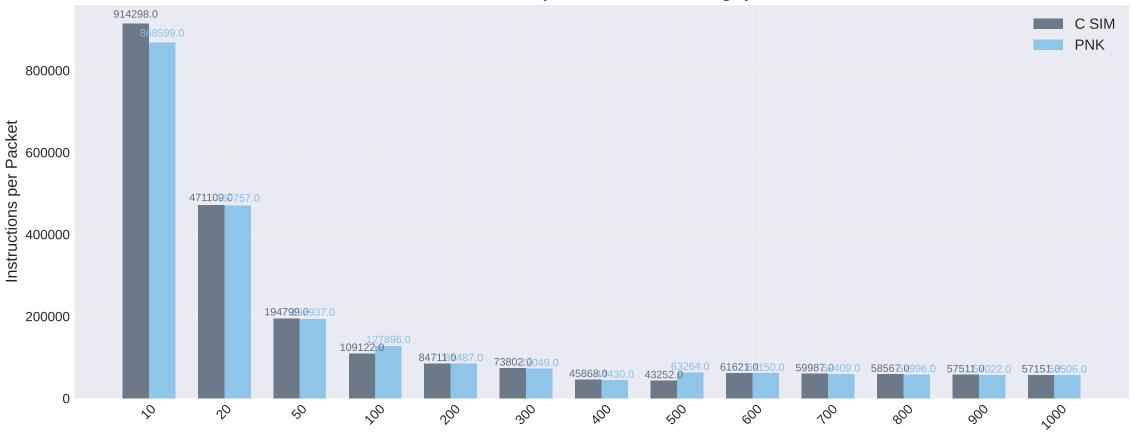




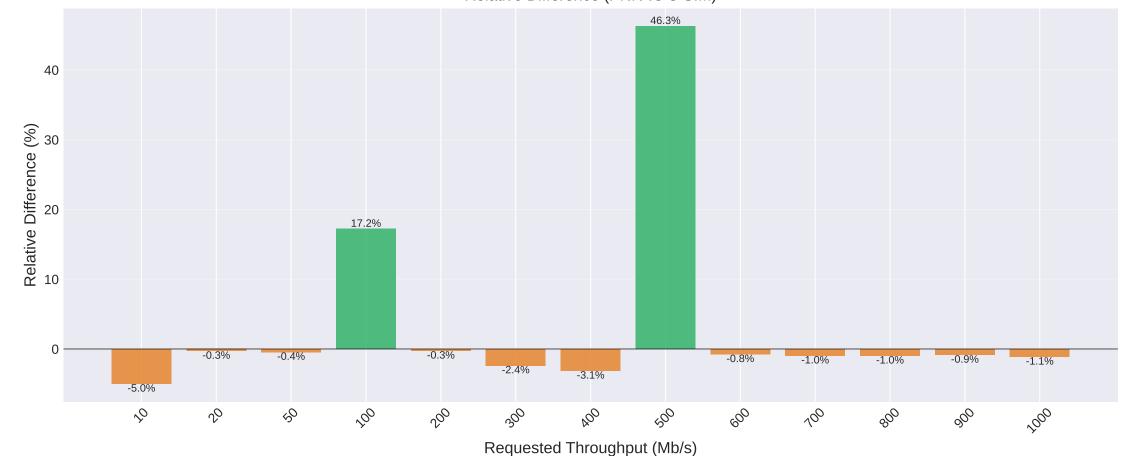
Requested Throughput (Mb/s)



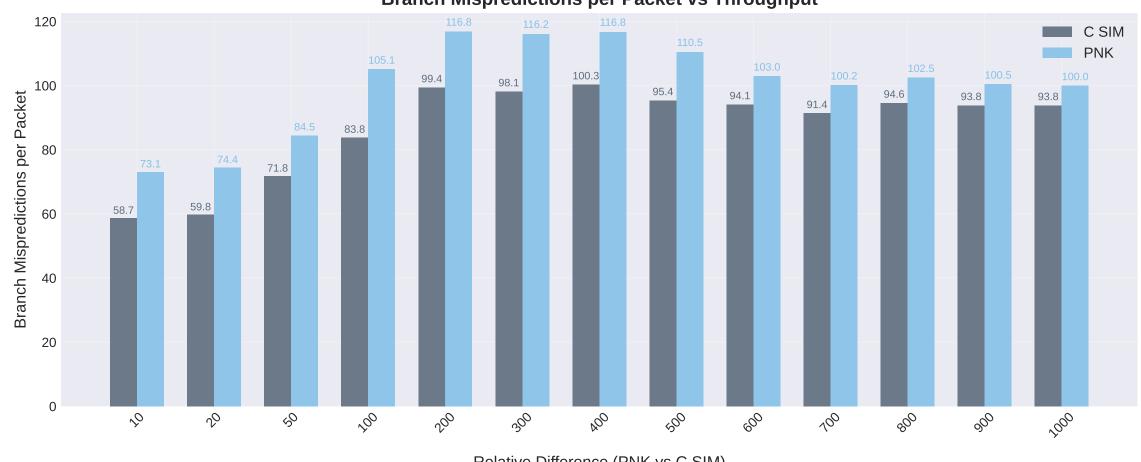
Instructions per Packet vs Throughput

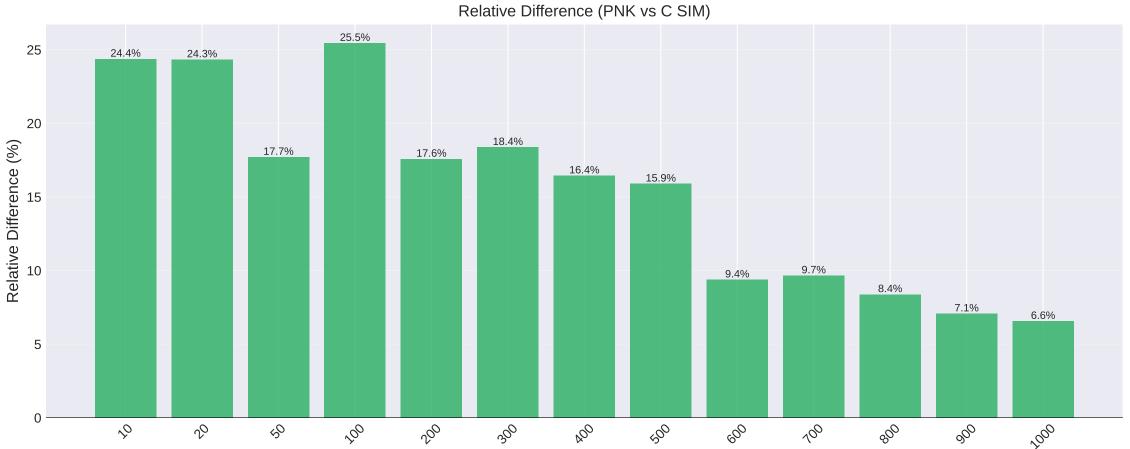




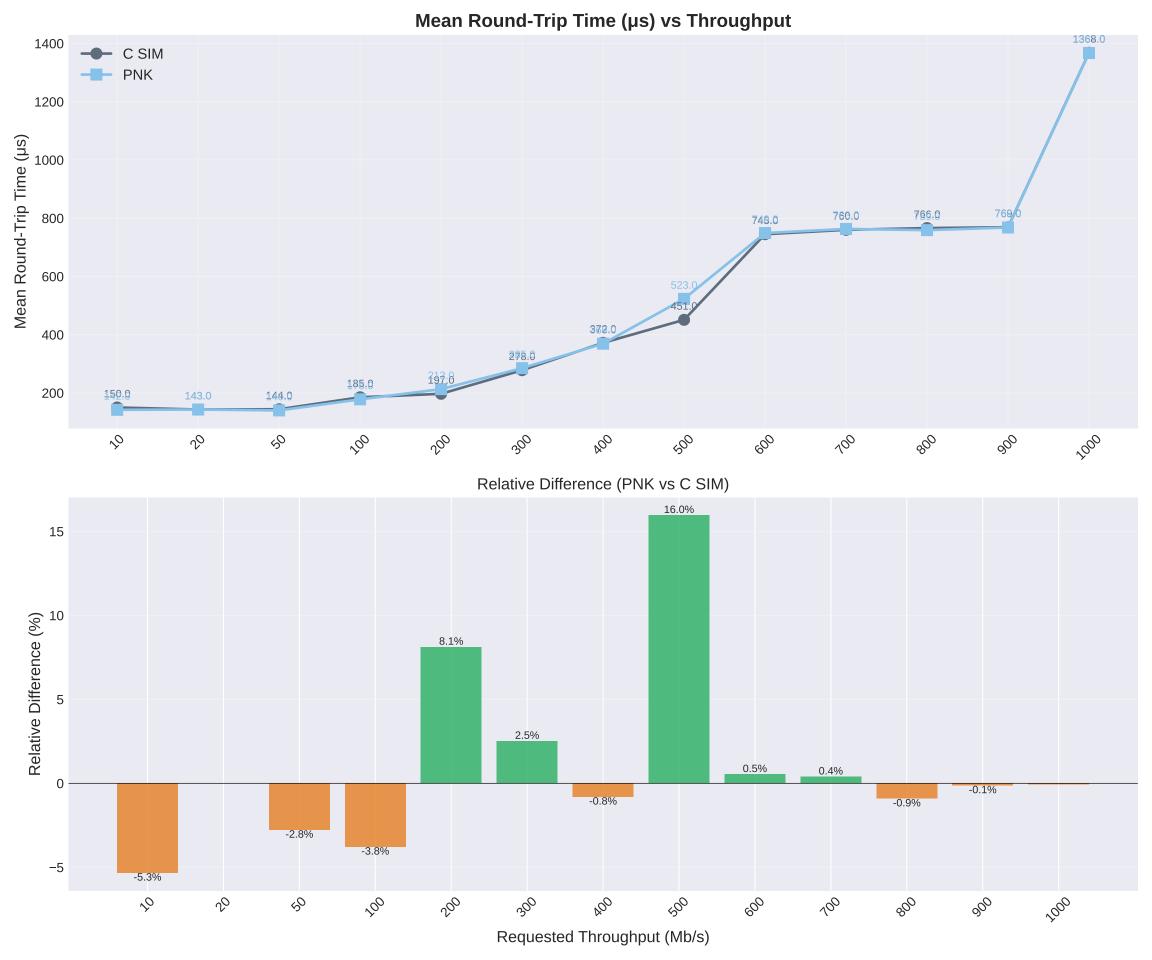


Branch Mispredictions per Packet vs Throughput

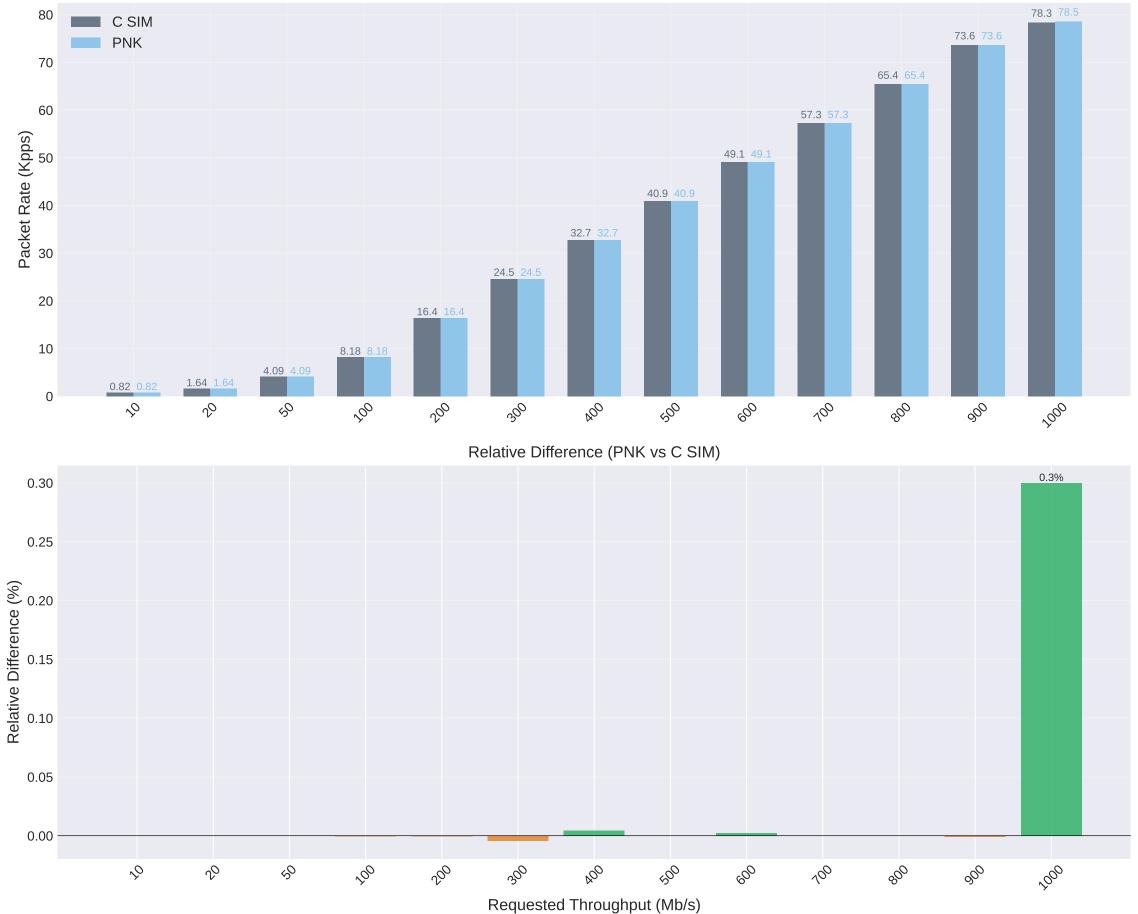


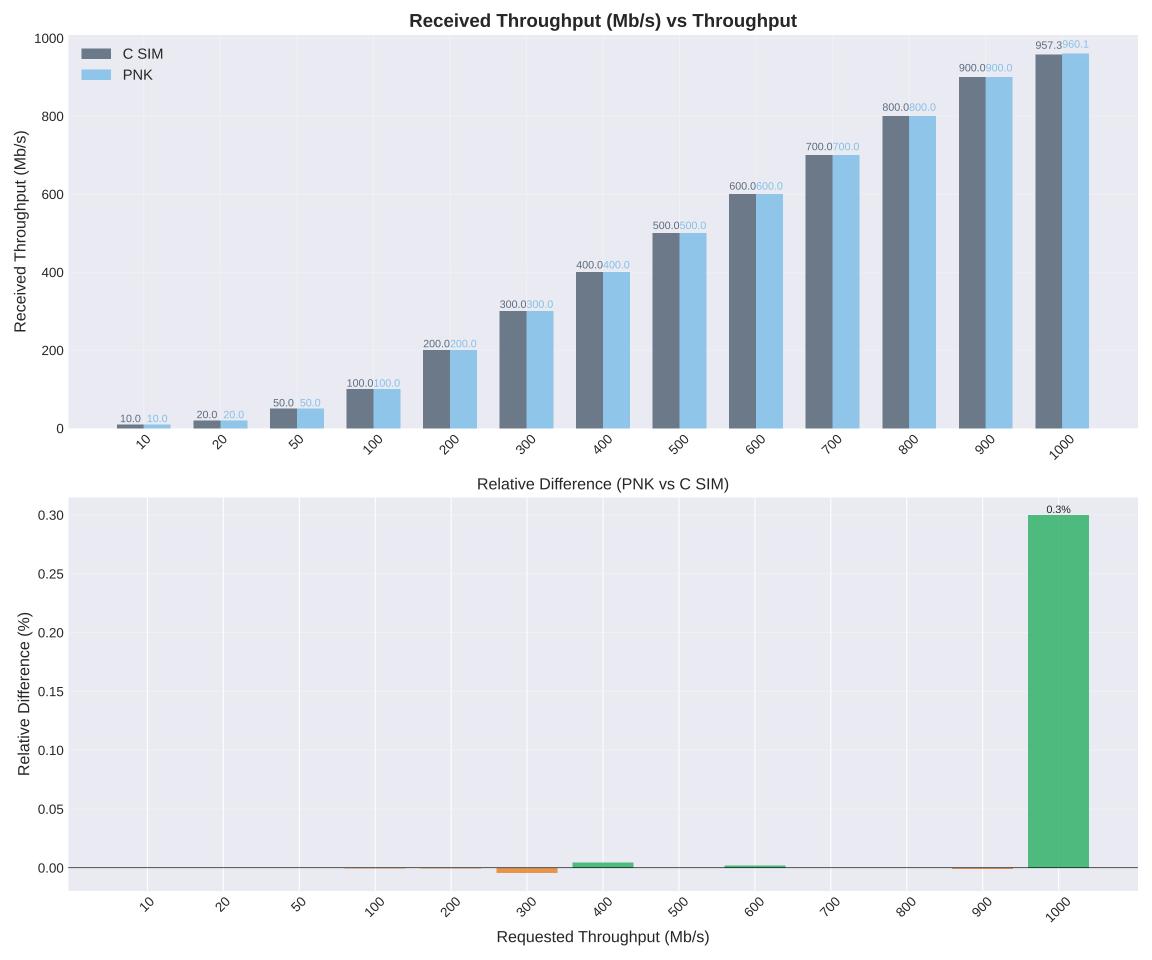


Requested Throughput (Mb/s)

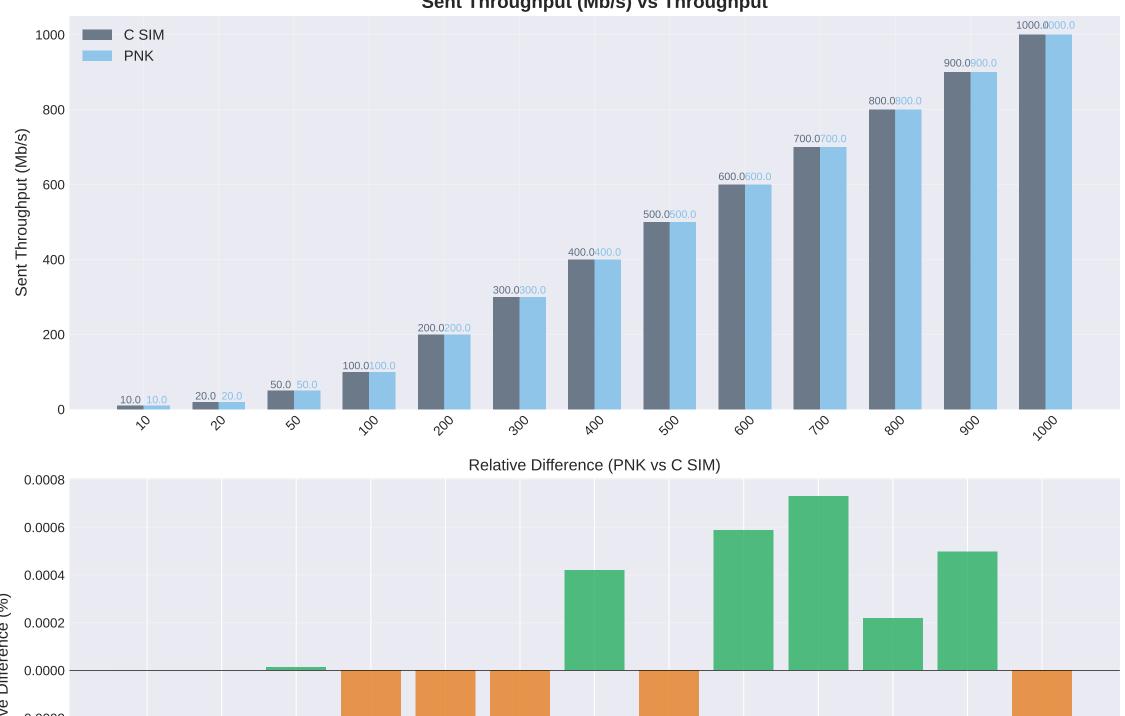


Packet Rate (packets/s) vs Throughput









Relative Difference (%) -0.0002 -0.0004 -0.0006 -0.0008 \$ 20 200 200 300 NOO 400 600 100 900 50 Requested Throughput (Mb/s)