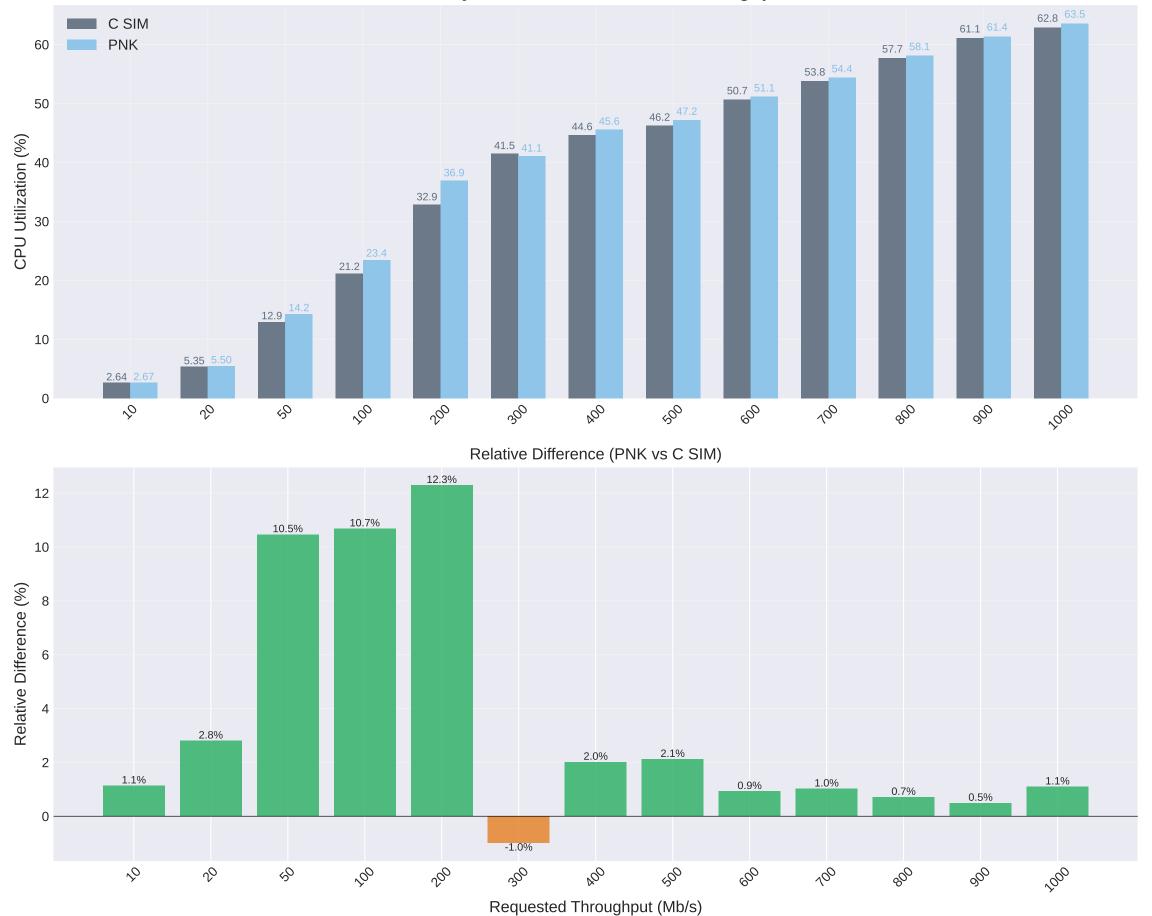
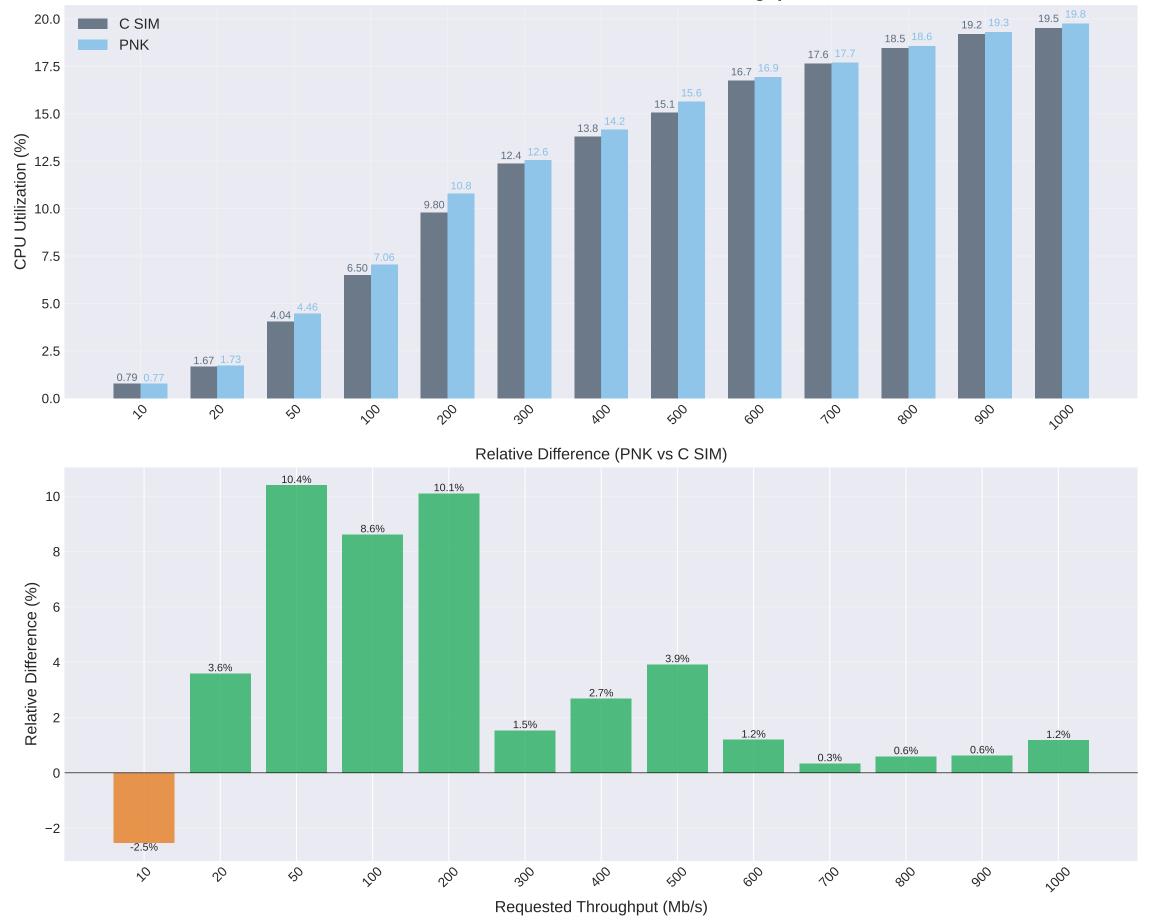
Instructions per Second vs Throughput 0.69 0.7 C SIM 0.66 0.66 PNK 0.6 0.56 Instructions per Second (Billions) 0.51 0.51 0.50 0.51 0.51 0.51 0.50 0.50 0.50 0.50 0.50 0.50 0.49 0.40 0.35 0.34 0.34 0.1 0.0 200 200 300 NOO 400 600 700 900 900 2000 30 20 60 Relative Difference (PNK vs C SIM) 27.9% 25 Relative Difference (%) 17.5% 9.9% 5 2.4% 0.1% 0 -0.1% -0.6% -0.9% -2.2% \$0 20 60 700 200 300 NOO 400 600 700 900

Received Throughput vs Requested with CPU Utilization Overlay 1000 C SIM Recv Throughput 957.3956.5 100 PNK Recv Throughput C SIM CPU Util 900.0900.0 PNK CPU Util 800.0800.0 800 80 700.0700.0 Received Throughput (Mb/s) 63.5% 61.4% 600.0600.0 600 CPU Utilization (%) 58.1% 54.4% 54.4% 500.0500.0 45:6% 400 300.0300.0 200.0200.0 200 20 100.0100.0 50.0 50.0 20.0 20 0 900 200 go NOO 400 2000 700 600 100 900 30 20 50

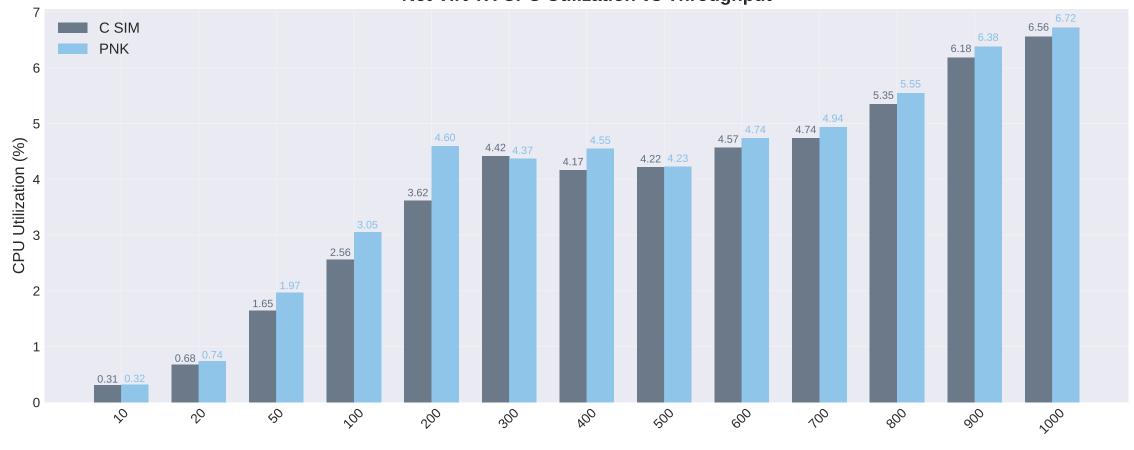
Total System CPU Utilization vs Throughput

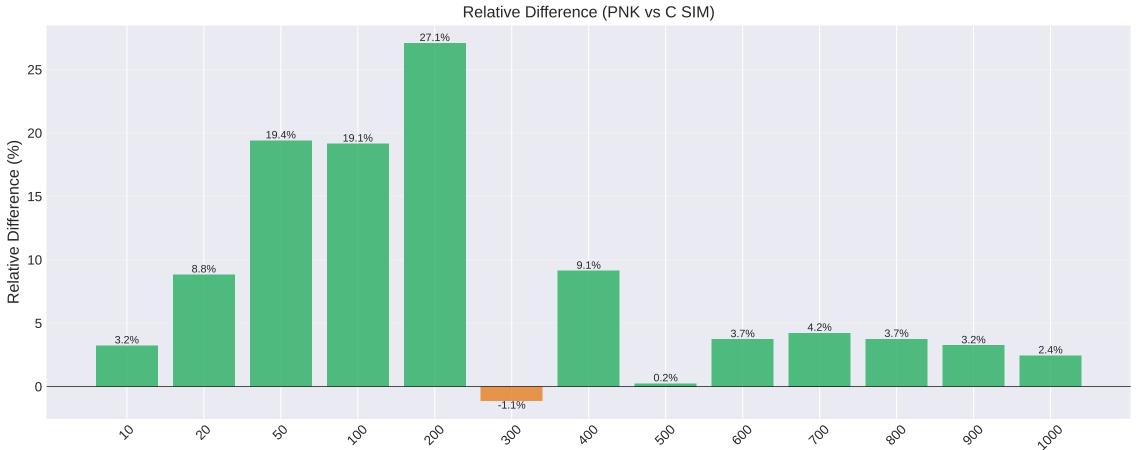


Ethernet Driver CPU Utilization vs Throughput

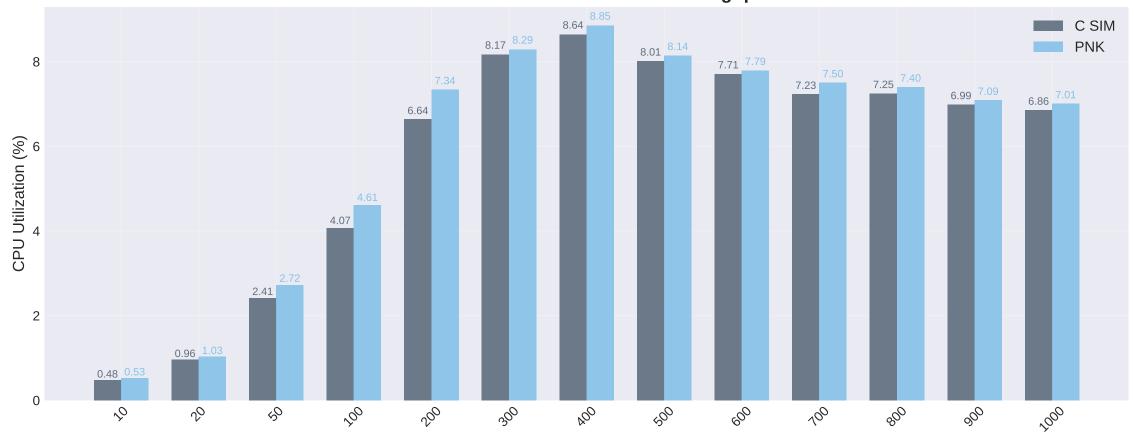


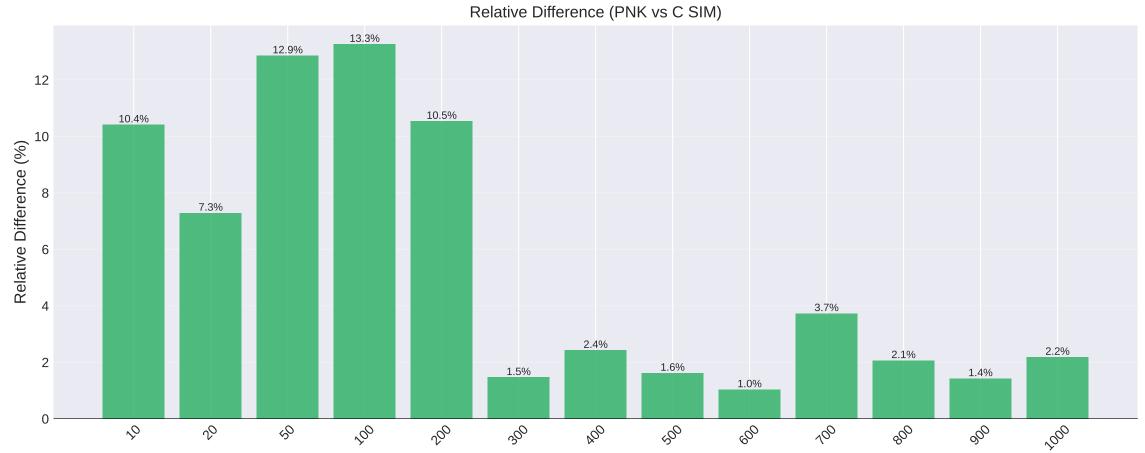
Net Virt TX CPU Utilization vs Throughput



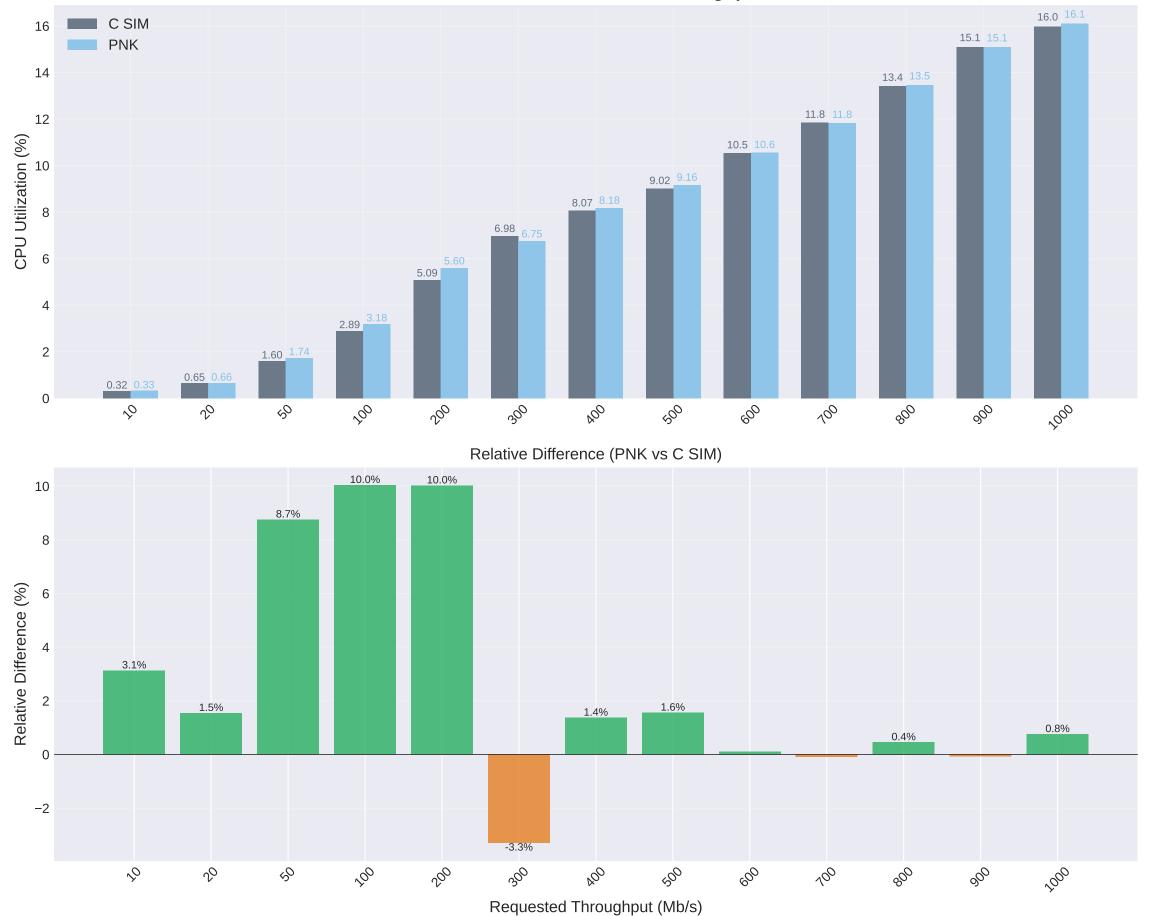


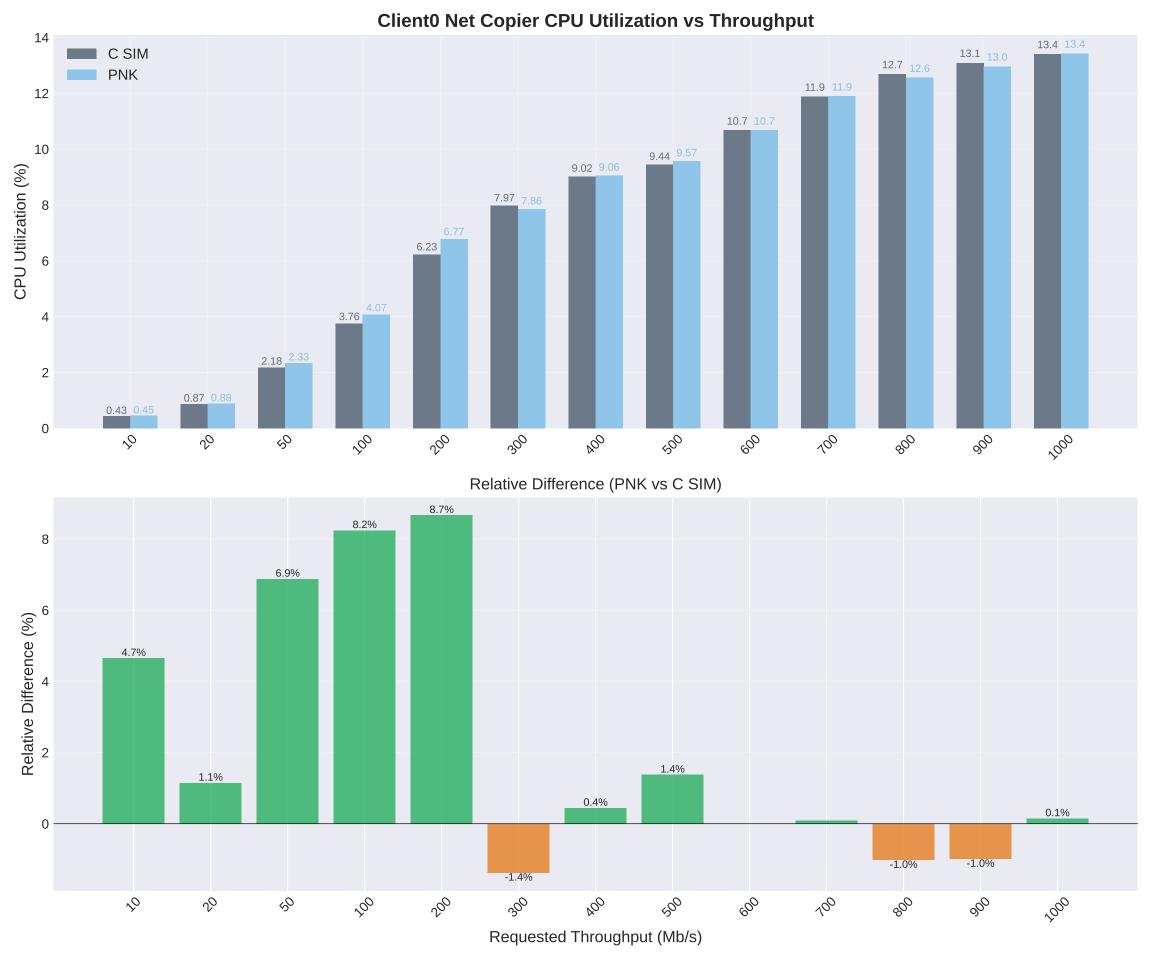
Net Virt RX CPU Utilization vs Throughput



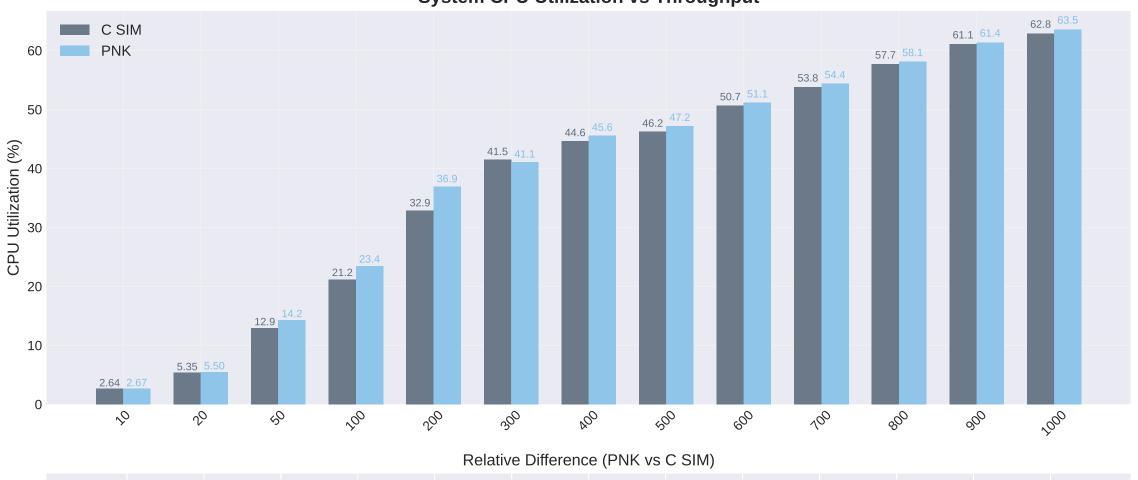


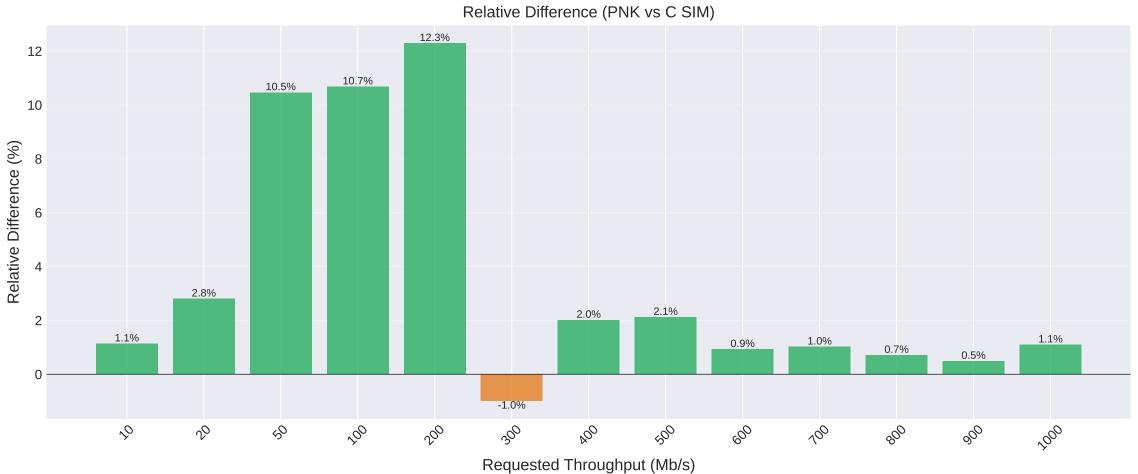
Client0 CPU Utilization vs Throughput

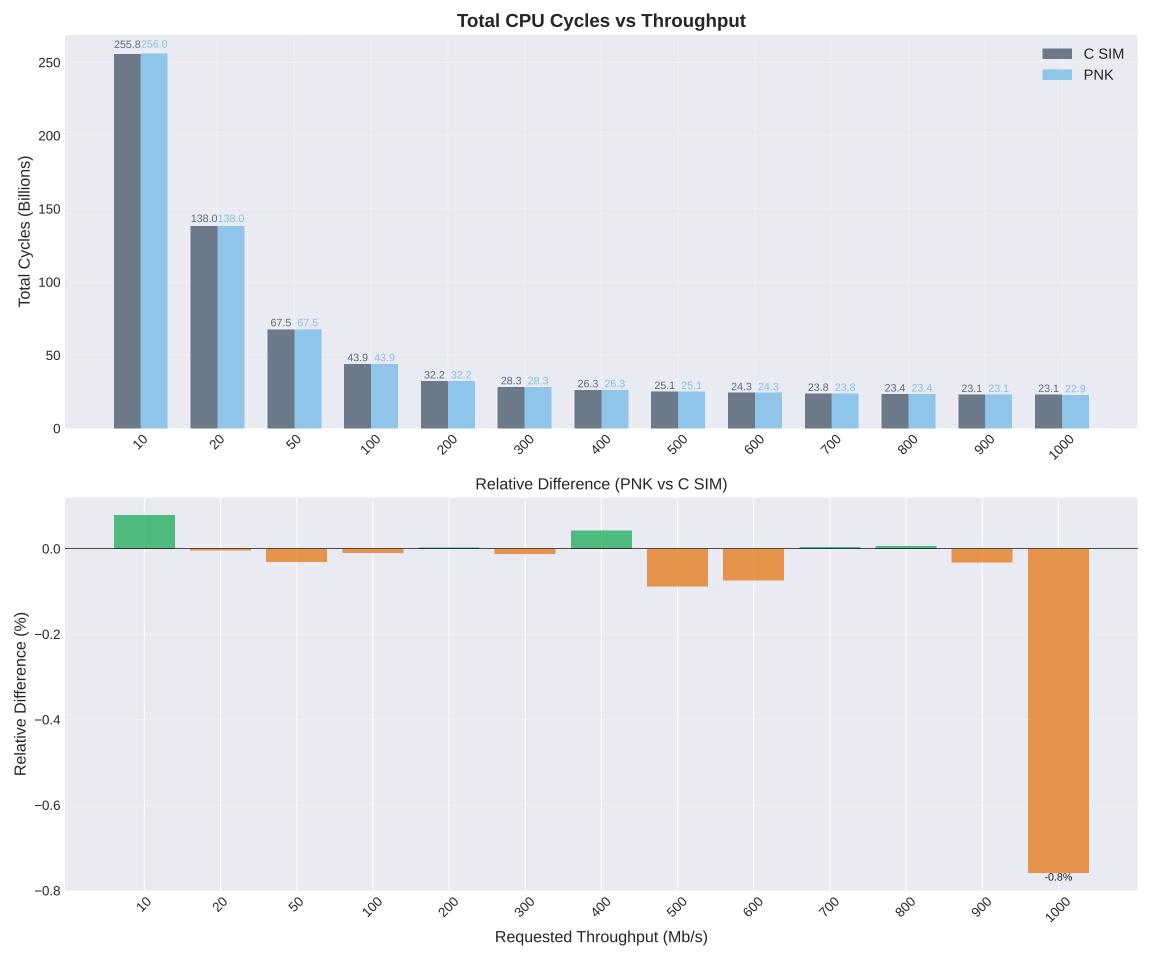




System CPU Utilization vs Throughput

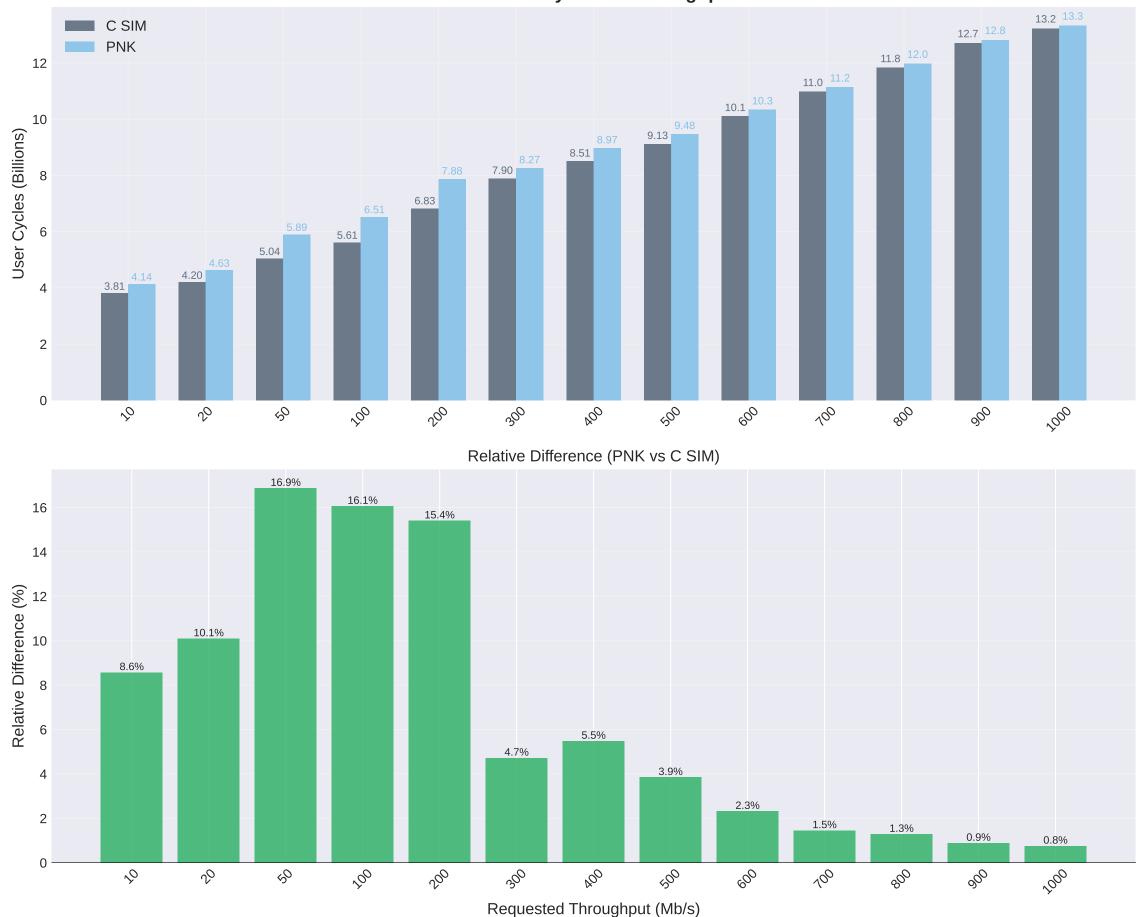






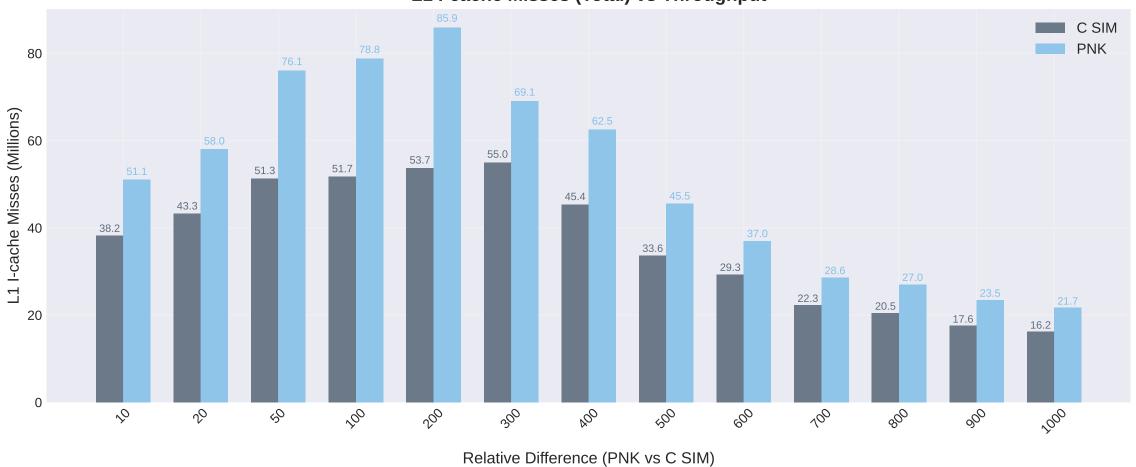
Kernel CPU Cycles vs Throughput 3.5 C SIM 3.30 3.10 PNK 3.02 3.07 2.99 3.0 2.52 Kernel Cycles (Billions) 2.36 2.26 2.10 1.73 1.69 1.55 1.27 1.15 1.0 0.5 0.0 200 200 300 400 400 600 700 800 900 2000 \$0 20 60 Relative Difference (PNK vs C SIM) 4.5% 4 2.0% 2 1.6% 0 Relative Difference (%) -1.8% -2 -3.6% -4 -3.9% -4.6% -4.5% -5.6% -6 -5.7% -6.2% -7.0% -8 -10 -11.6% -12 30 20 60 200 200 300 500 600 100 900 NO Requested Throughput (Mb/s)

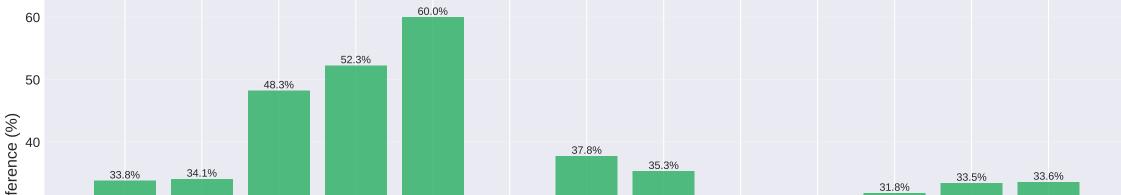
User CPU Cycles vs Throughput

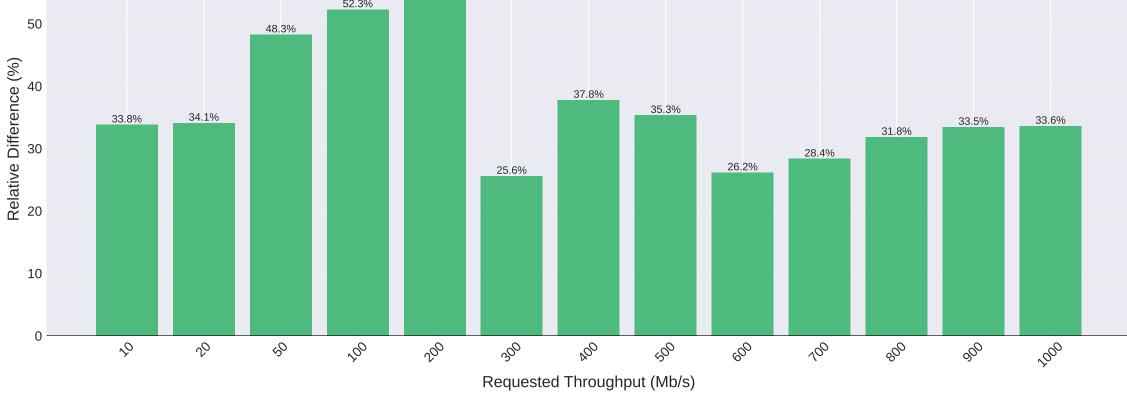


Idle CPU Cycles vs Throughput 249.0249.1 250 C SIM PNK 200 Idle Cycles (Billions) 130.6130.4 58.8 57.9 50 34.6 33.6 21.6 20.3 16.6 16.7 14.5 14.3 13.5 13.2 12.0 11.9 11.0 10.9 9.90 9.80 8.57 8.34 0 \$0 200 200 300 NOO 400 600 100 800 900 2000 20 50 Relative Difference (PNK vs C SIM) 1 0.7% 0 -0.2% -0.8% -1.0% -1.0% Relative Difference (%) -1.2% -1.6% -1.6% -1.9% -2.6% -2.9% -5 -6 -6.0% 30 20 SO 200 200 300 NOO 500 600 700 900 Requested Throughput (Mb/s)

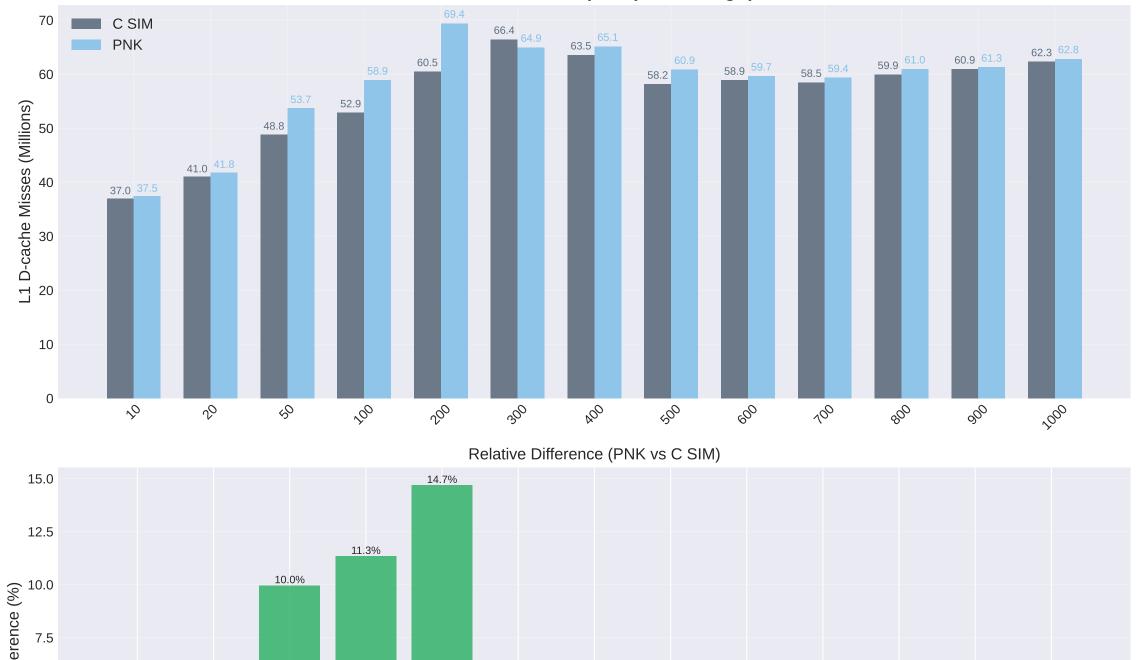
L1 I-cache Misses (Total) vs Throughput





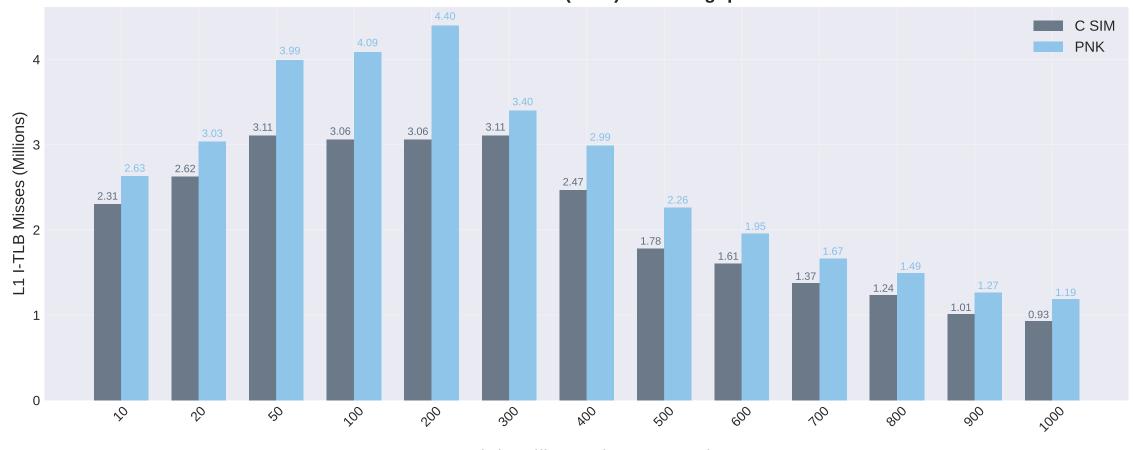


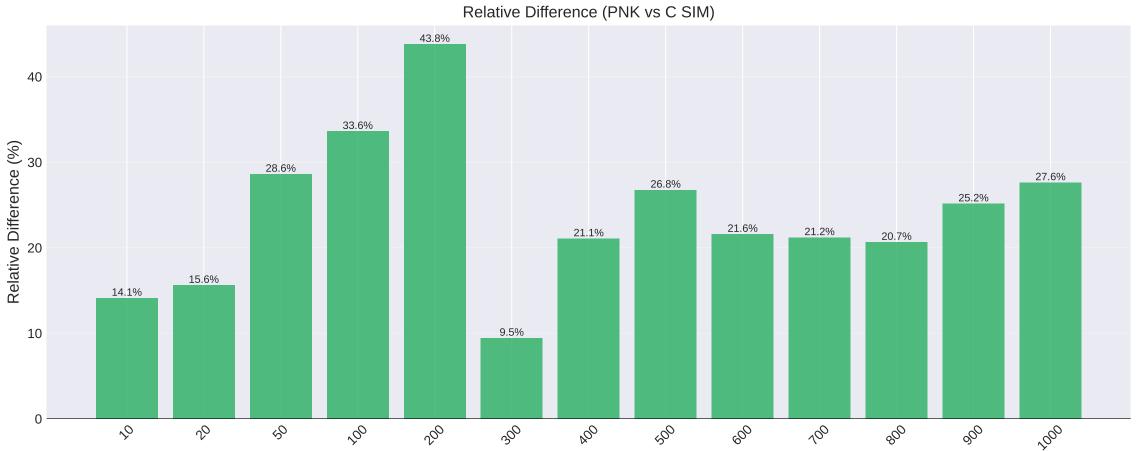
L1 D-cache Misses (Total) vs Throughput



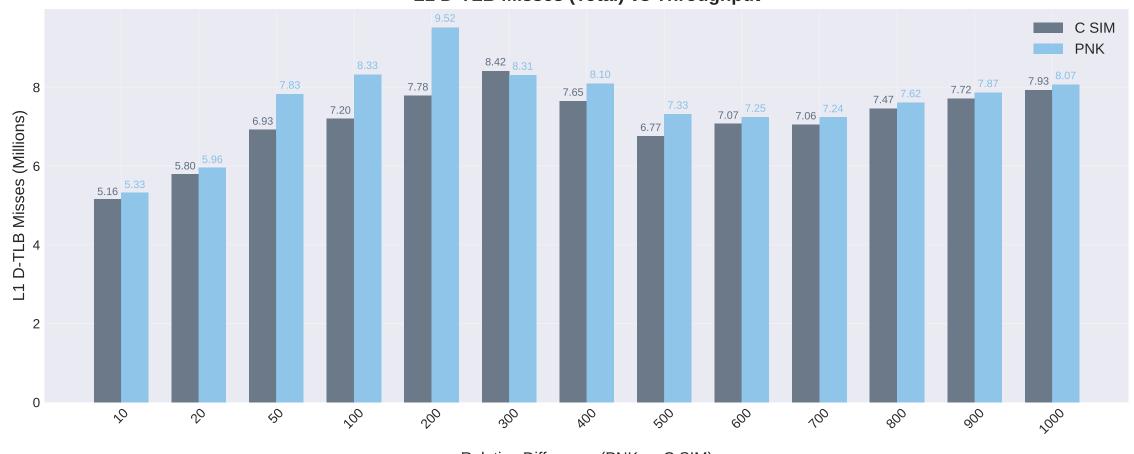
4.6% 2.5% 2.5 2.0% 1.7% 1.5% 1.3% 1.2% 0.9% 0.6% 0.0 -2.2% -2.5 30 20 SO 200 200 300 NOO 400 600 100 900 900 Requested Throughput (Mb/s)

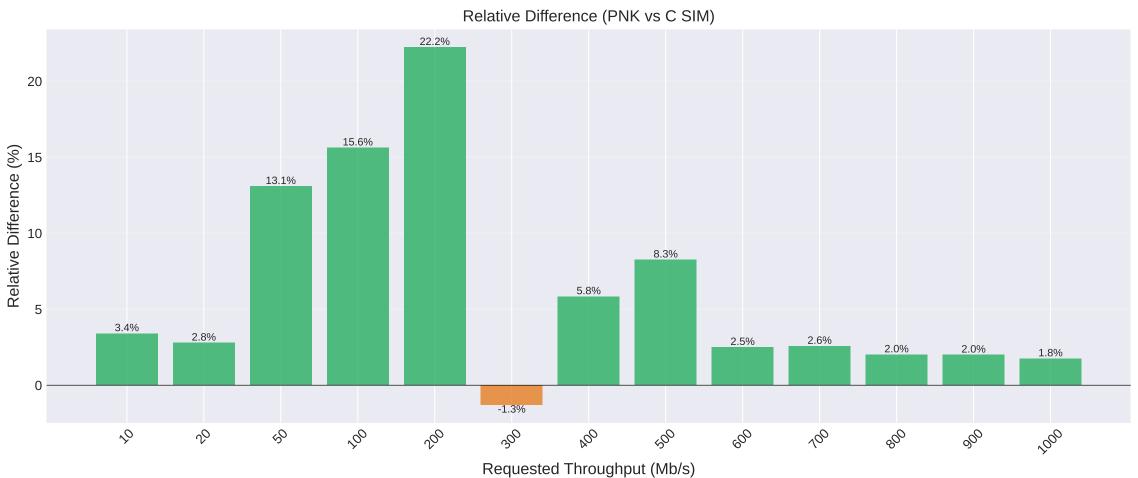
L1 I-TLB Misses (Total) vs Throughput



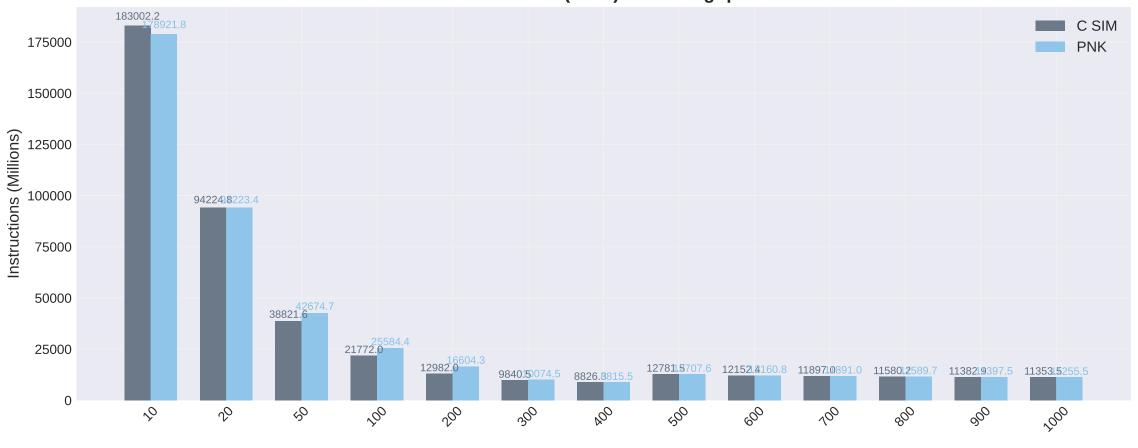


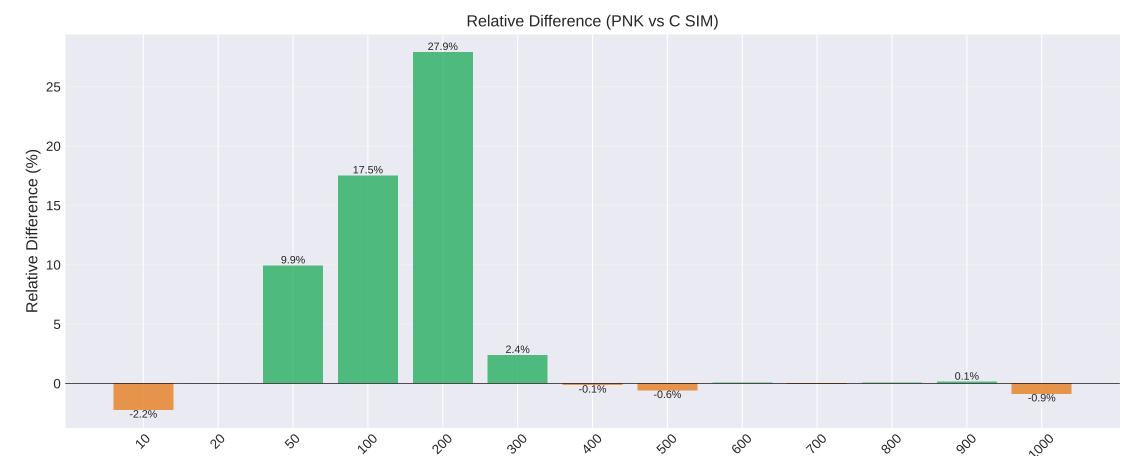
L1 D-TLB Misses (Total) vs Throughput

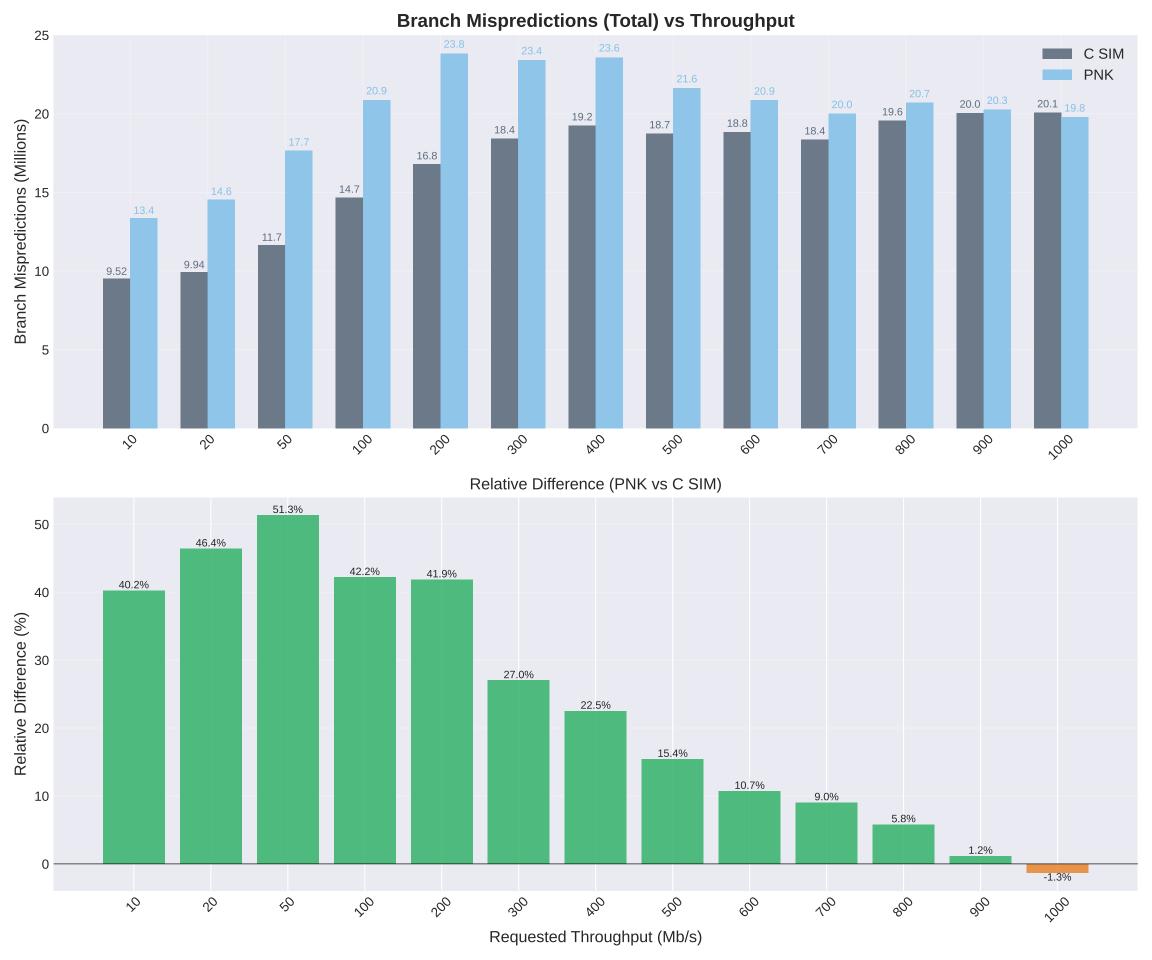




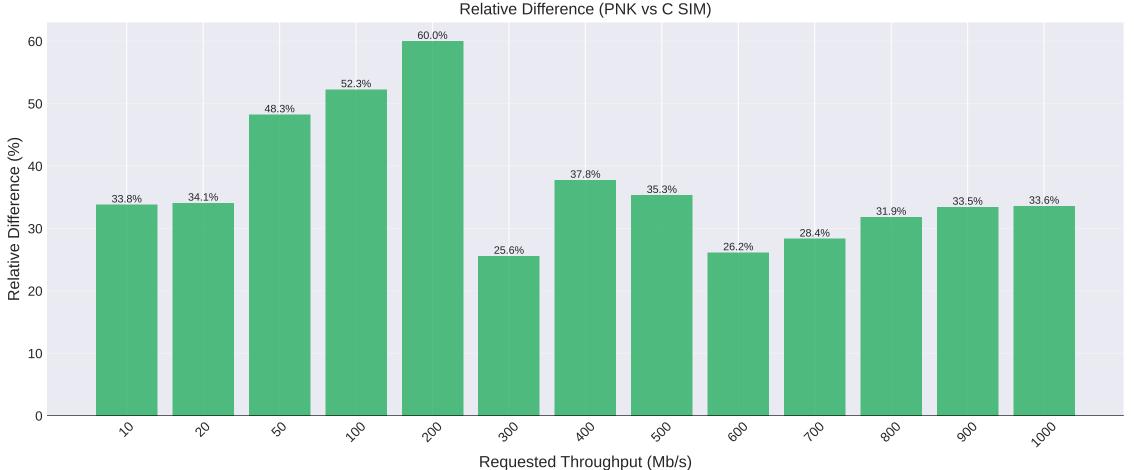




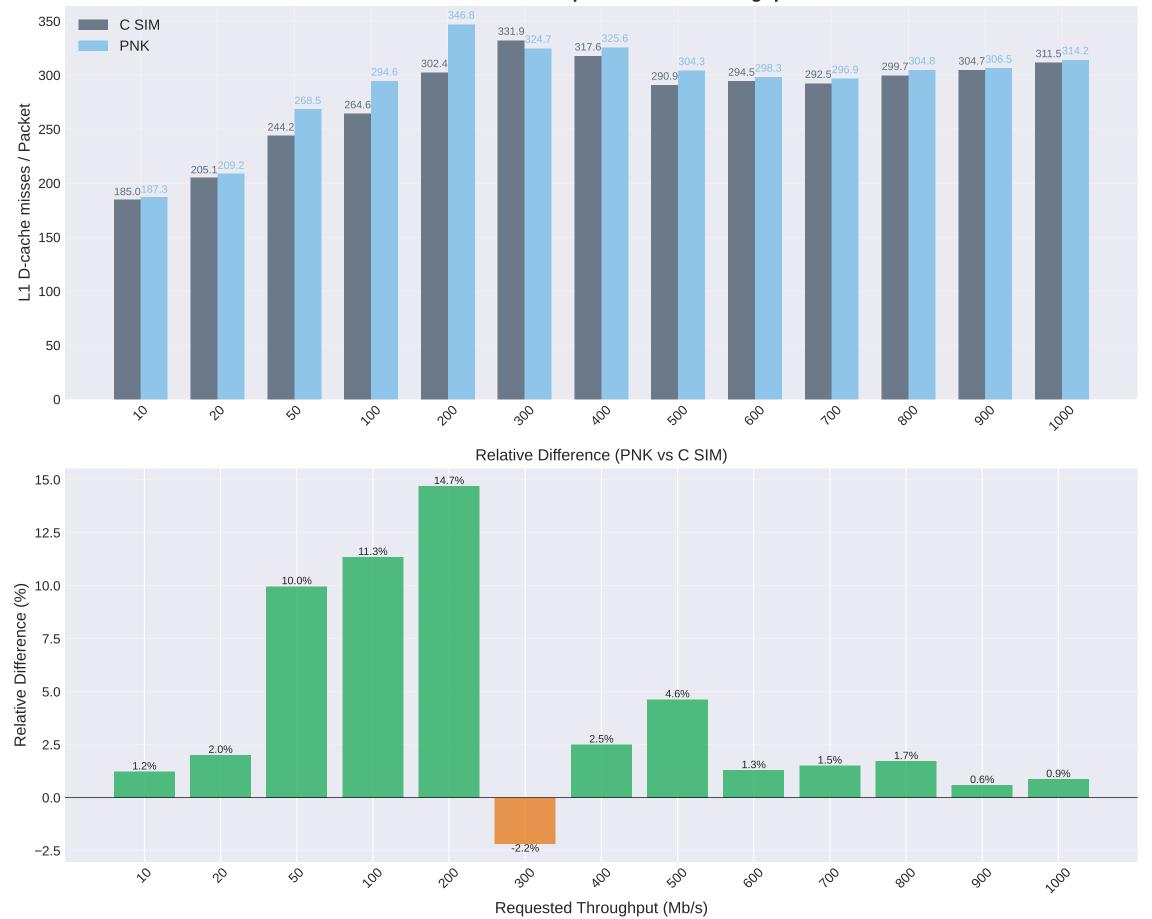




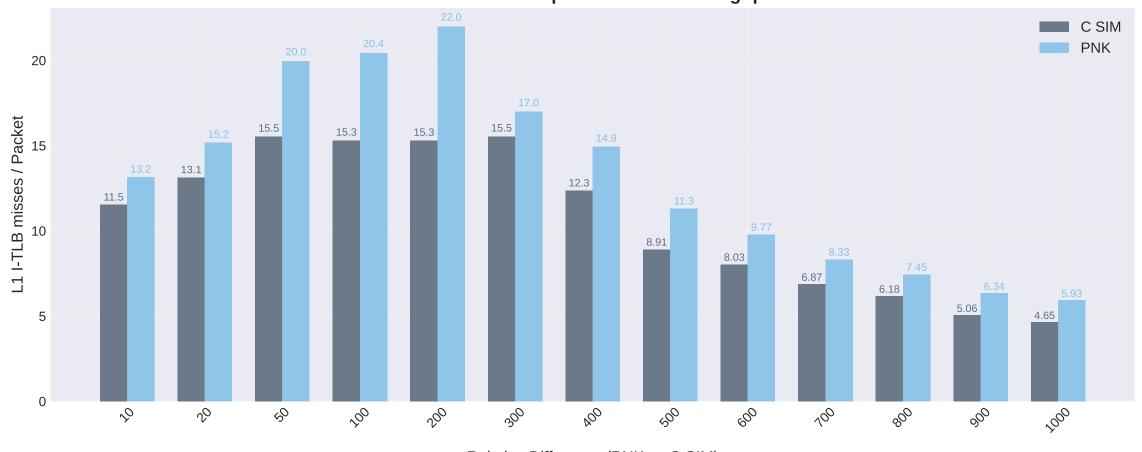


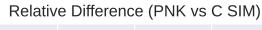


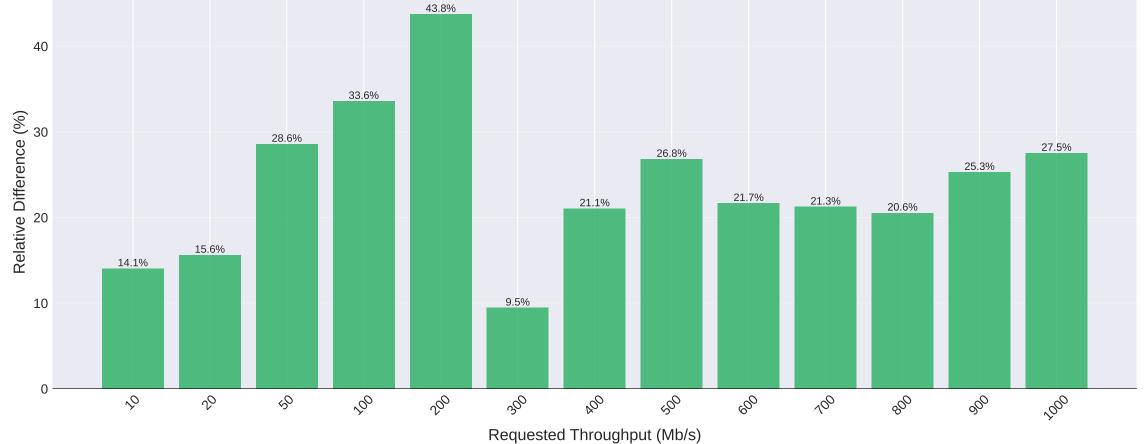
L1 D-cache Misses per Packet vs Throughput



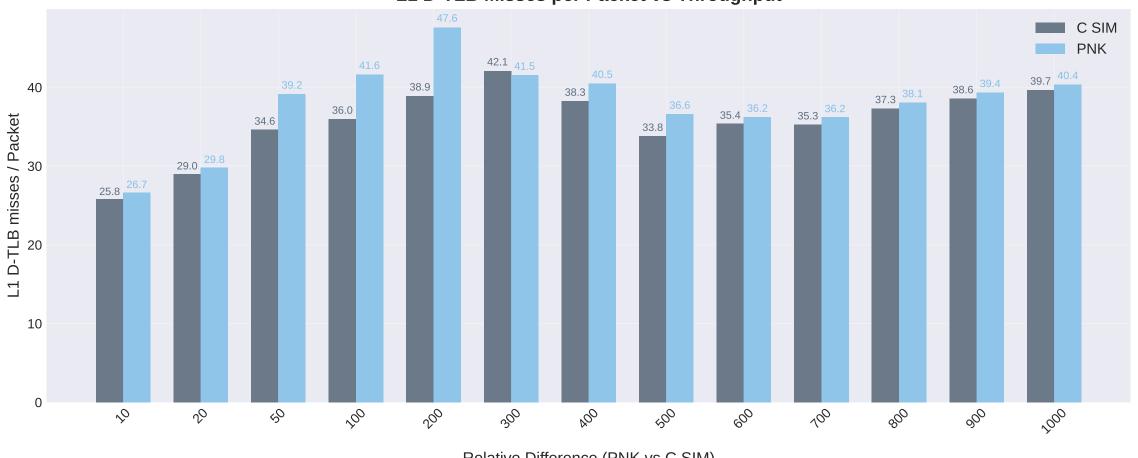
L1 I-TLB Misses per Packet vs Throughput

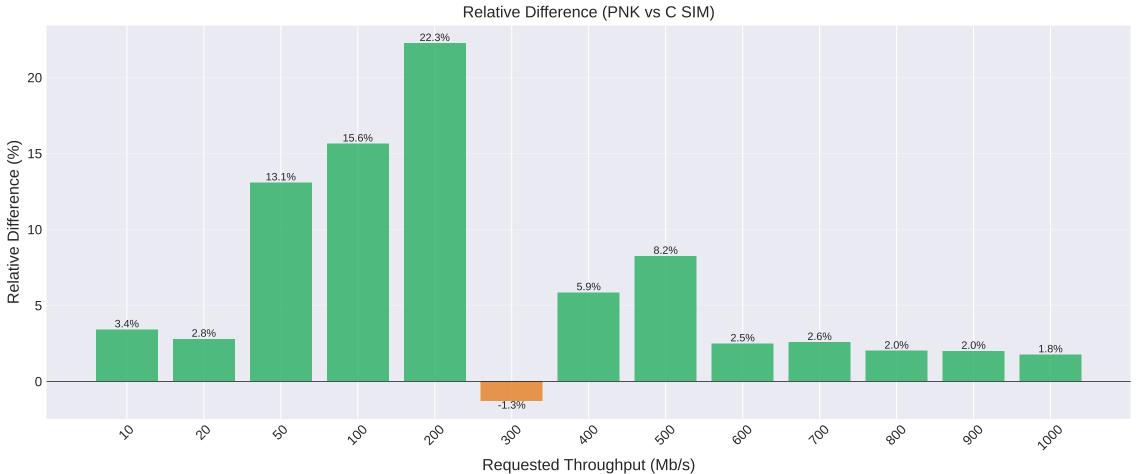




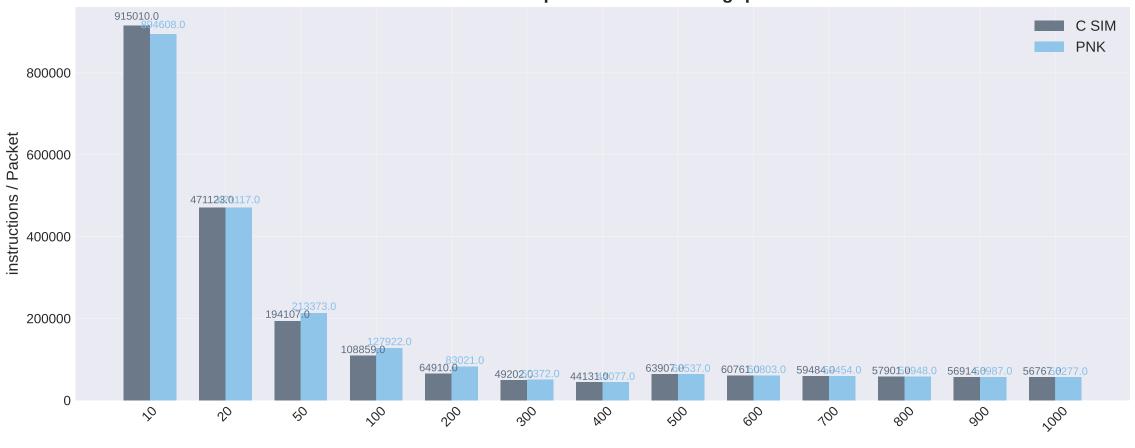


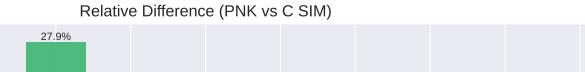
L1 D-TLB Misses per Packet vs Throughput

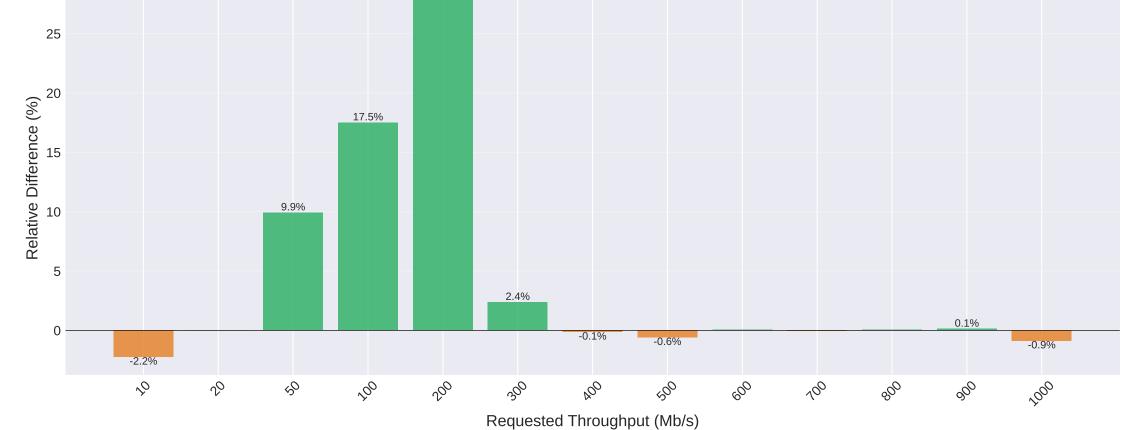




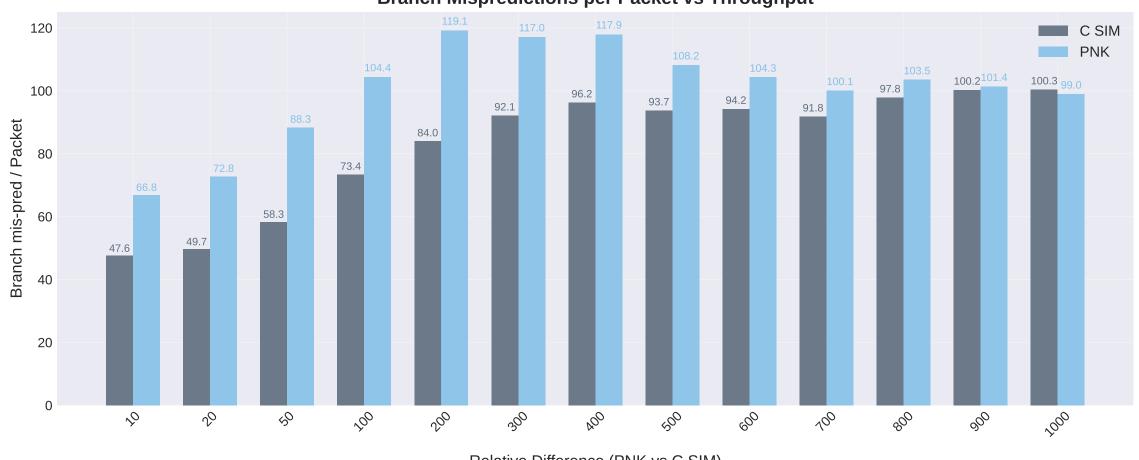


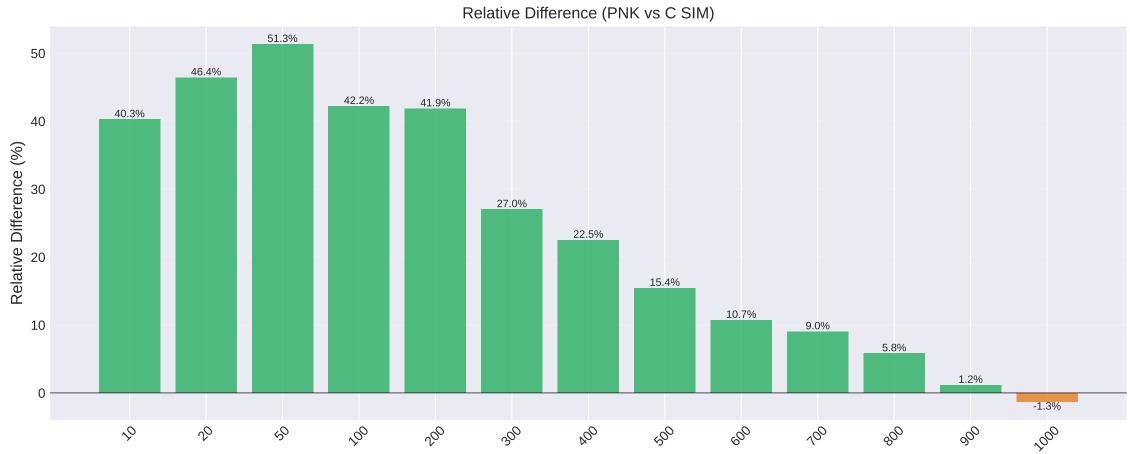


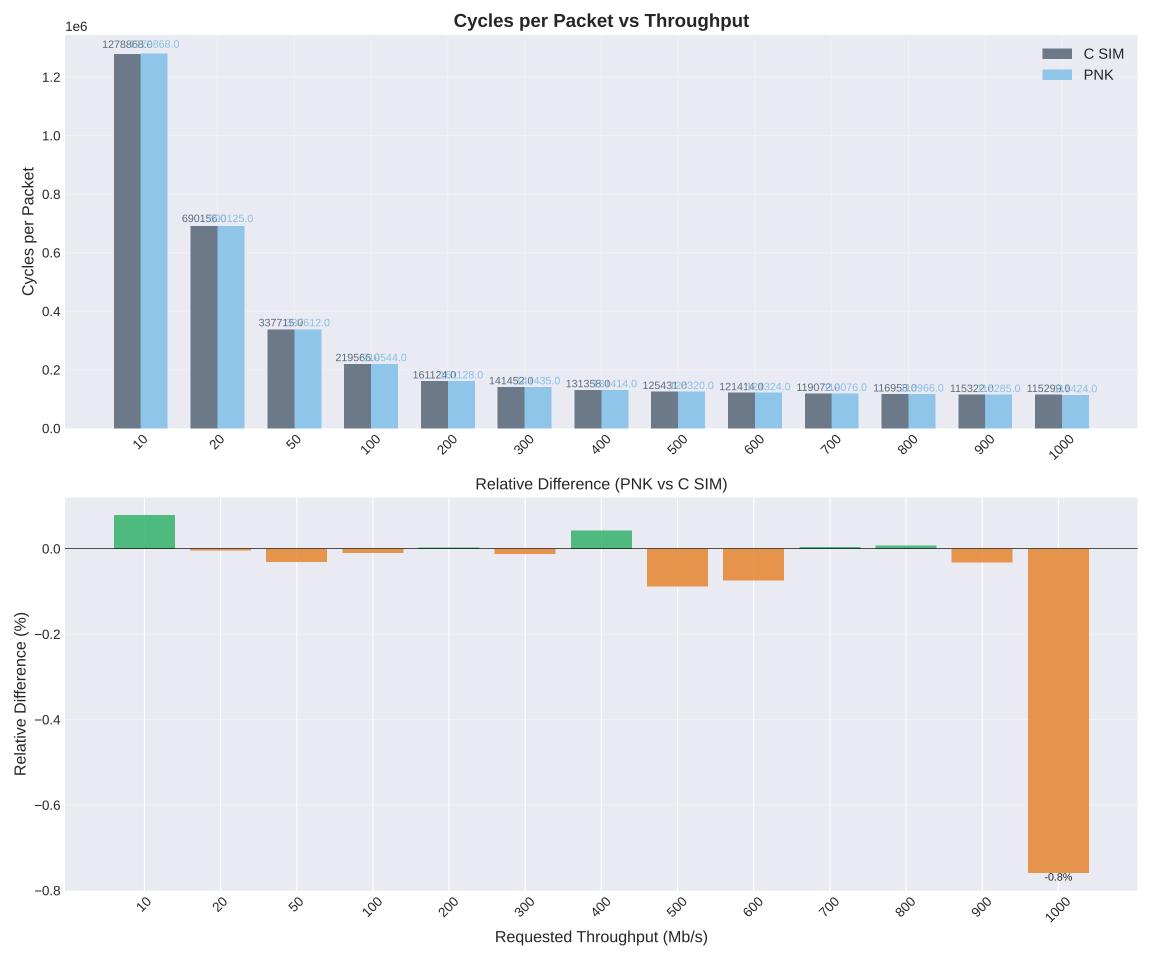




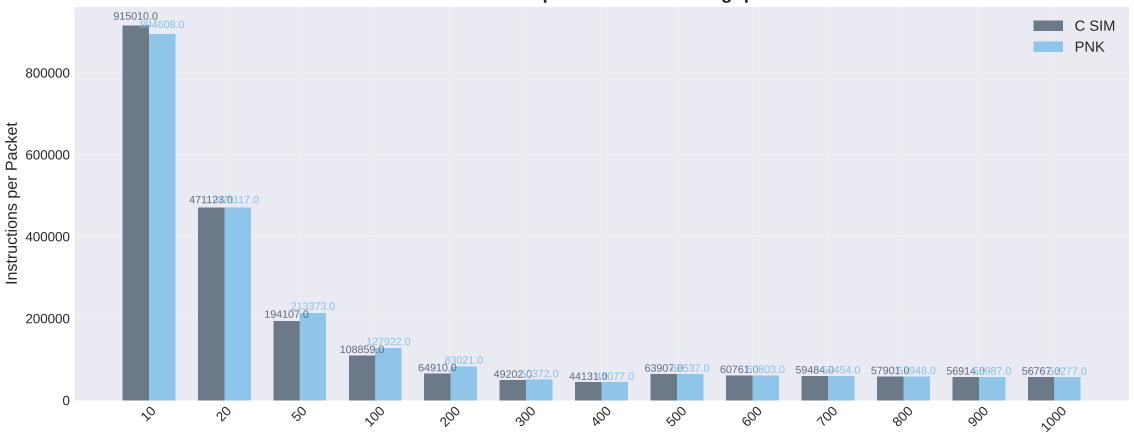
Branch Mispredictions per Packet vs Throughput

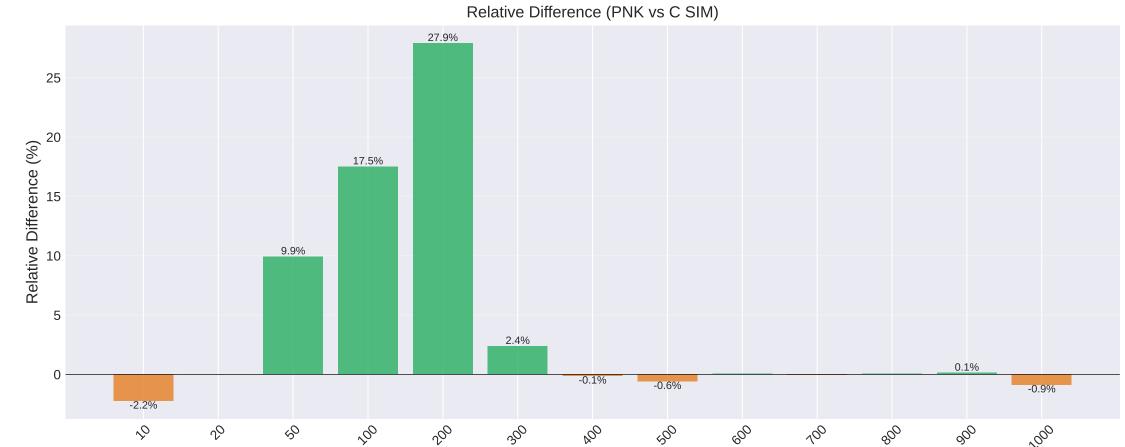












Branch Mispredictions per Packet vs Throughput

