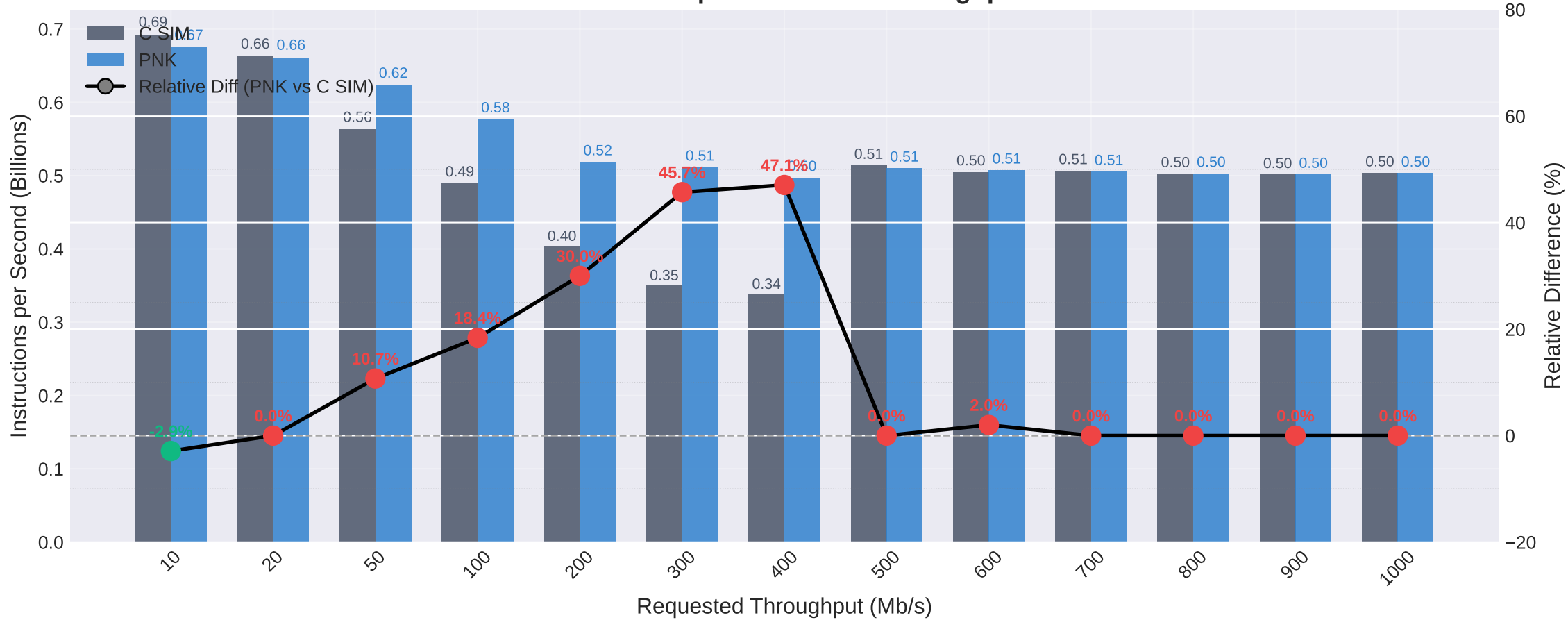


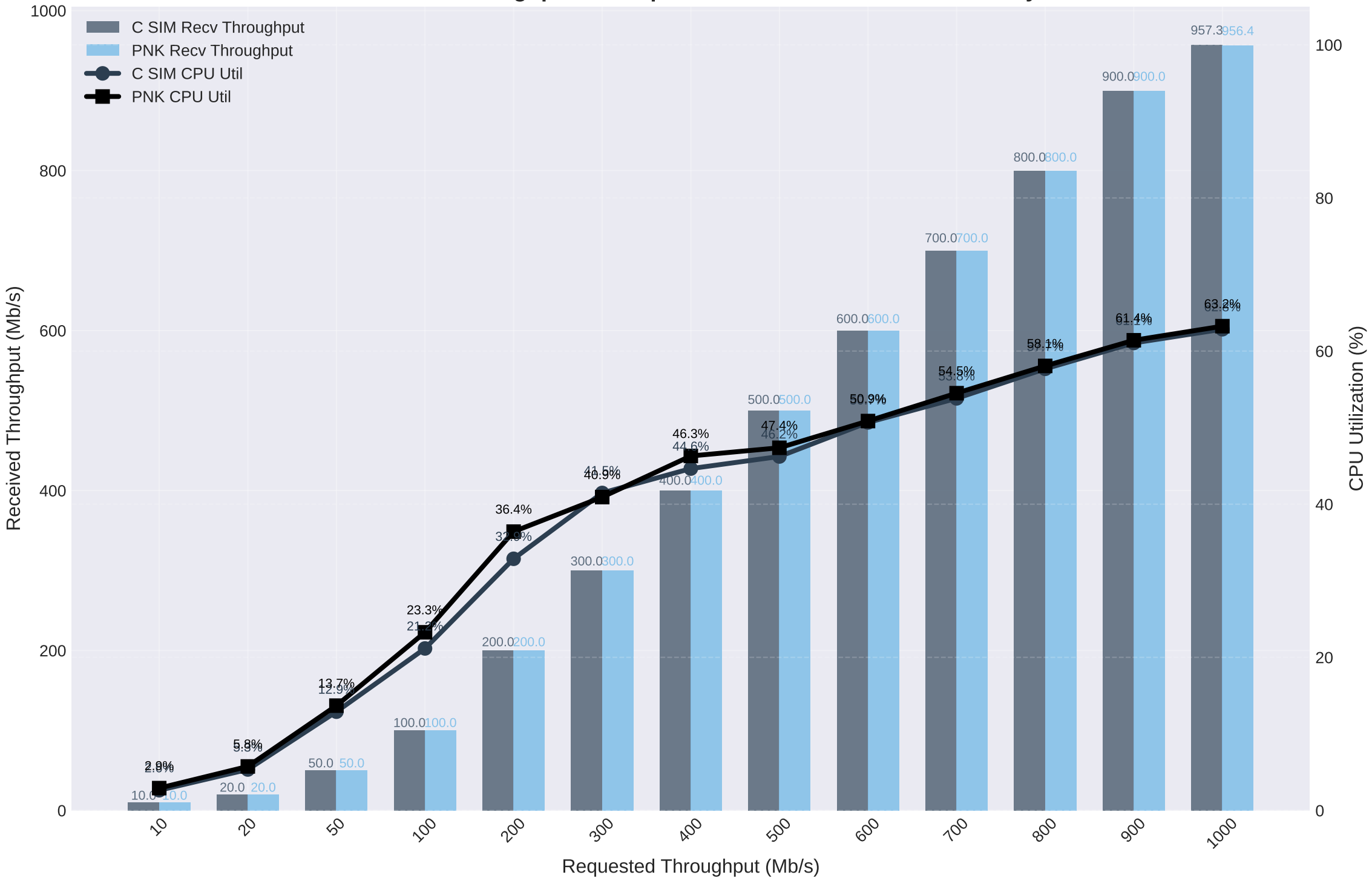
Instructions per Second vs Throughput



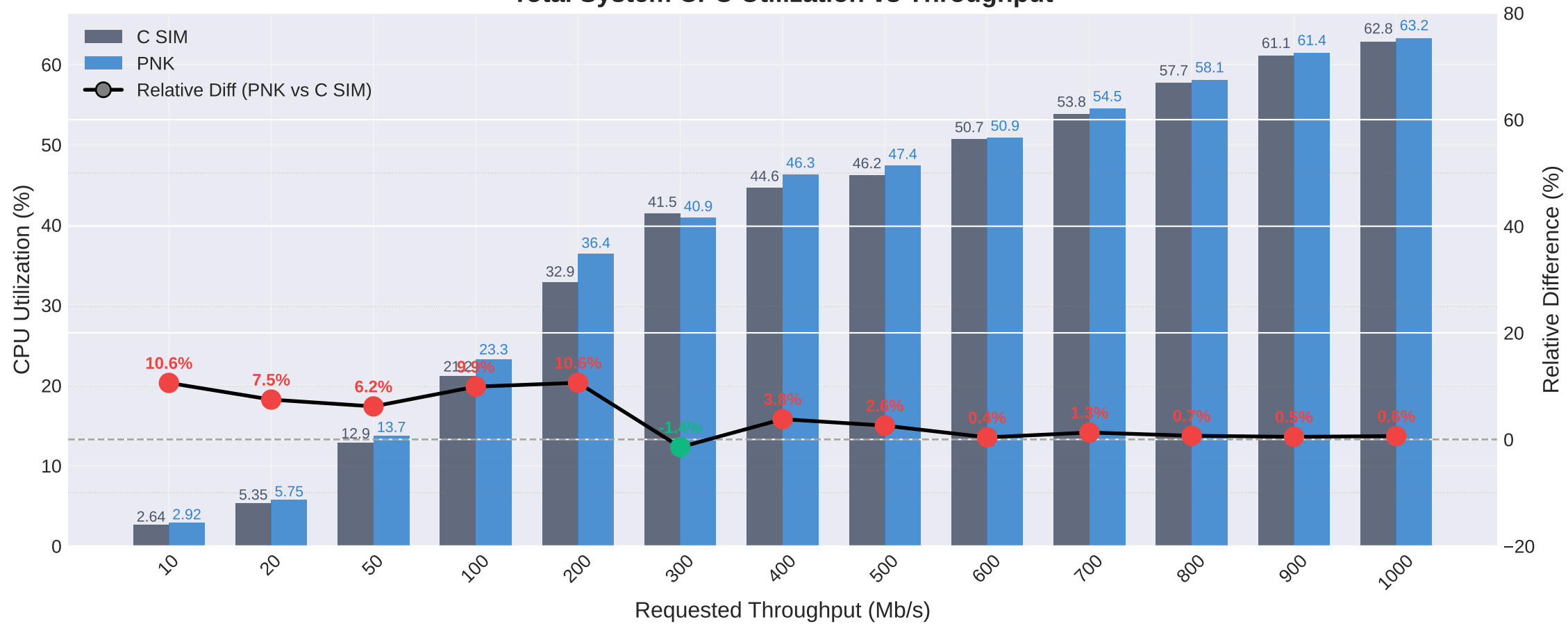
Relative Difference (PNK vs C SIM baseline)



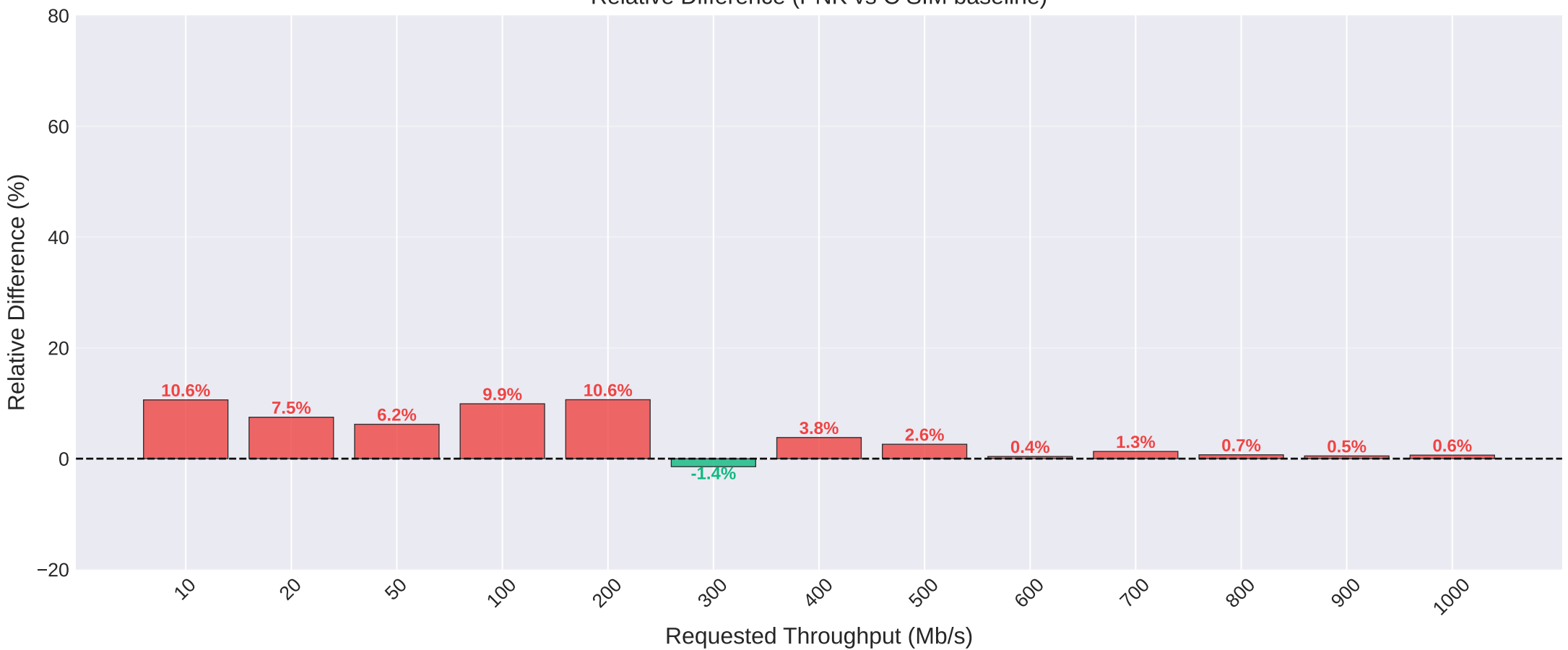
Received Throughput vs Requested with CPU Utilization Overlay



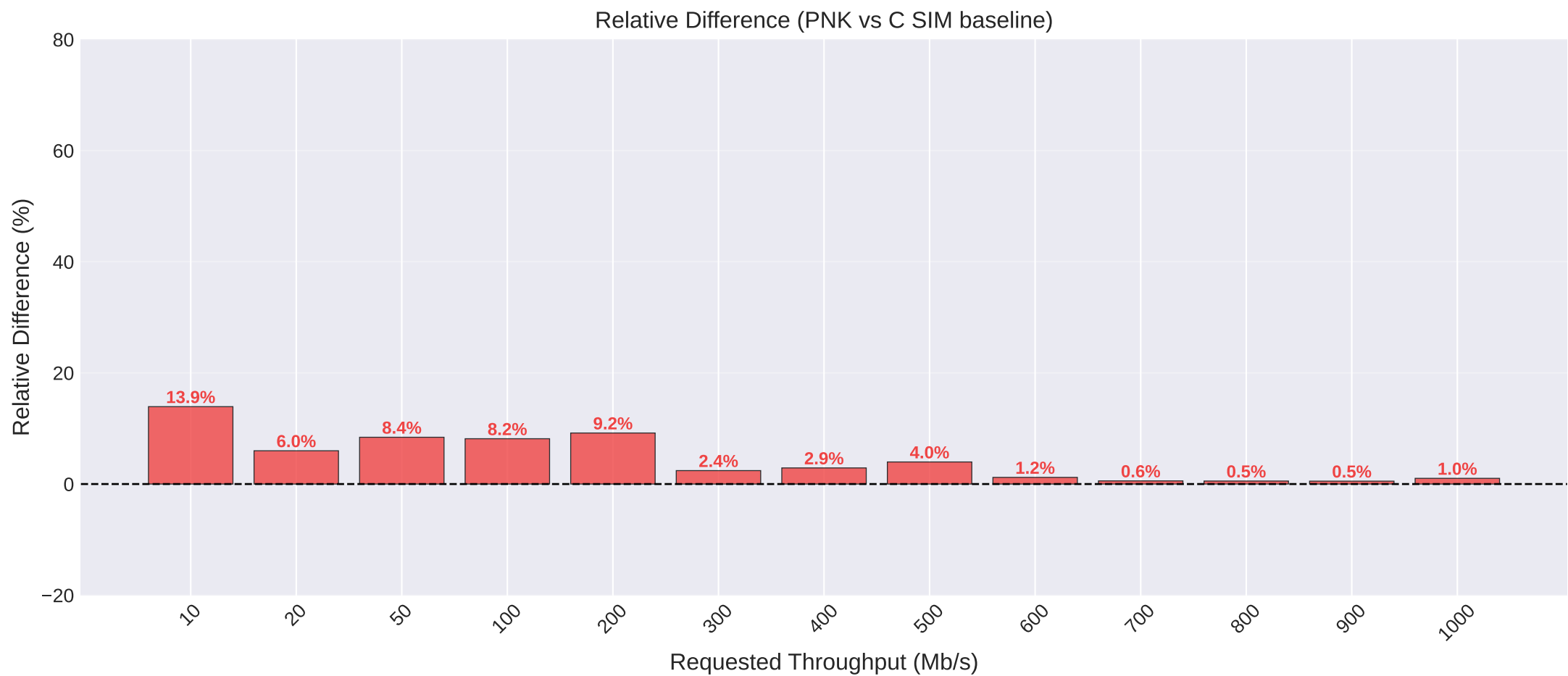
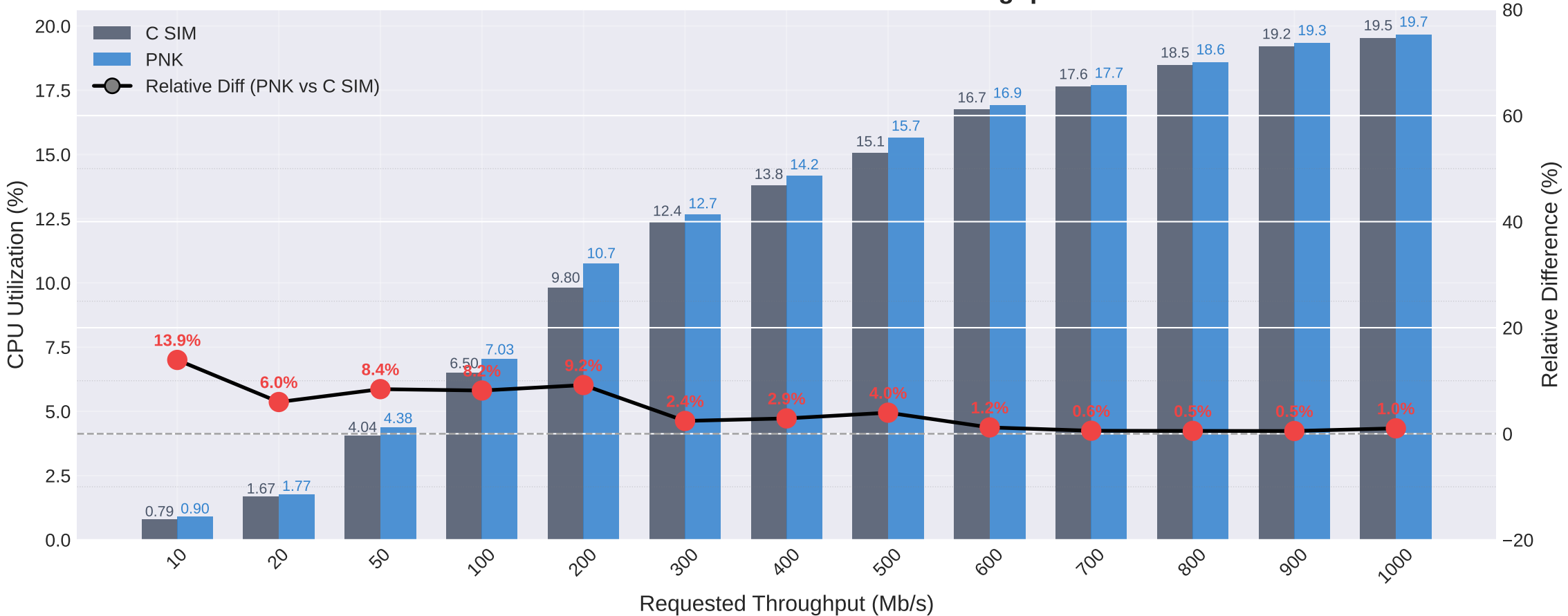
Total System CPU Utilization vs Throughput



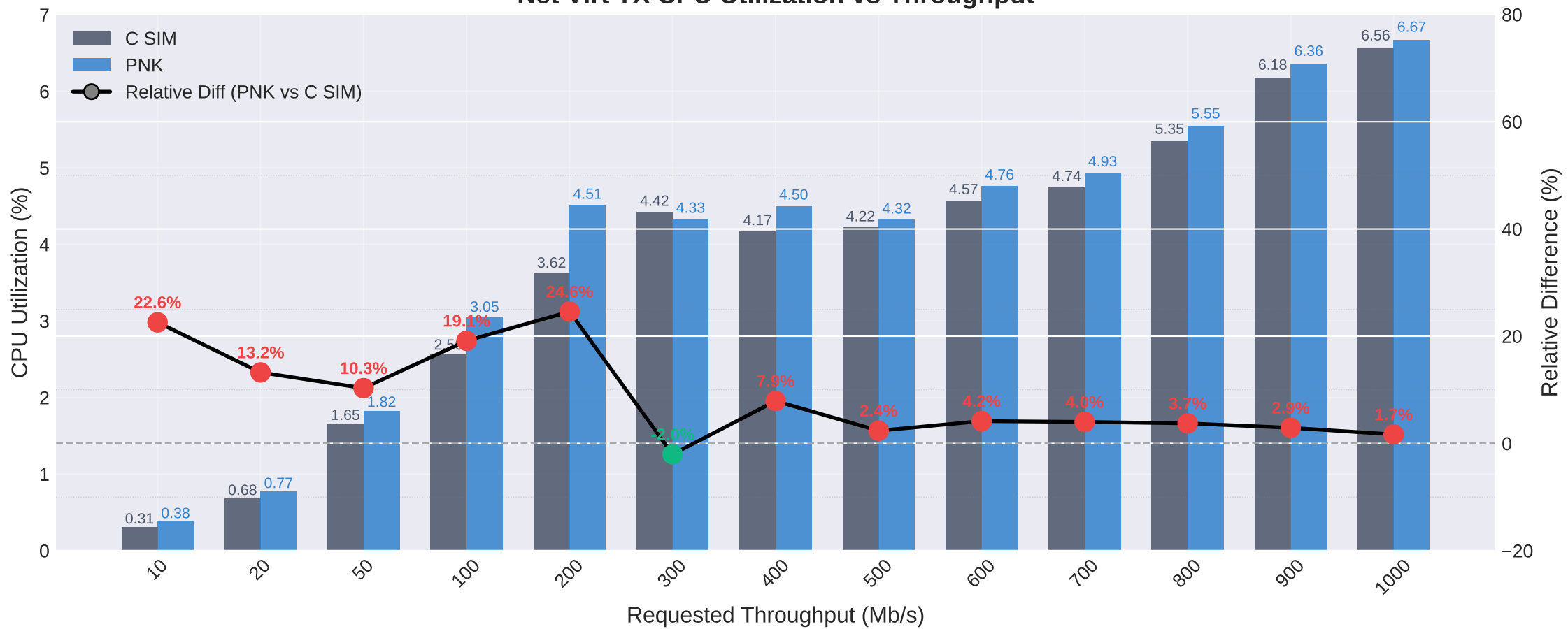
Relative Difference (PNK vs C SIM baseline)



Ethernet Driver CPU Utilization vs Throughput



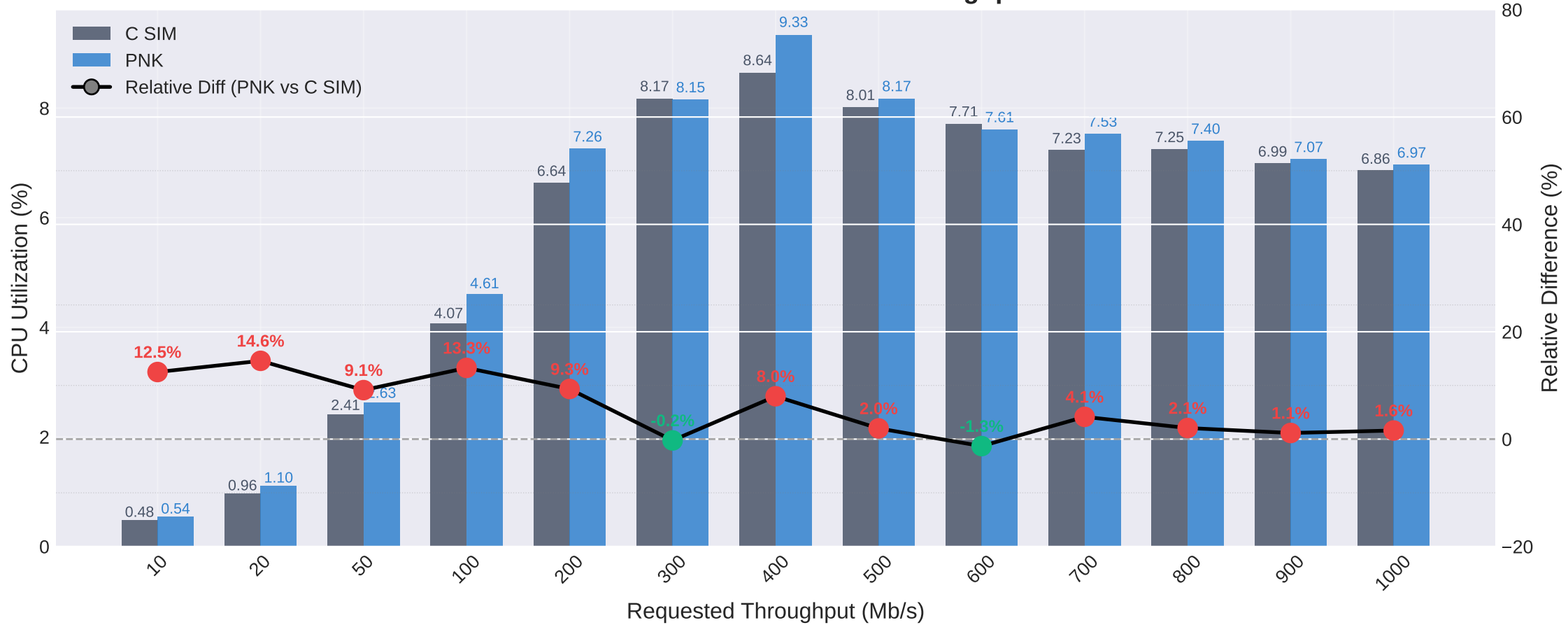
Net Virt TX CPU Utilization vs Throughput



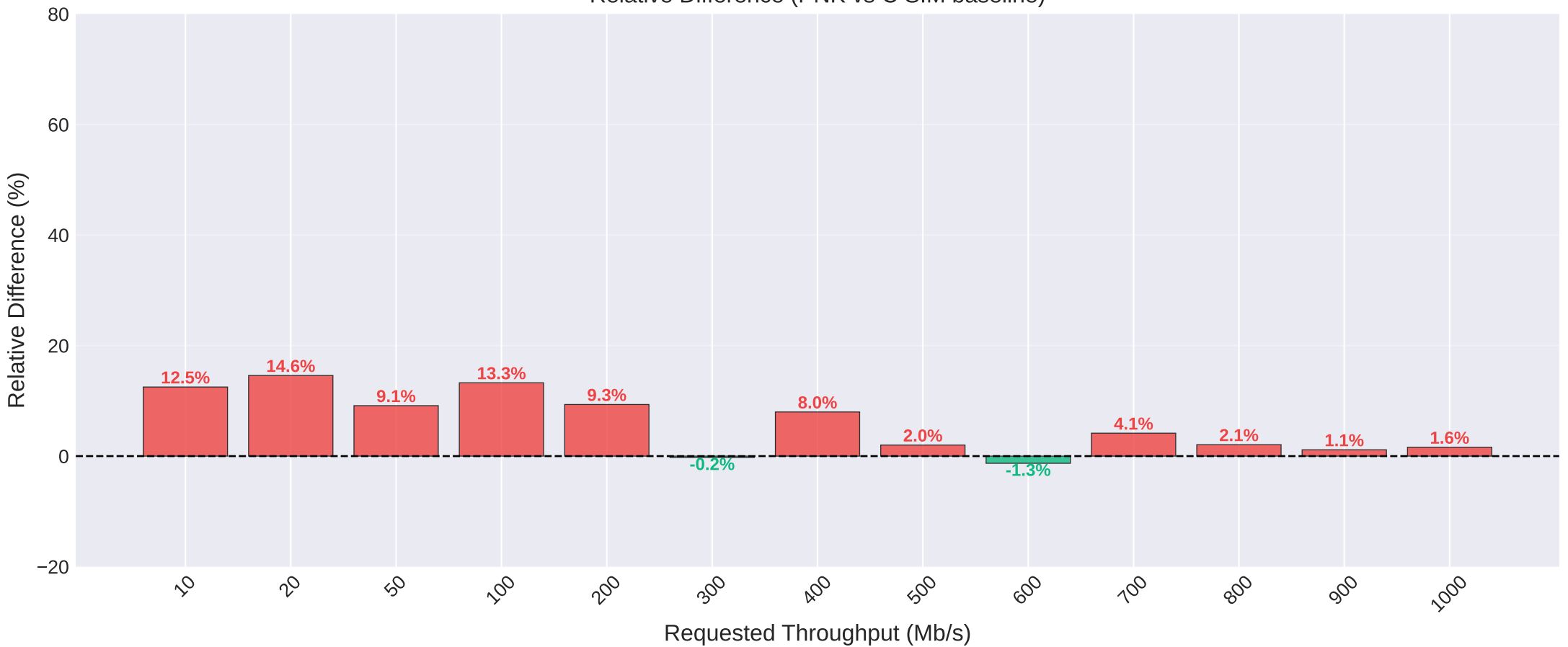
Relative Difference (PNK vs C SIM baseline)



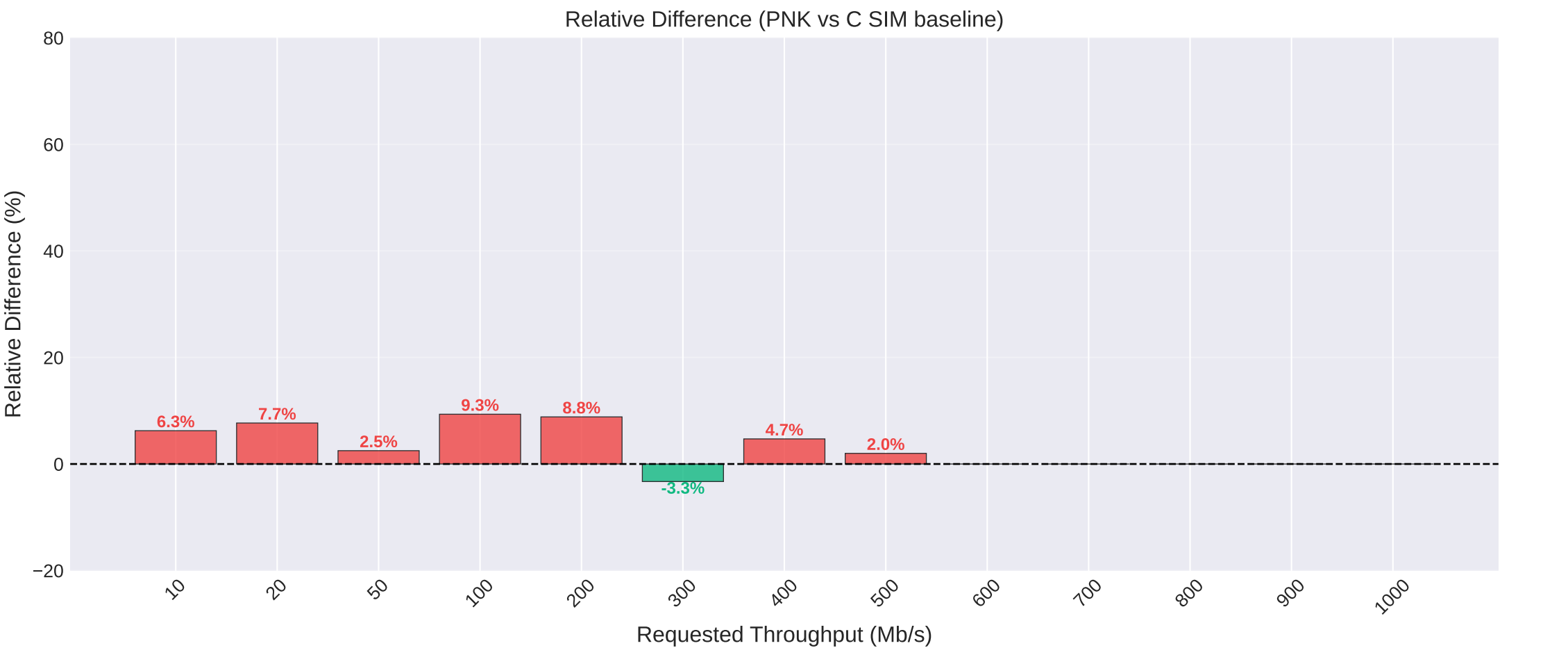
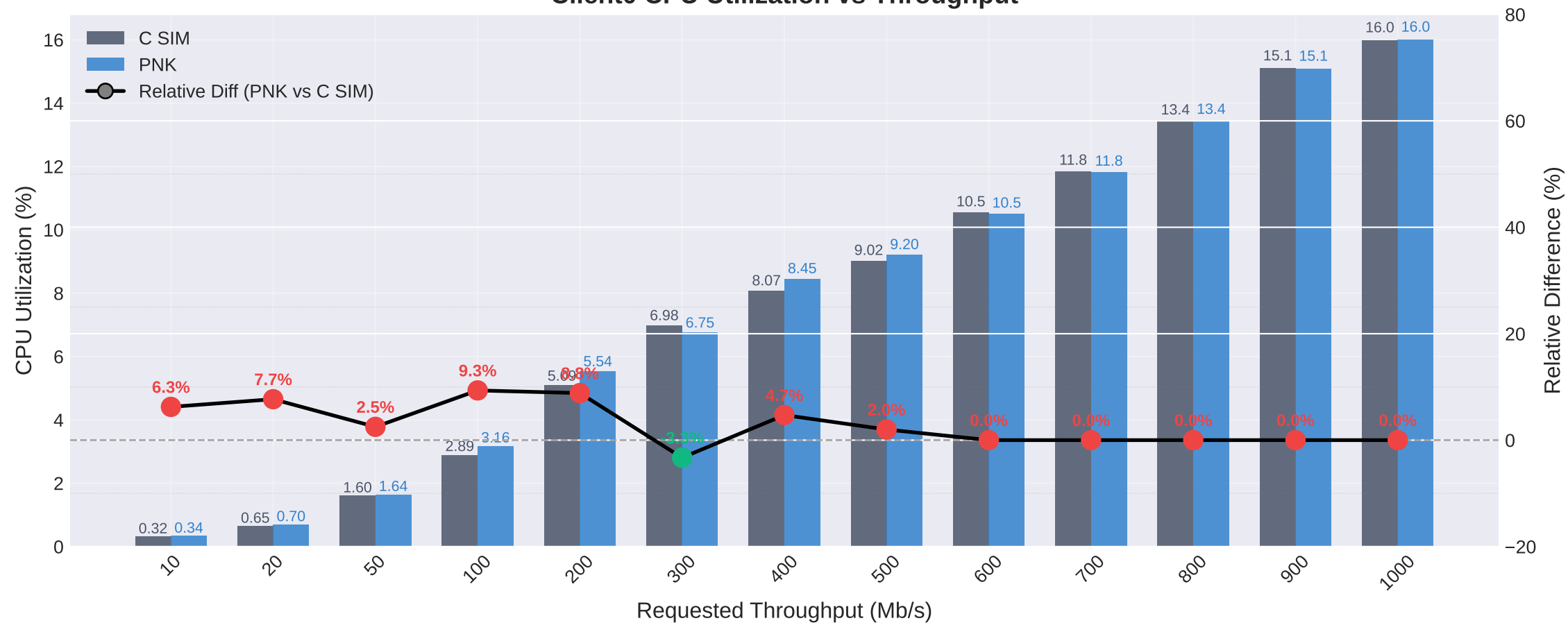
Net Virt RX CPU Utilization vs Throughput



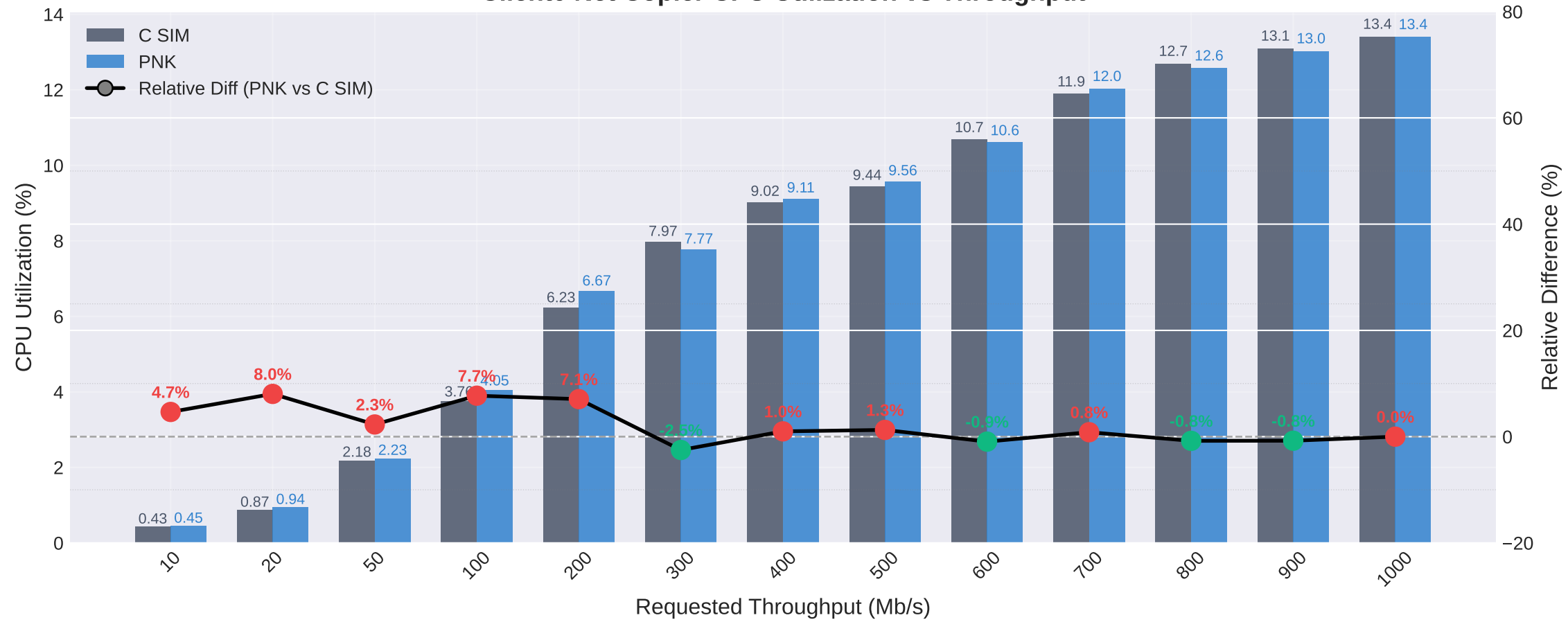
Relative Difference (PNK vs C SIM baseline)



Client0 CPU Utilization vs Throughput



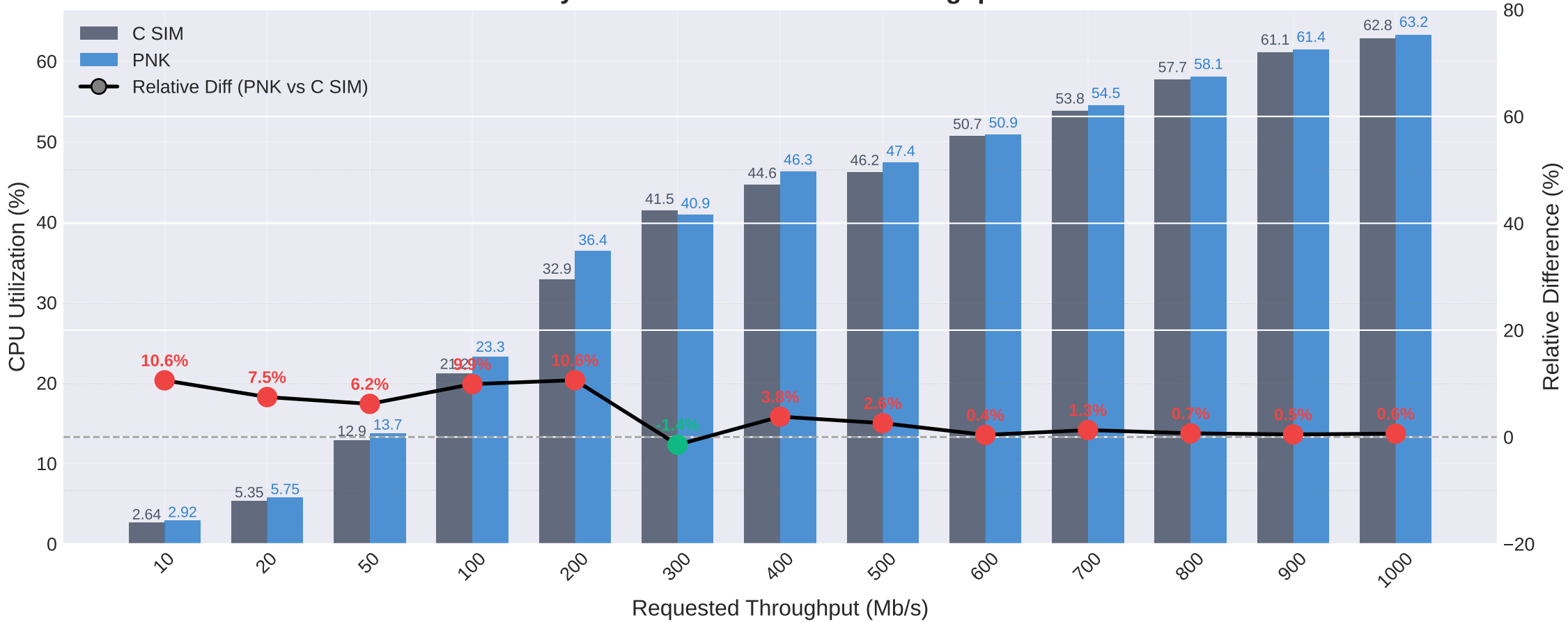
Client0 Net Copier CPU Utilization vs Throughput



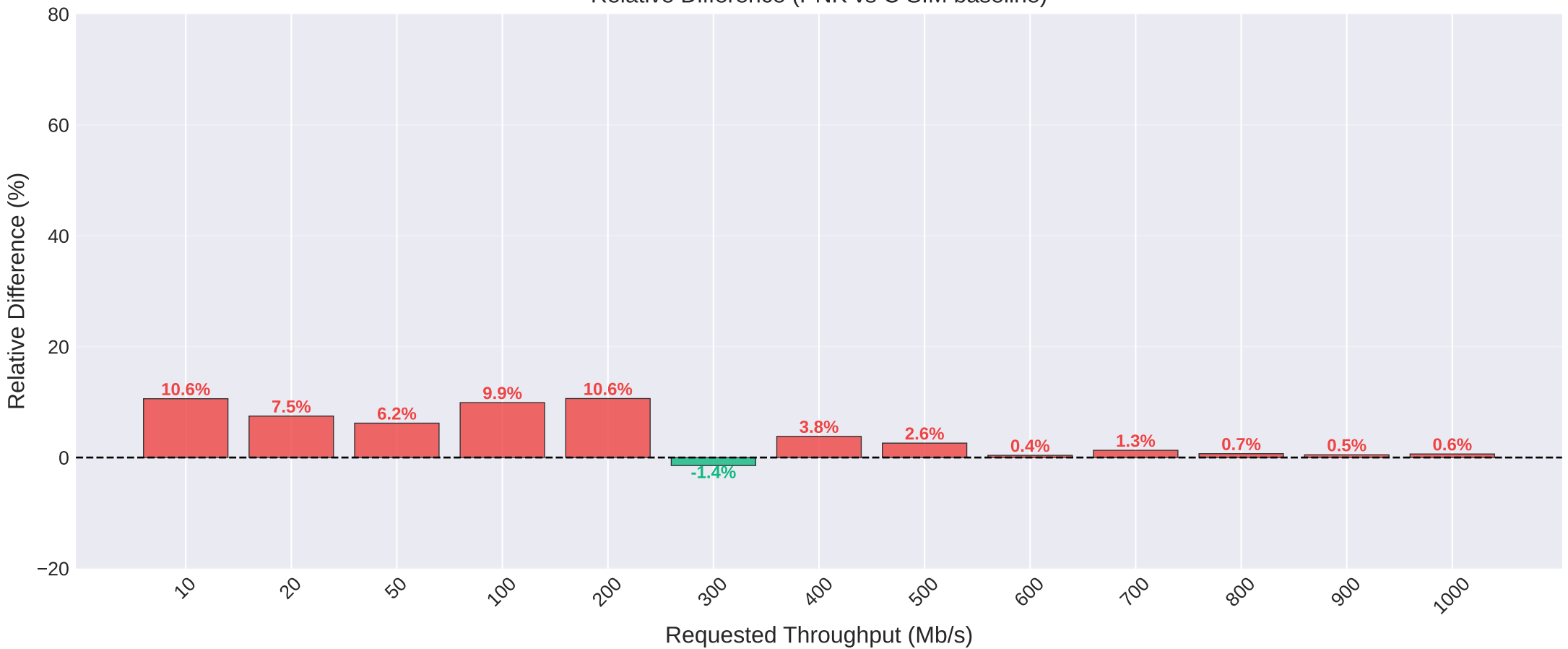
Relative Difference (PNK vs C SIM baseline)



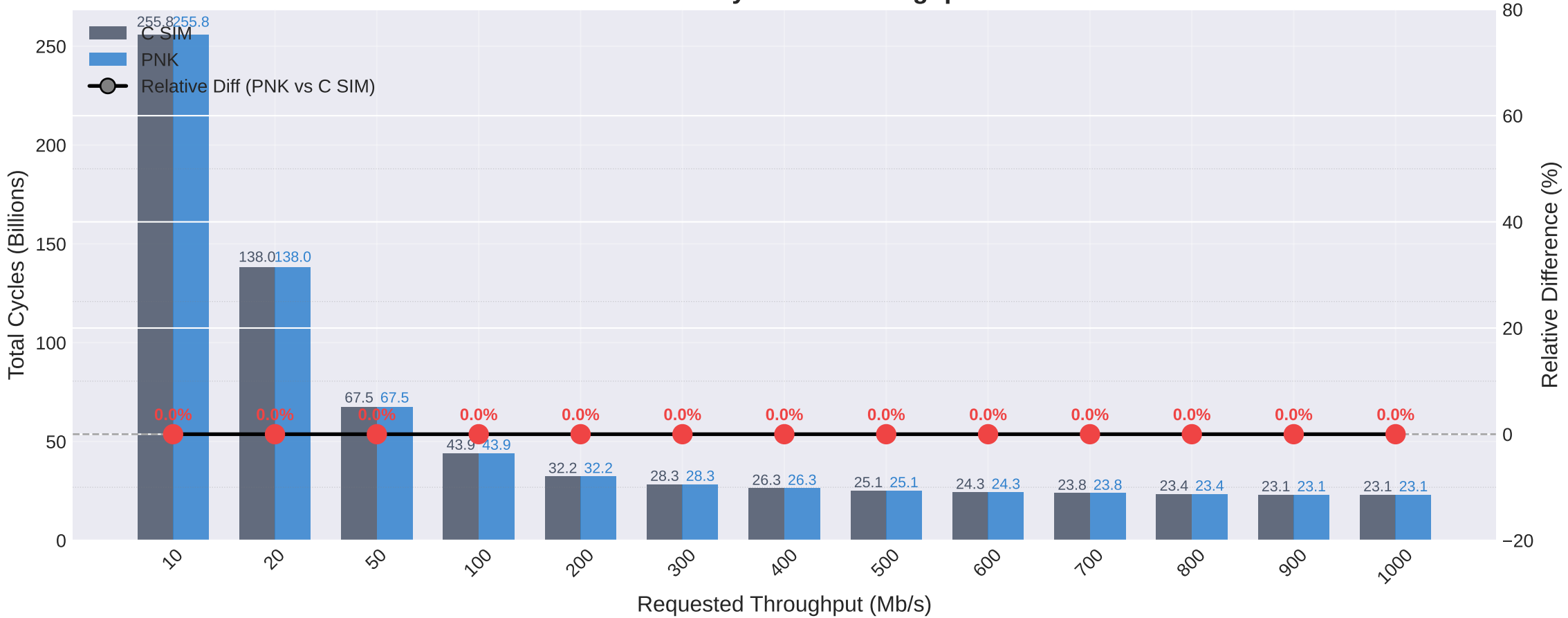
System CPU Utilization vs Throughput



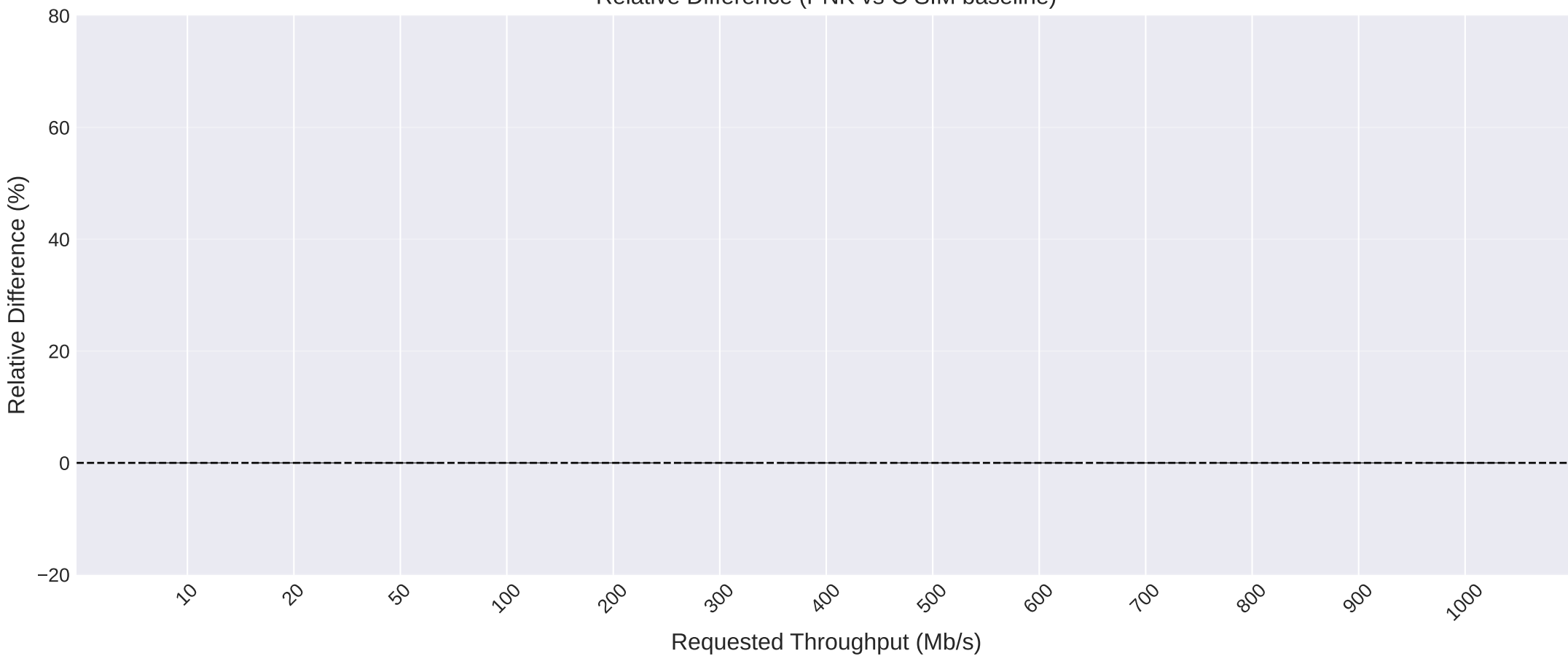
Relative Difference (PNK vs C SIM baseline)



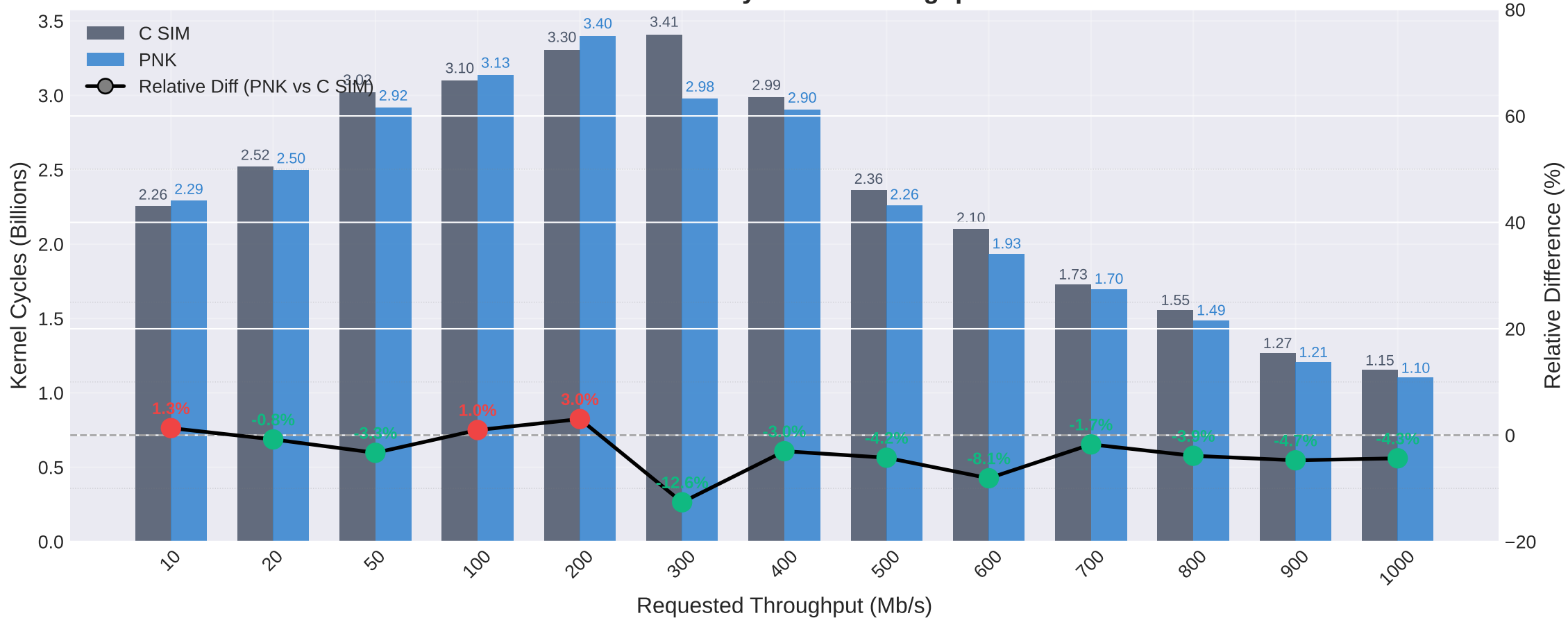
Total CPU Cycles vs Throughput



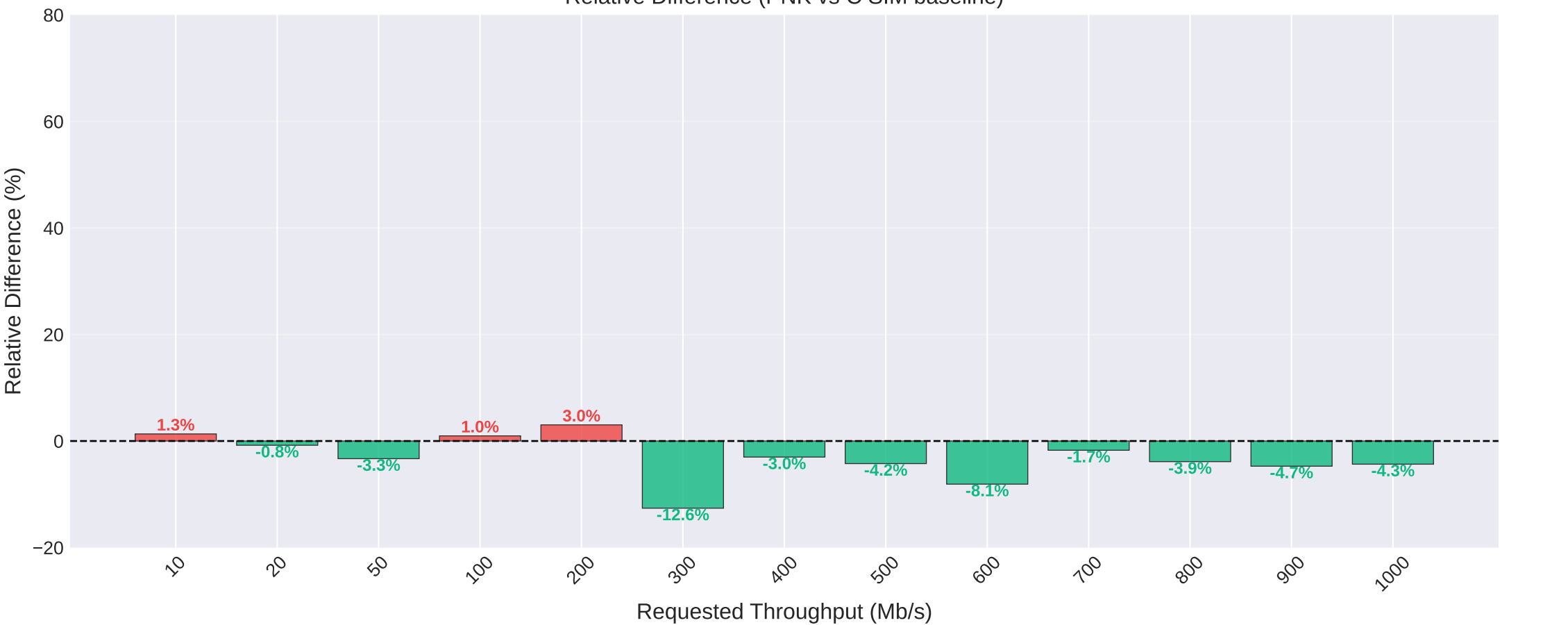
Relative Difference (PNK vs C SIM baseline)



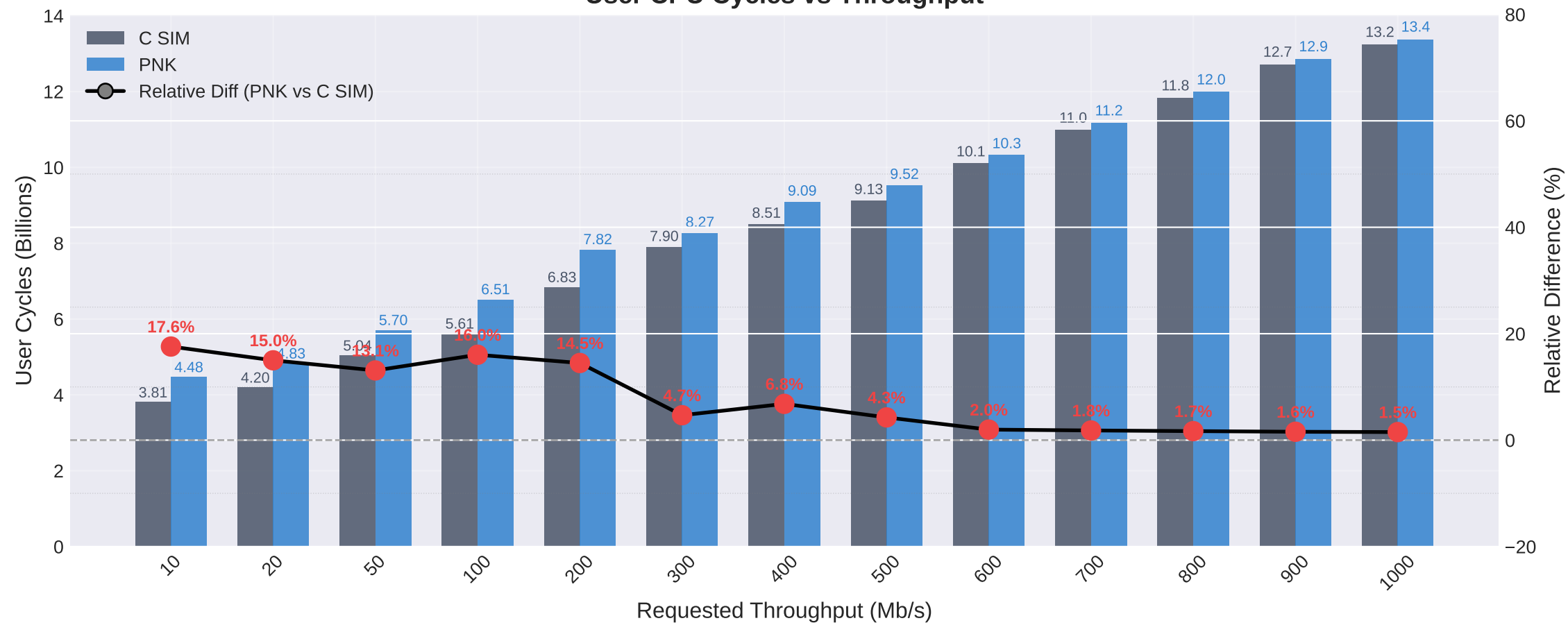
Kernel CPU Cycles vs Throughput



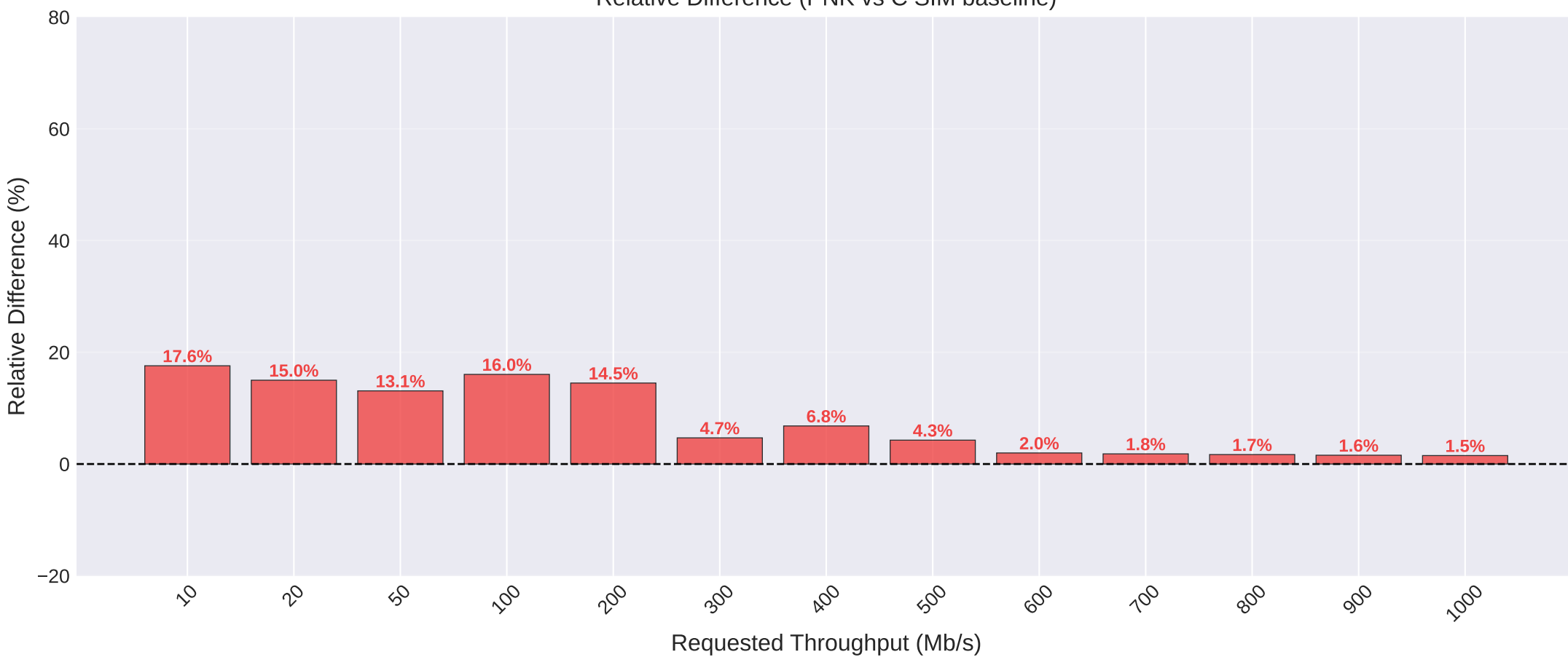
Relative Difference (PNK vs C SIM baseline)



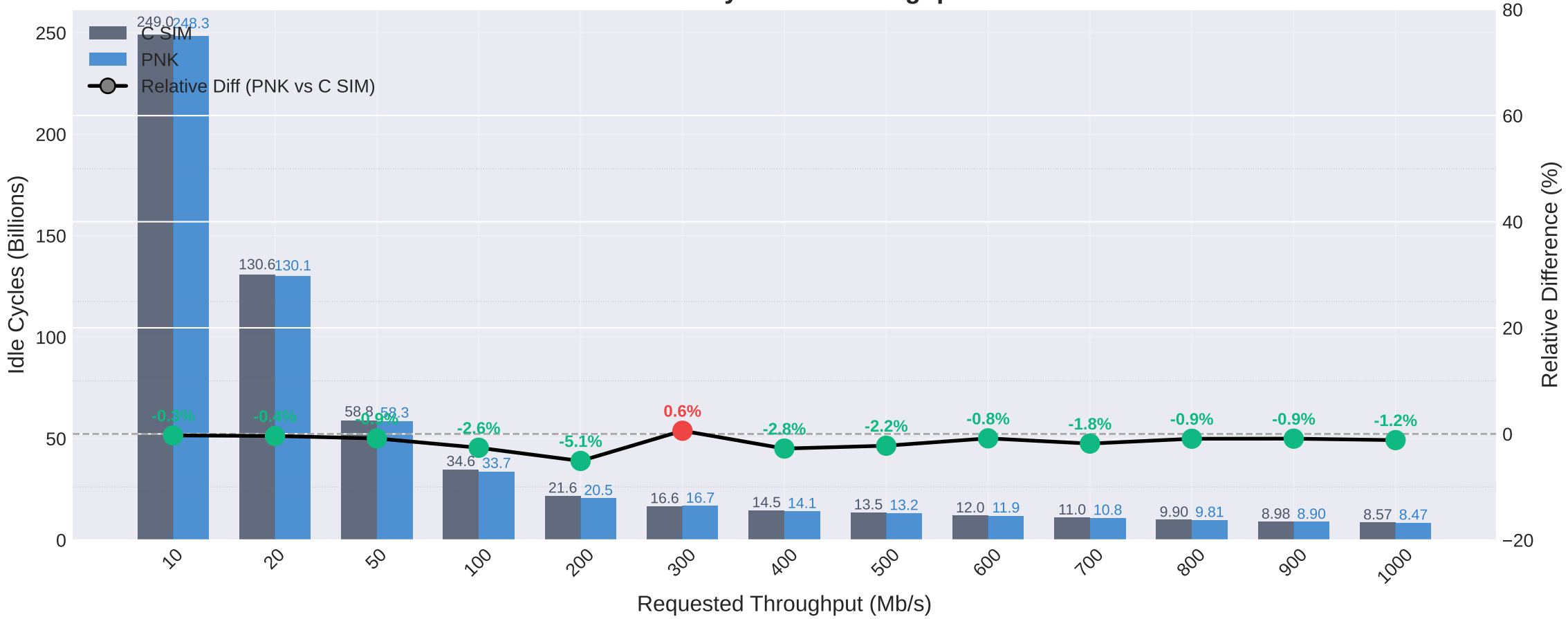
User CPU Cycles vs Throughput



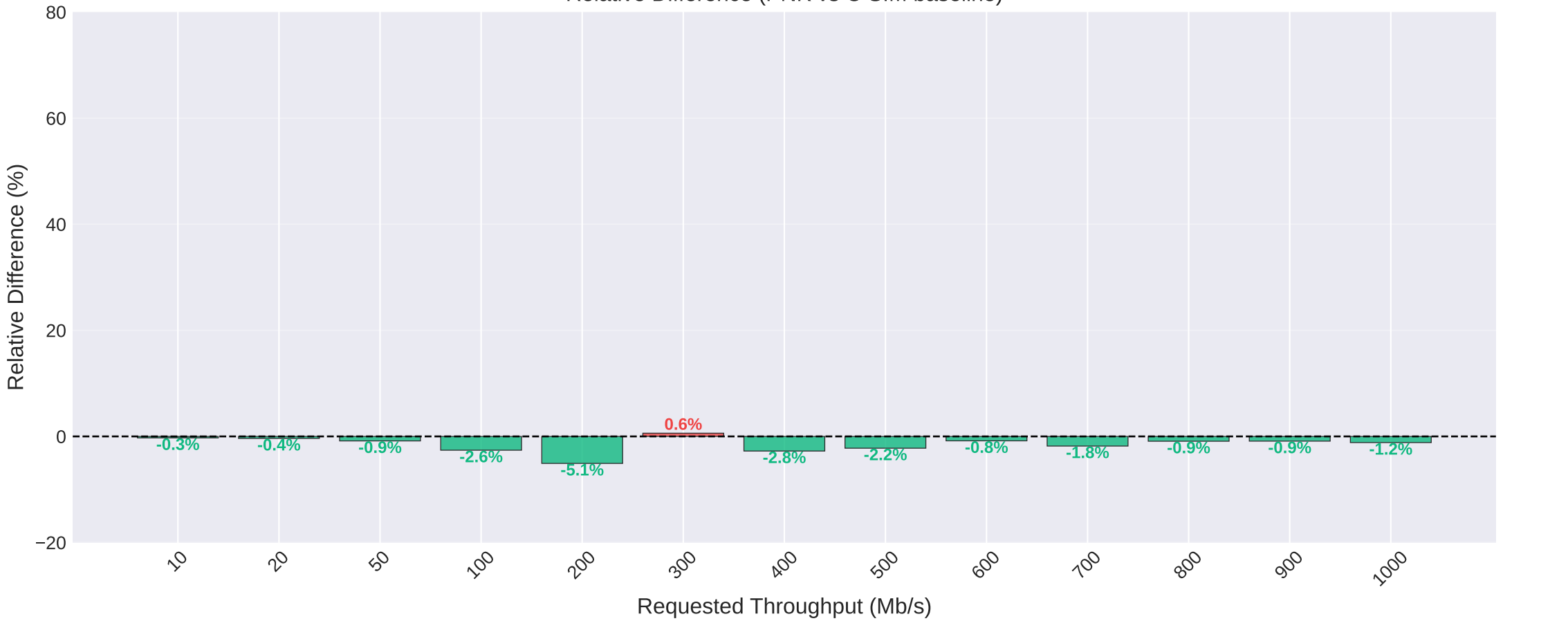
Relative Difference (PNK vs C SIM baseline)



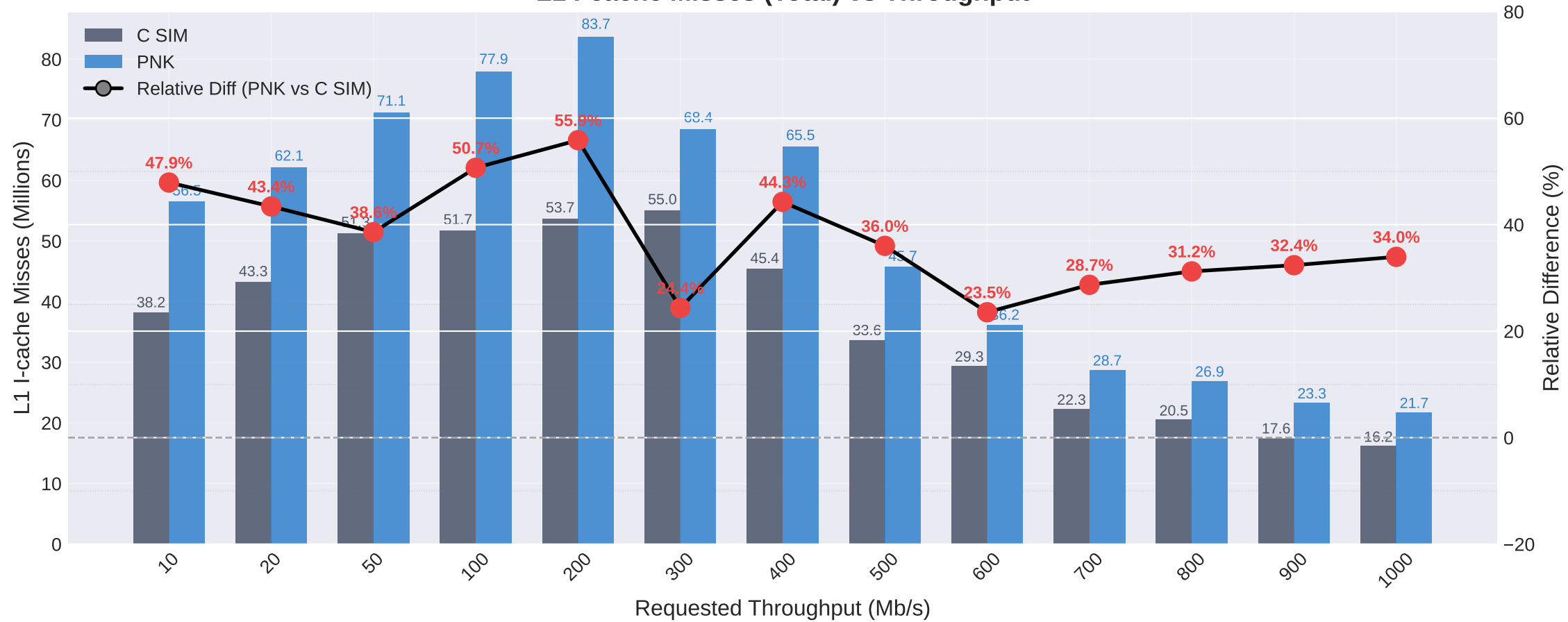
Idle CPU Cycles vs Throughput



Relative Difference (PNK vs C SIM baseline)



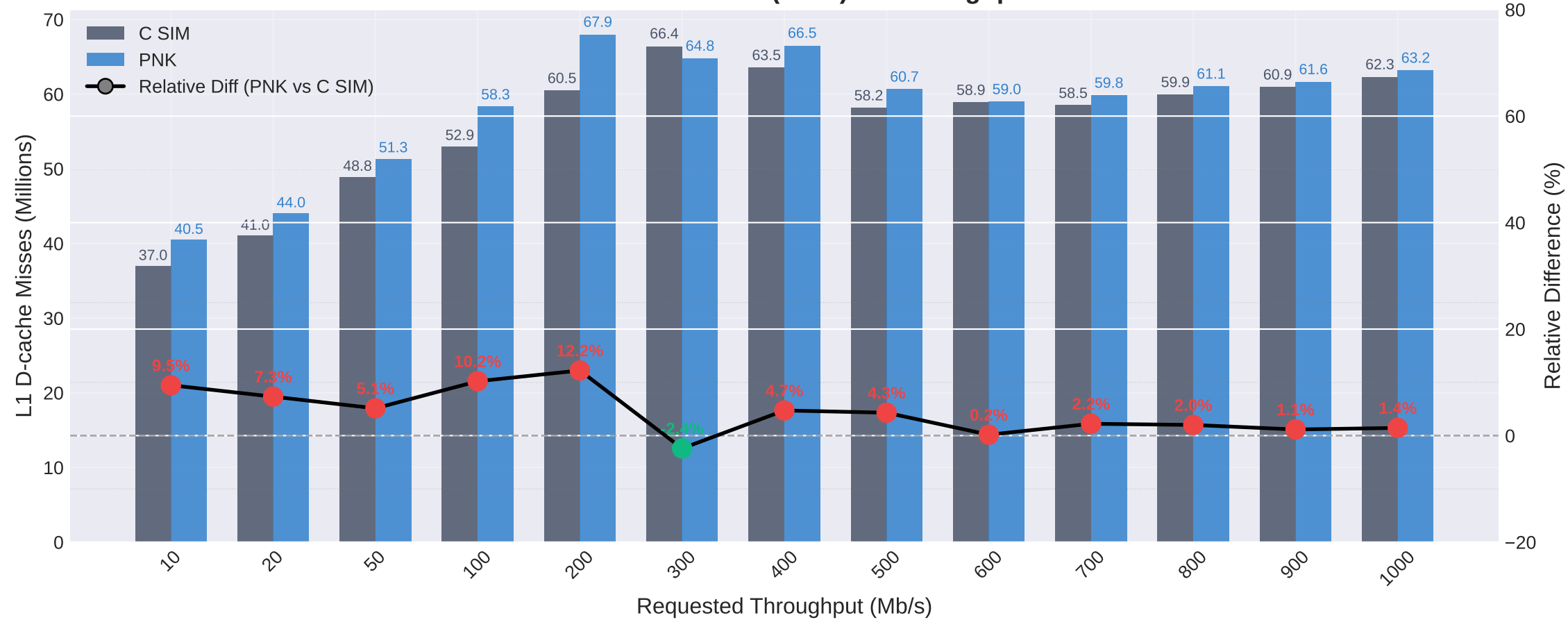
L1 I-cache Misses (Total) vs Throughput



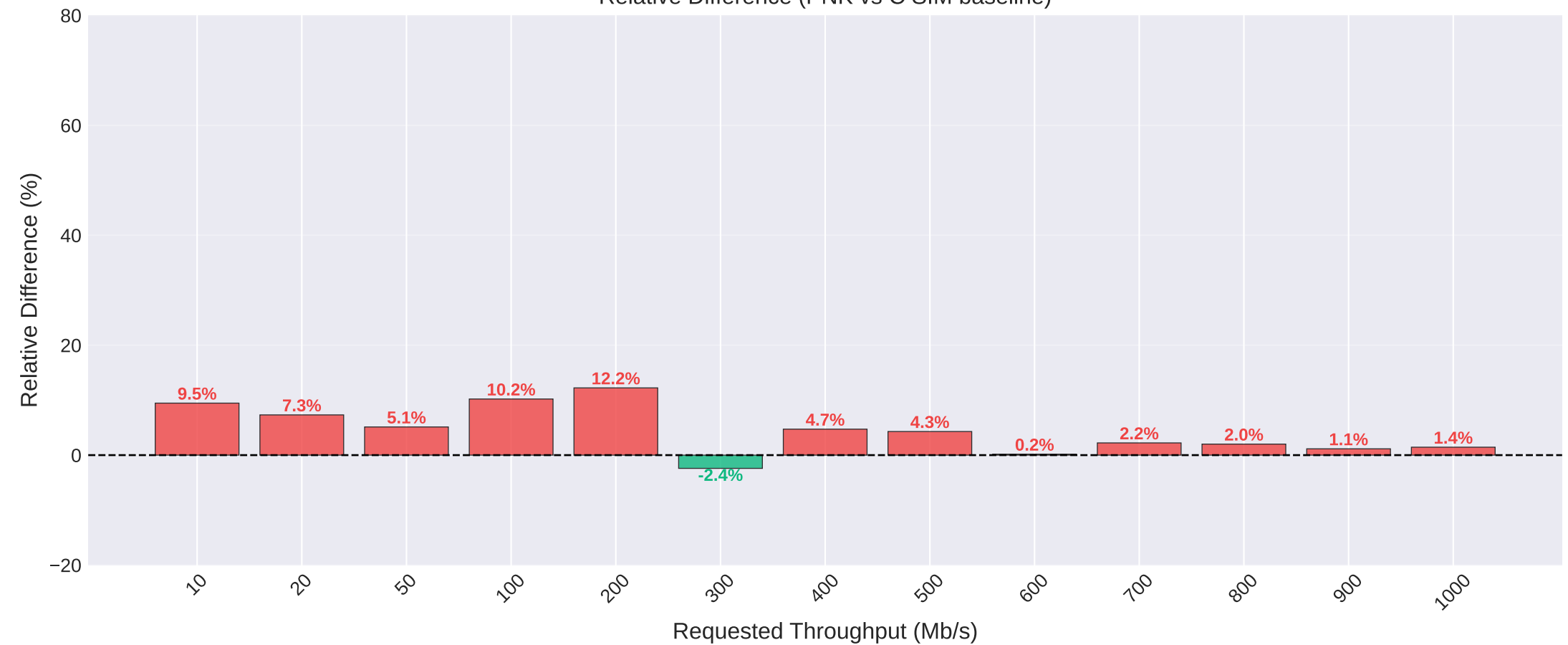
Relative Difference (PNK vs C SIM baseline)



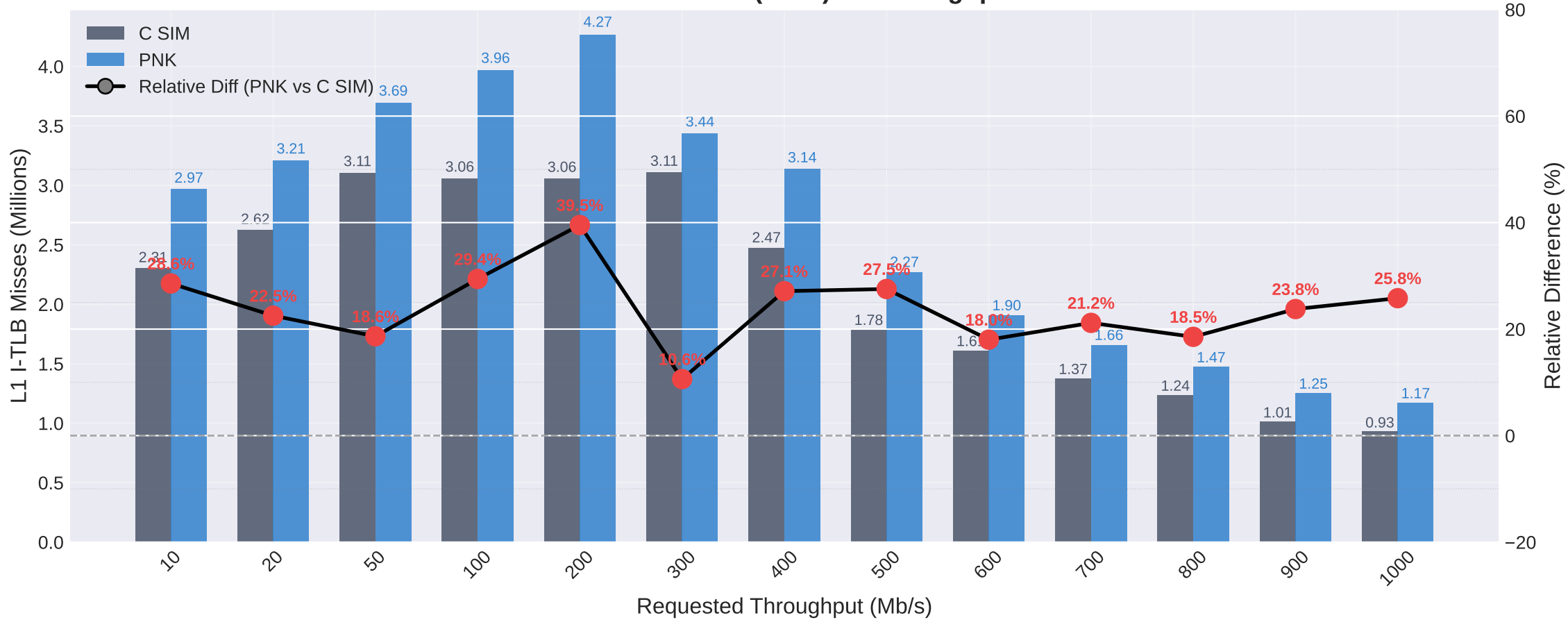
L1 D-cache Misses (Total) vs Throughput



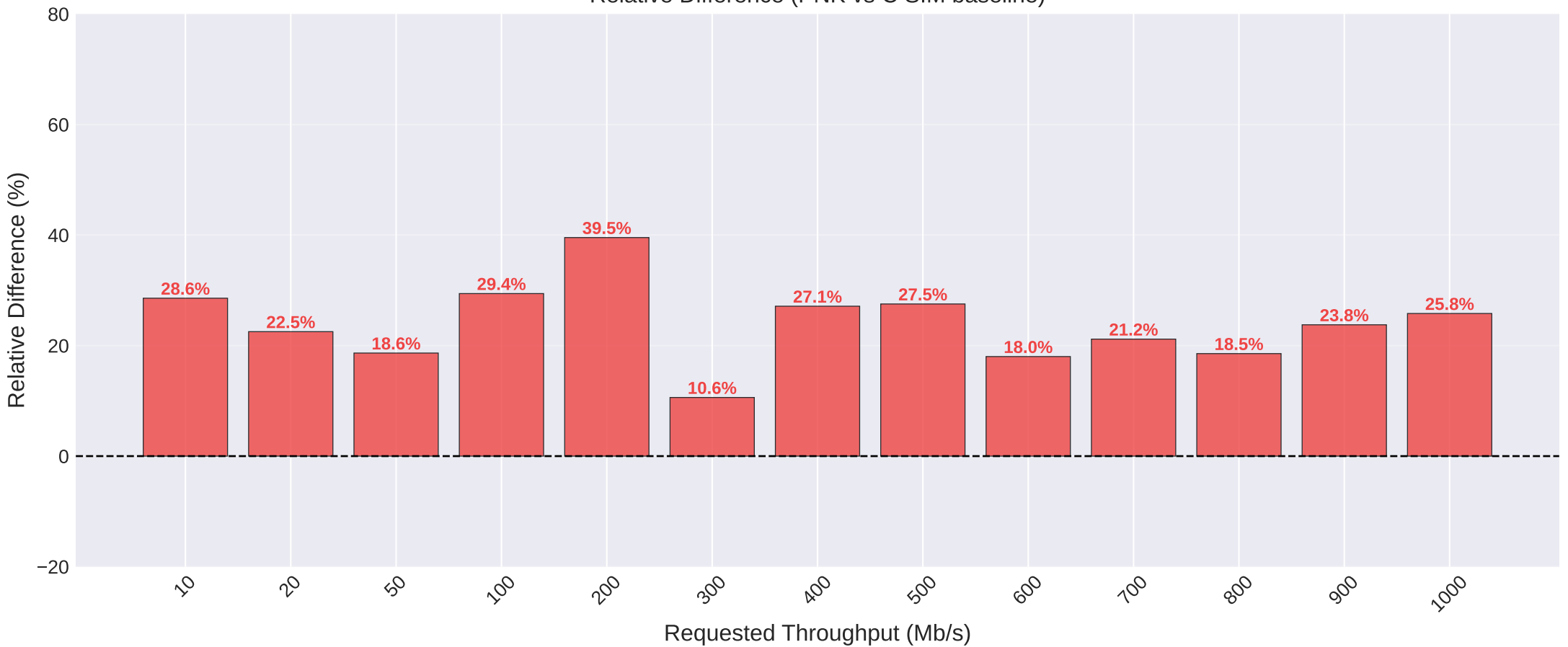
Relative Difference (PNK vs C SIM baseline)



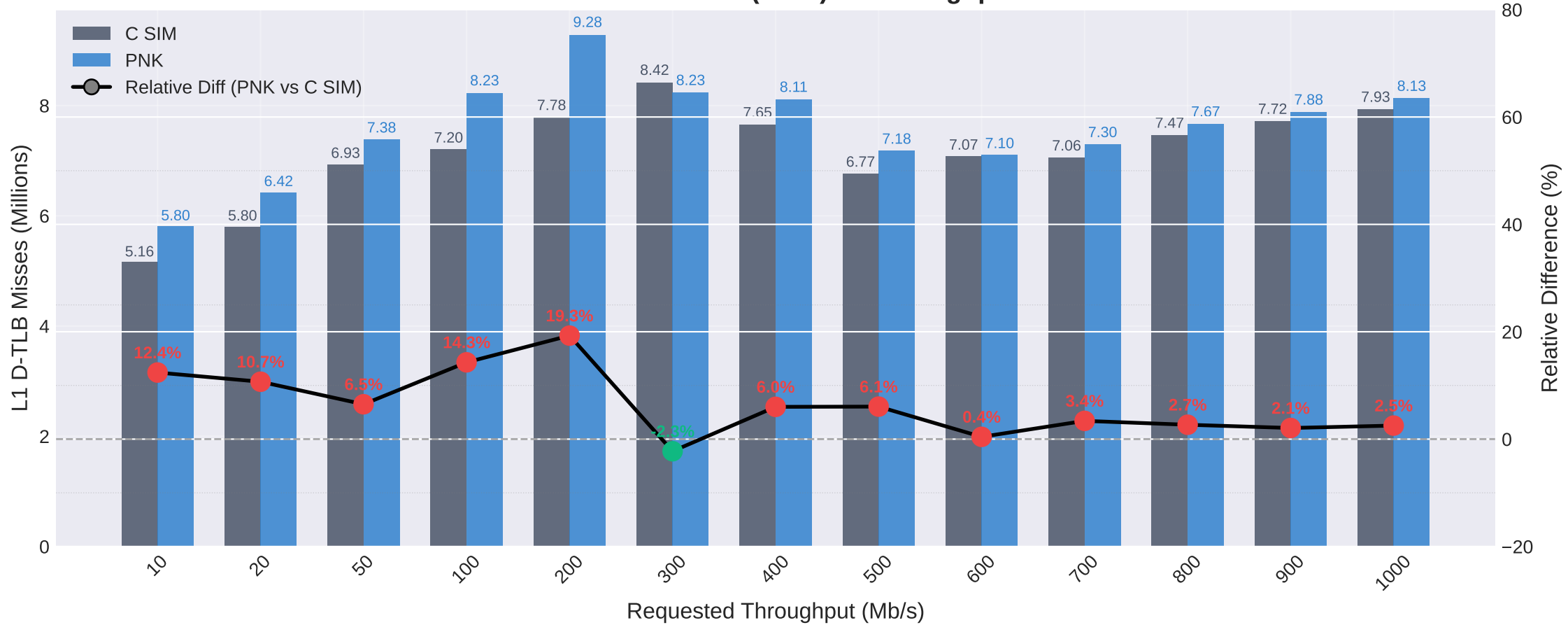
L1 I-TLB Misses (Total) vs Throughput



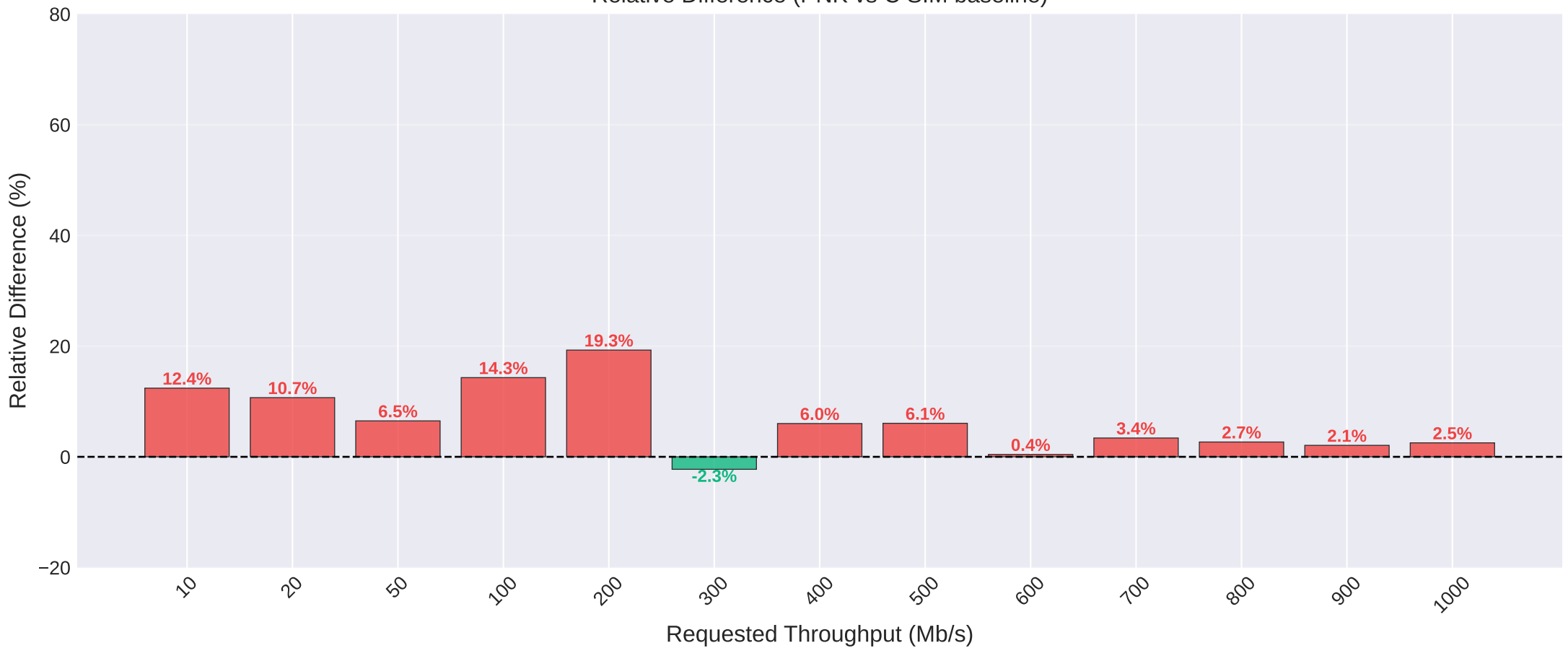
Relative Difference (PNK vs C SIM baseline)



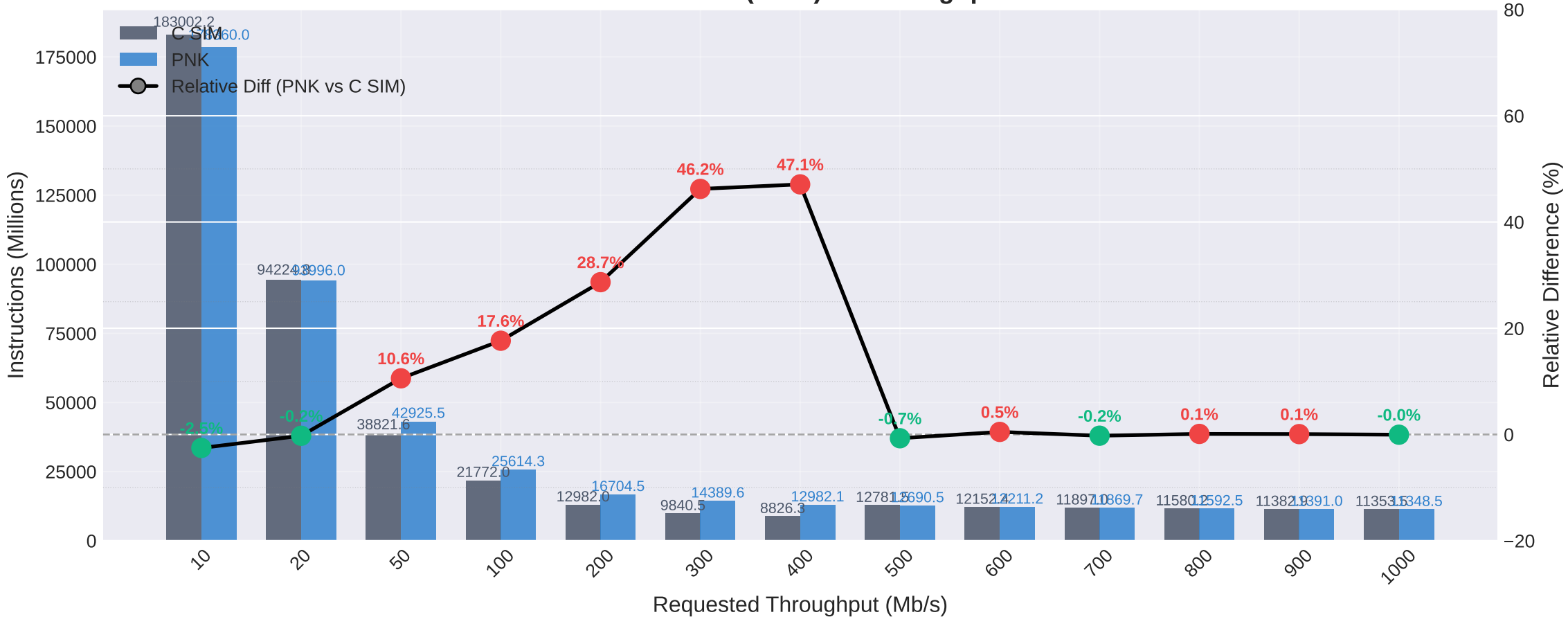
L1 D-TLB Misses (Total) vs Throughput



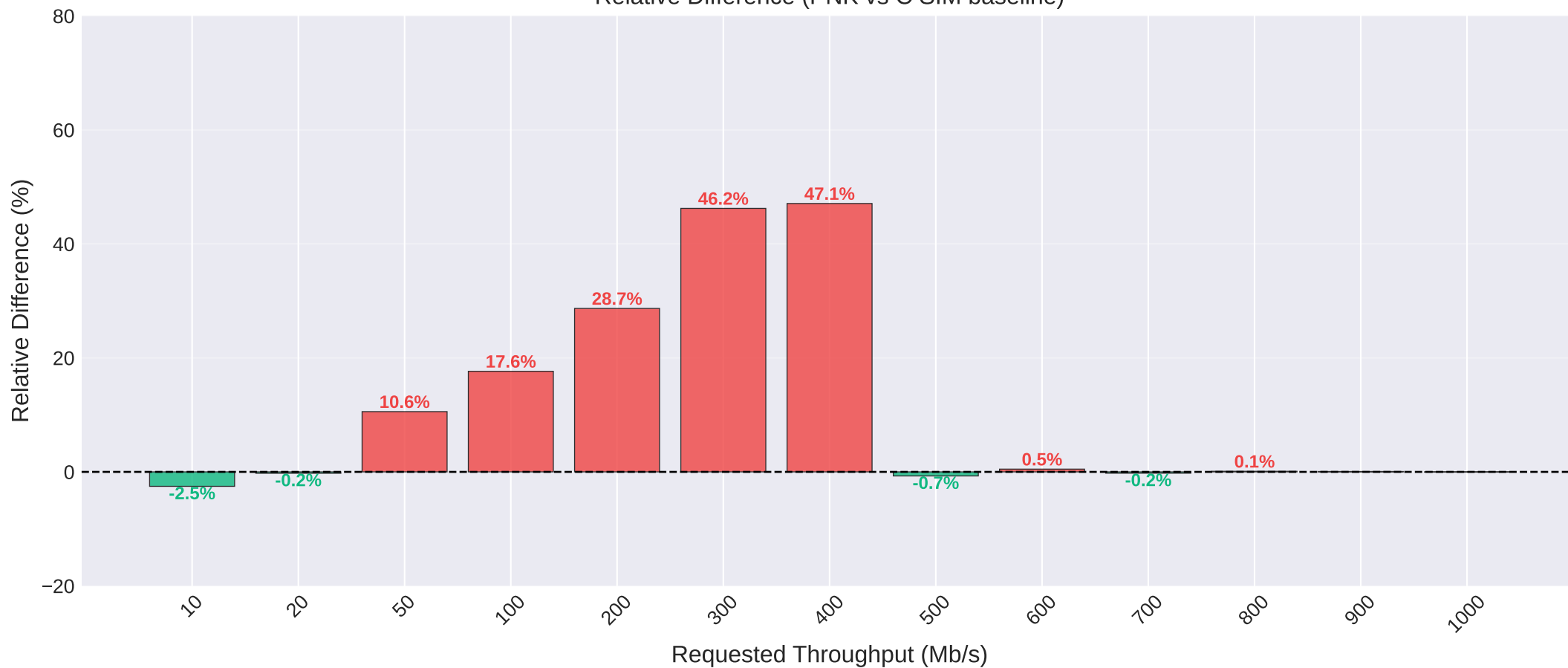
Relative Difference (PNK vs C SIM baseline)



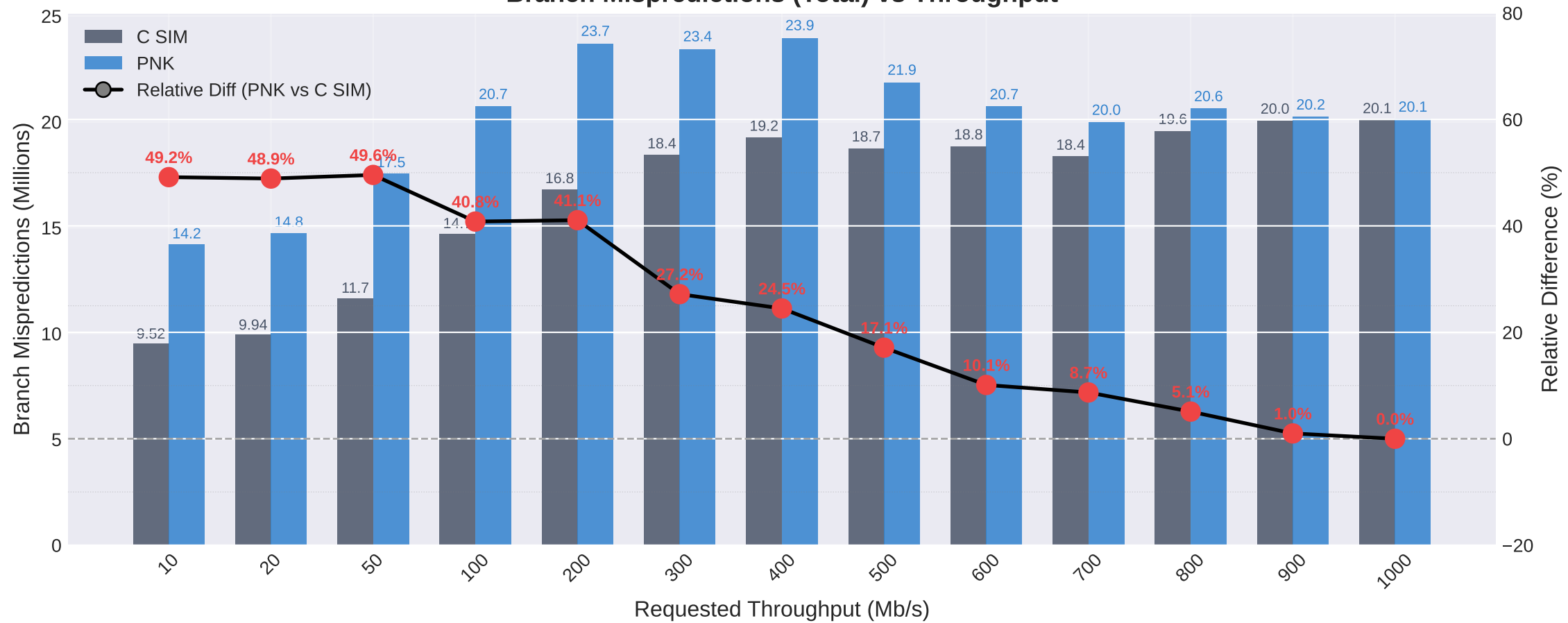
Instructions (Total) vs Throughput



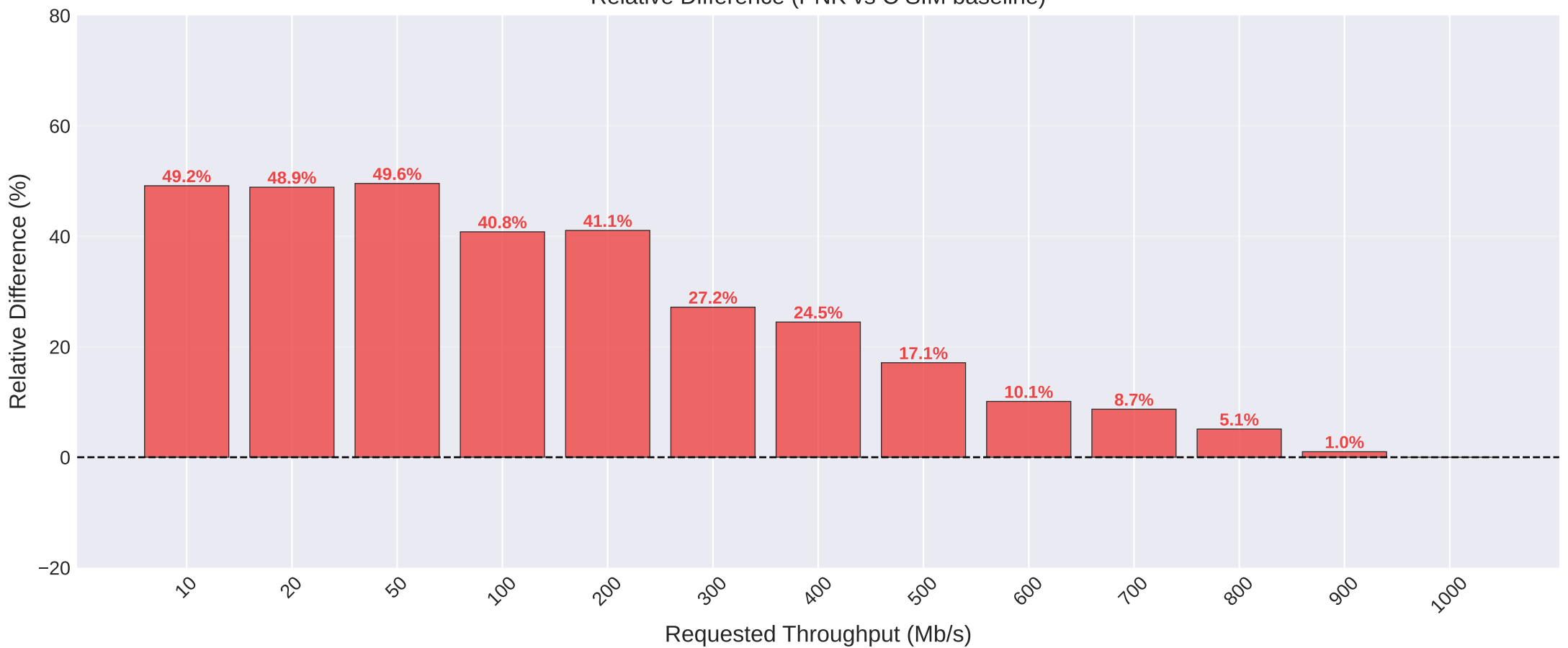
Relative Difference (PNK vs C SIM baseline)



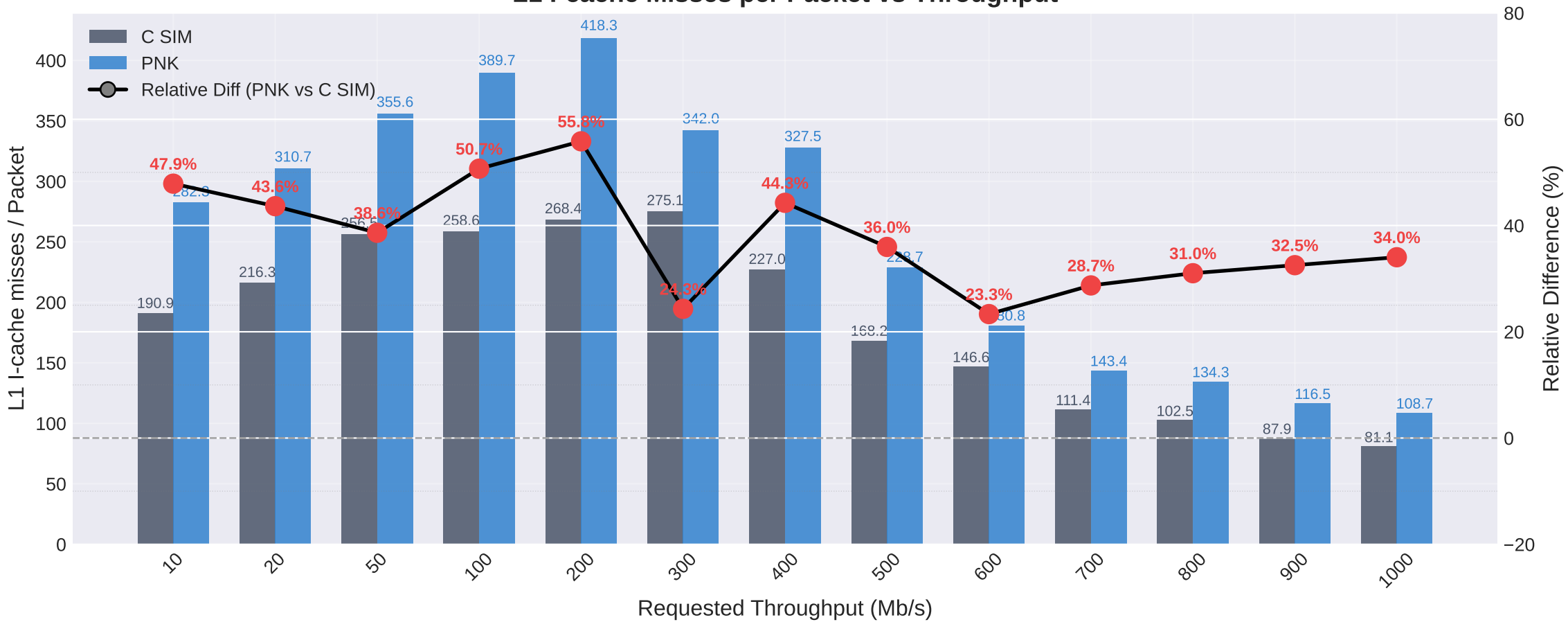
Branch Mispredictions (Total) vs Throughput



Relative Difference (PNK vs C SIM baseline)



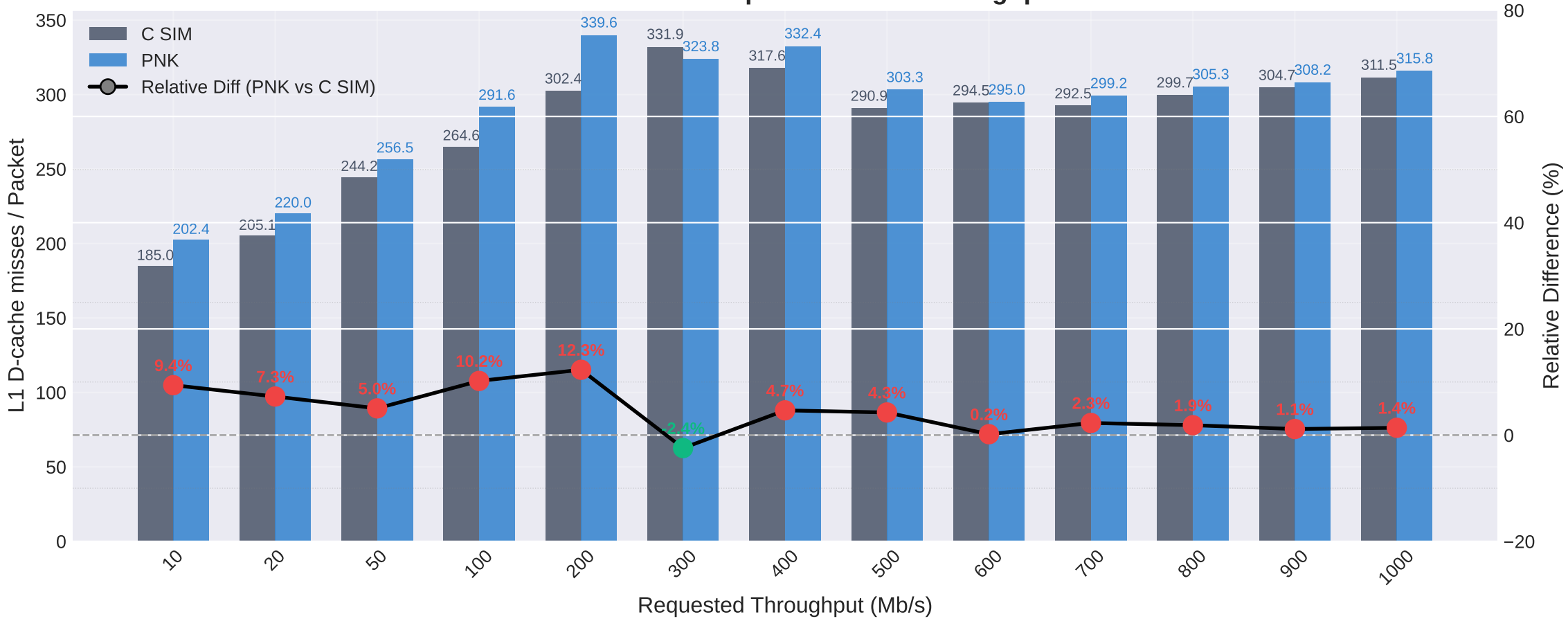
L1 I-cache Misses per Packet vs Throughput



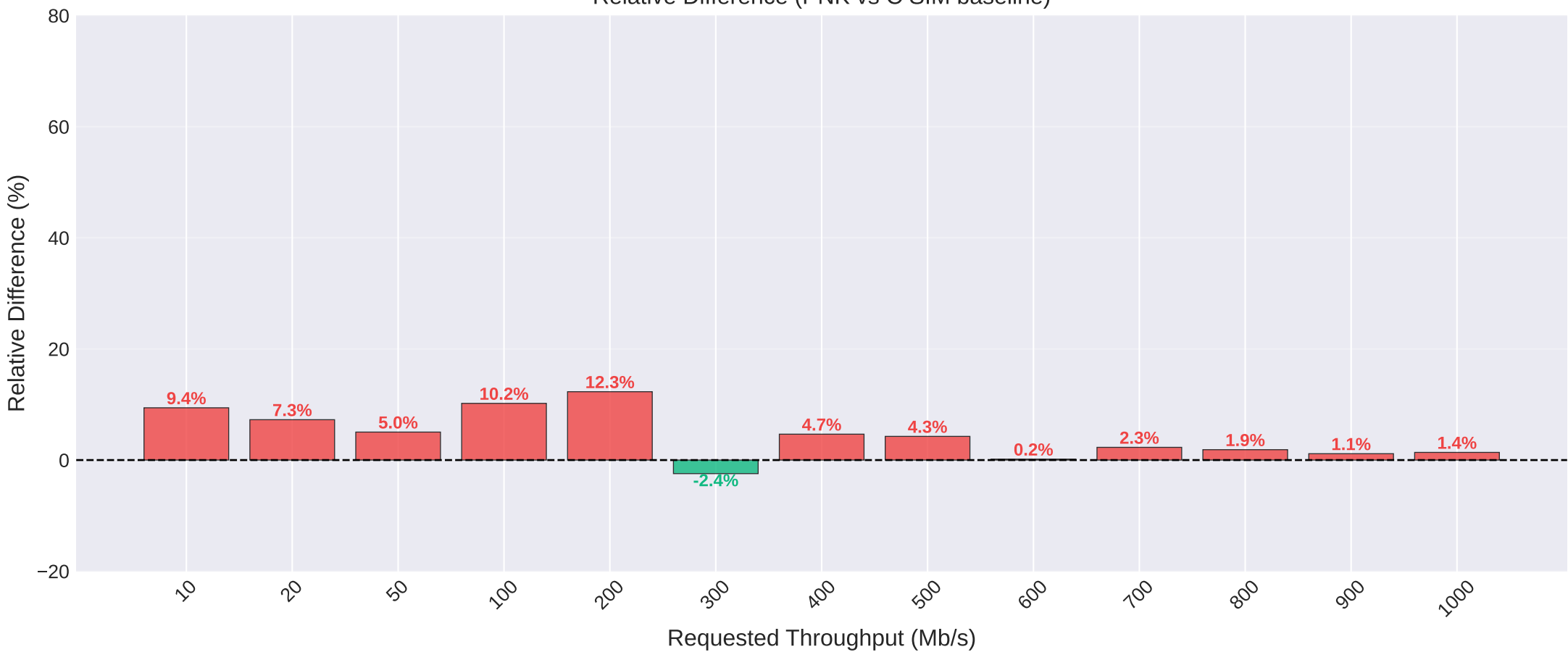
Relative Difference (PNK vs C SIM baseline)



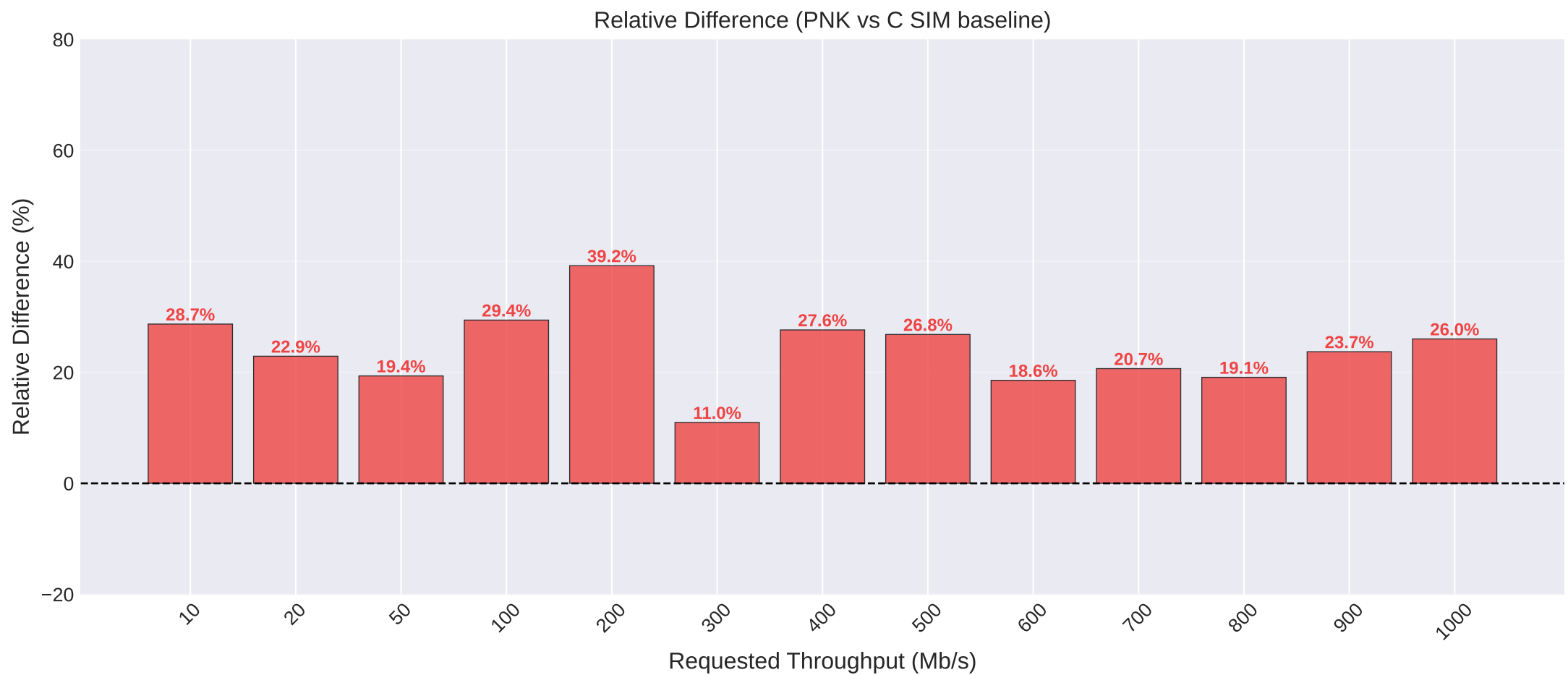
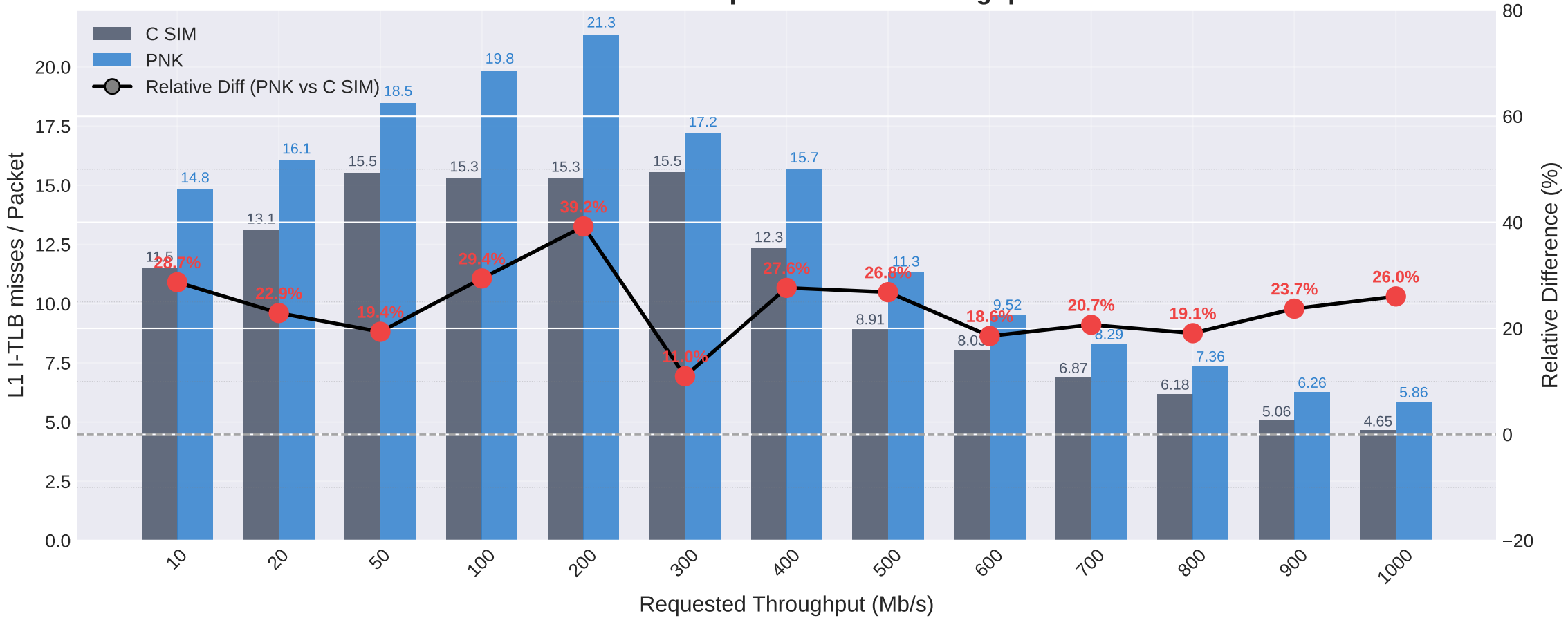
L1 D-cache Misses per Packet vs Throughput



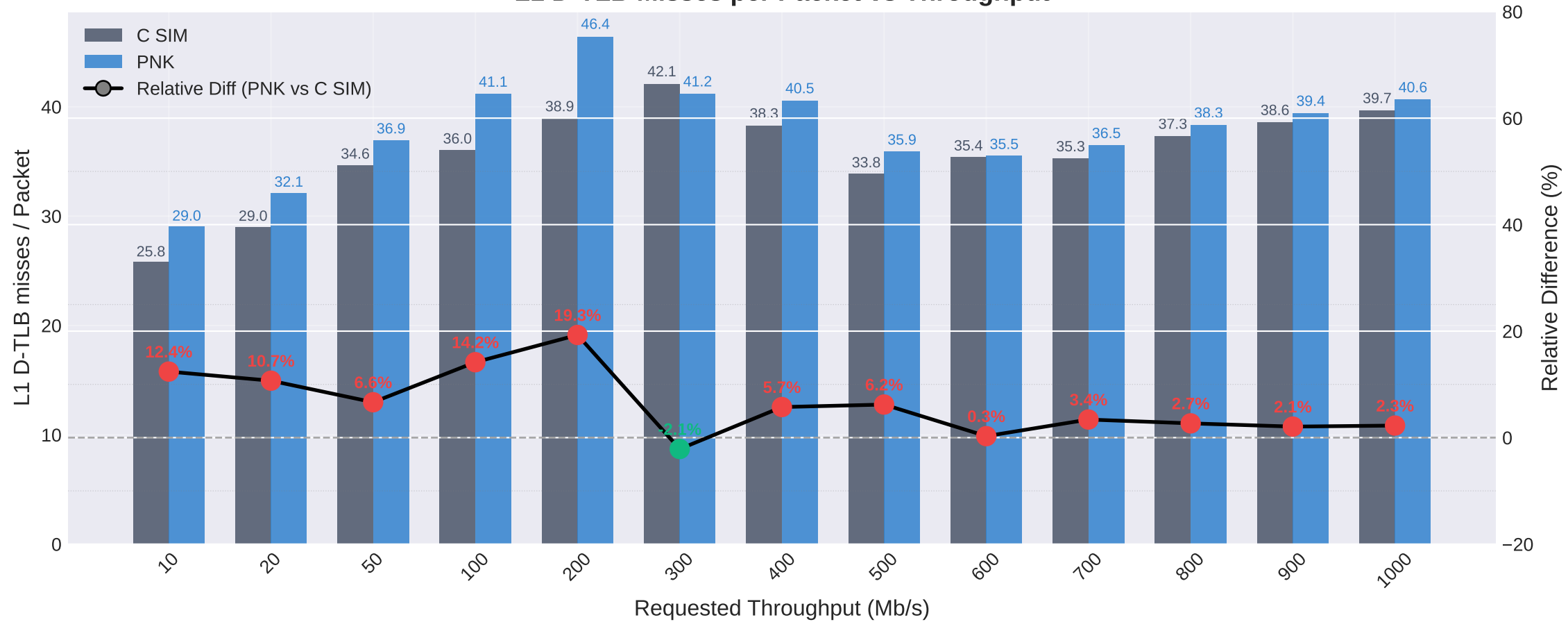
Relative Difference (PNK vs C SIM baseline)



L1 I-TLB Misses per Packet vs Throughput



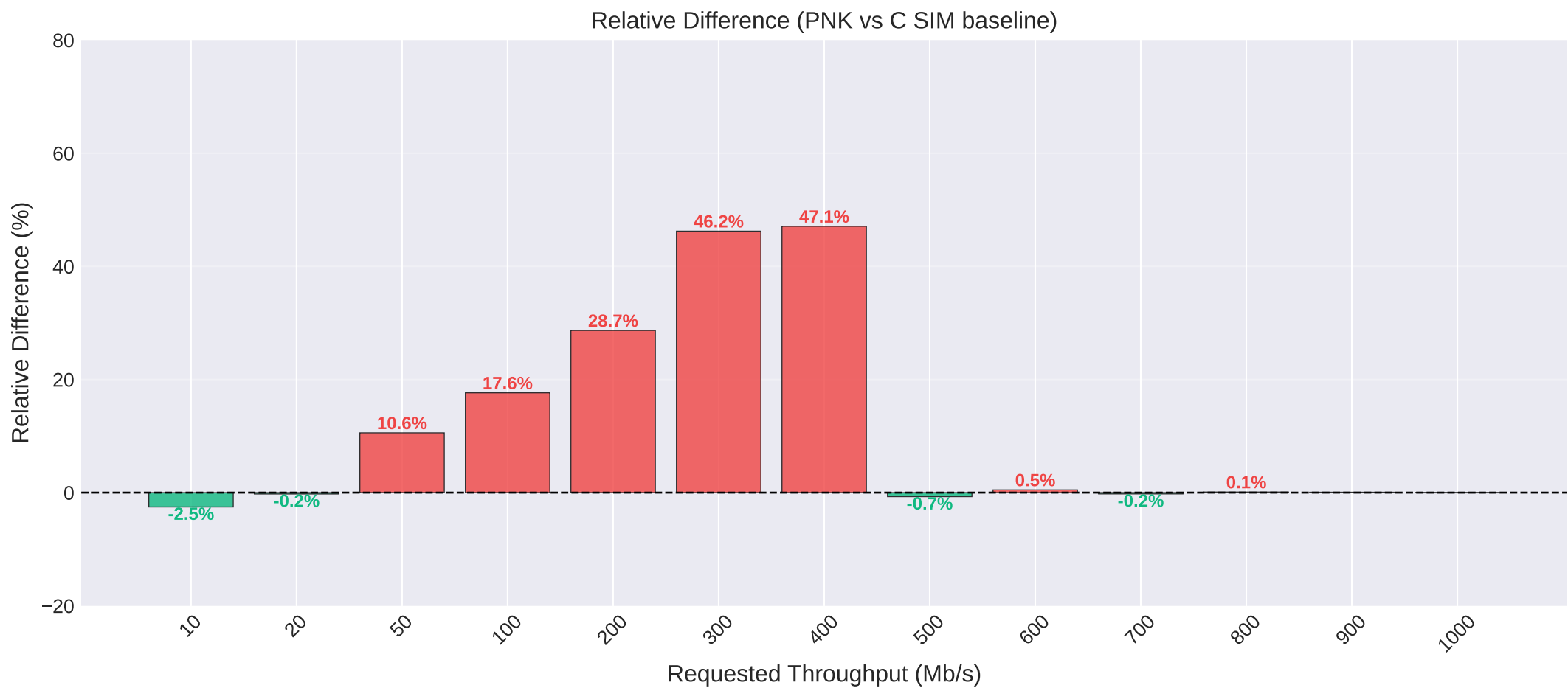
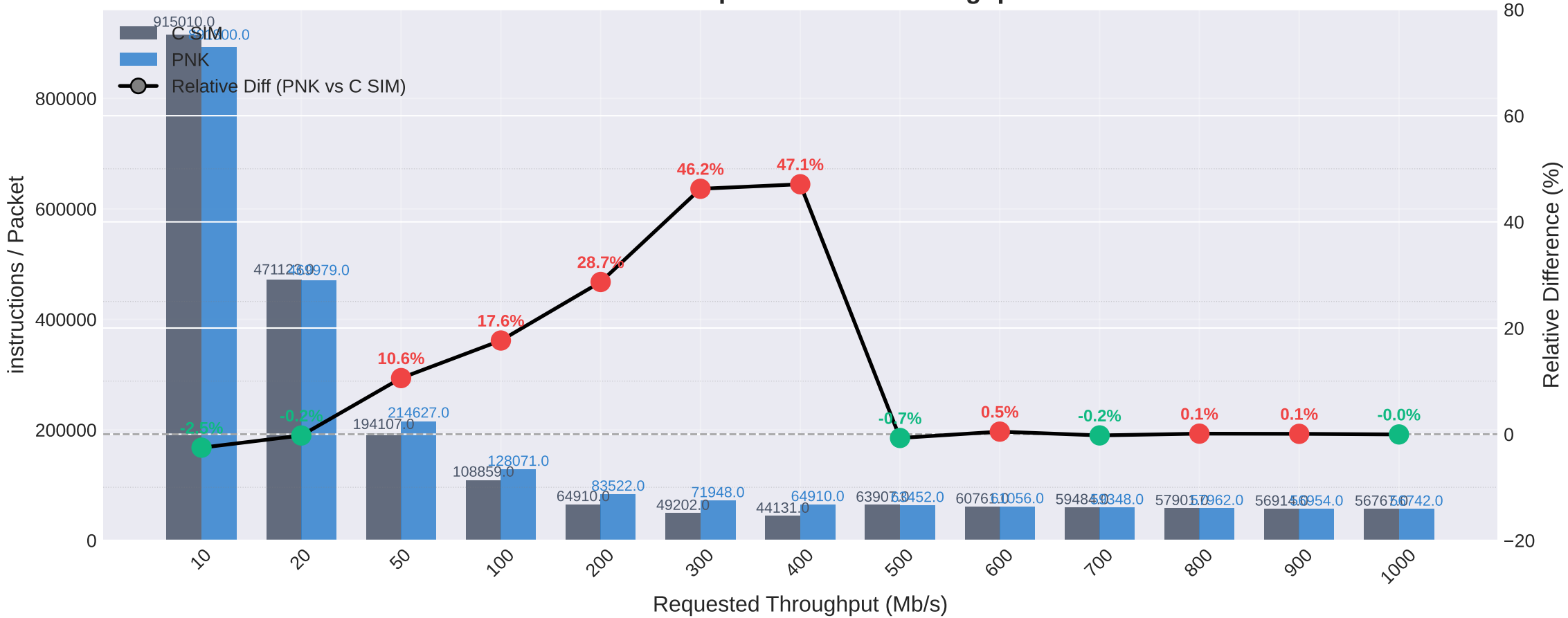
L1 D-TLB Misses per Packet vs Throughput



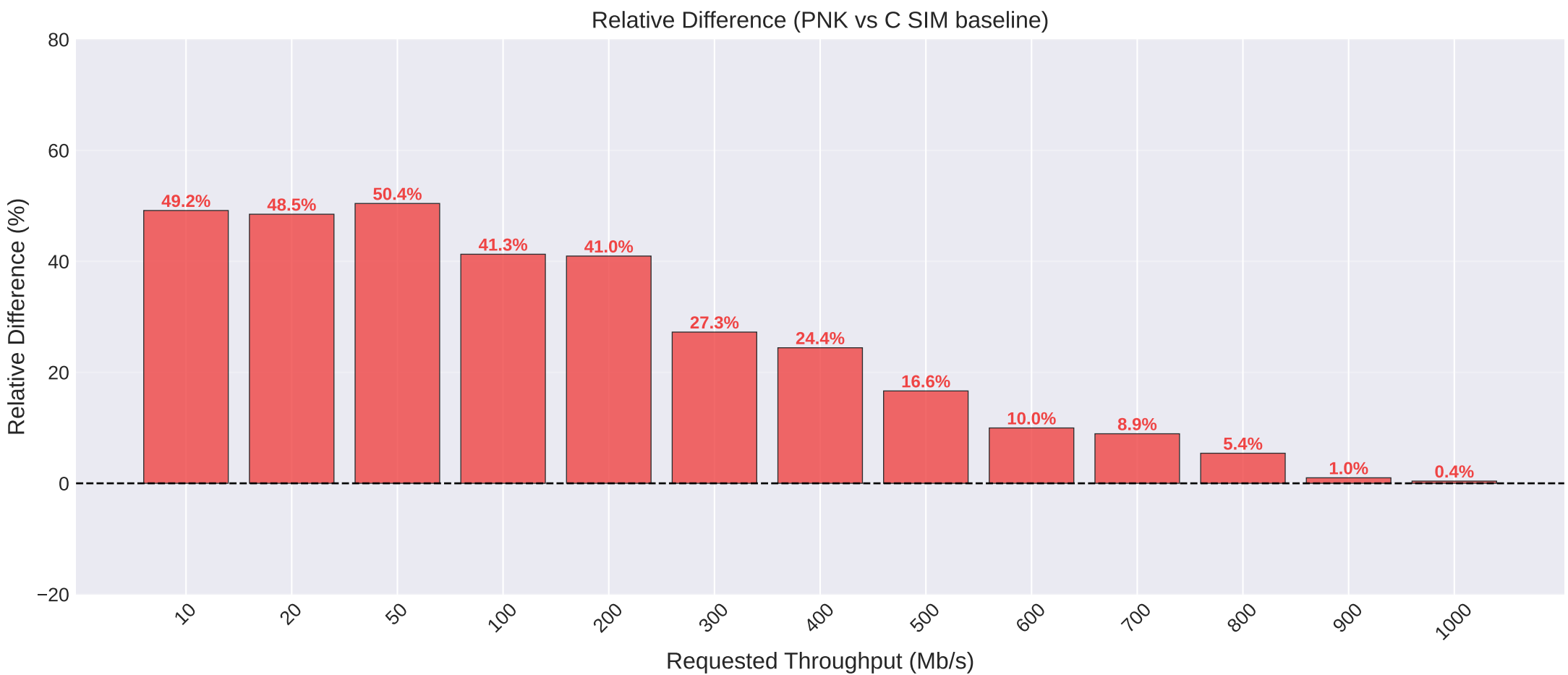
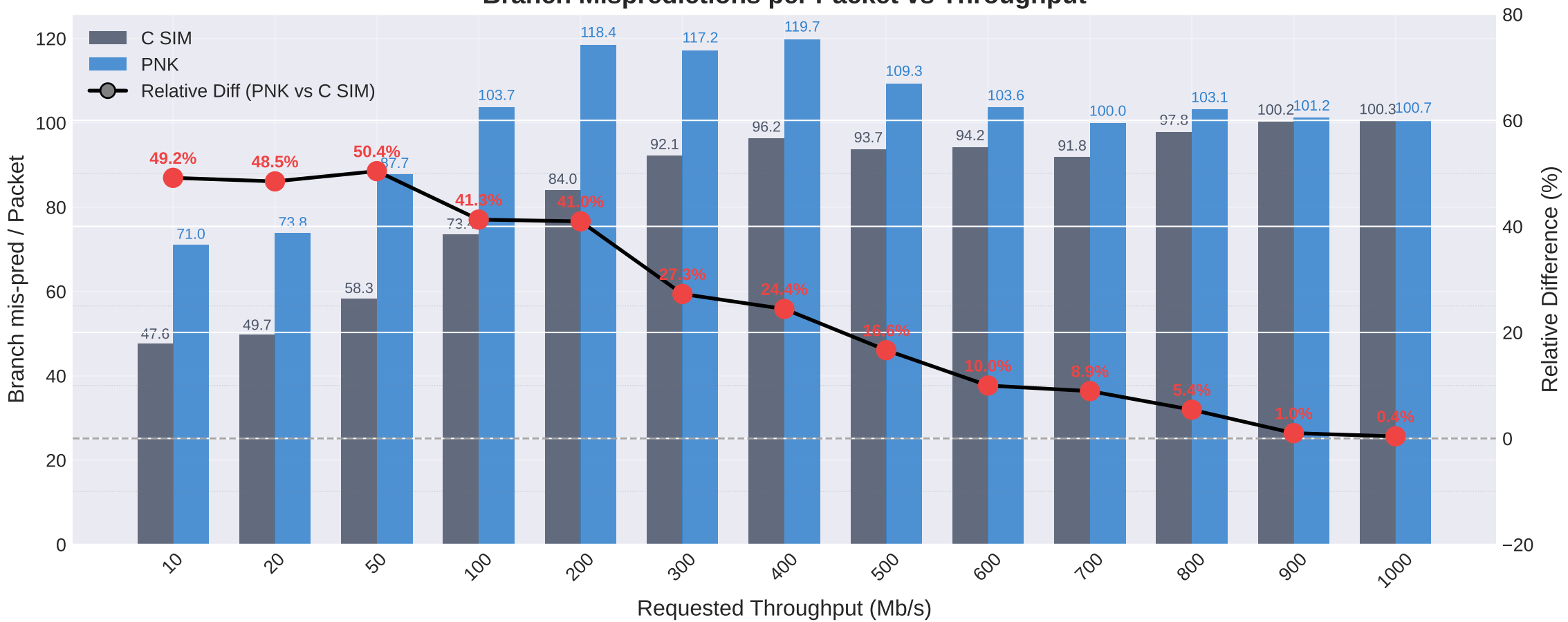
Relative Difference (PNK vs C SIM baseline)



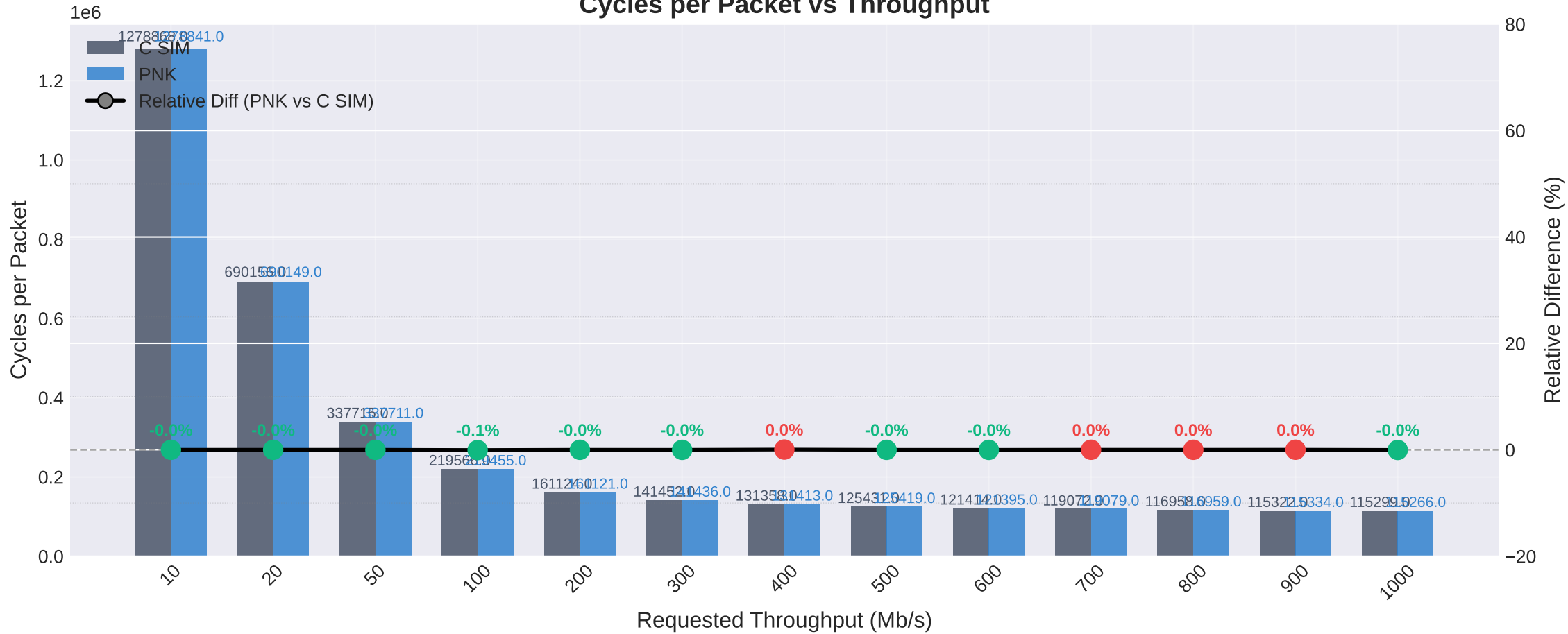
Instructions per Packet vs Throughput



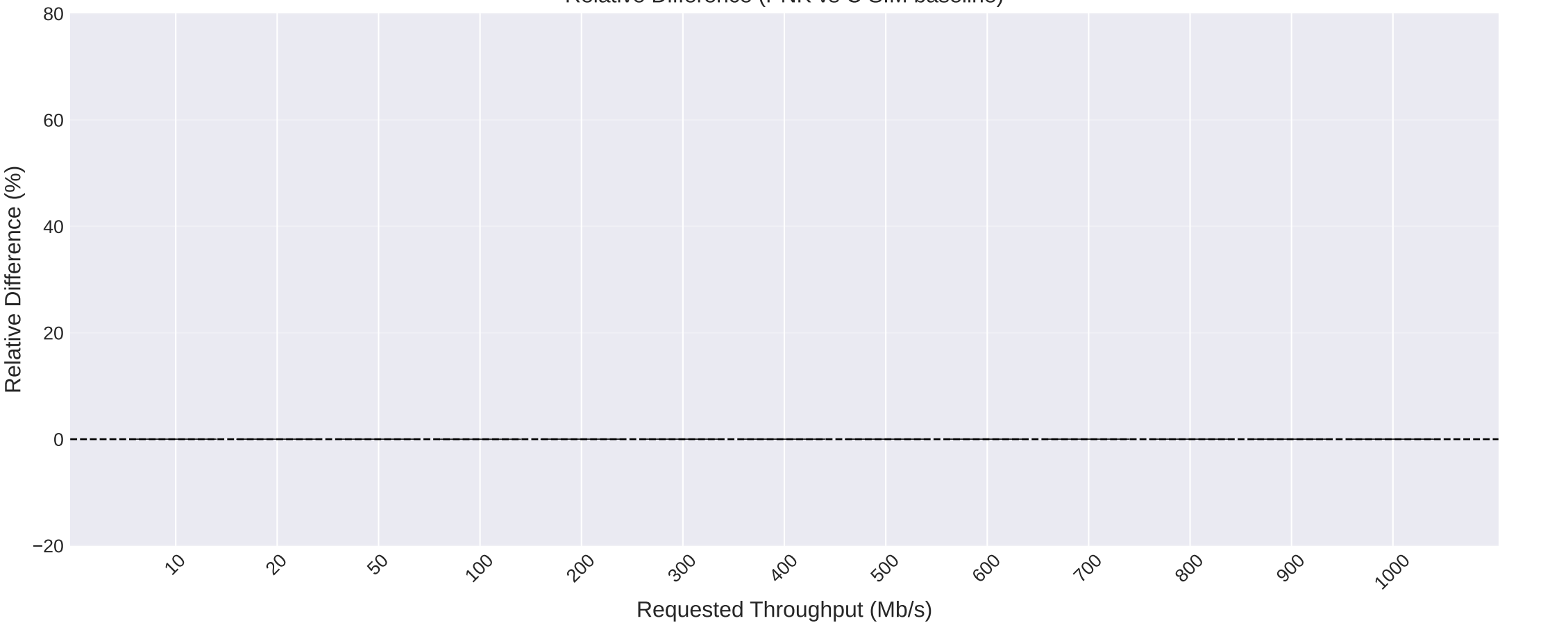
Branch Mispredictions per Packet vs Throughput



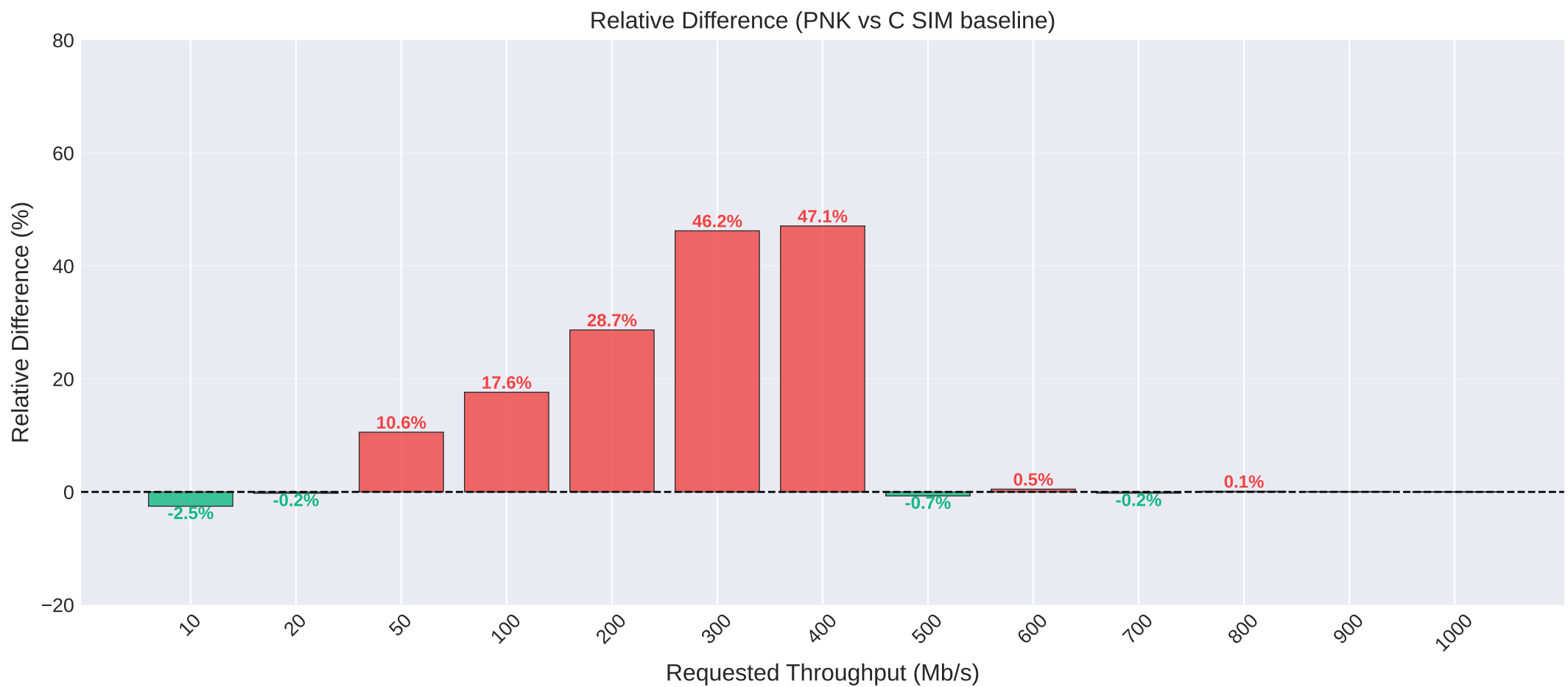
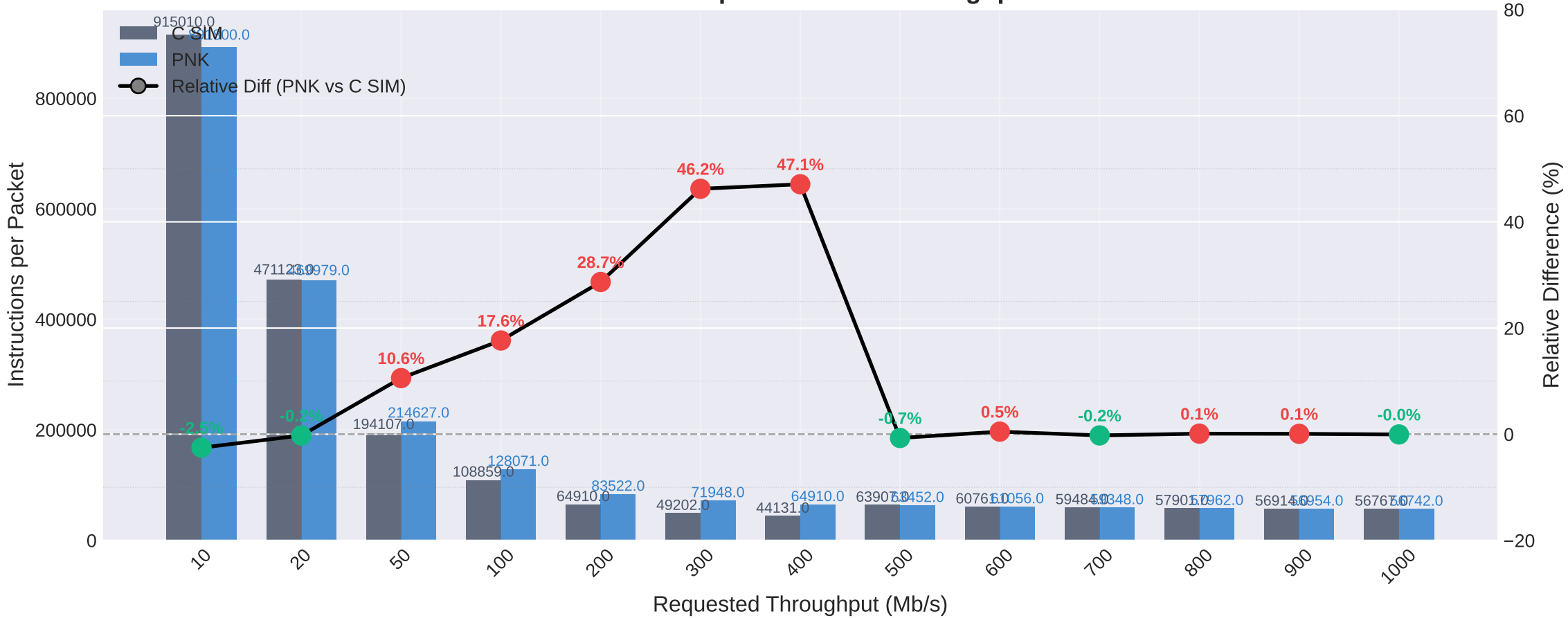
Cycles per Packet vs Throughput



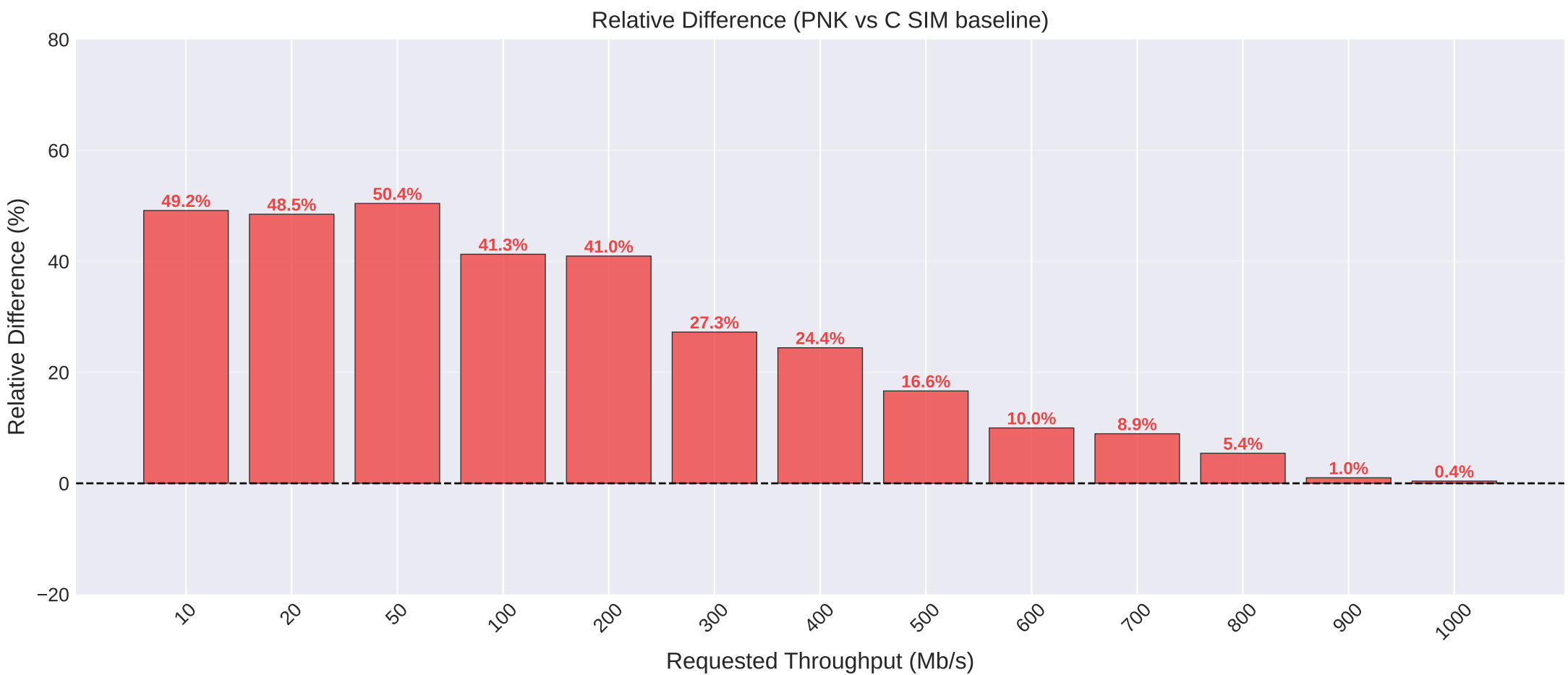
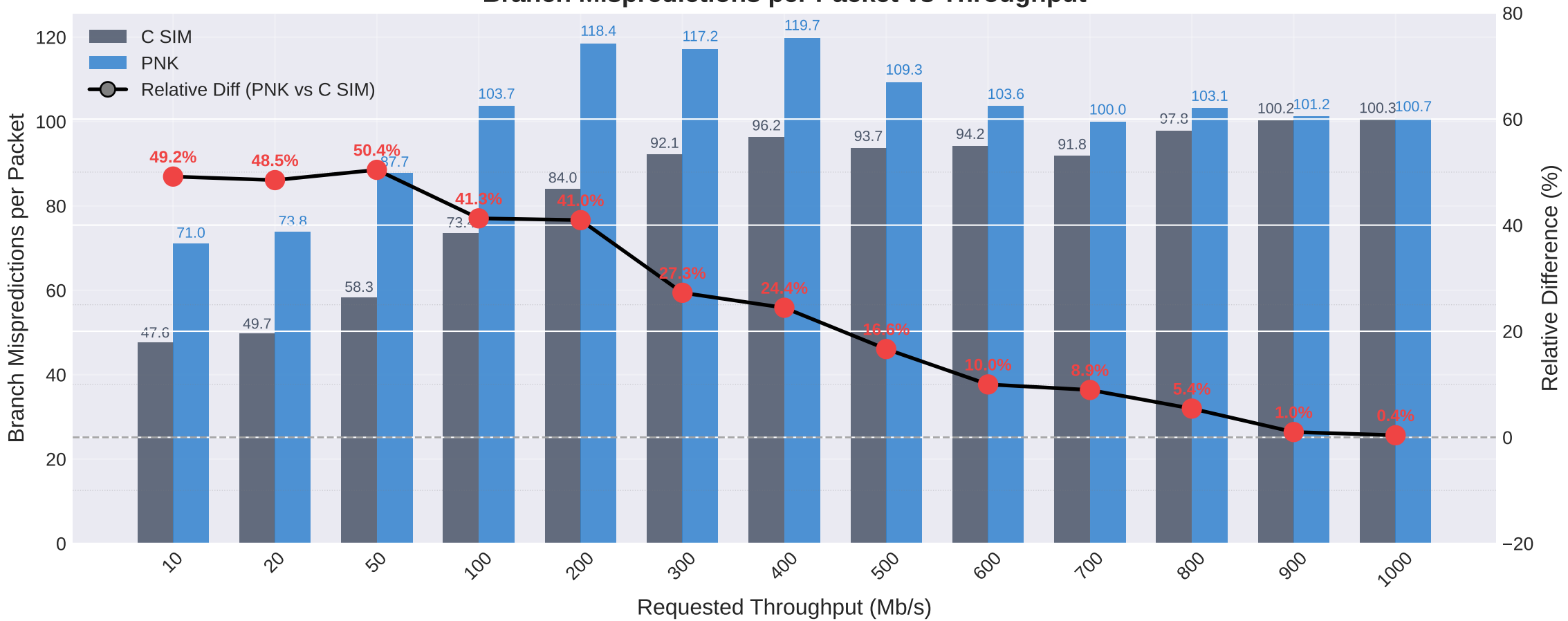
Relative Difference (PNK vs C SIM baseline)



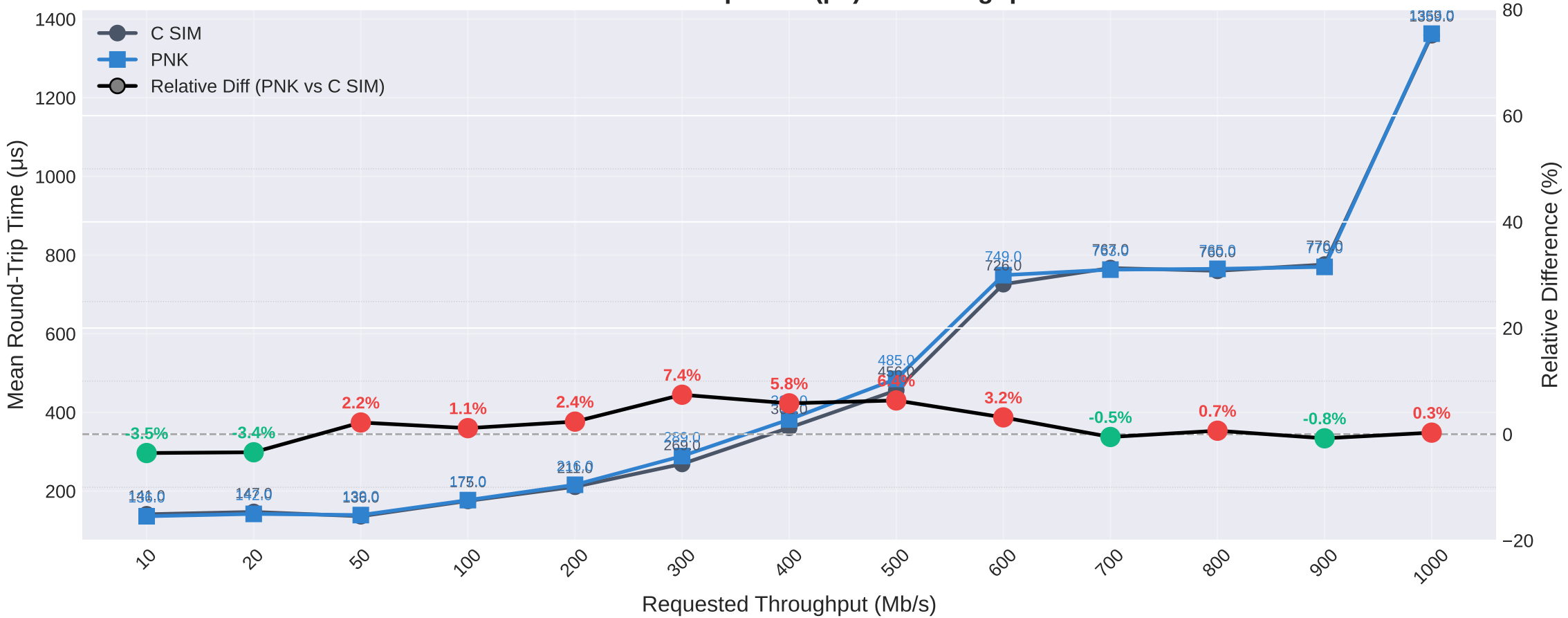
Instructions per Packet vs Throughput



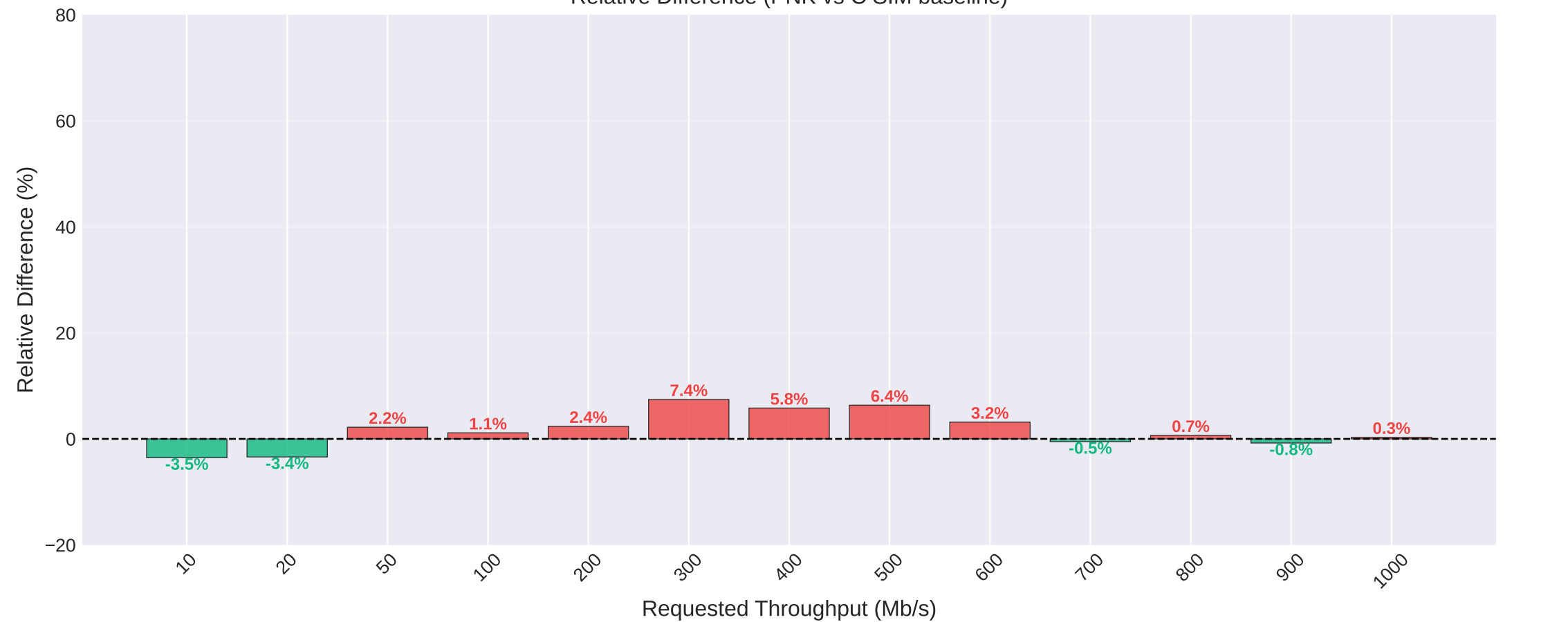
Branch Mispredictions per Packet vs Throughput



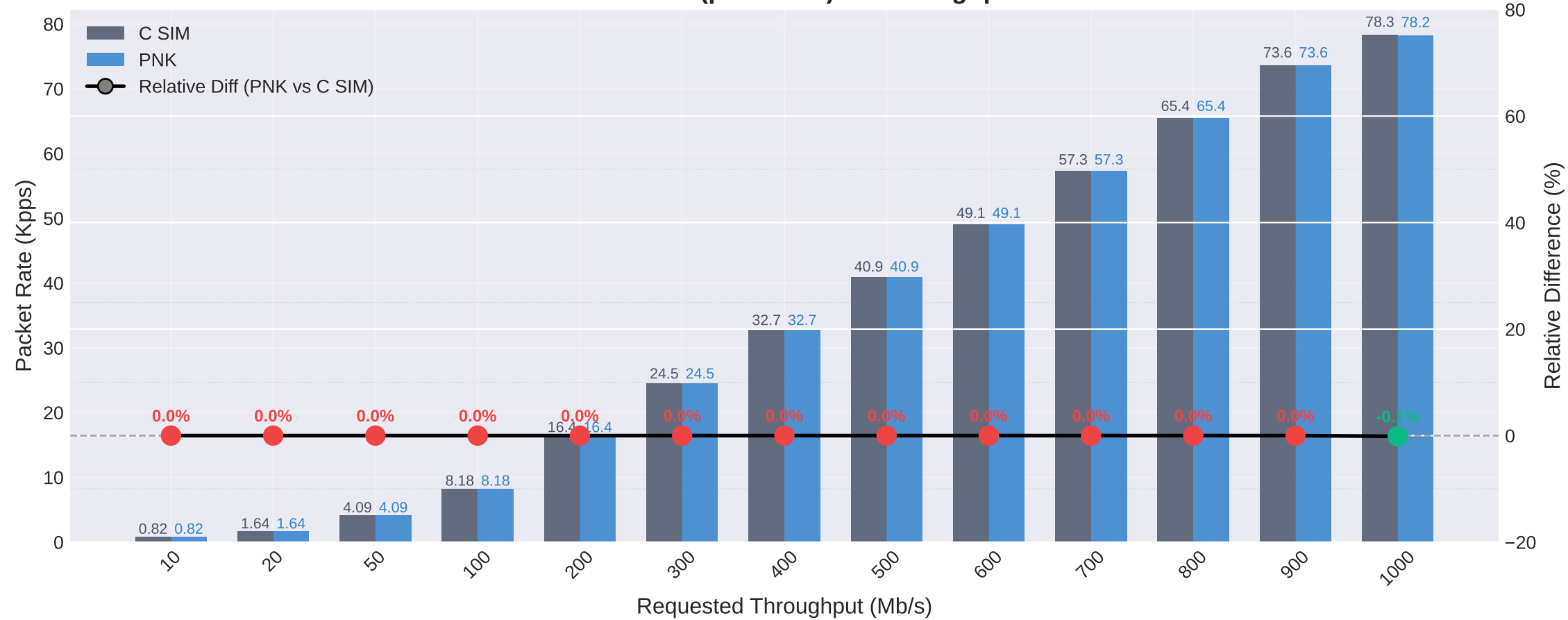
Mean Round-Trip Time (μ s) vs Throughput



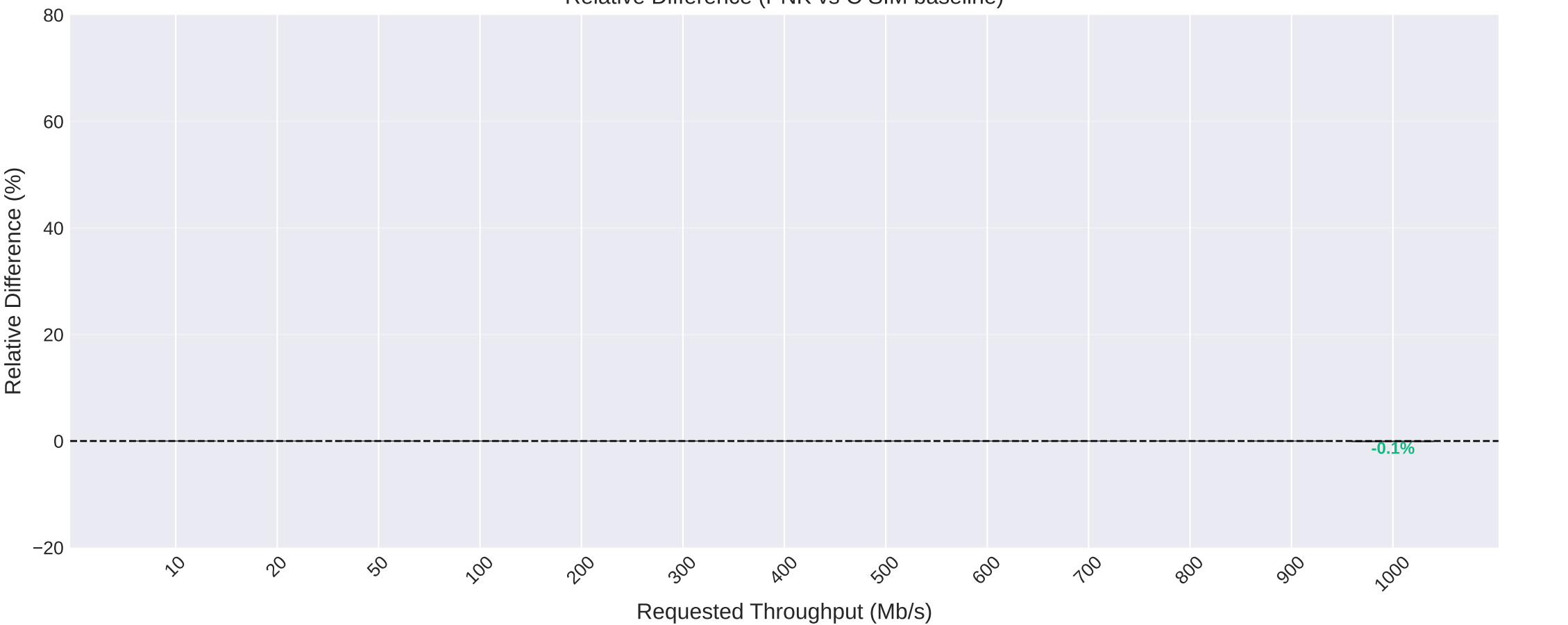
Relative Difference (PNK vs C SIM baseline)



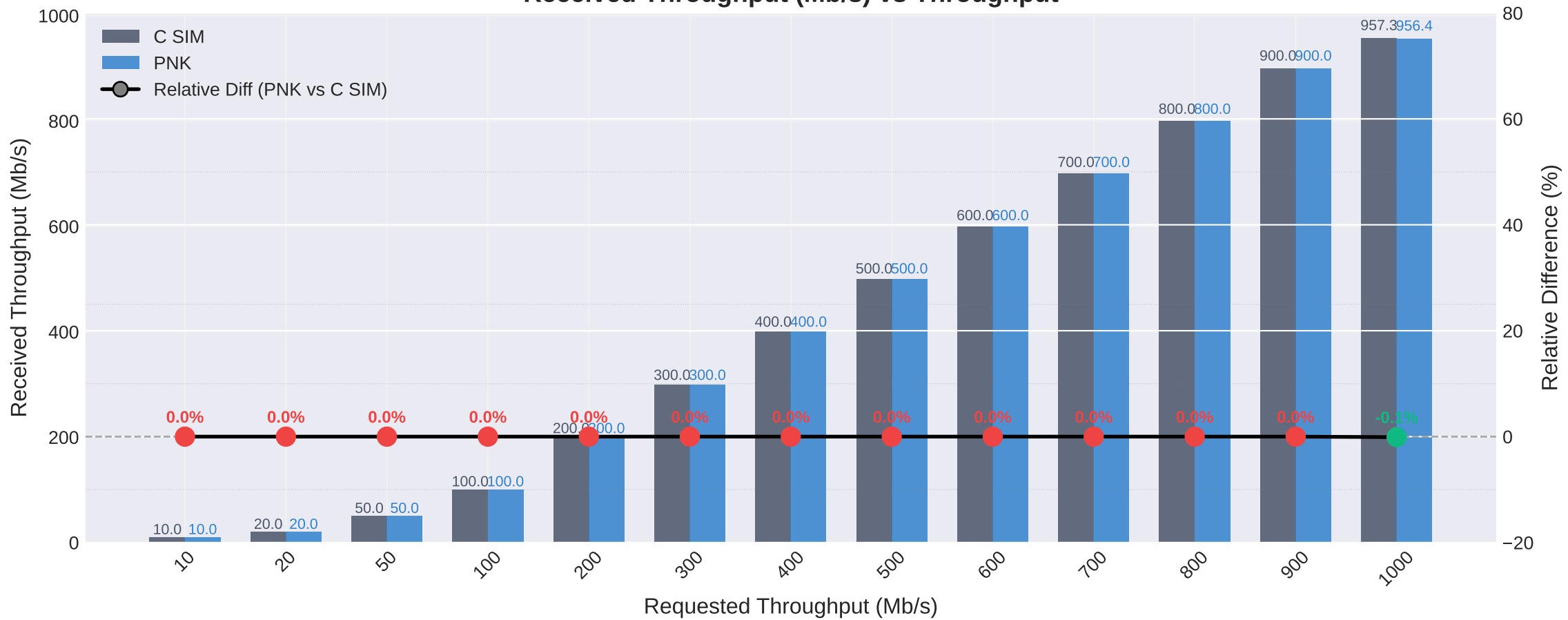
Packet Rate (packets/s) vs Throughput



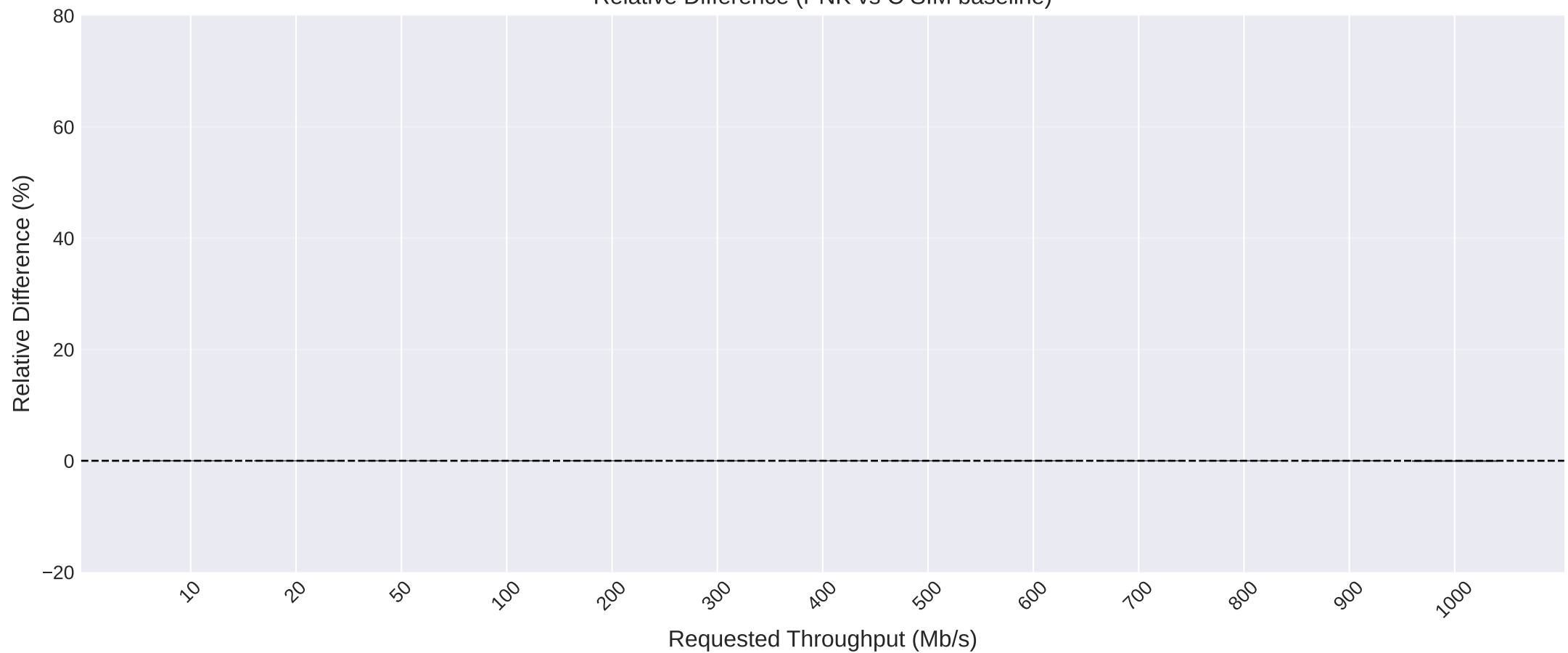
Relative Difference (PNK vs C SIM baseline)



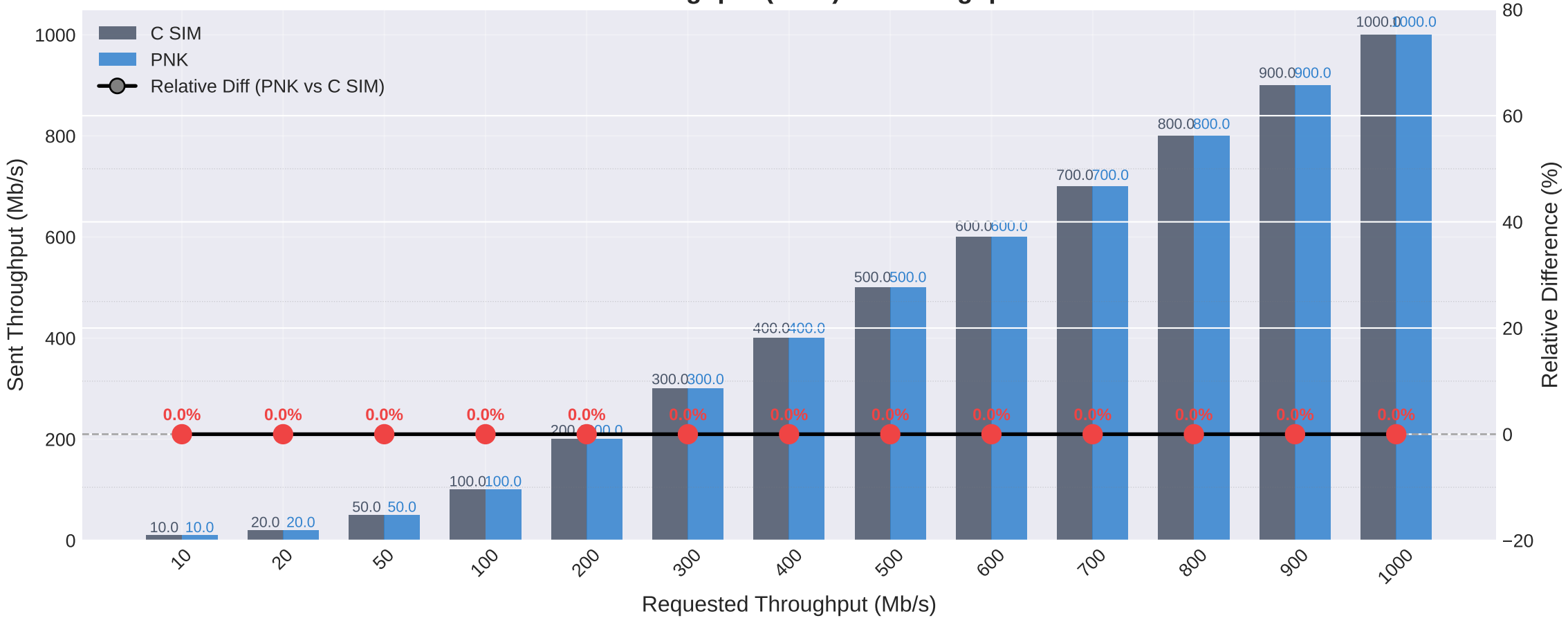
Received Throughput (Mb/s) vs Throughput



Relative Difference (PNK vs C SIM baseline)



Sent Throughput (Mb/s) vs Throughput



Relative Difference (PNK vs C SIM baseline)

