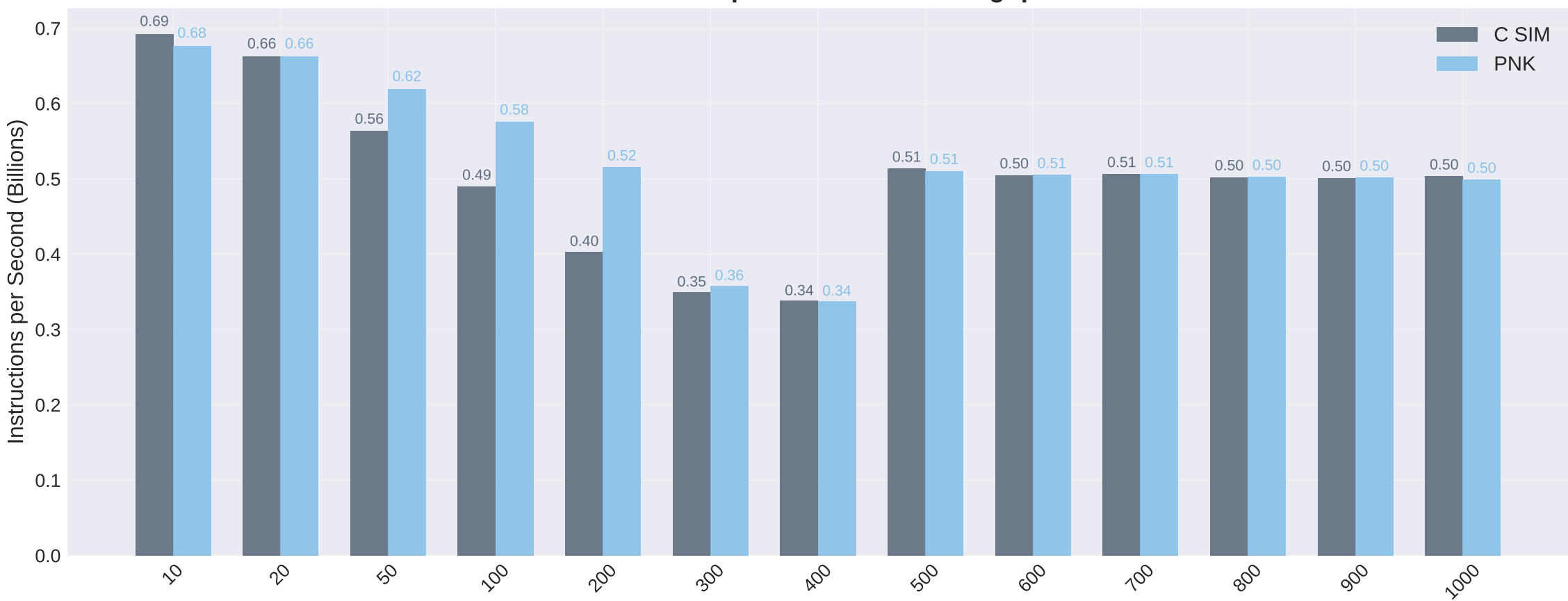
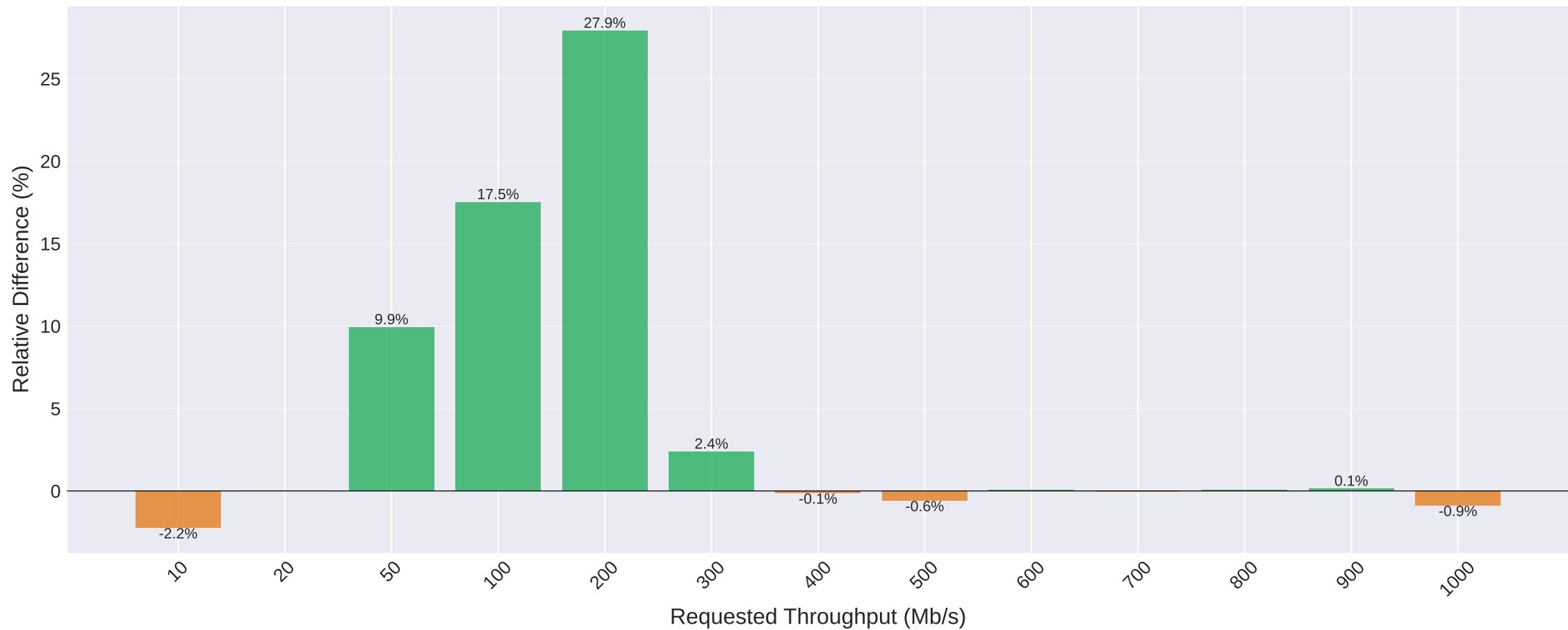


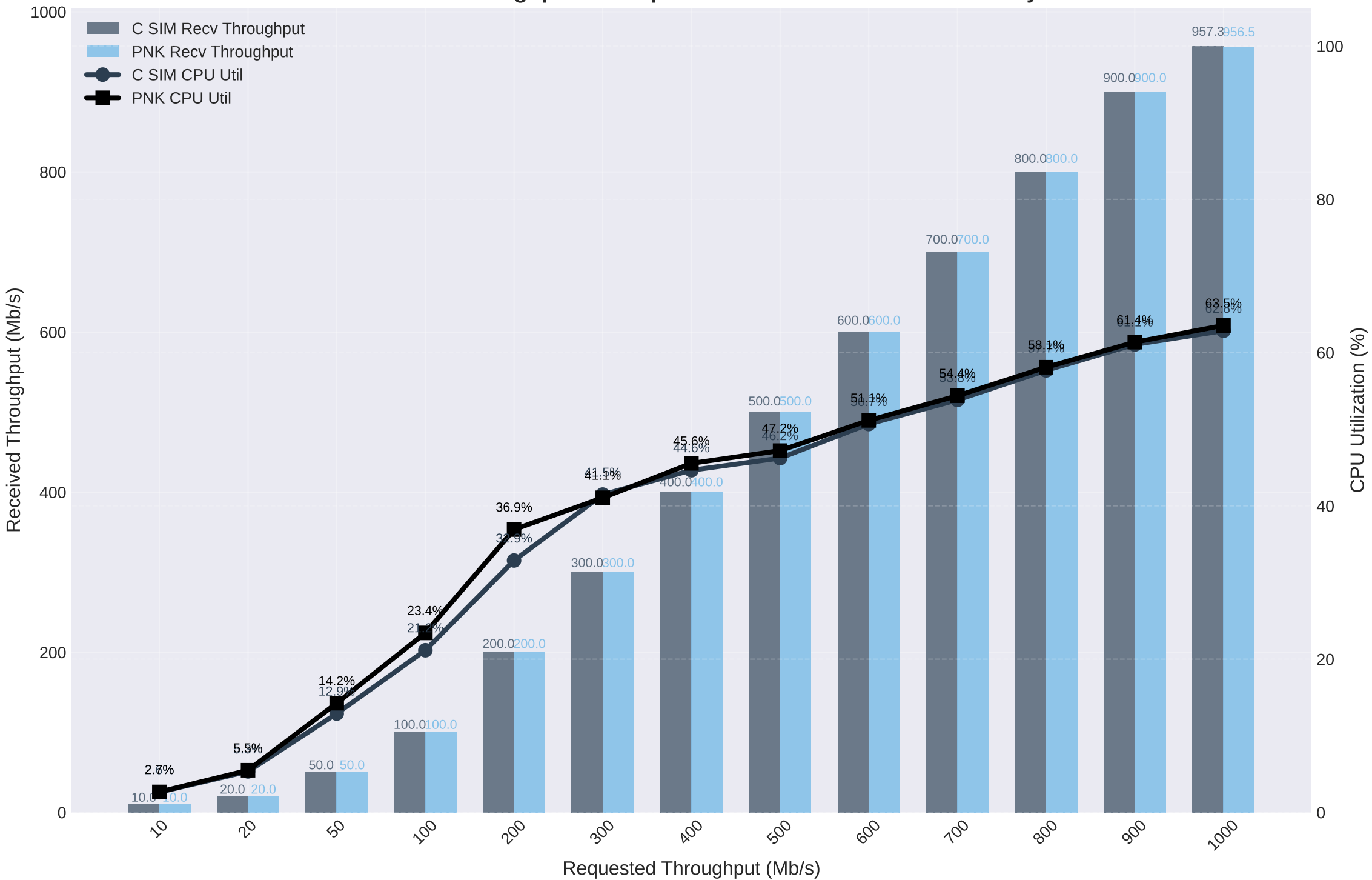
# Instructions per Second vs Throughput



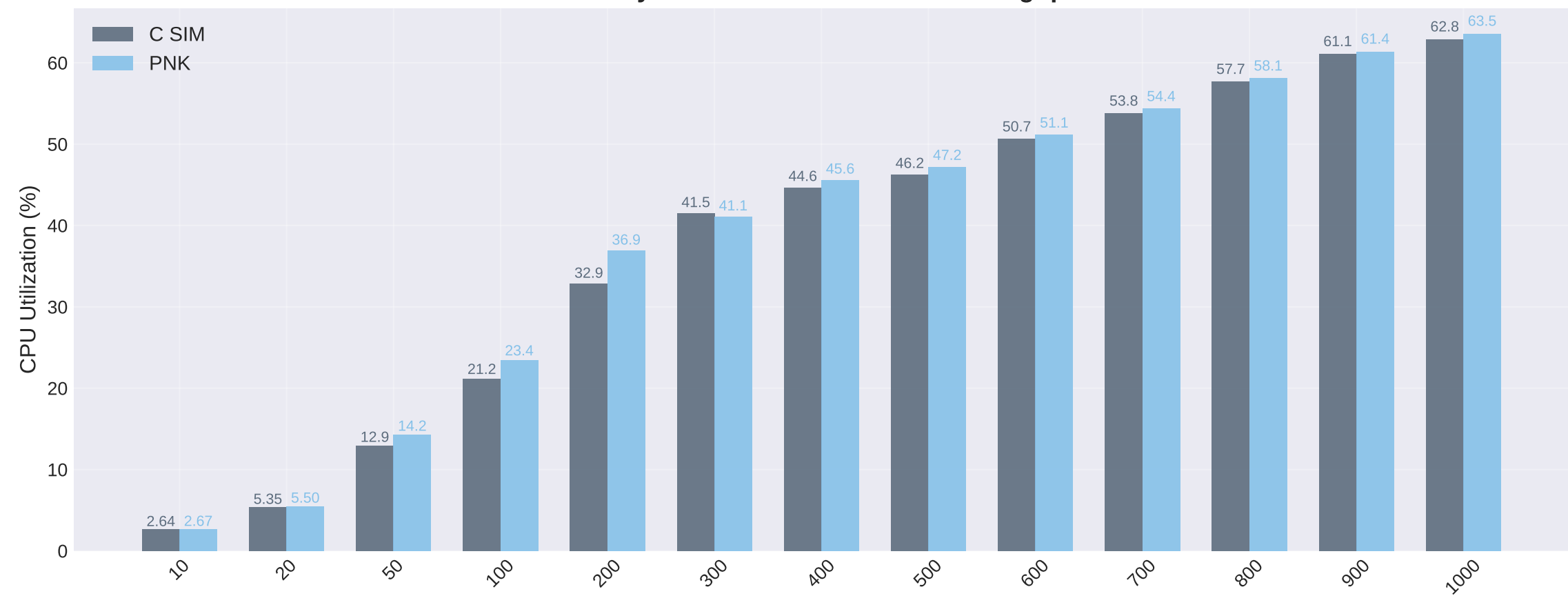
## Relative Difference (PNK vs C SIM)



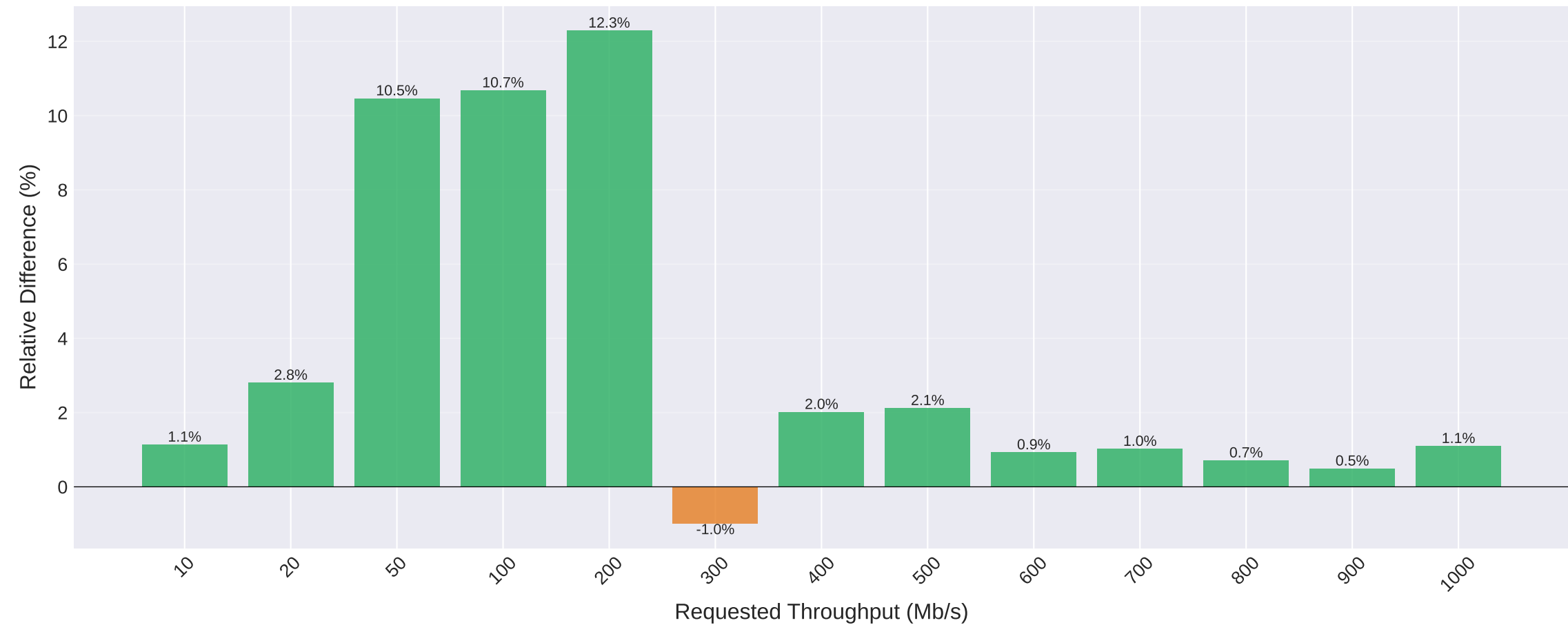
# Received Throughput vs Requested with CPU Utilization Overlay



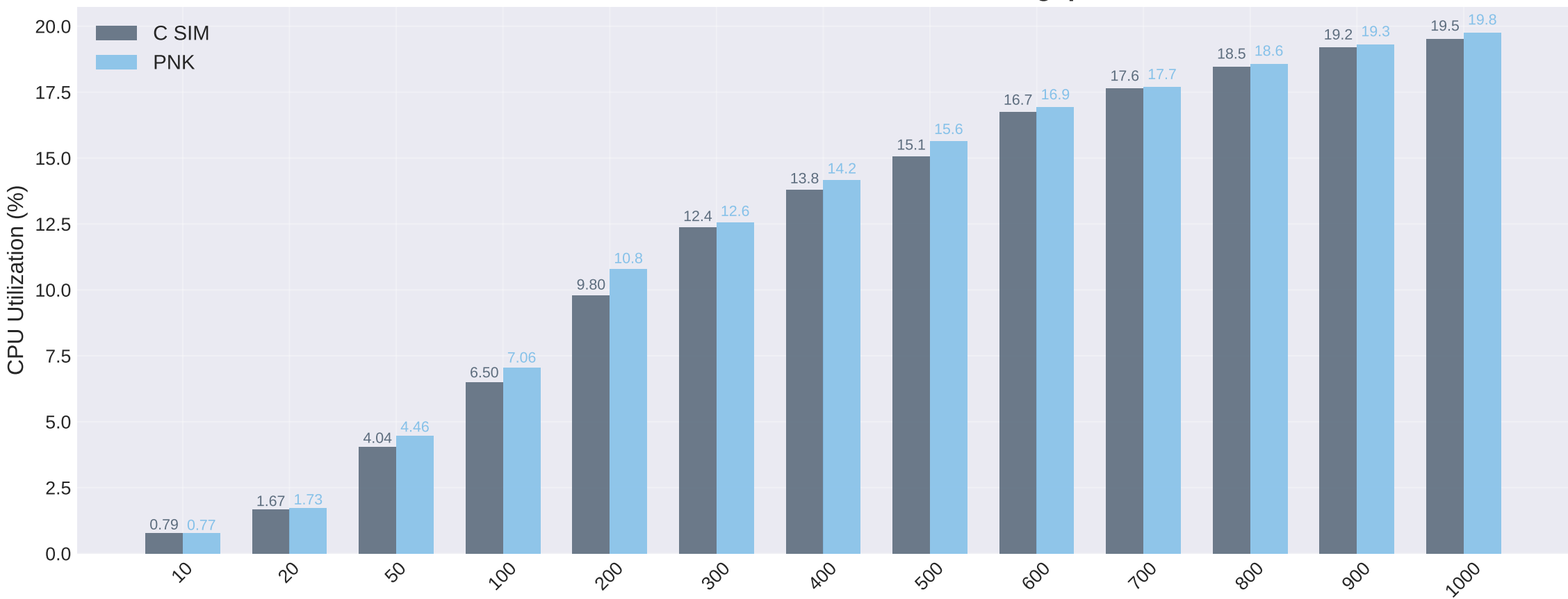
# Total System CPU Utilization vs Throughput



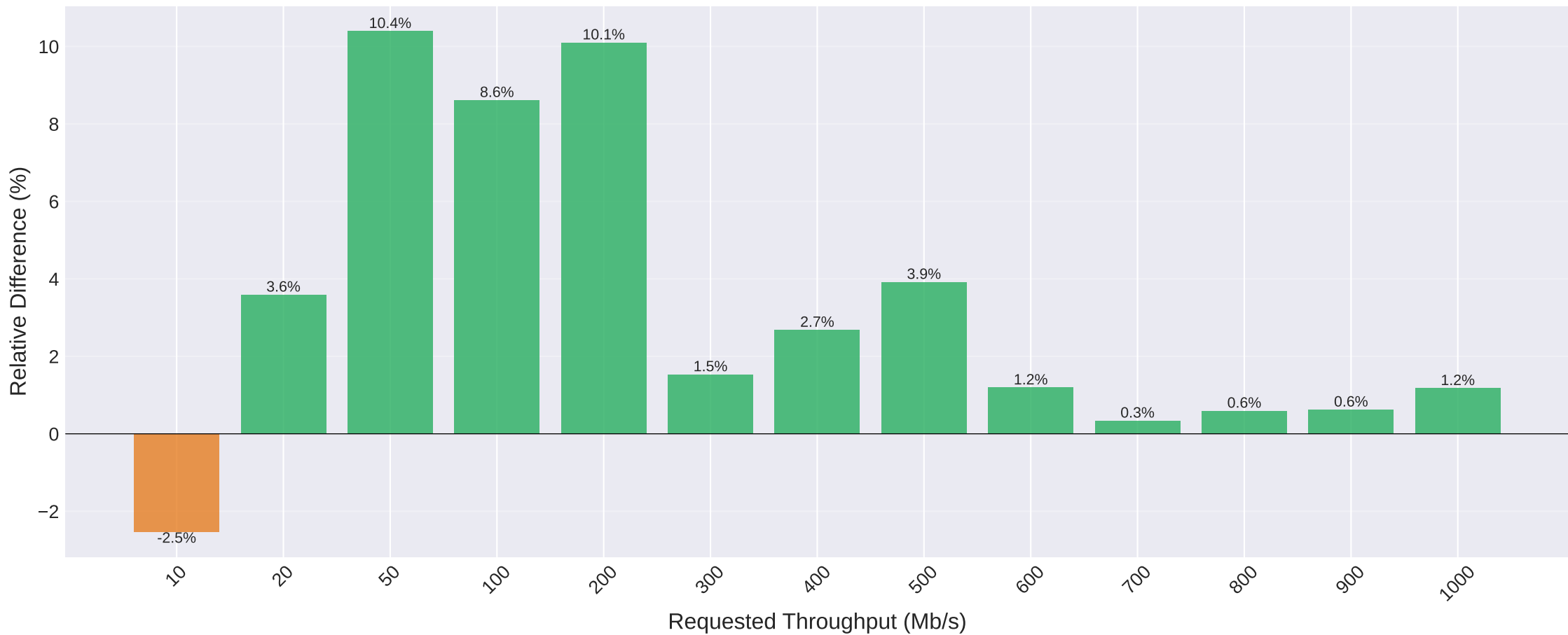
## Relative Difference (PNK vs C SIM)



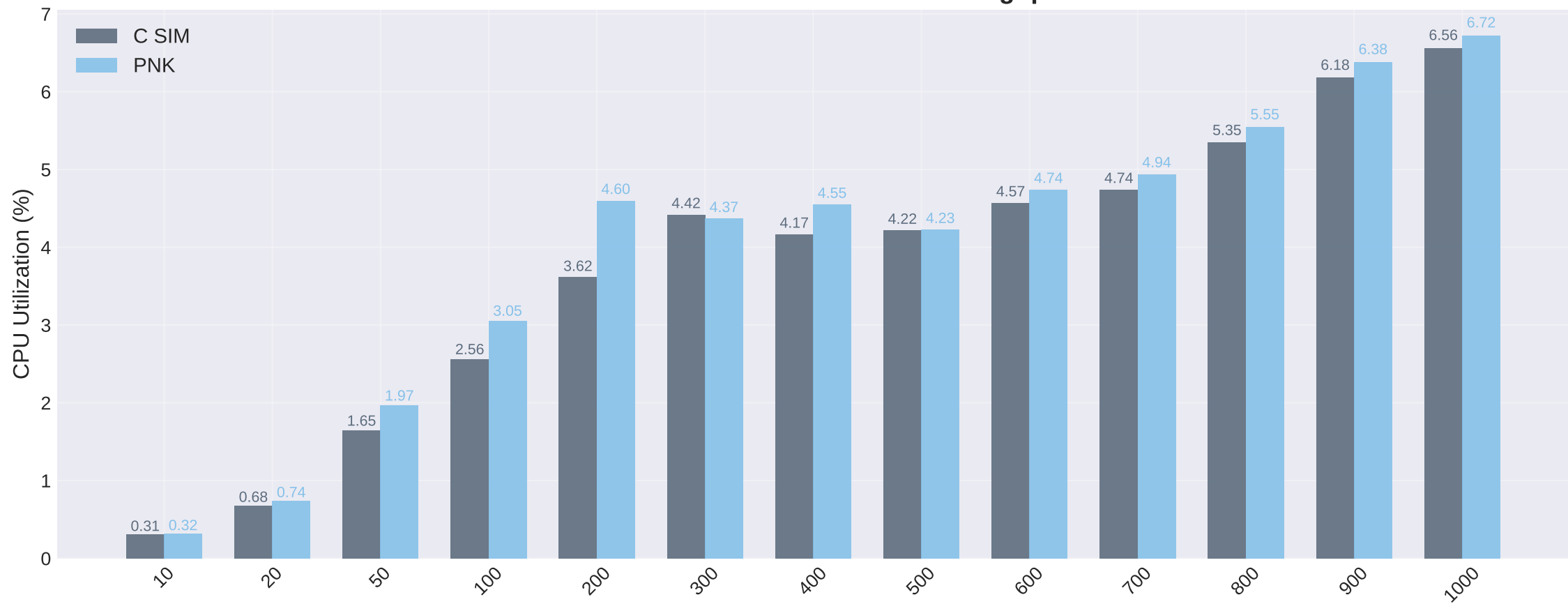
# Ethernet Driver CPU Utilization vs Throughput



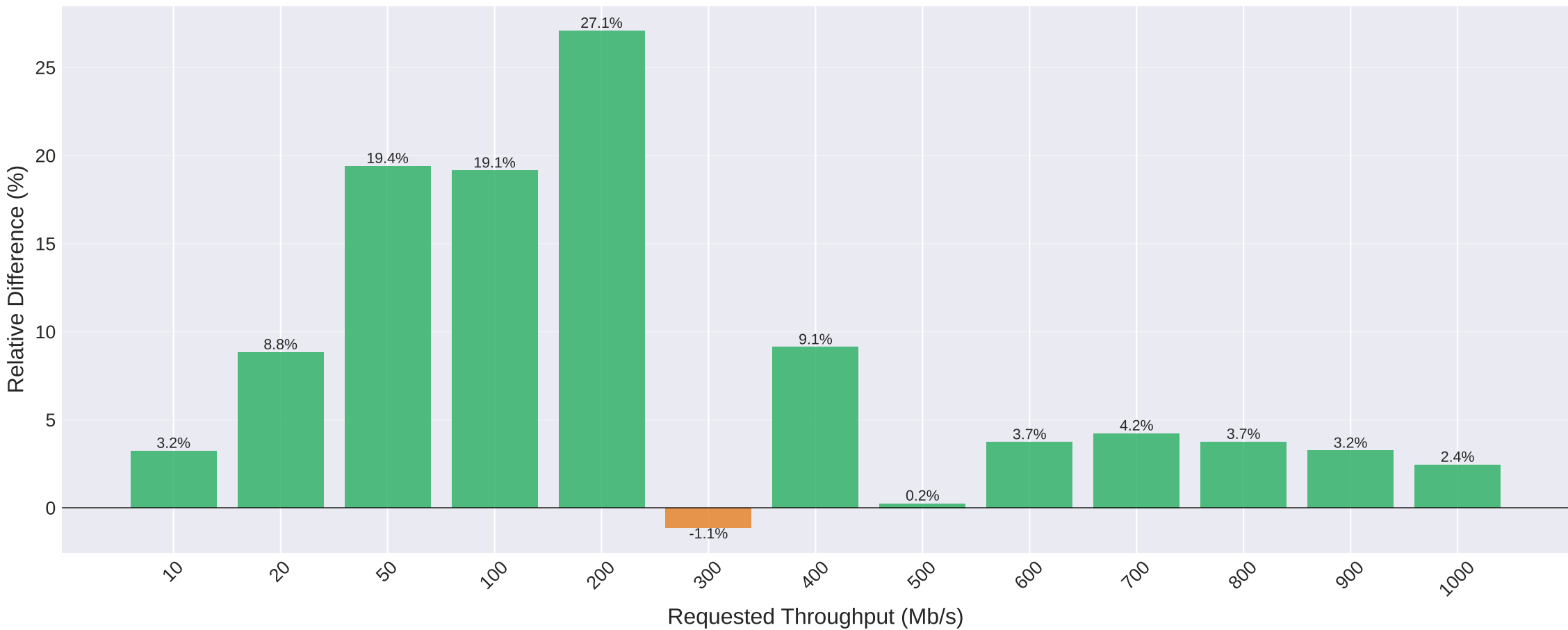
## Relative Difference (PNK vs C SIM)



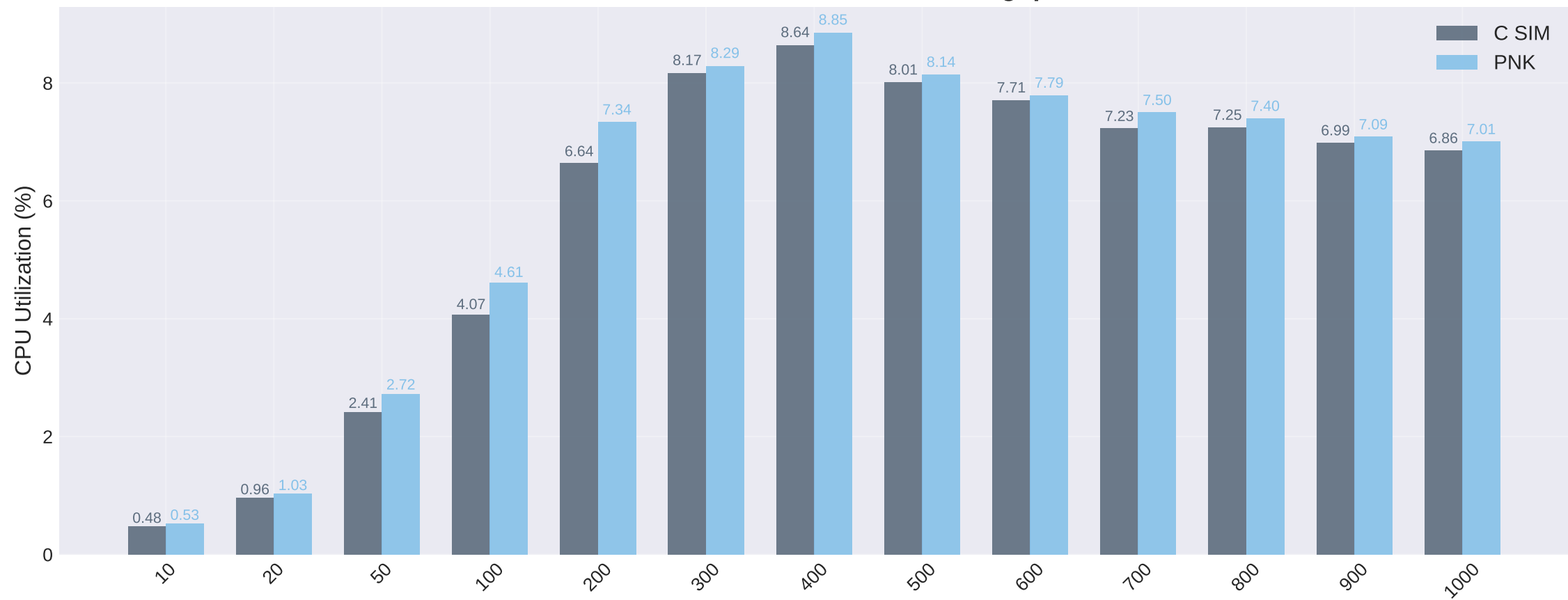
# Net Virt TX CPU Utilization vs Throughput



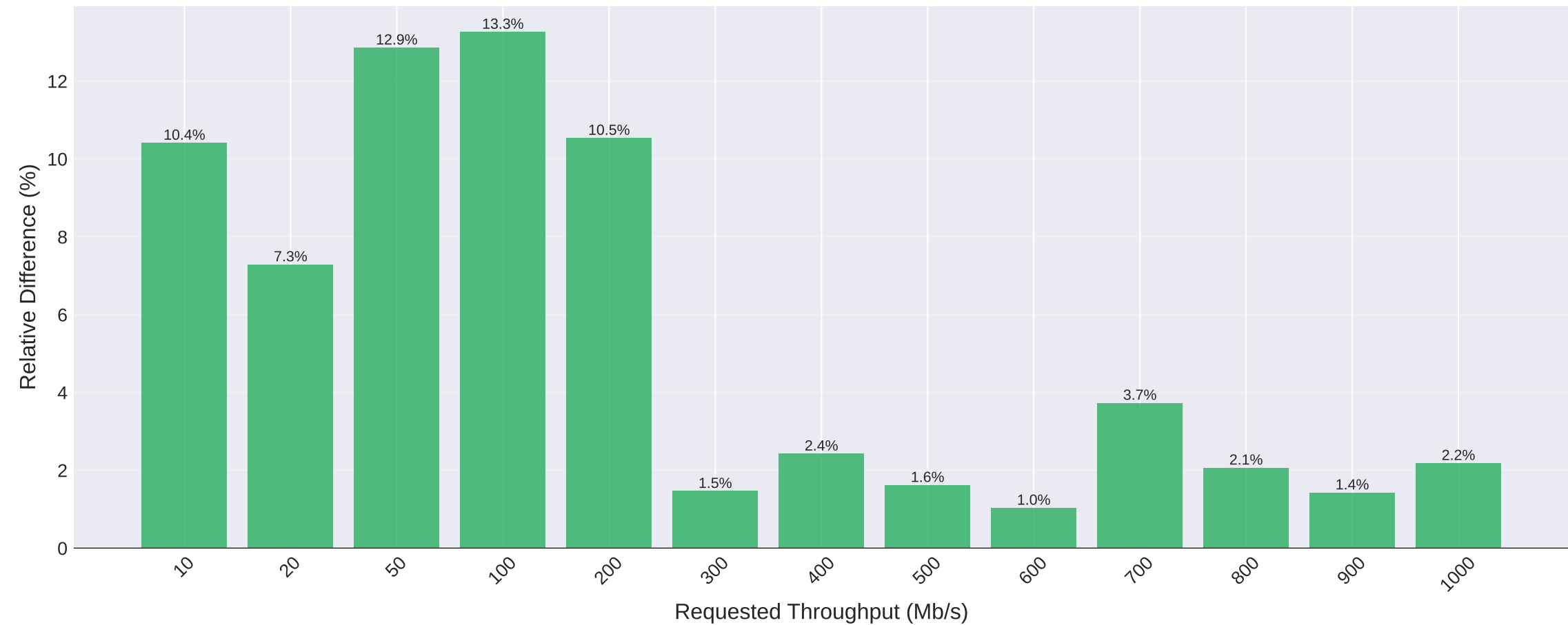
## Relative Difference (PNK vs C SIM)



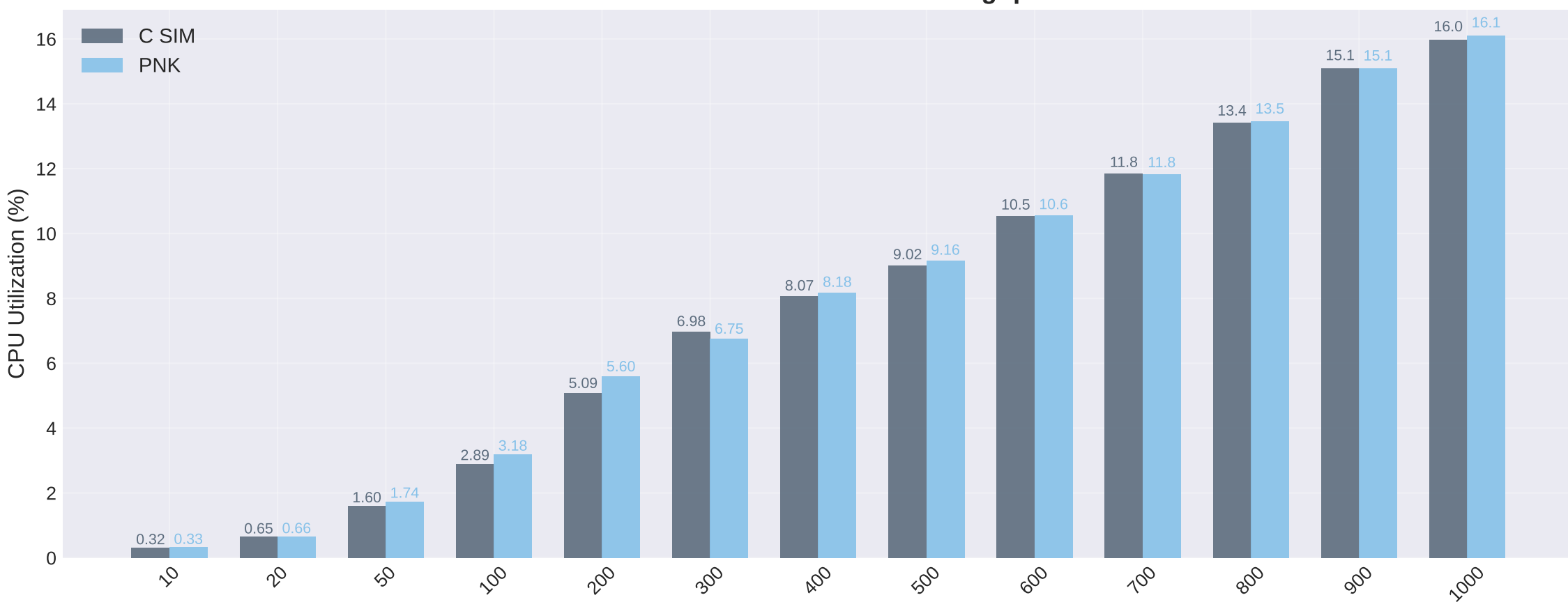
# Net Virt RX CPU Utilization vs Throughput



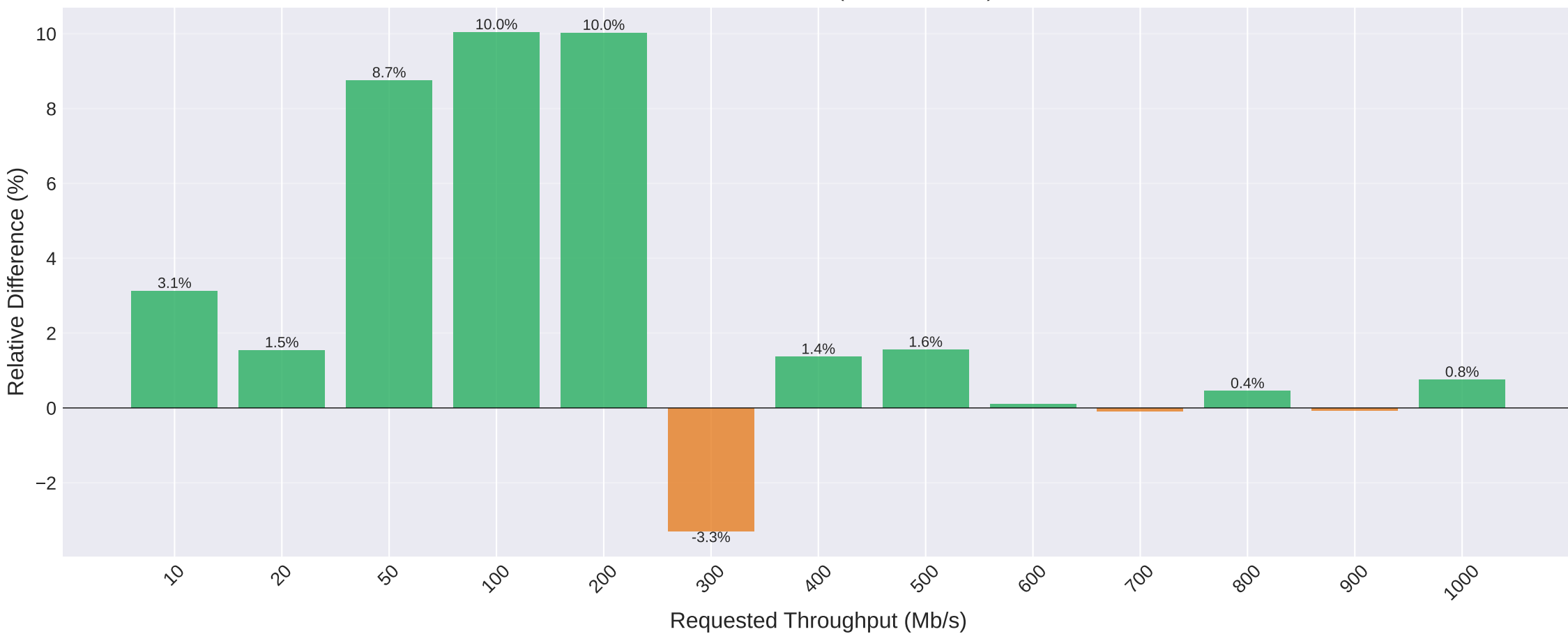
## Relative Difference (PNK vs C SIM)



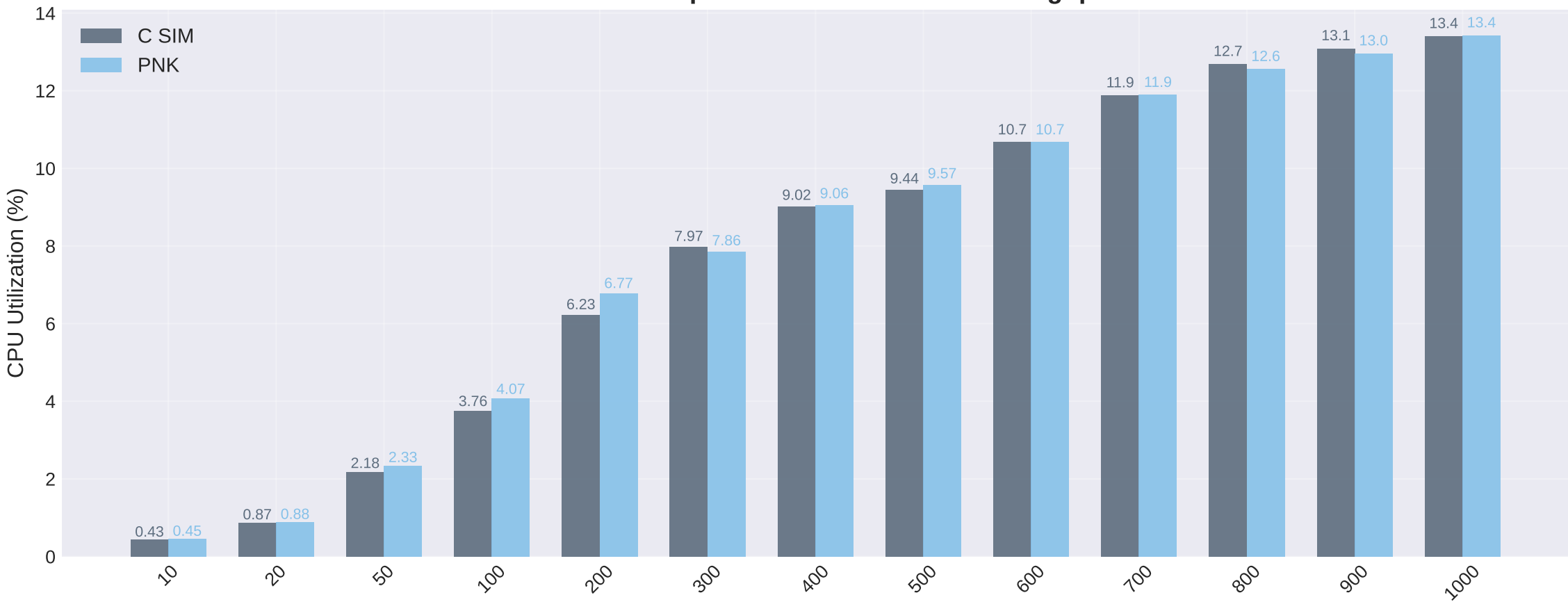
Client0 CPU Utilization vs Throughput



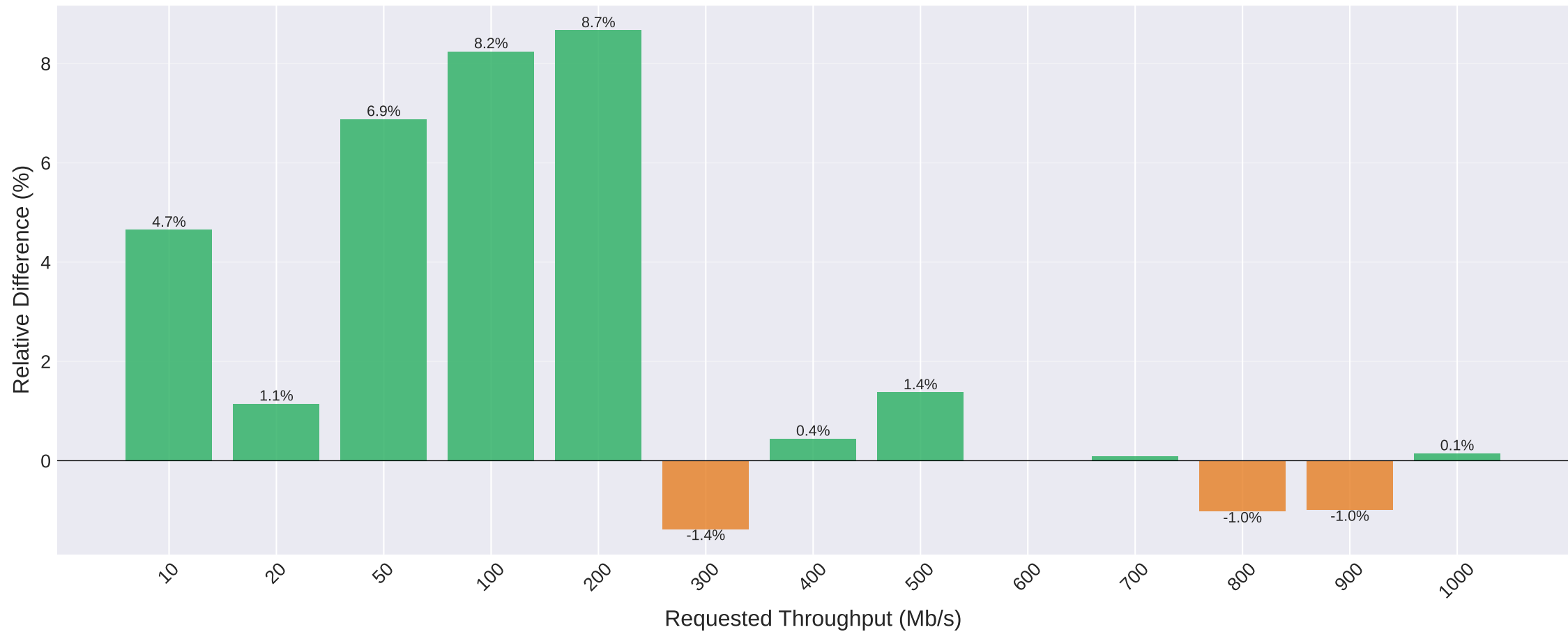
Relative Difference (PNK vs C SIM)



# Client0 Net Copier CPU Utilization vs Throughput

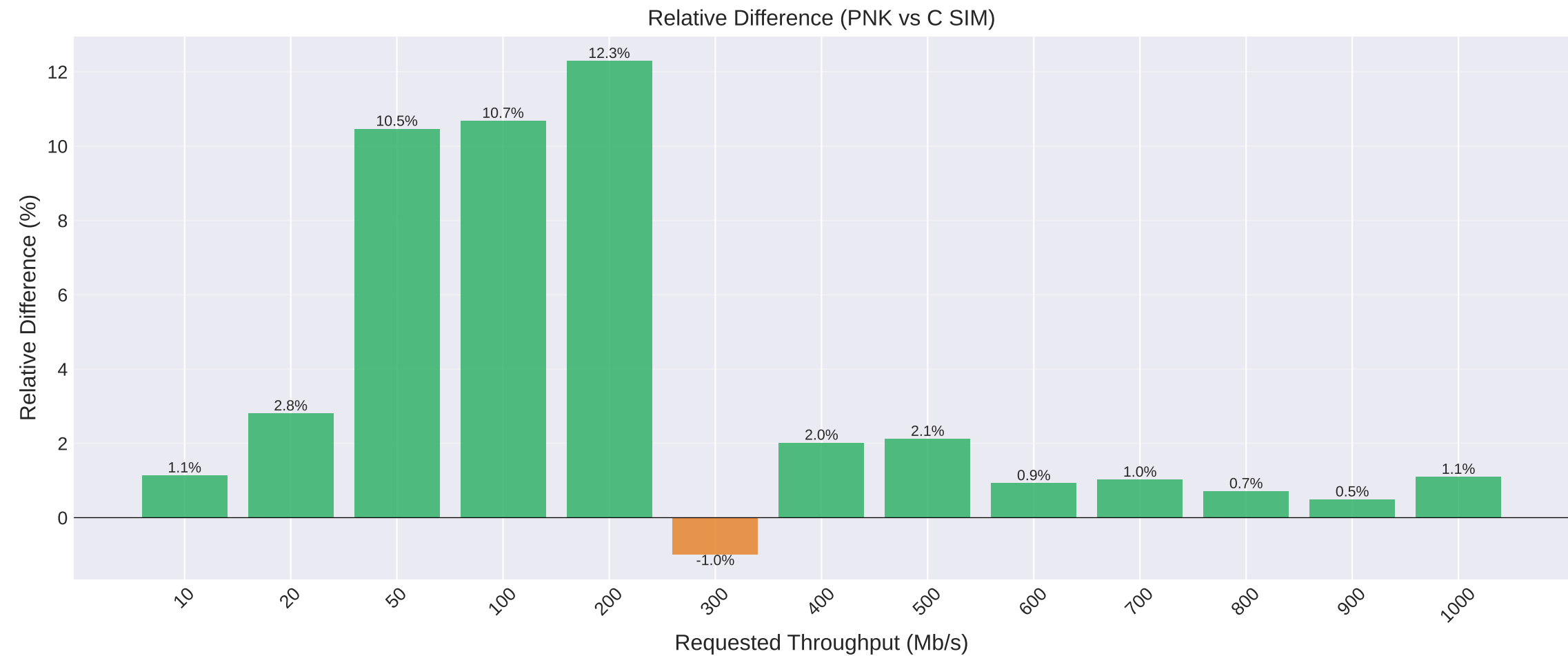
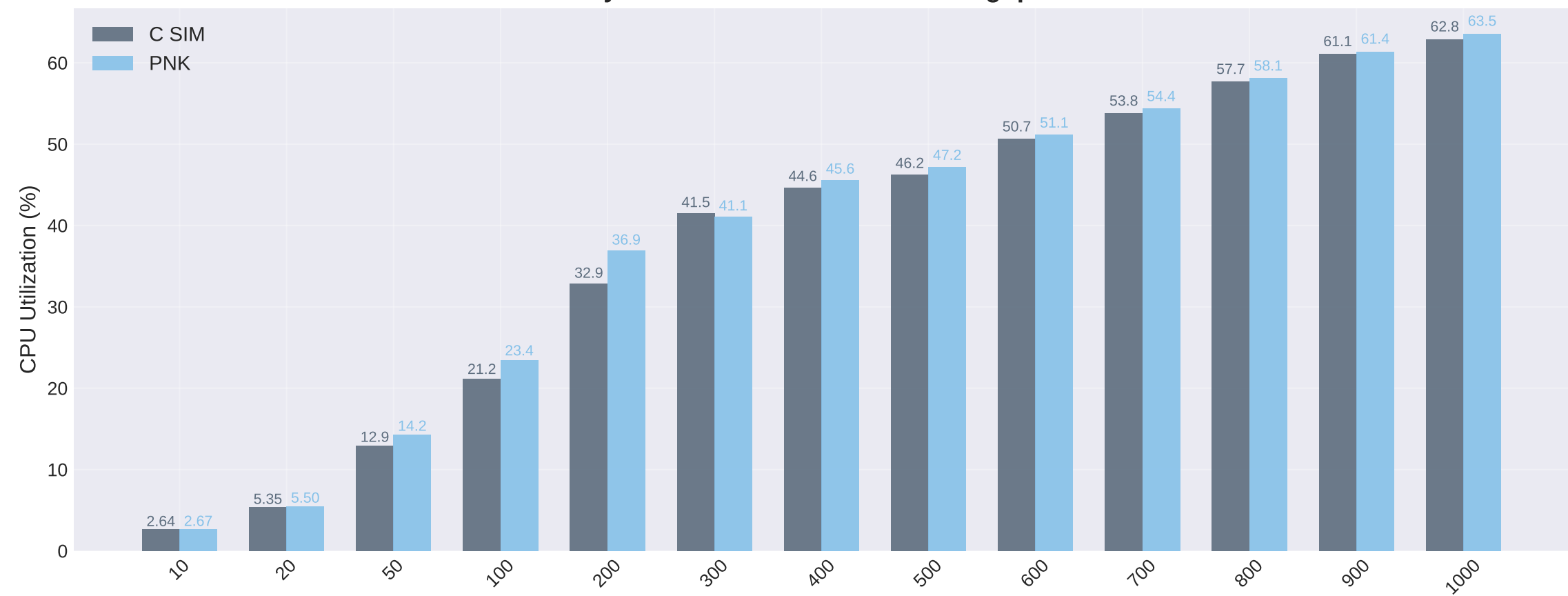


## Relative Difference (PNK vs C SIM)

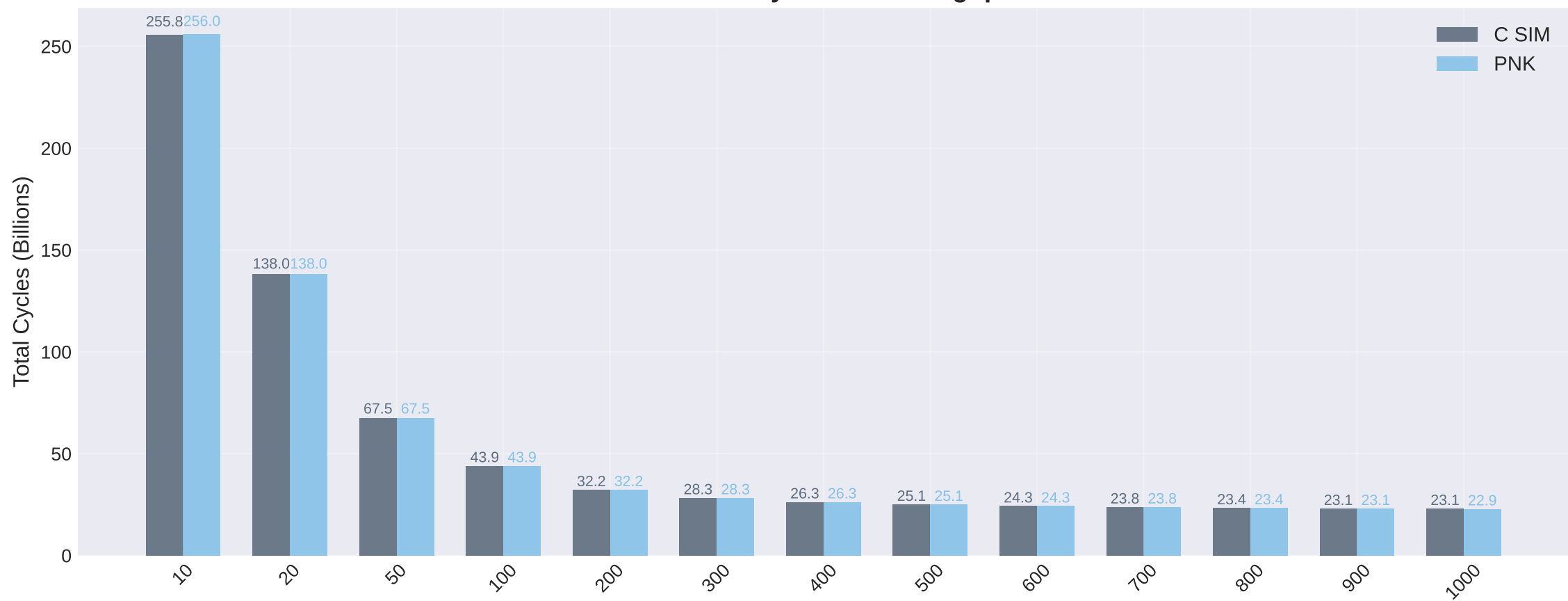




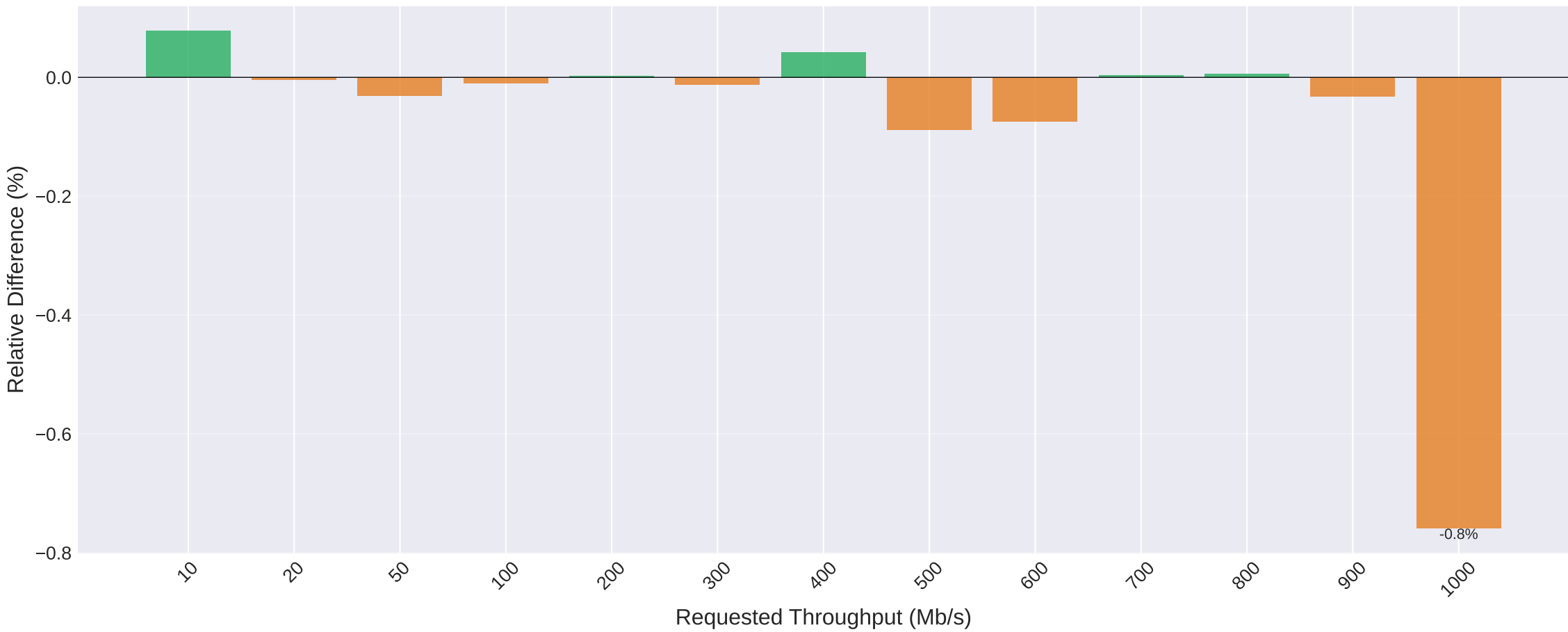
# System CPU Utilization vs Throughput



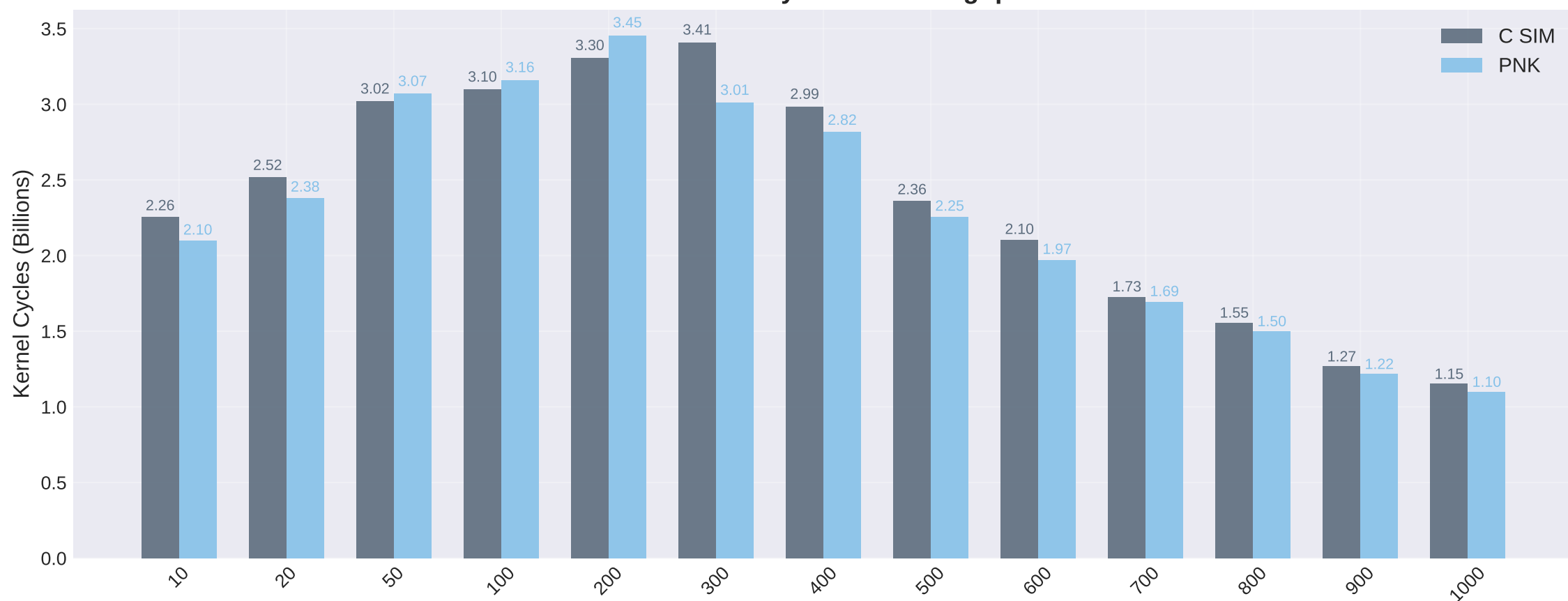
# Total CPU Cycles vs Throughput



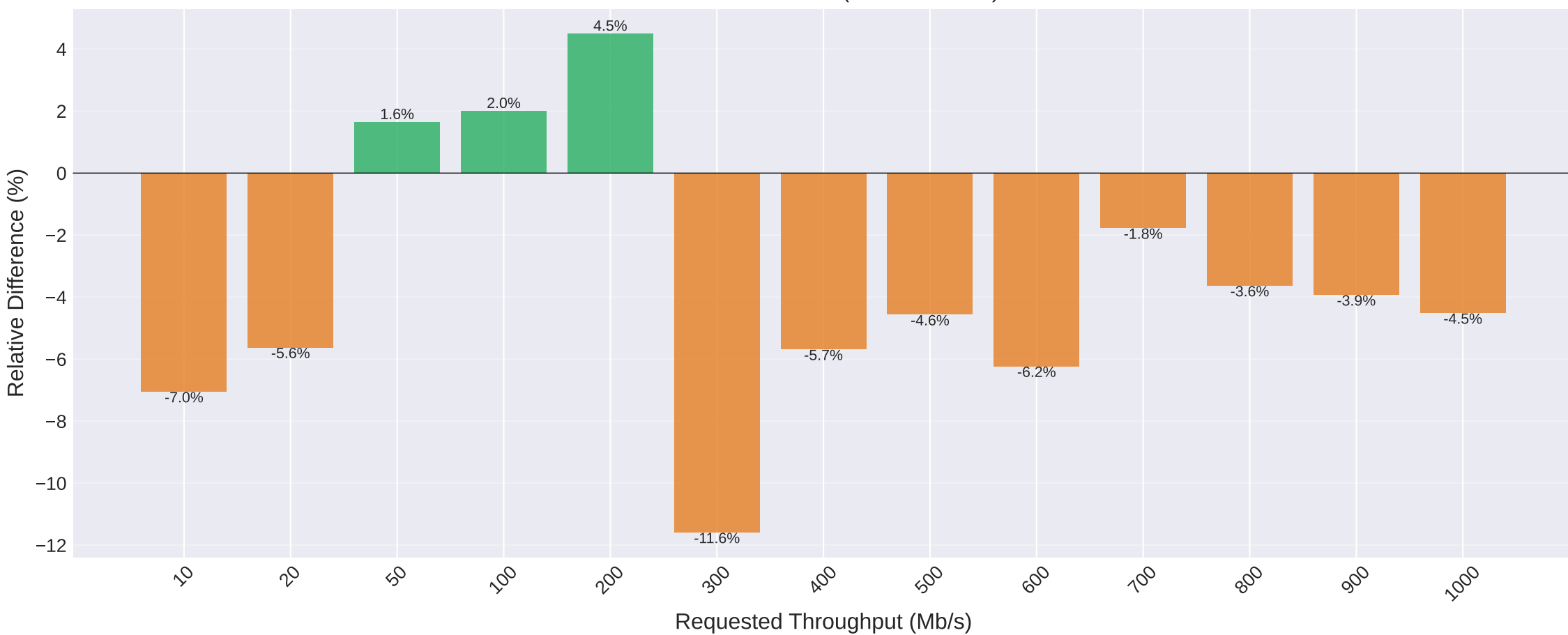
## Relative Difference (PNK vs C SIM)



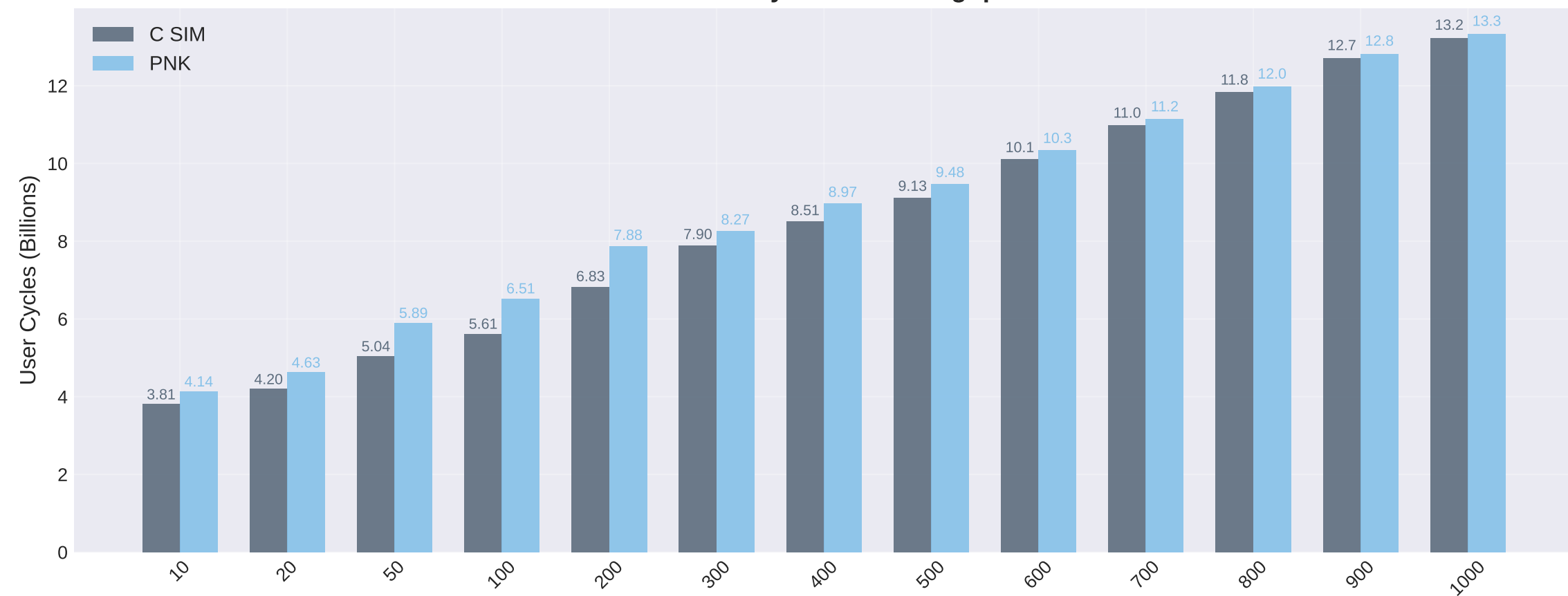
Kernel CPU Cycles vs Throughput



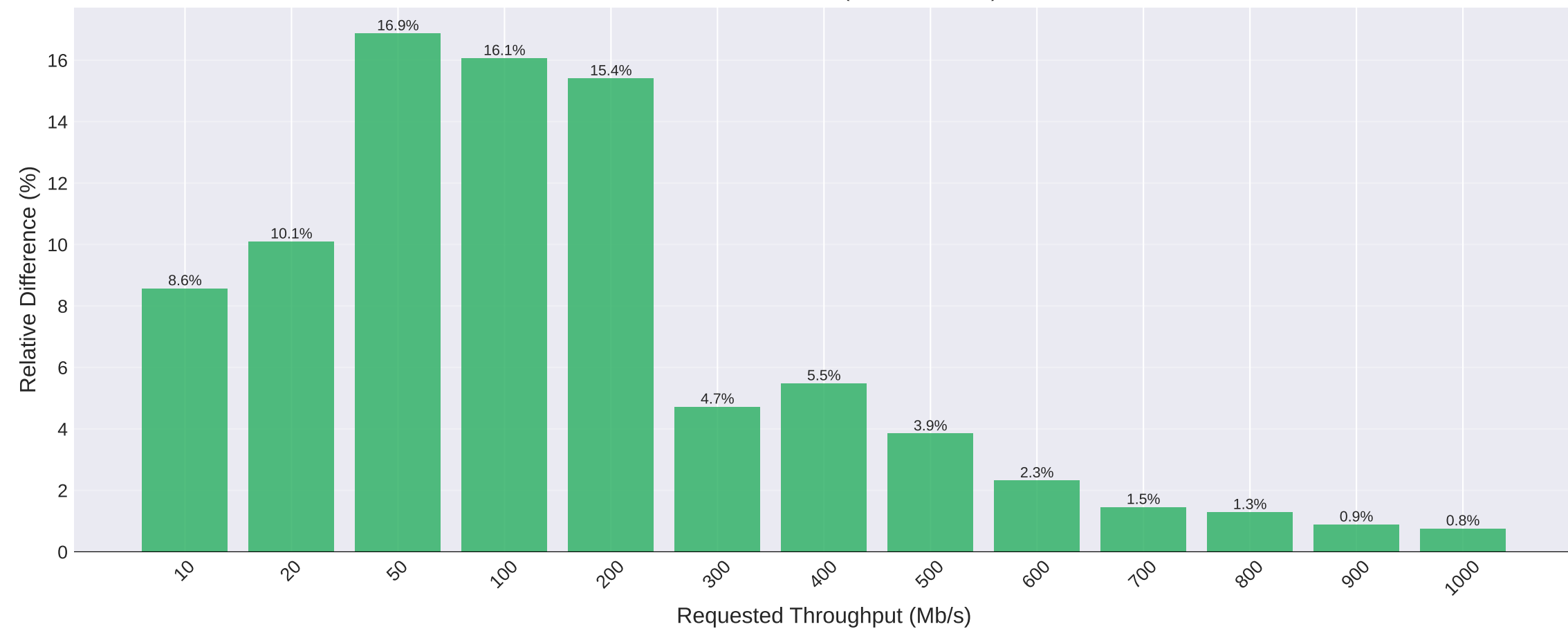
Relative Difference (PNK vs C SIM)



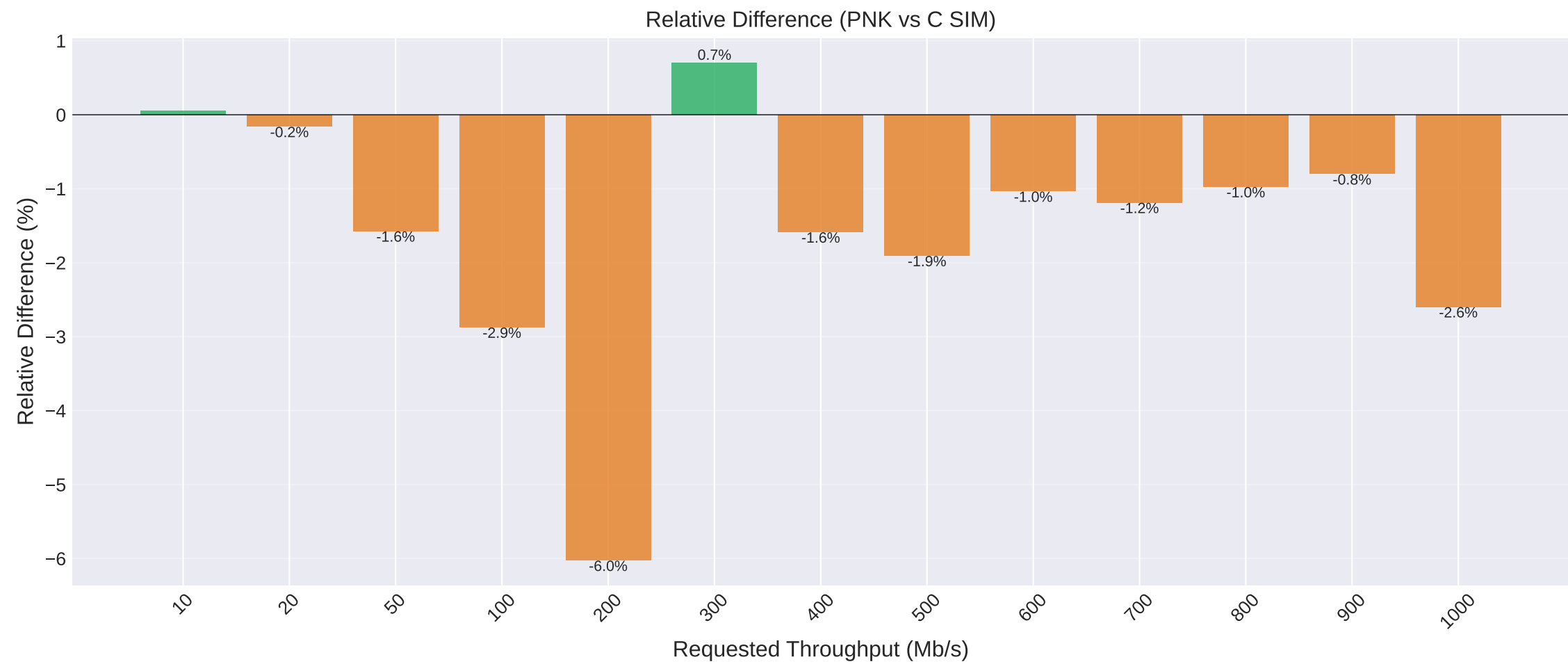
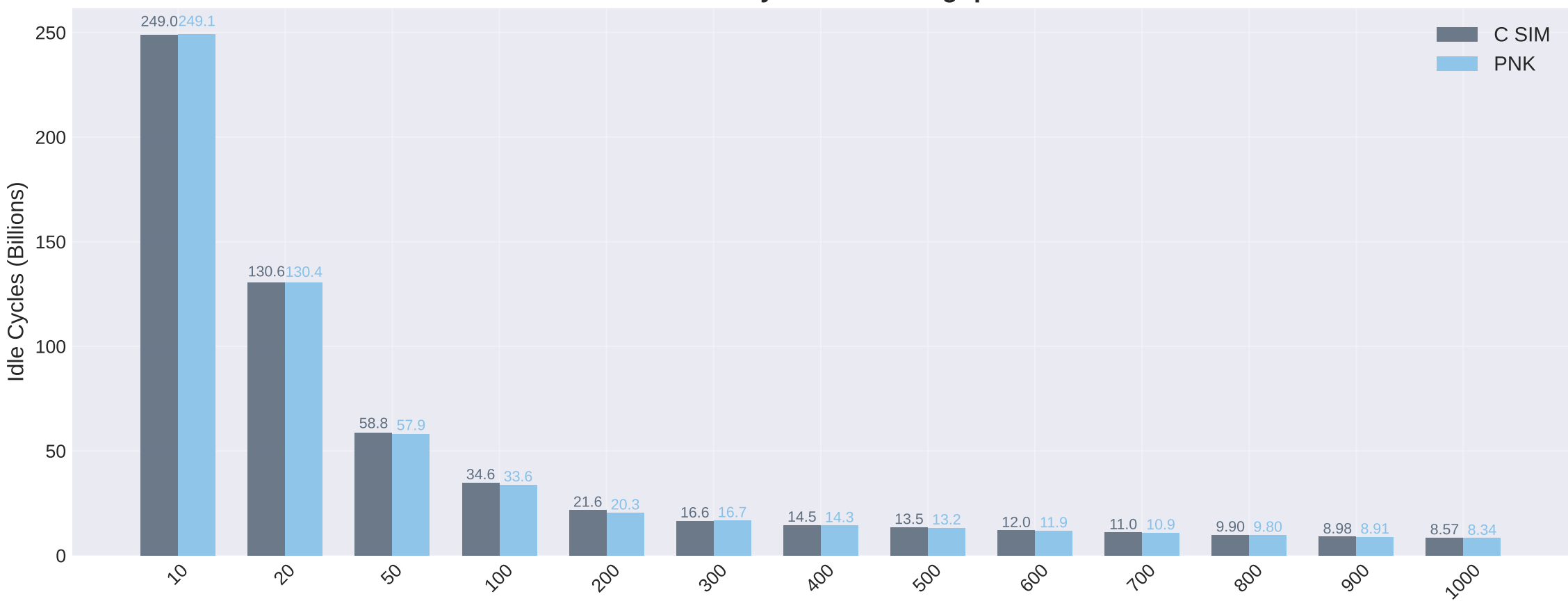
# User CPU Cycles vs Throughput



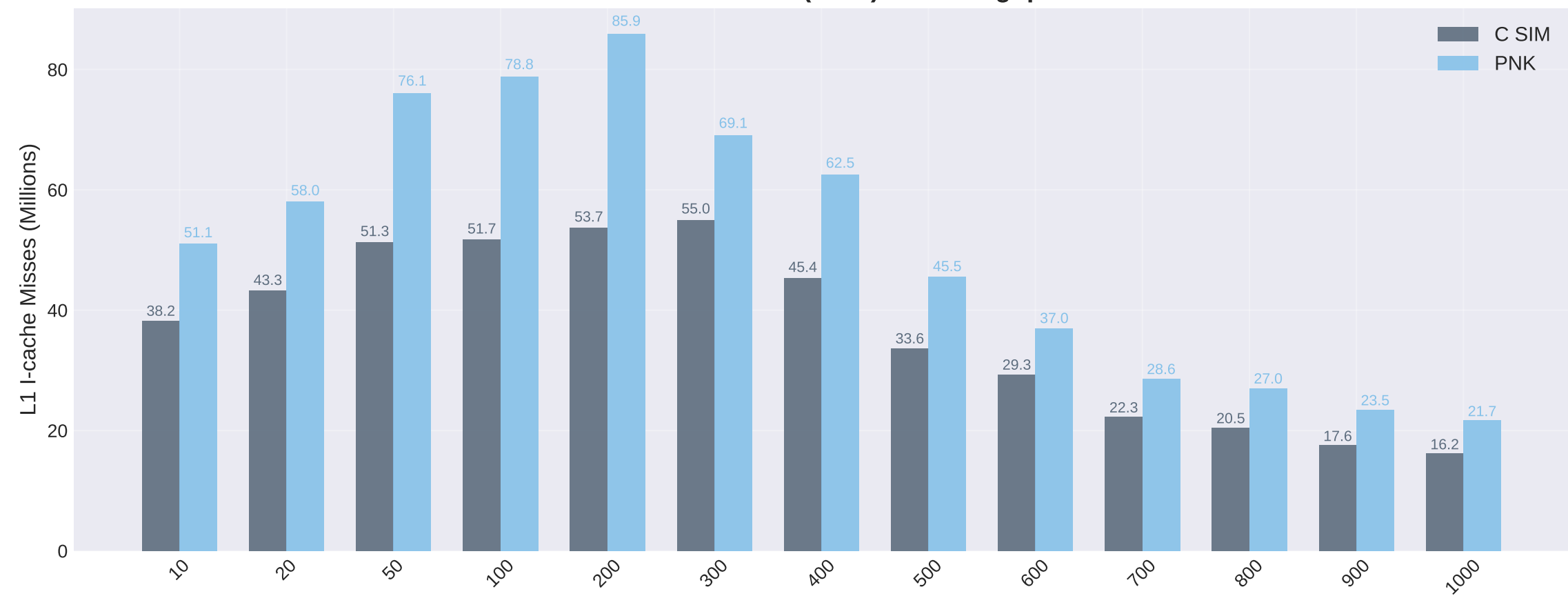
## Relative Difference (PNK vs C SIM)



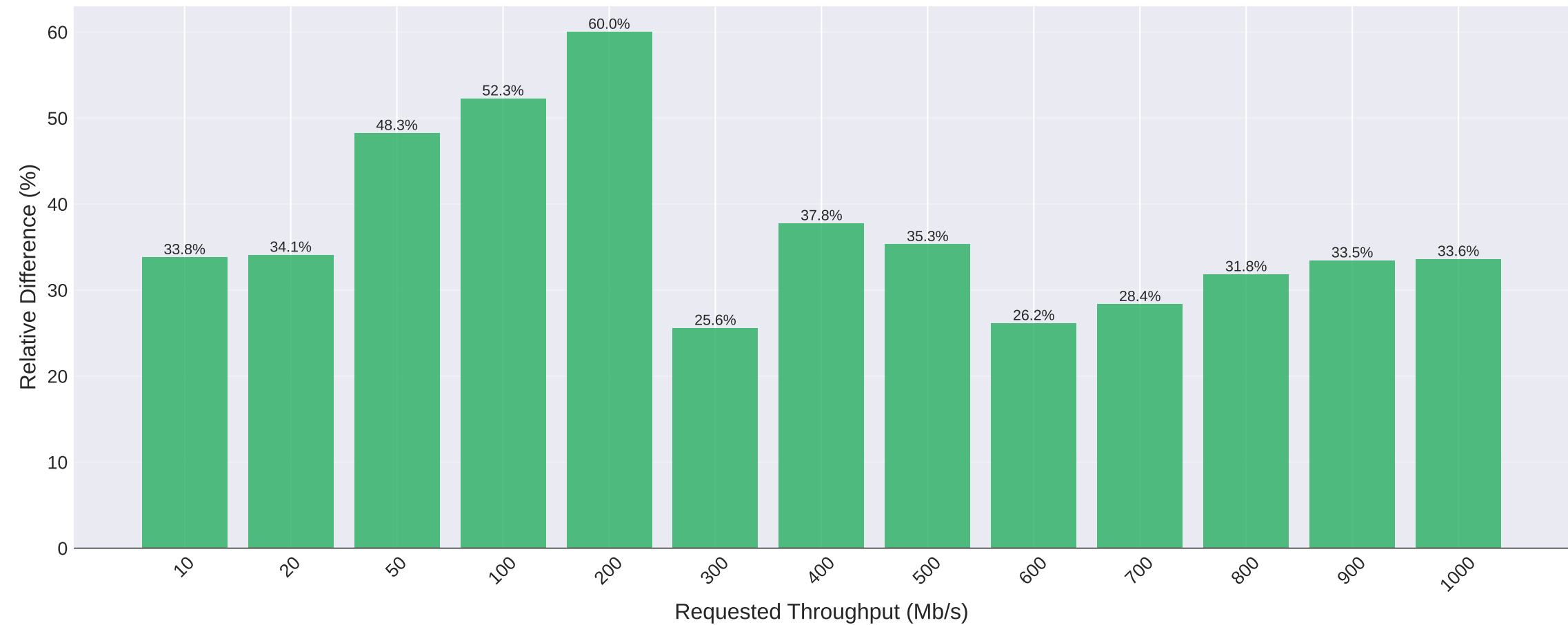
# Idle CPU Cycles vs Throughput



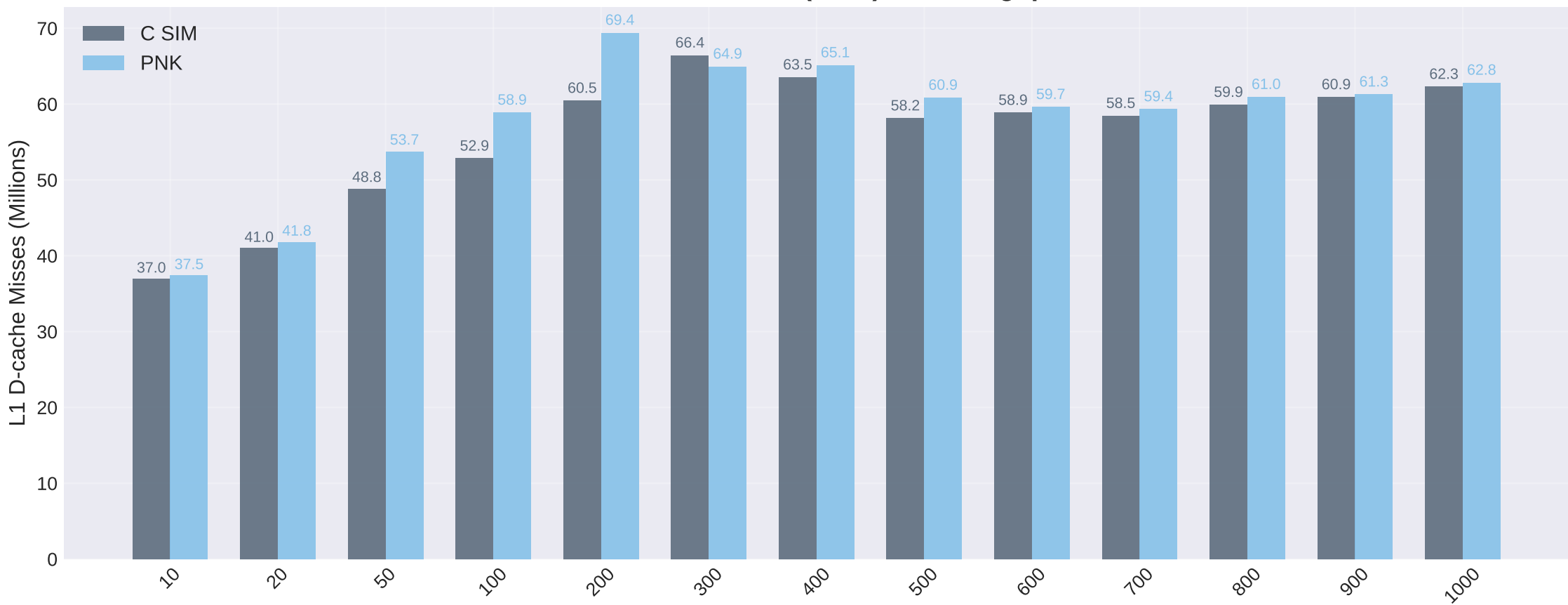
# L1 I-cache Misses (Total) vs Throughput



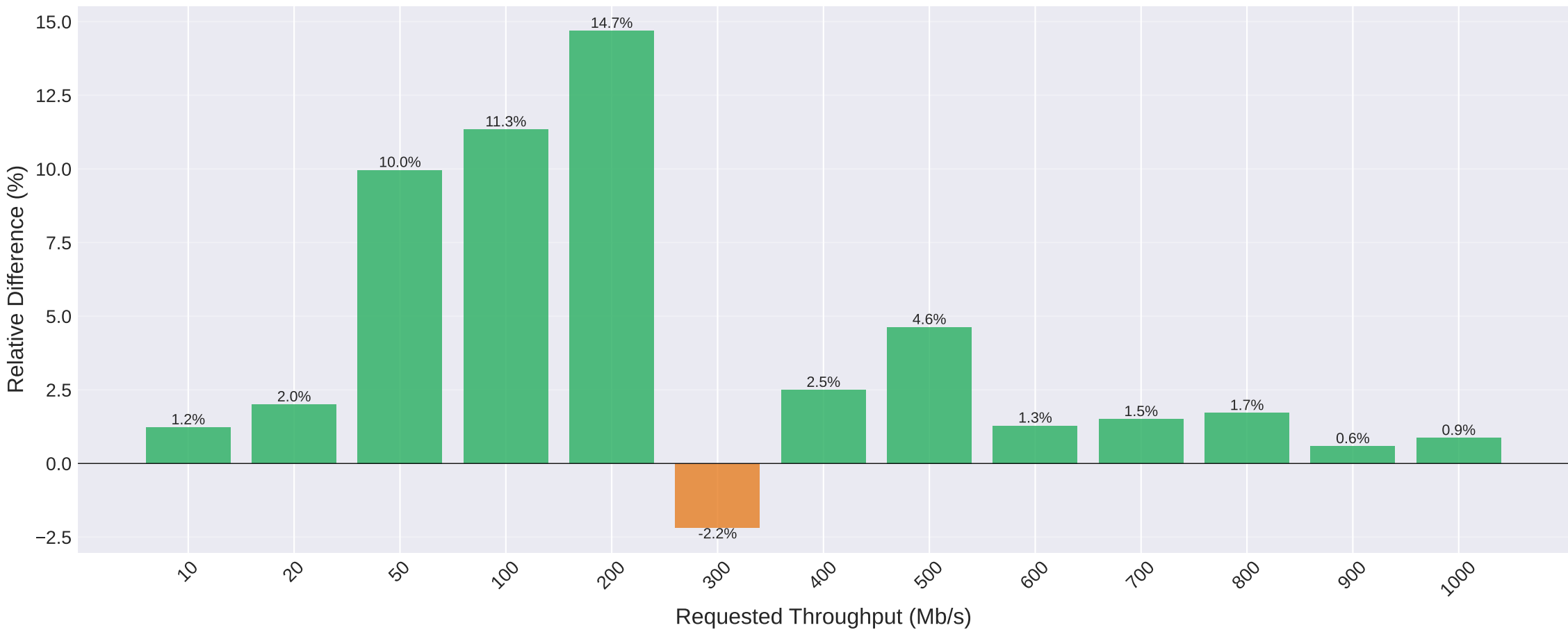
## Relative Difference (PNK vs C SIM)



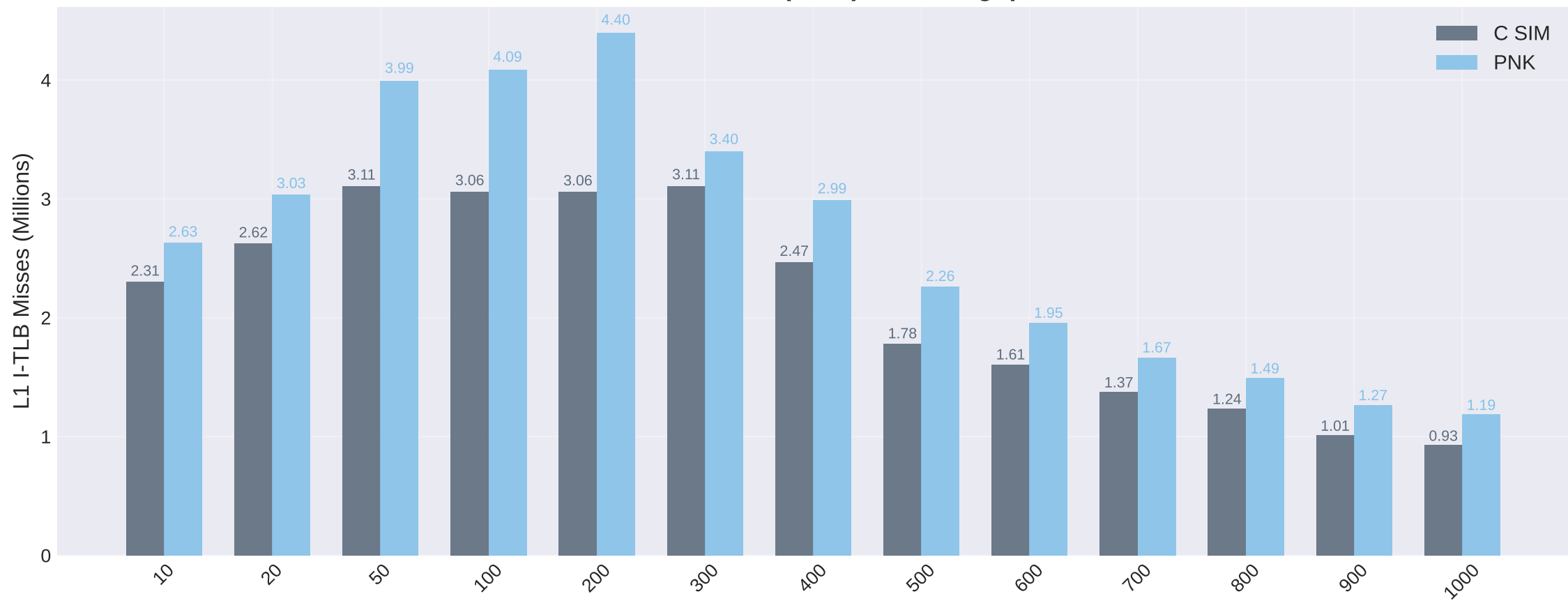
# L1 D-cache Misses (Total) vs Throughput



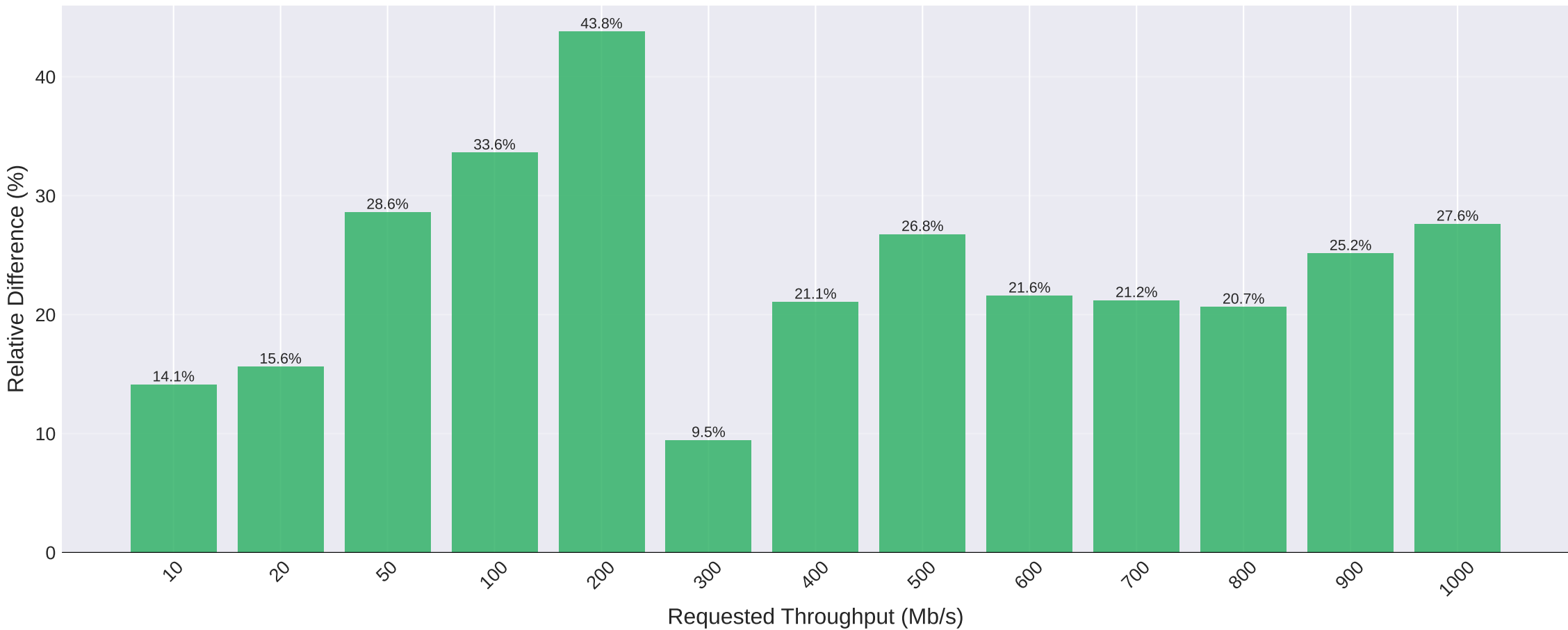
## Relative Difference (PNK vs C SIM)



# L1 I-TLB Misses (Total) vs Throughput

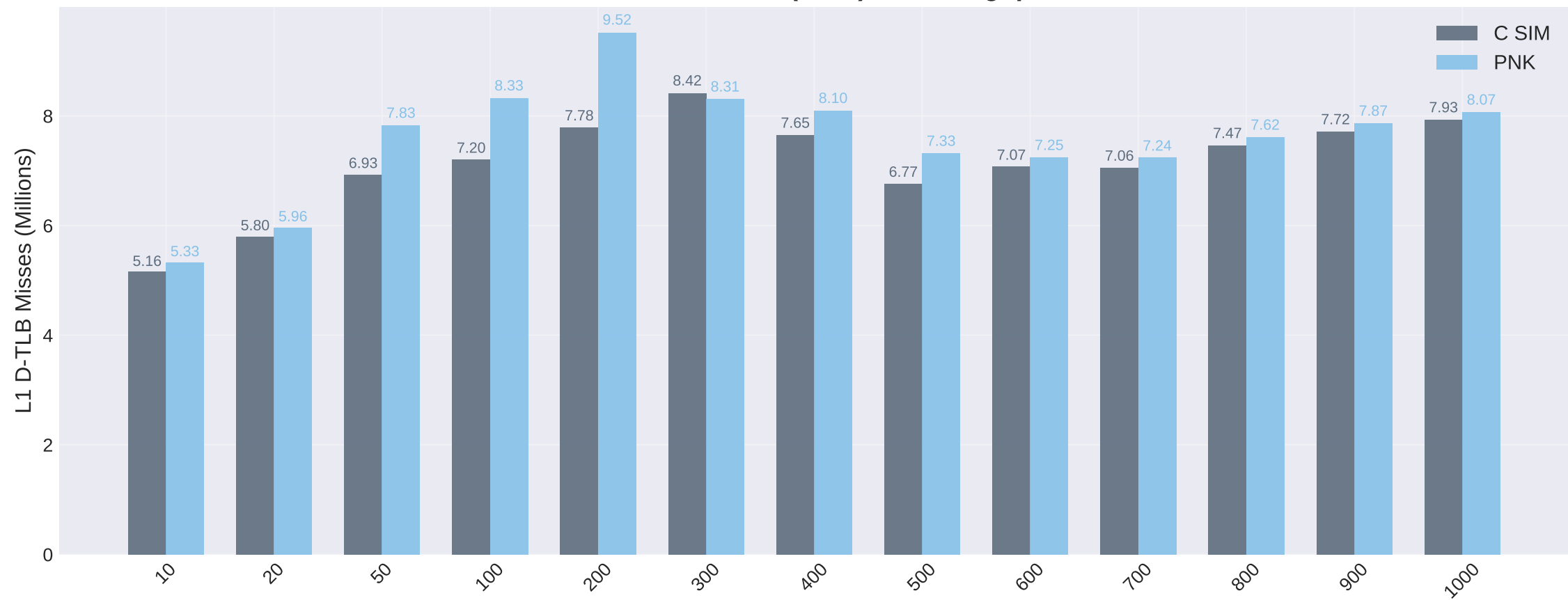


## Relative Difference (PNK vs C SIM)

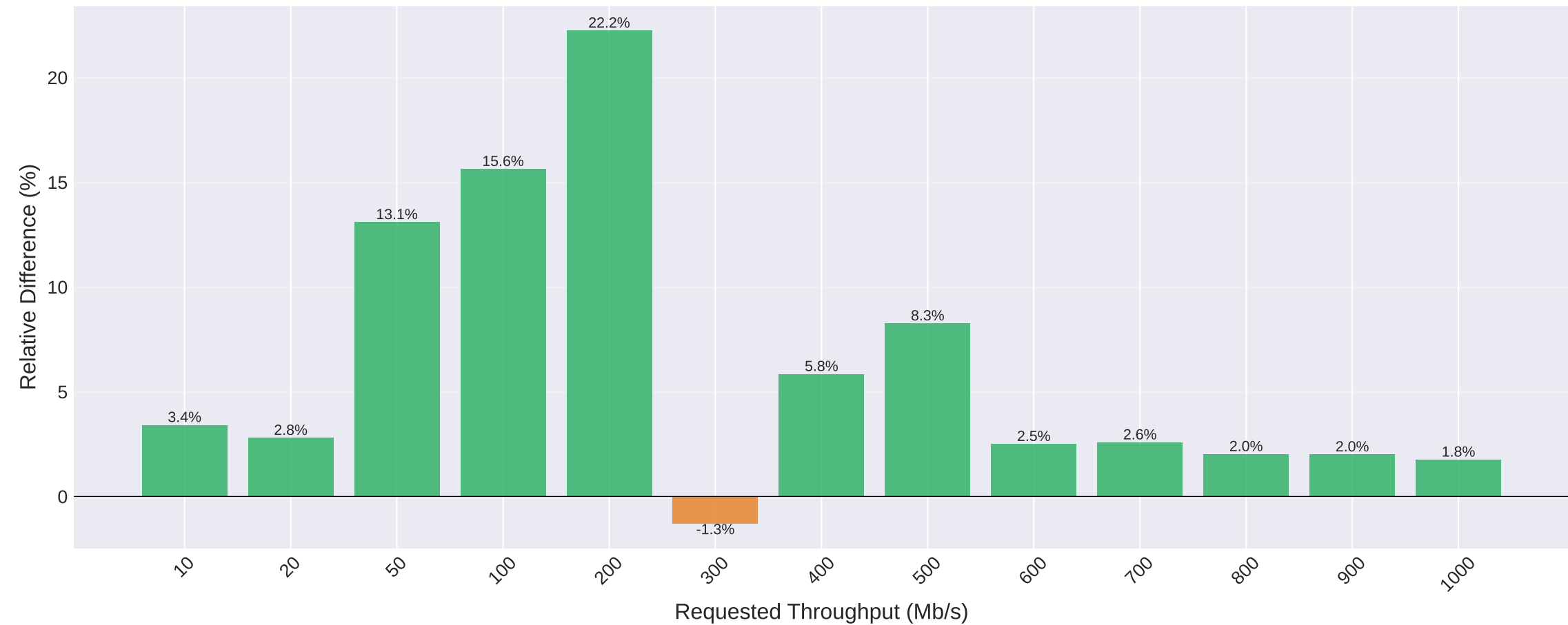




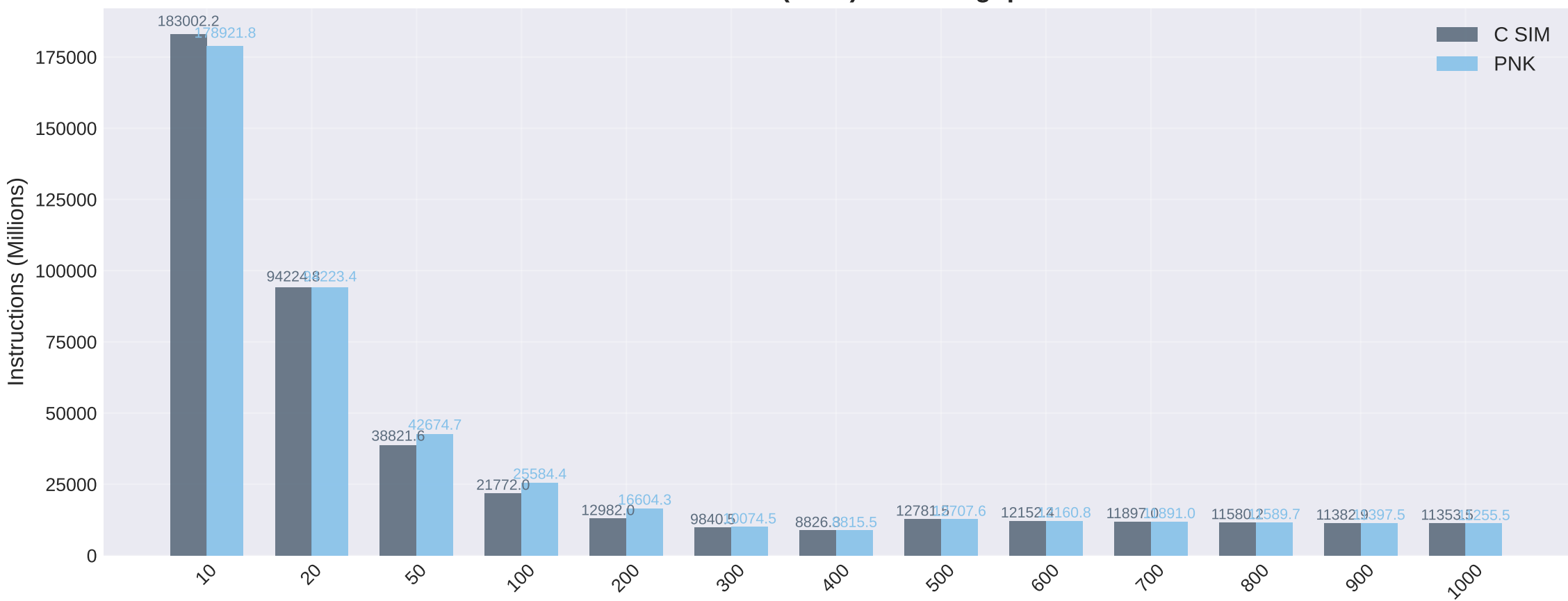
# L1 D-TLB Misses (Total) vs Throughput



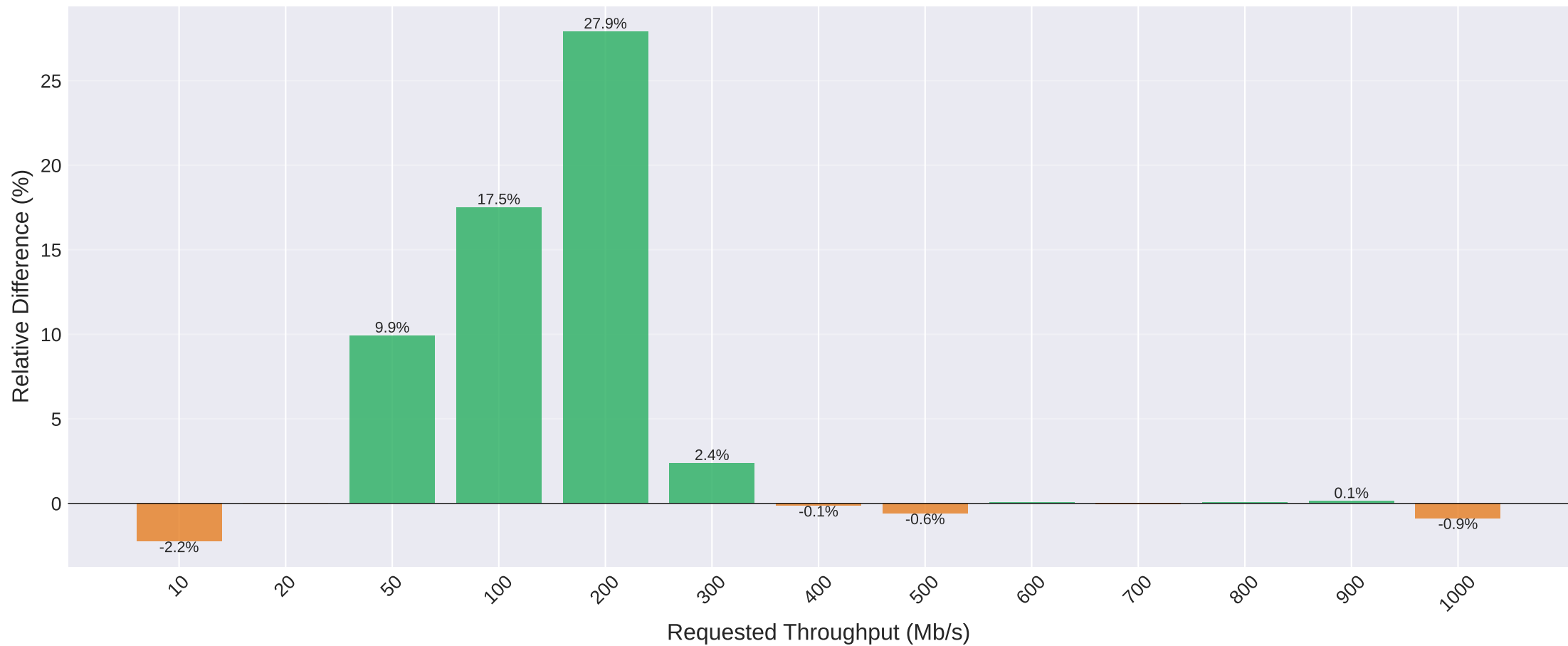
## Relative Difference (PNK vs C SIM)



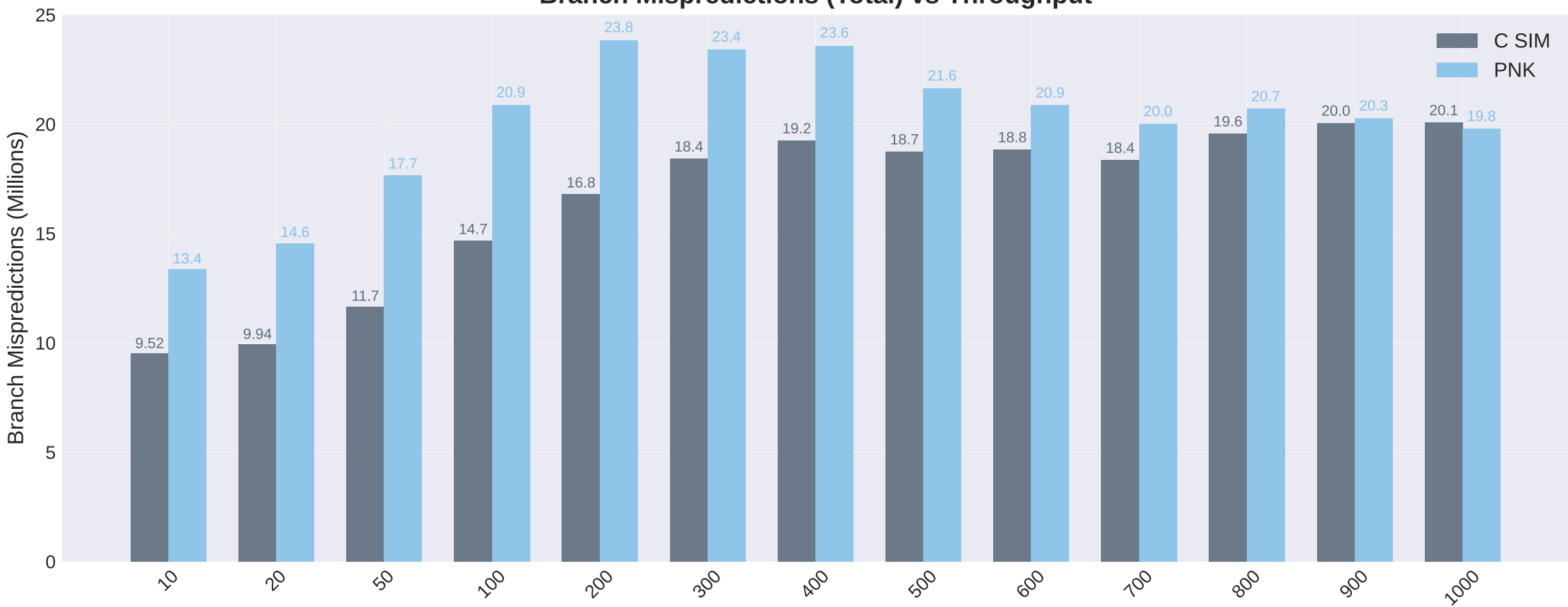
# Instructions (Total) vs Throughput



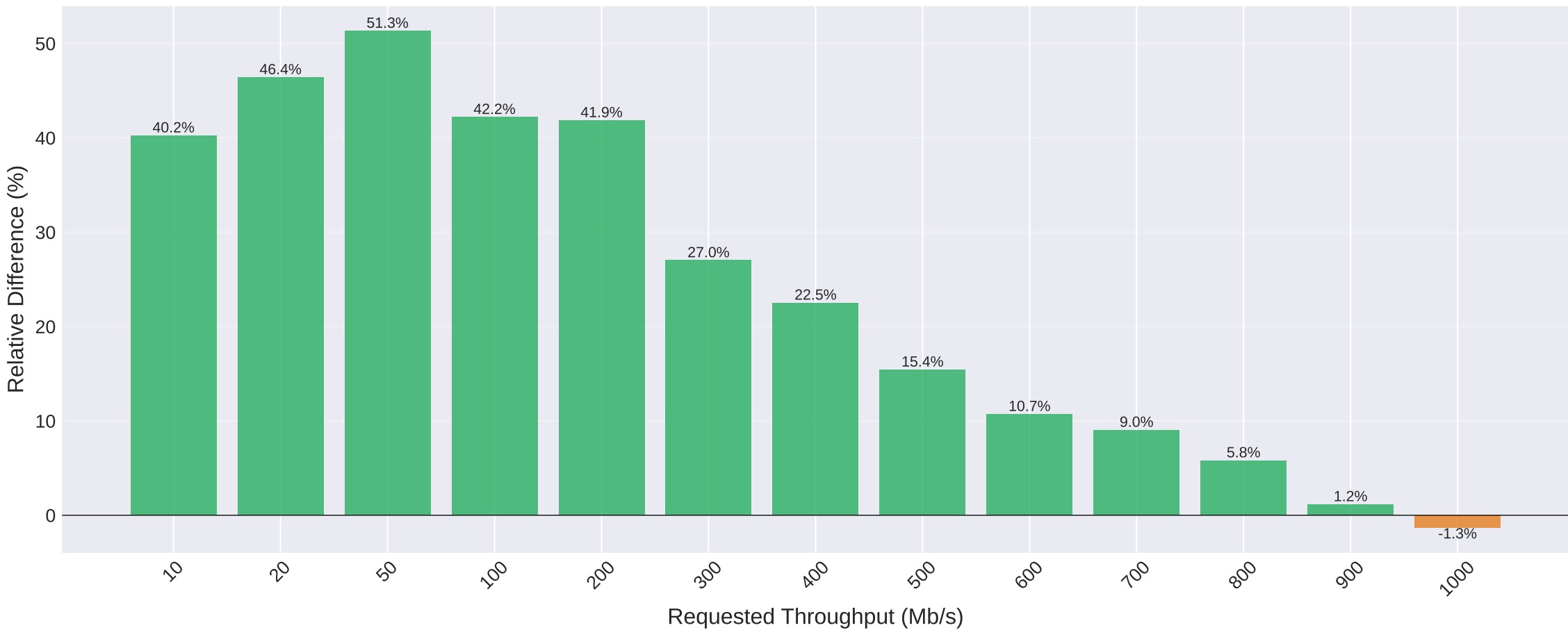
## Relative Difference (PNK vs C SIM)



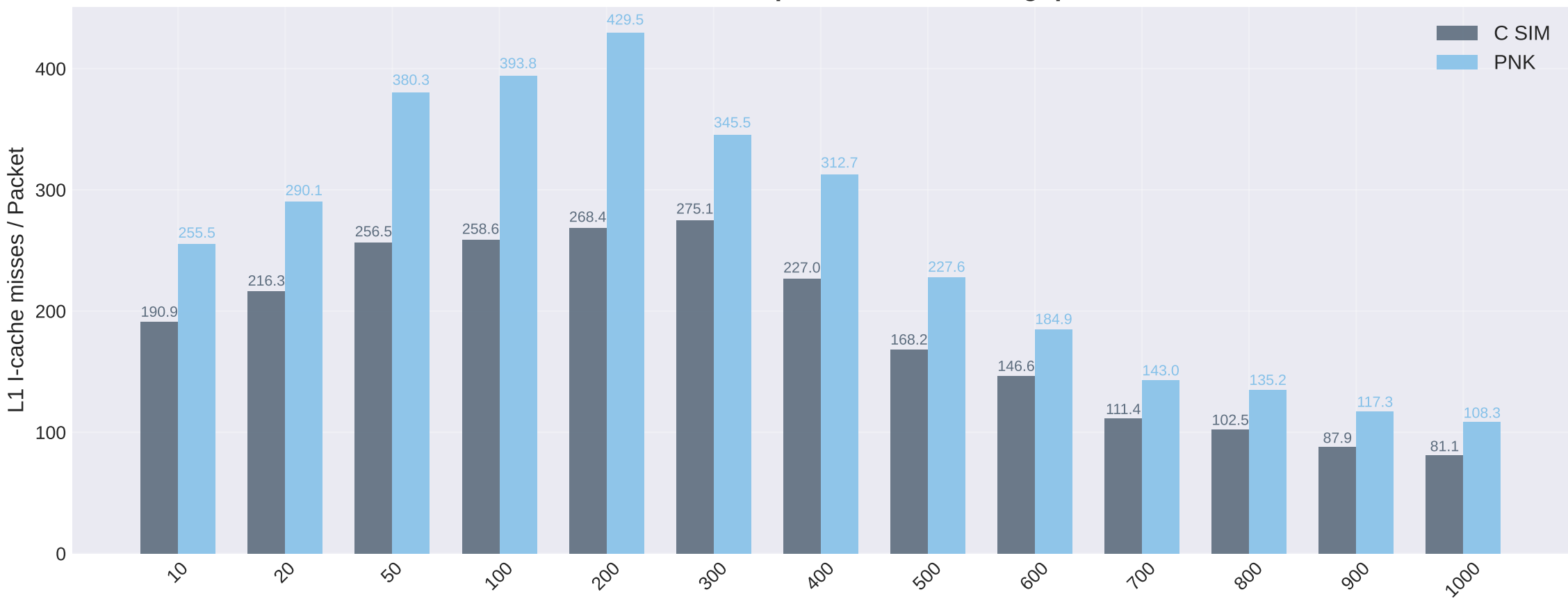
# Branch Mispredictions (Total) vs Throughput



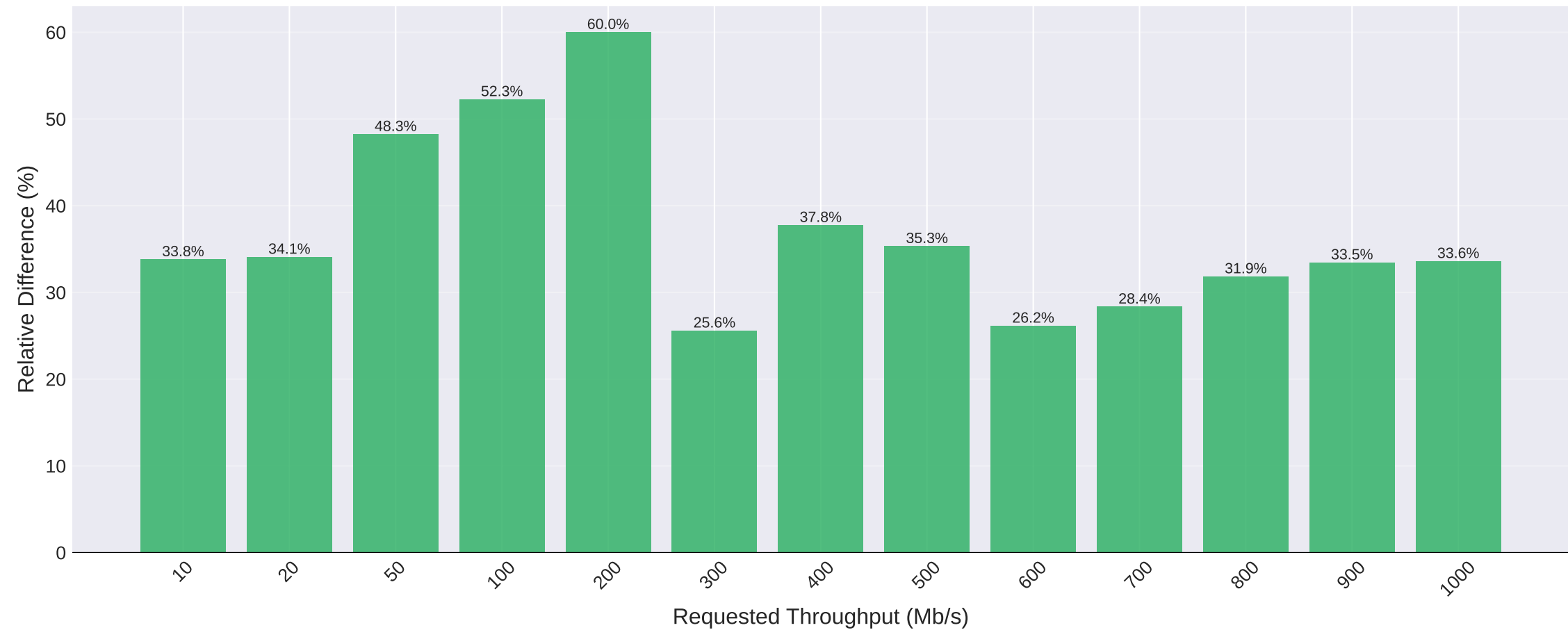
## Relative Difference (PNK vs C SIM)



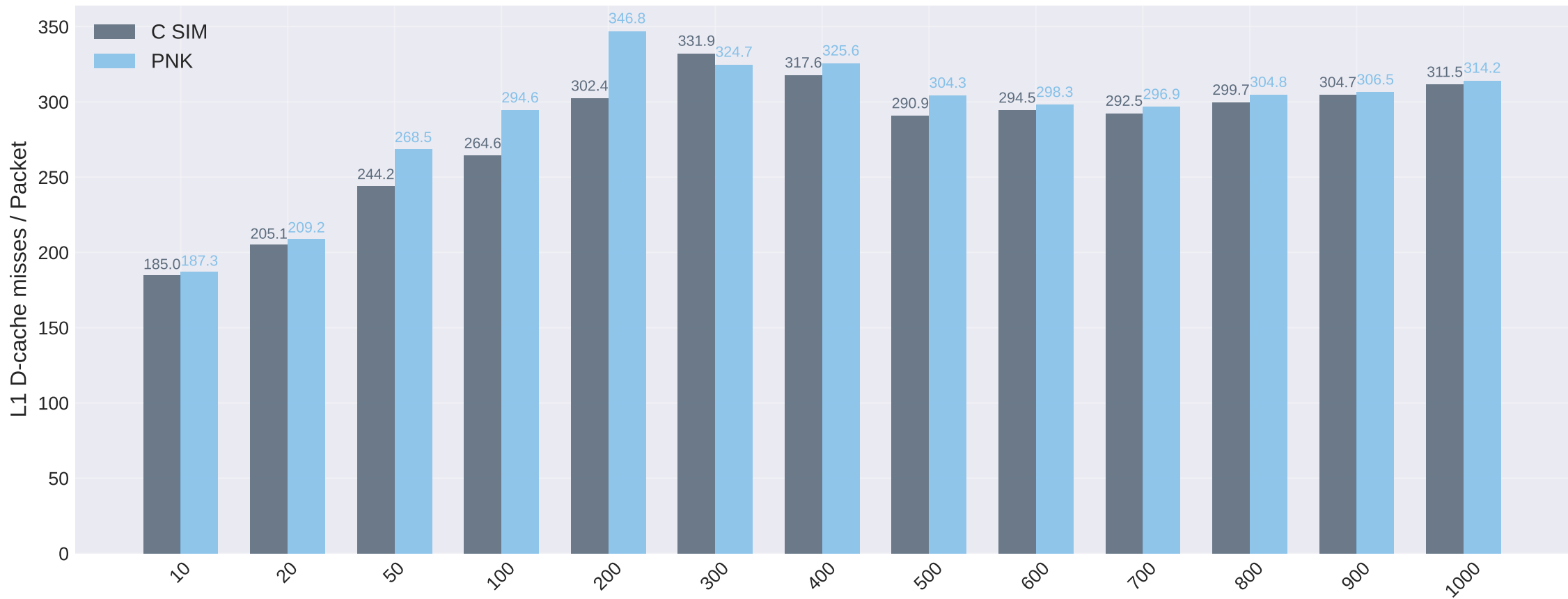
# L1 I-cache Misses per Packet vs Throughput



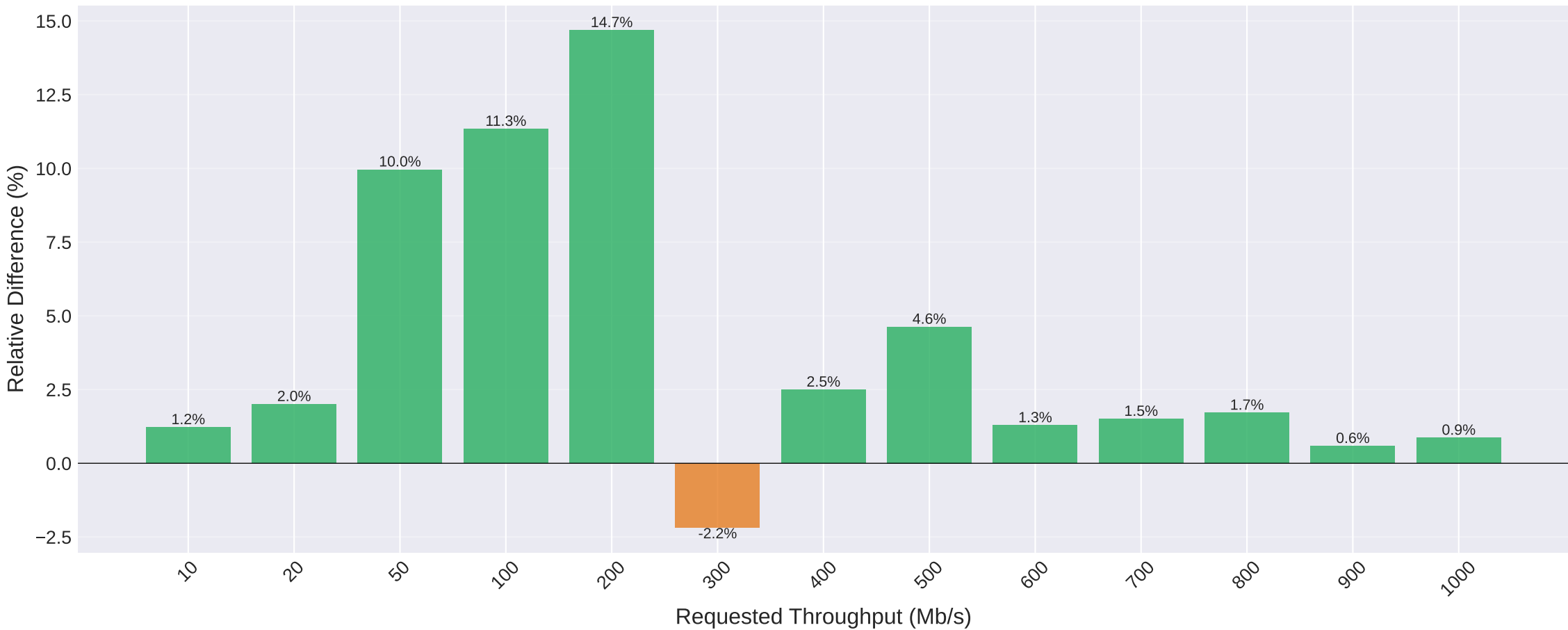
## Relative Difference (PNK vs C SIM)



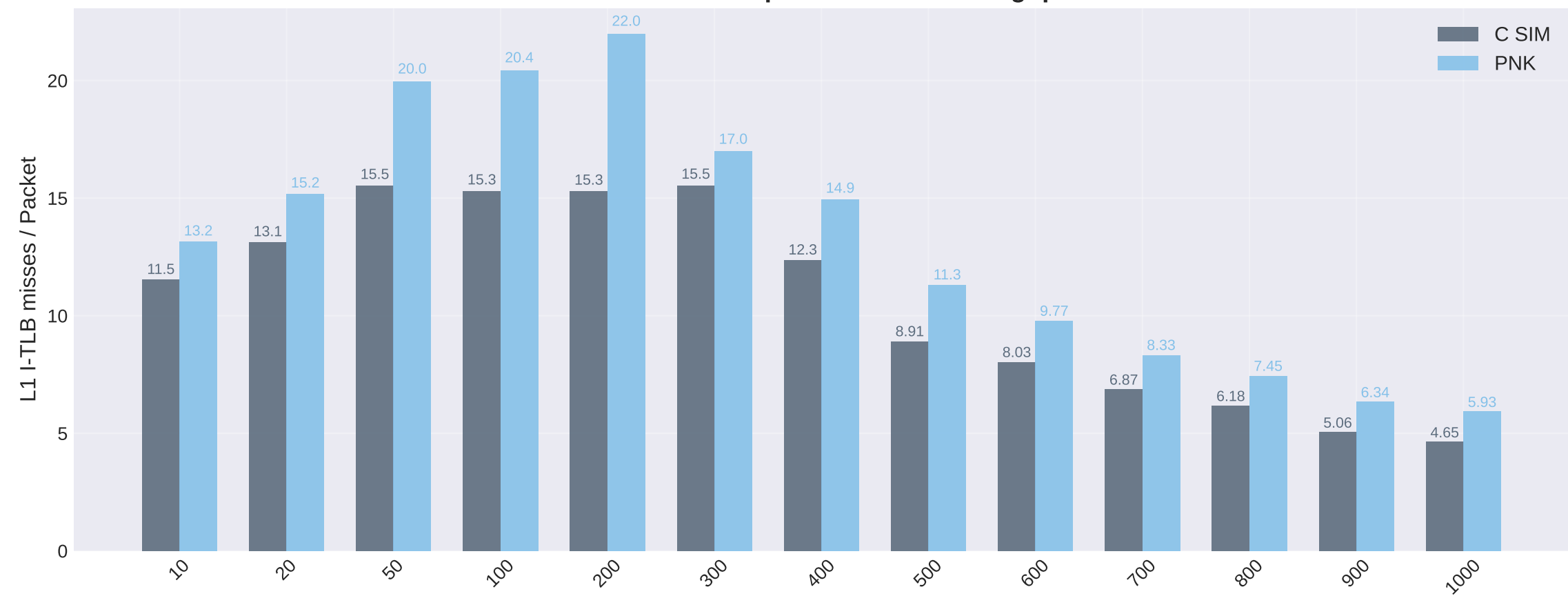
# L1 D-cache Misses per Packet vs Throughput



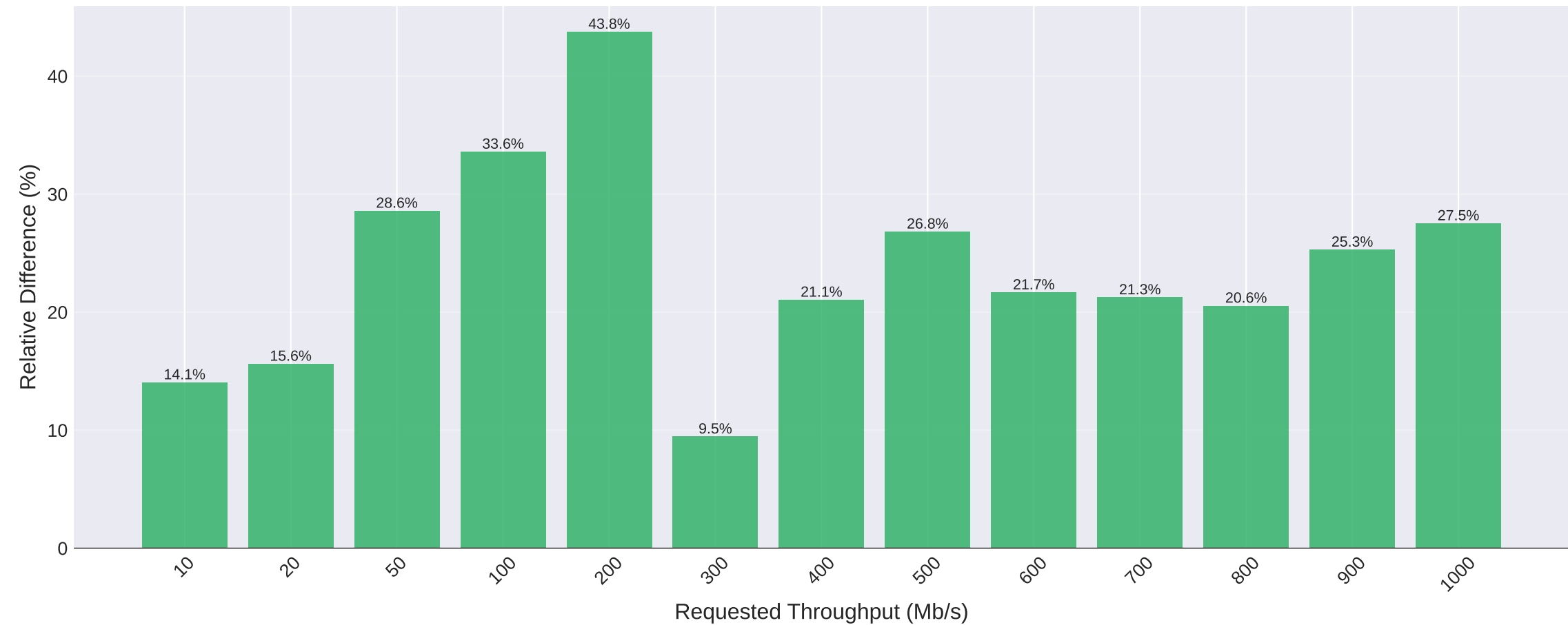
## Relative Difference (PNK vs C SIM)



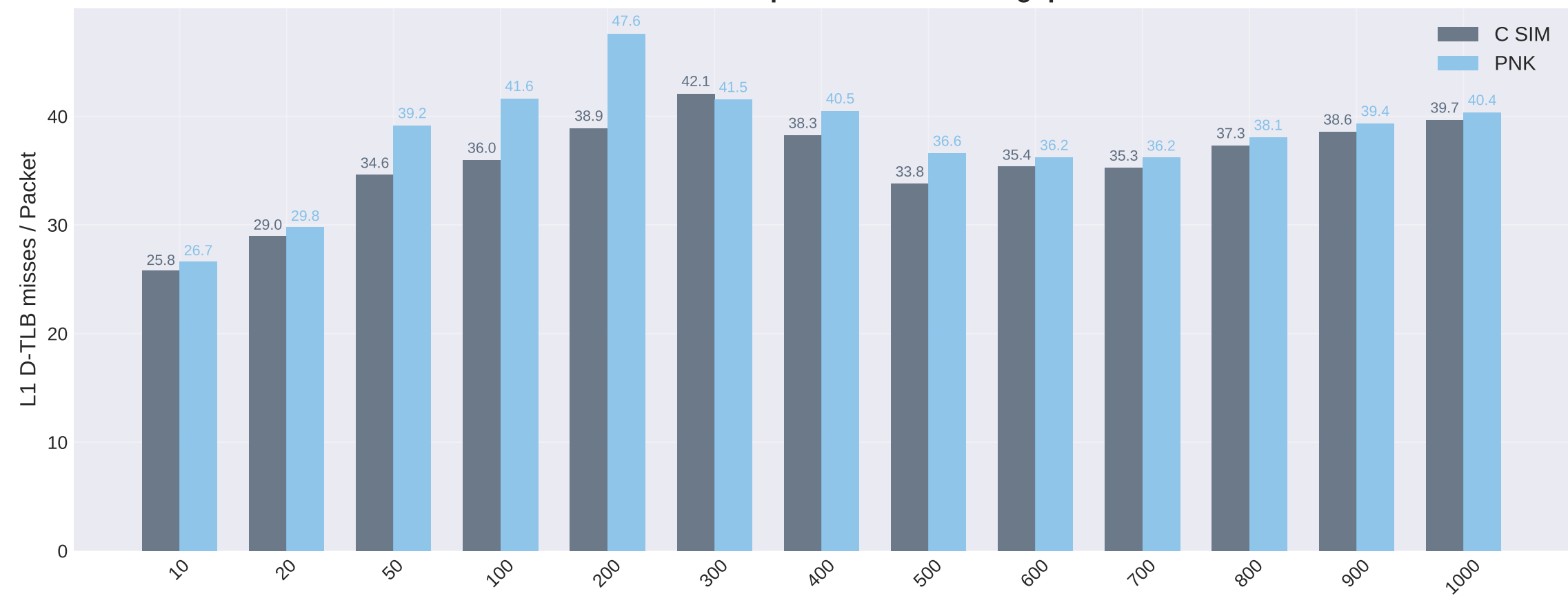
# L1 I-TLB Misses per Packet vs Throughput



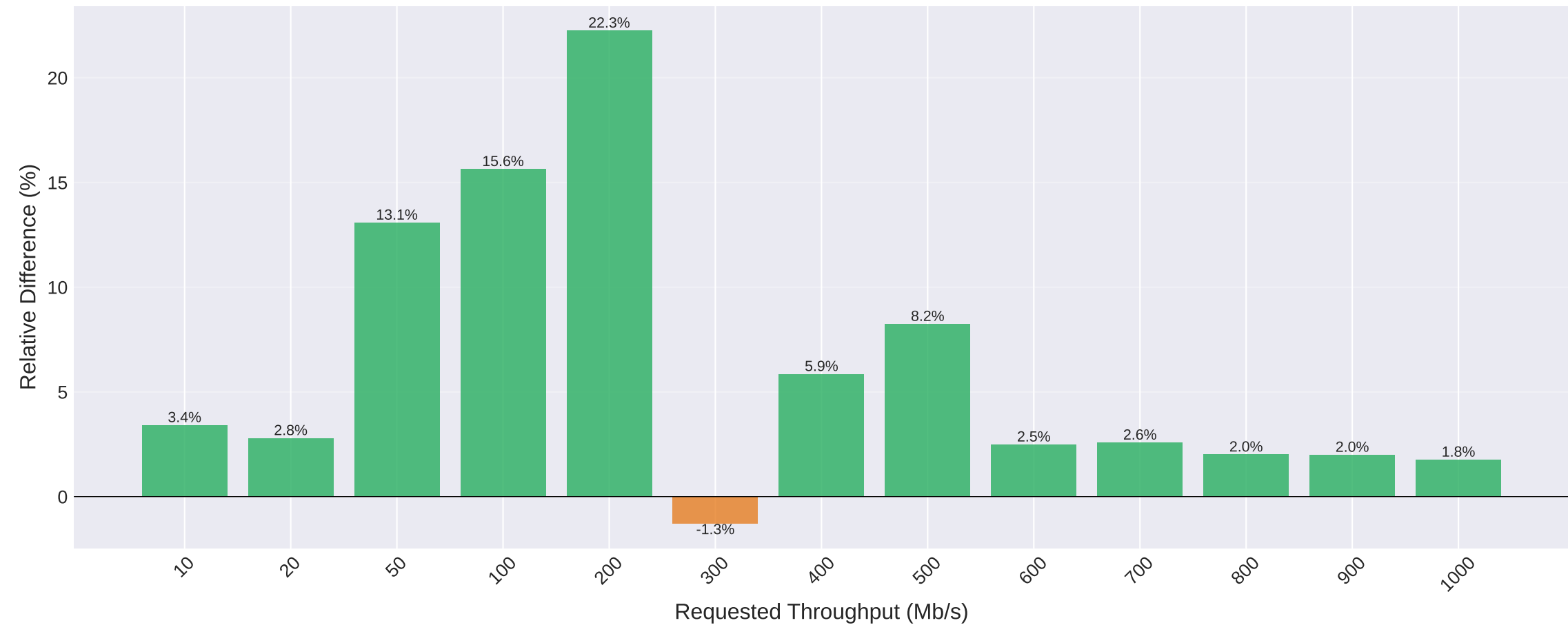
## Relative Difference (PNK vs C SIM)



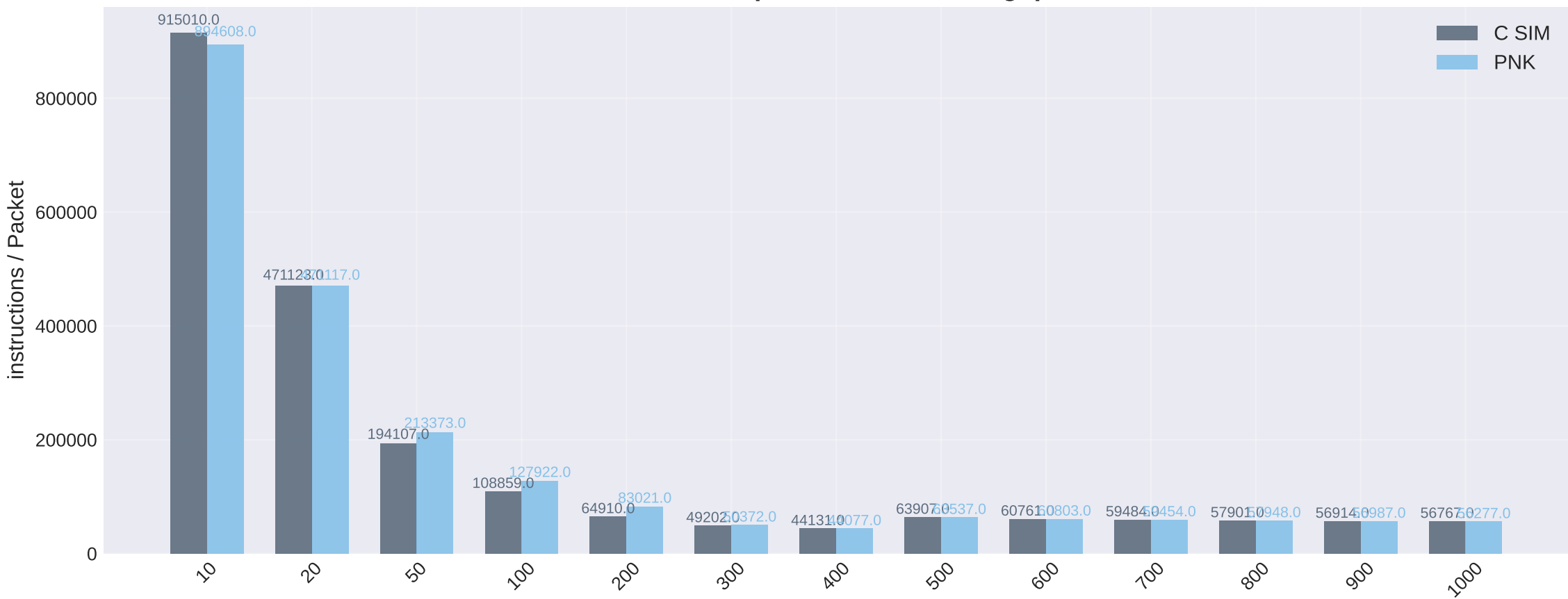
# L1 D-TLB Misses per Packet vs Throughput



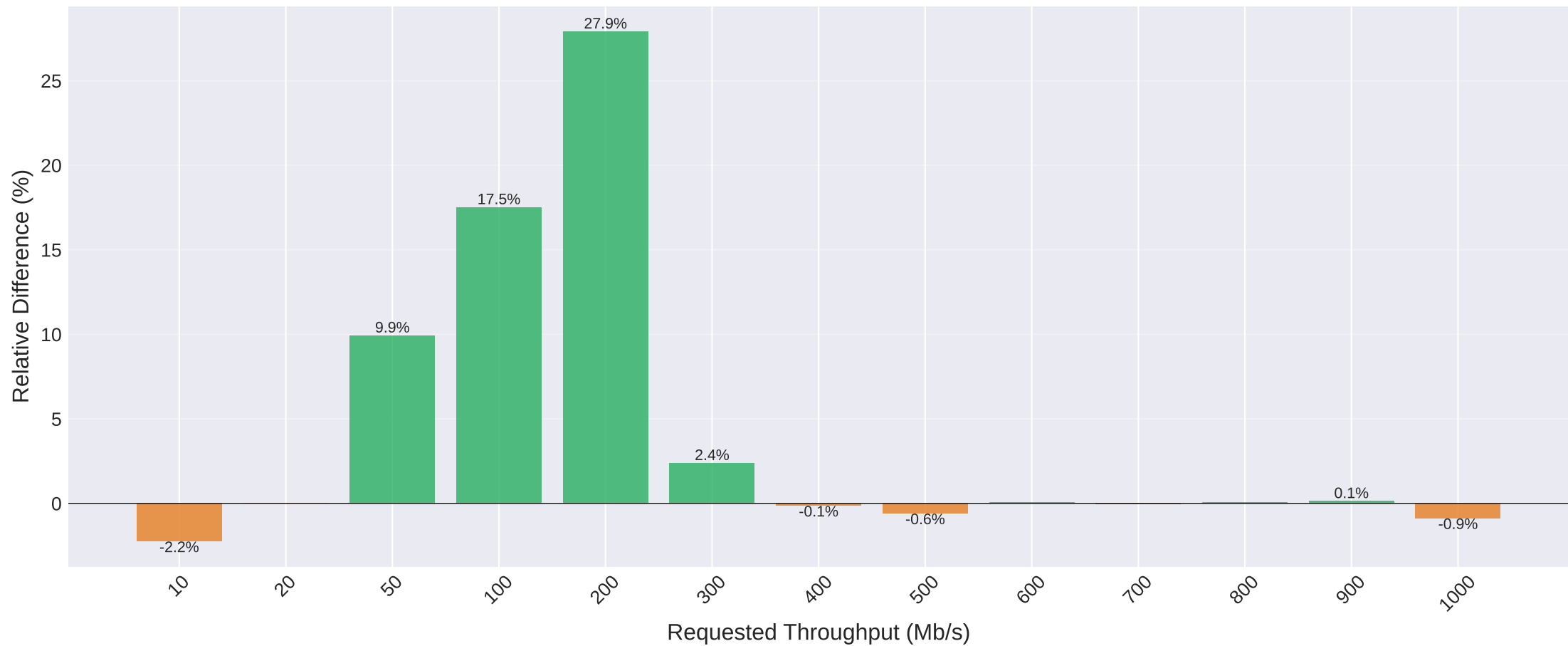
## Relative Difference (PNK vs C SIM)



# Instructions per Packet vs Throughput

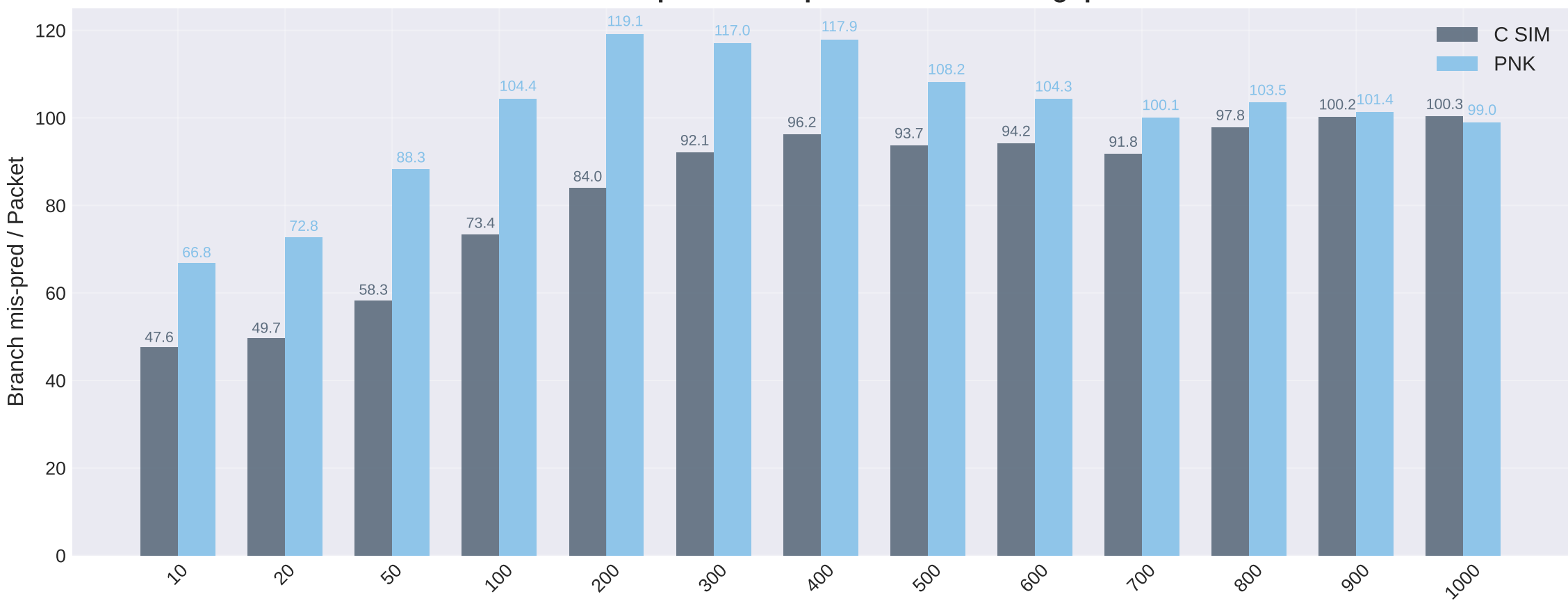


## Relative Difference (PNK vs C SIM)

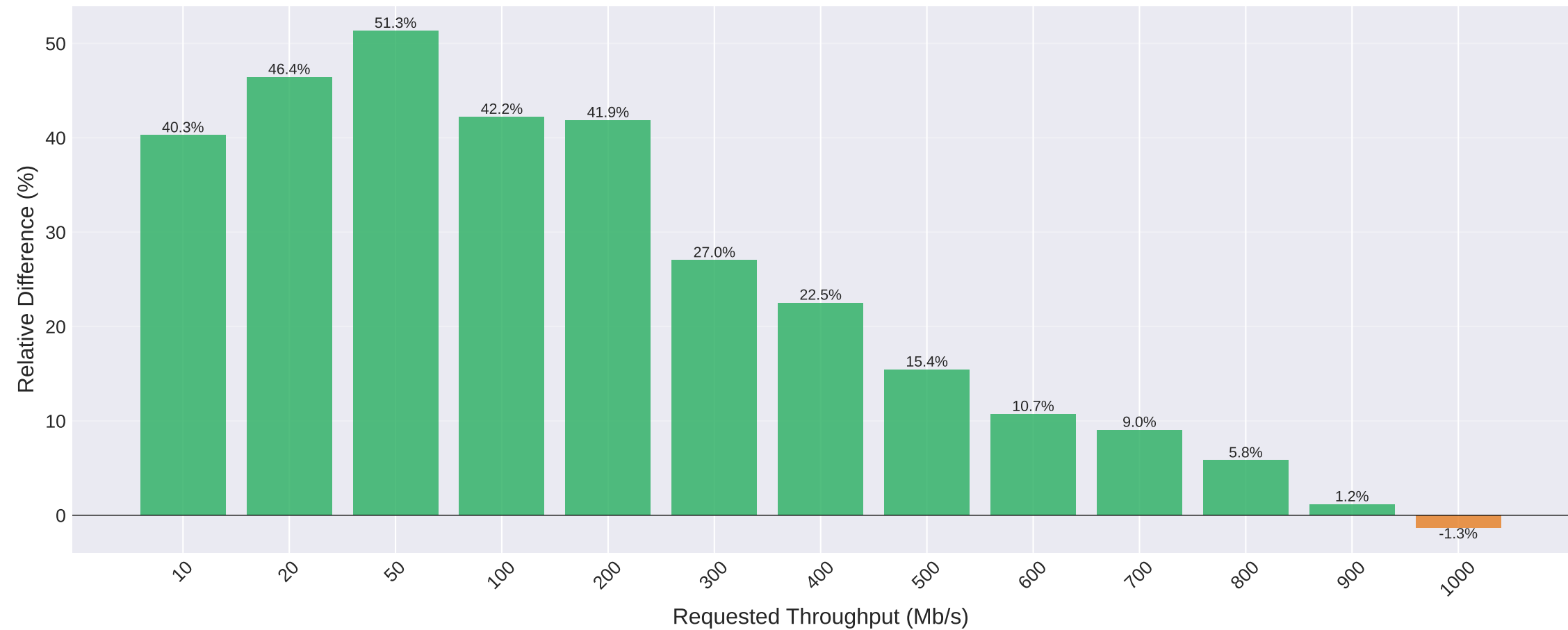




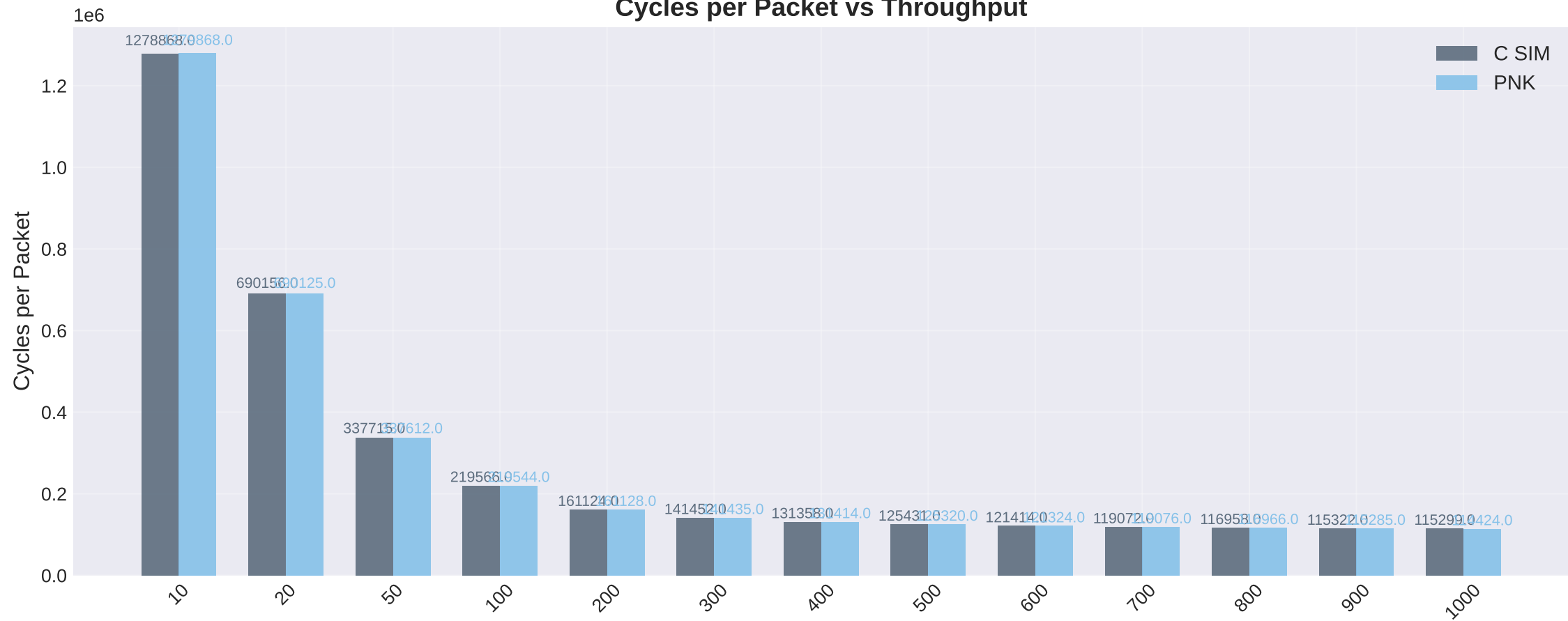
# Branch Mispredictions per Packet vs Throughput



## Relative Difference (PNK vs C SIM)



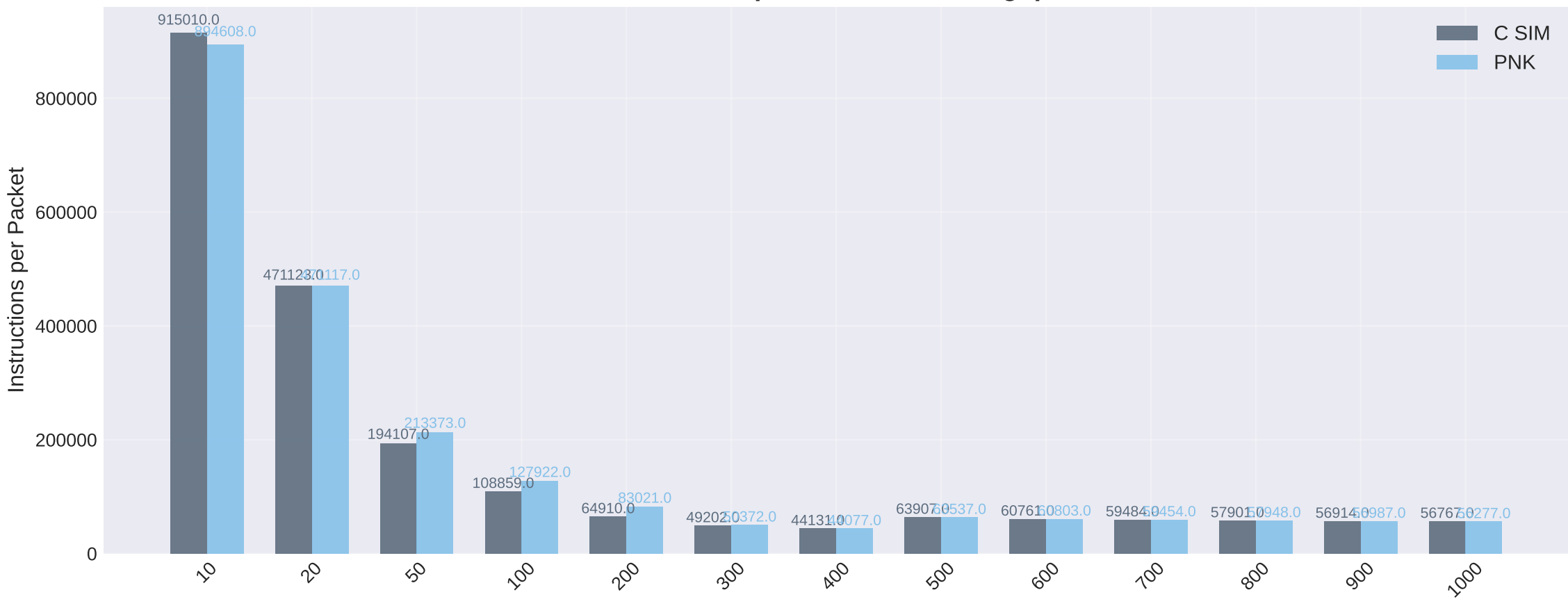
Cycles per Packet vs Throughput



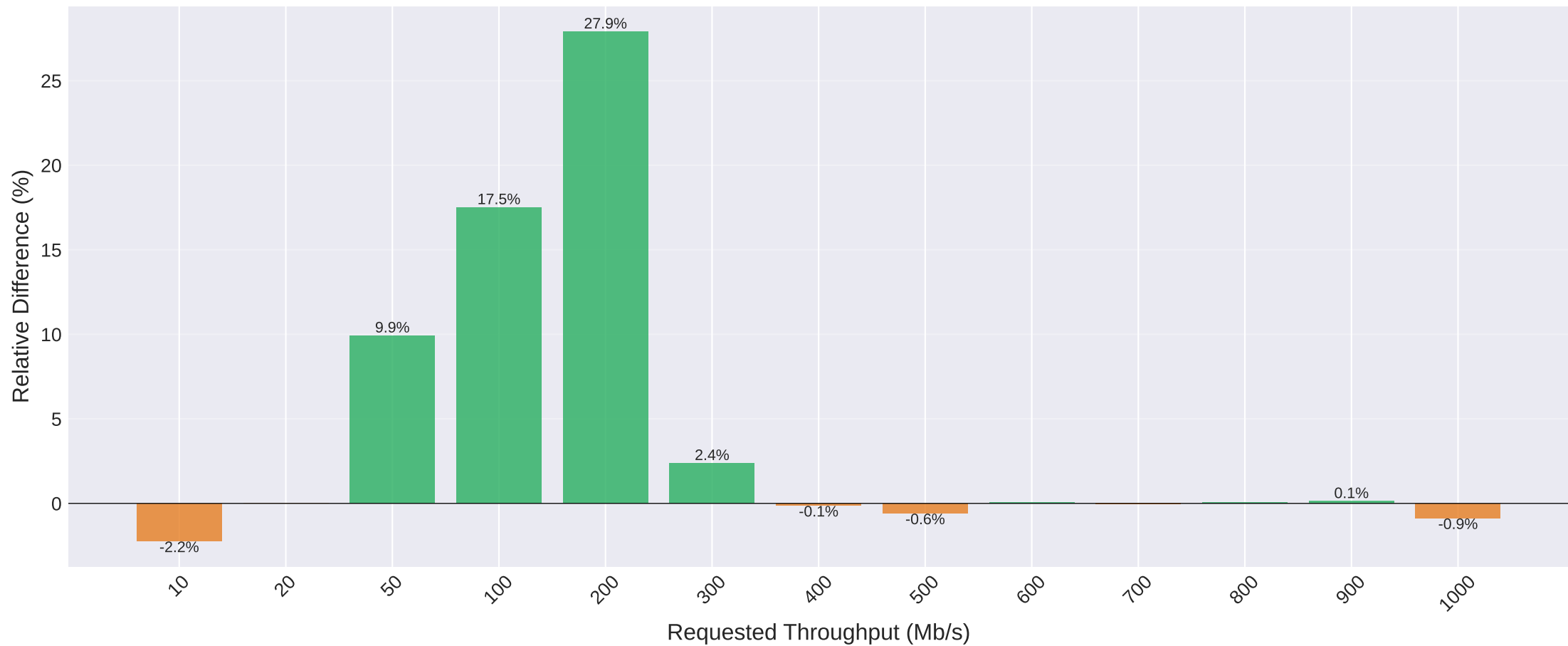
Relative Difference (PNK vs C SIM)



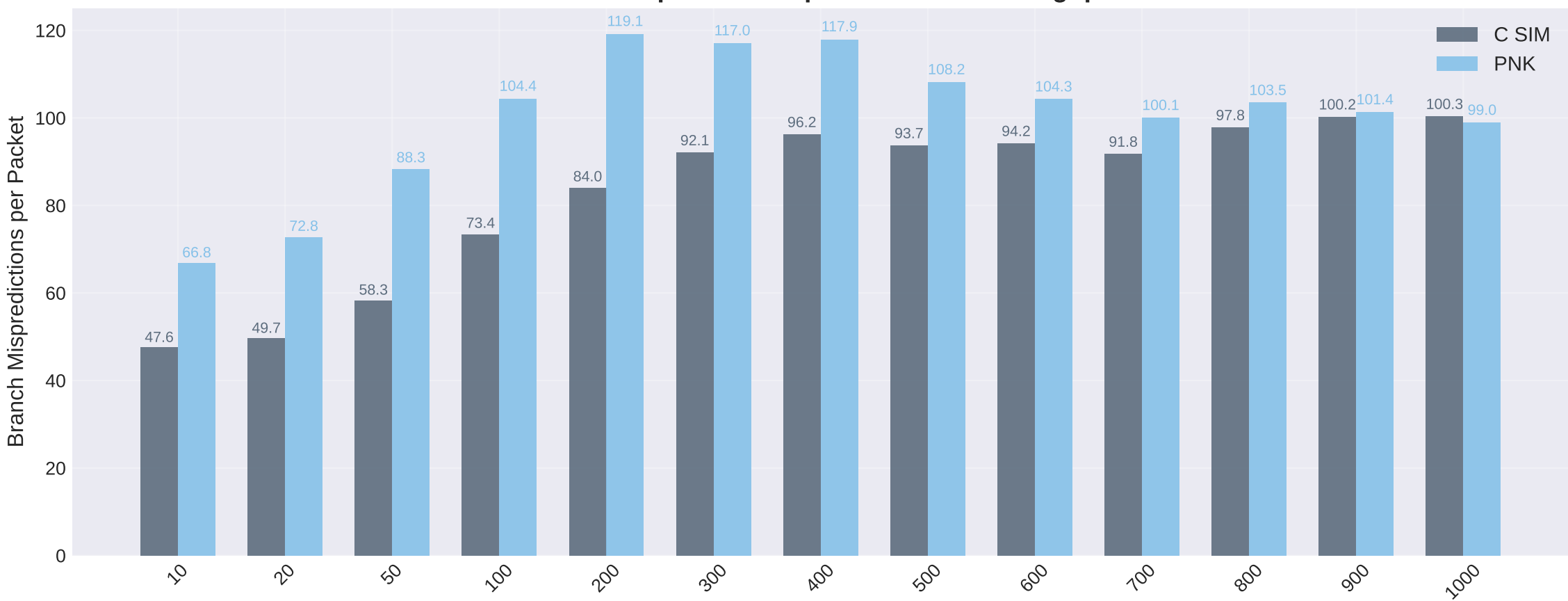
# Instructions per Packet vs Throughput



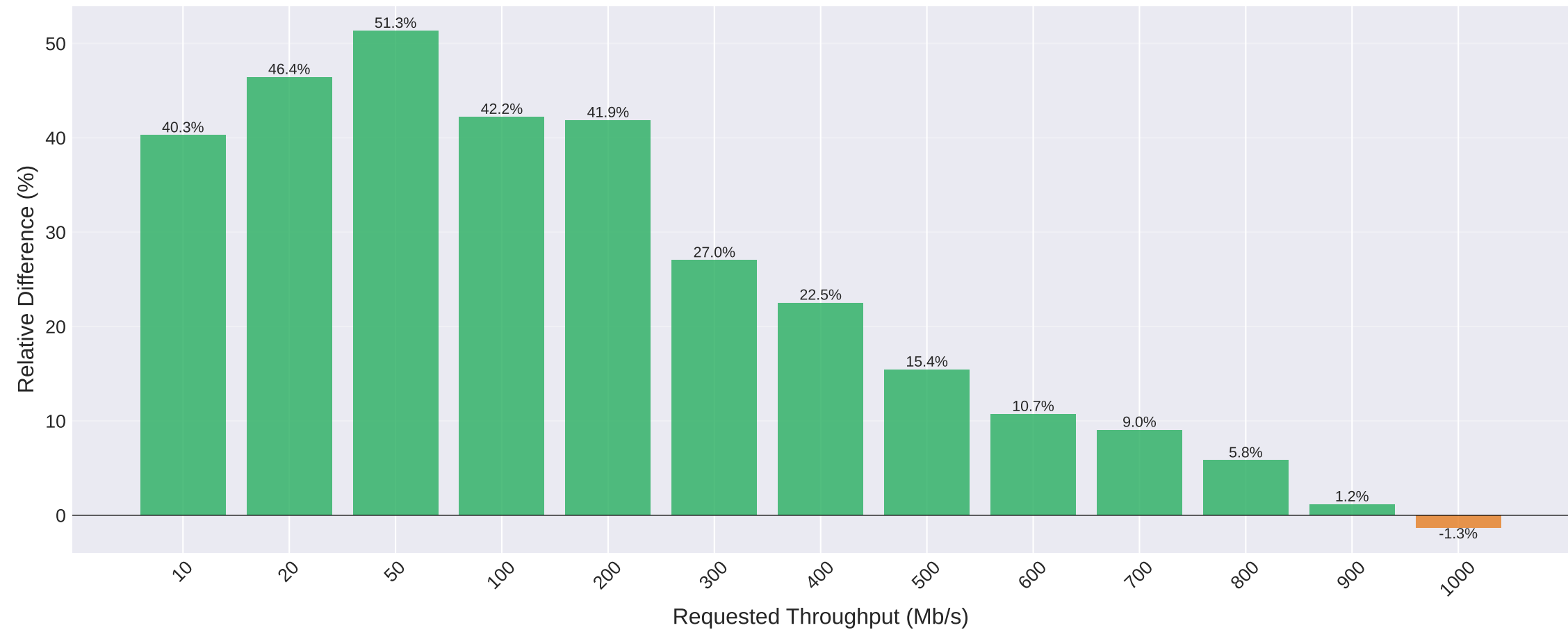
## Relative Difference (PNK vs C SIM)



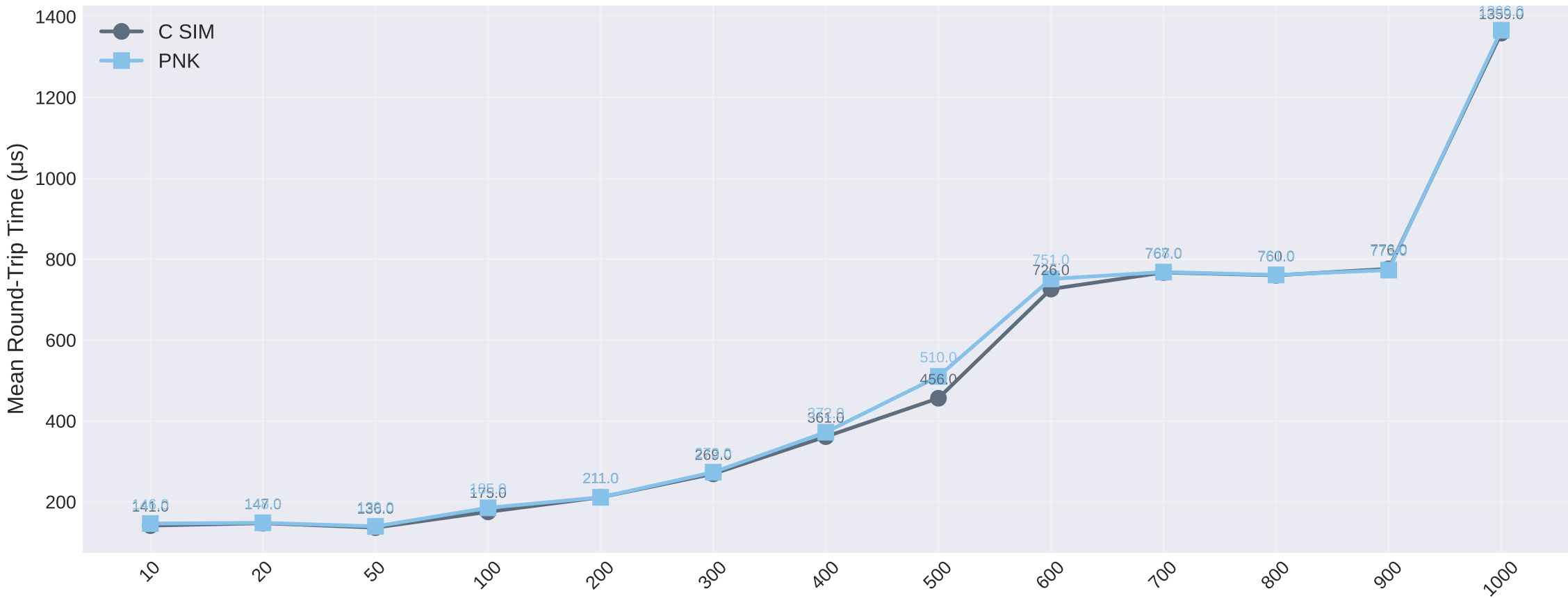
# Branch Mispredictions per Packet vs Throughput



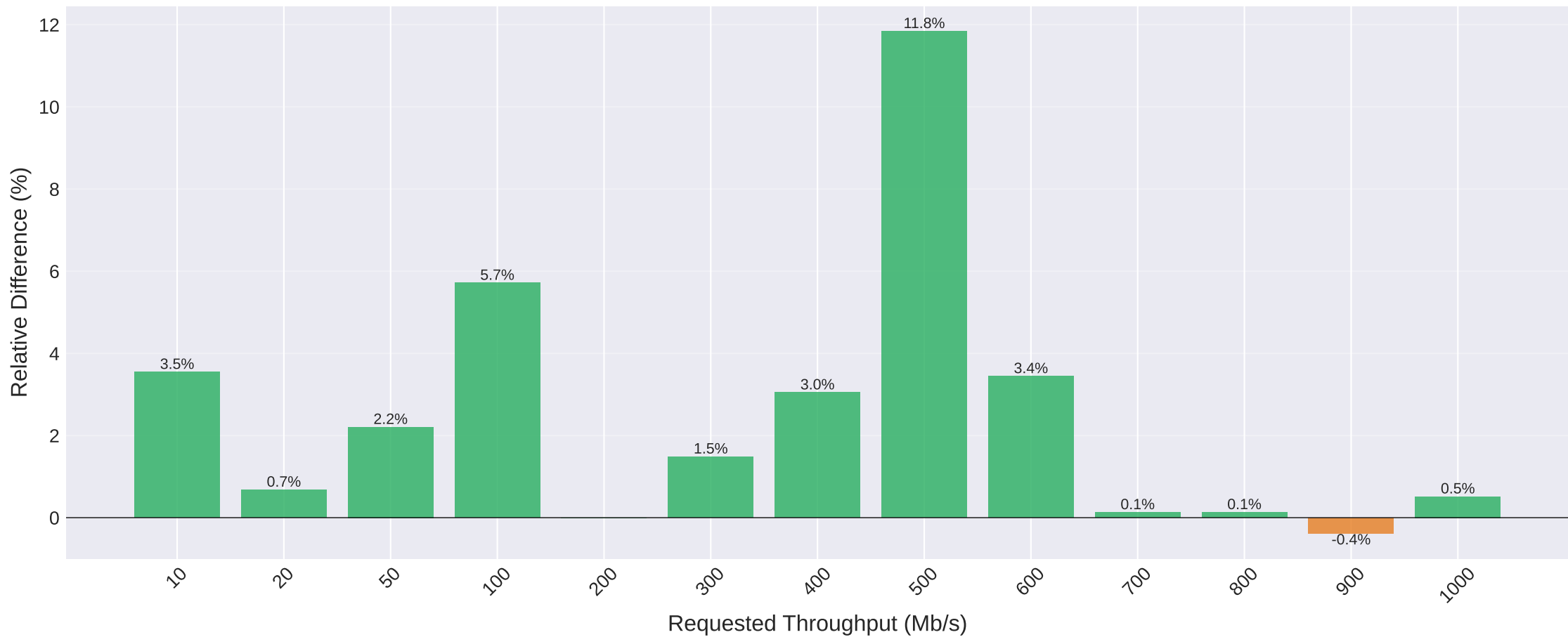
## Relative Difference (PNK vs C SIM)



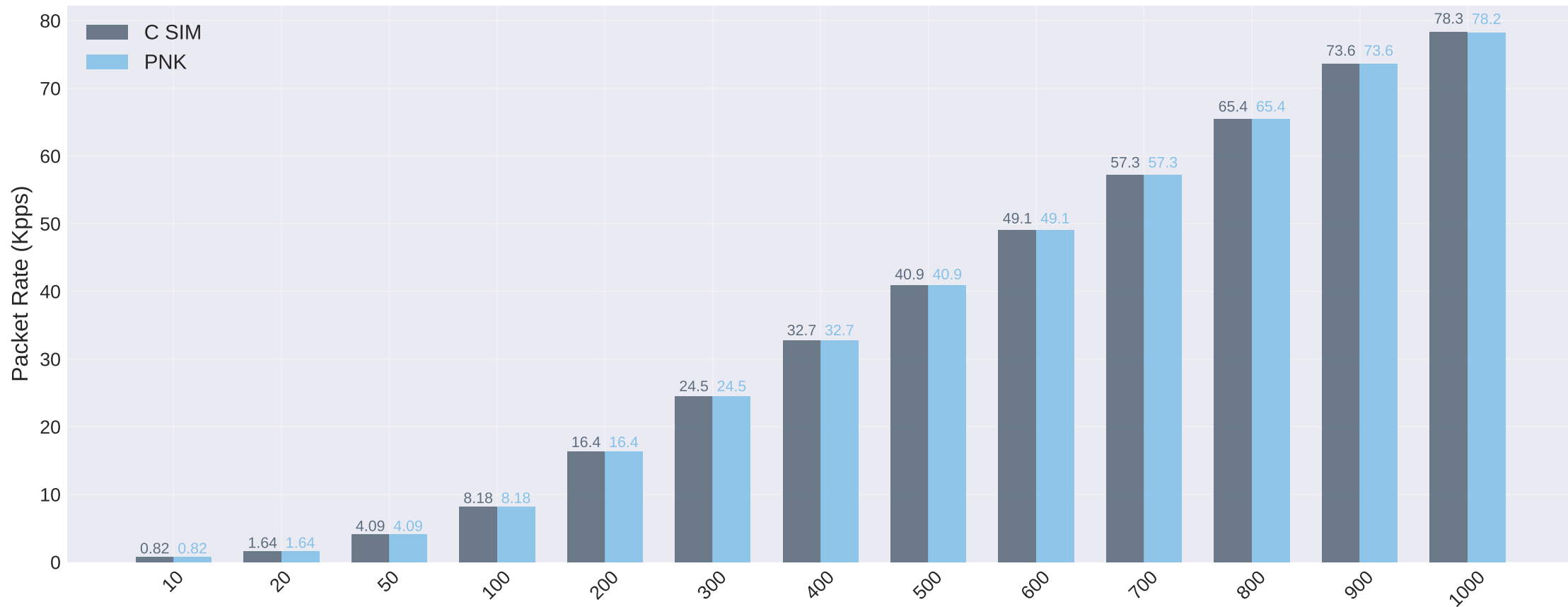
# Mean Round-Trip Time ( $\mu$ s) vs Throughput



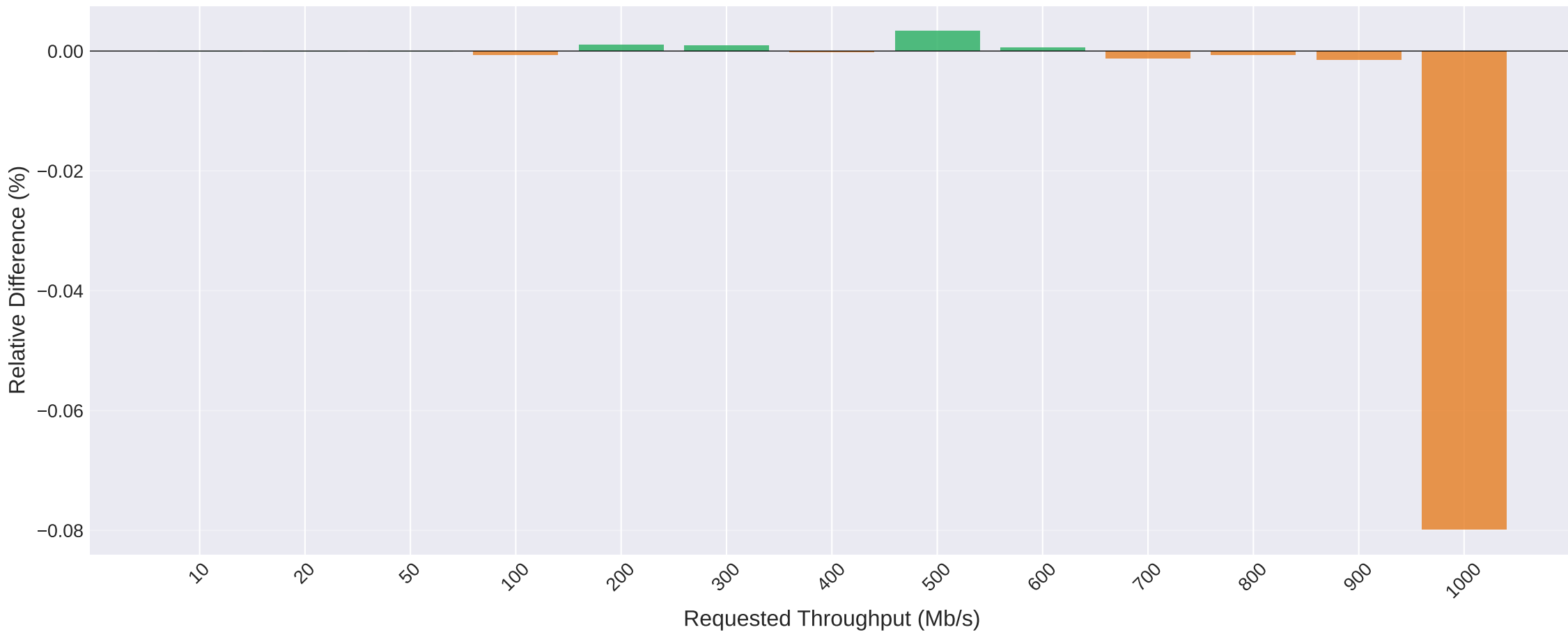
## Relative Difference (PNK vs C SIM)



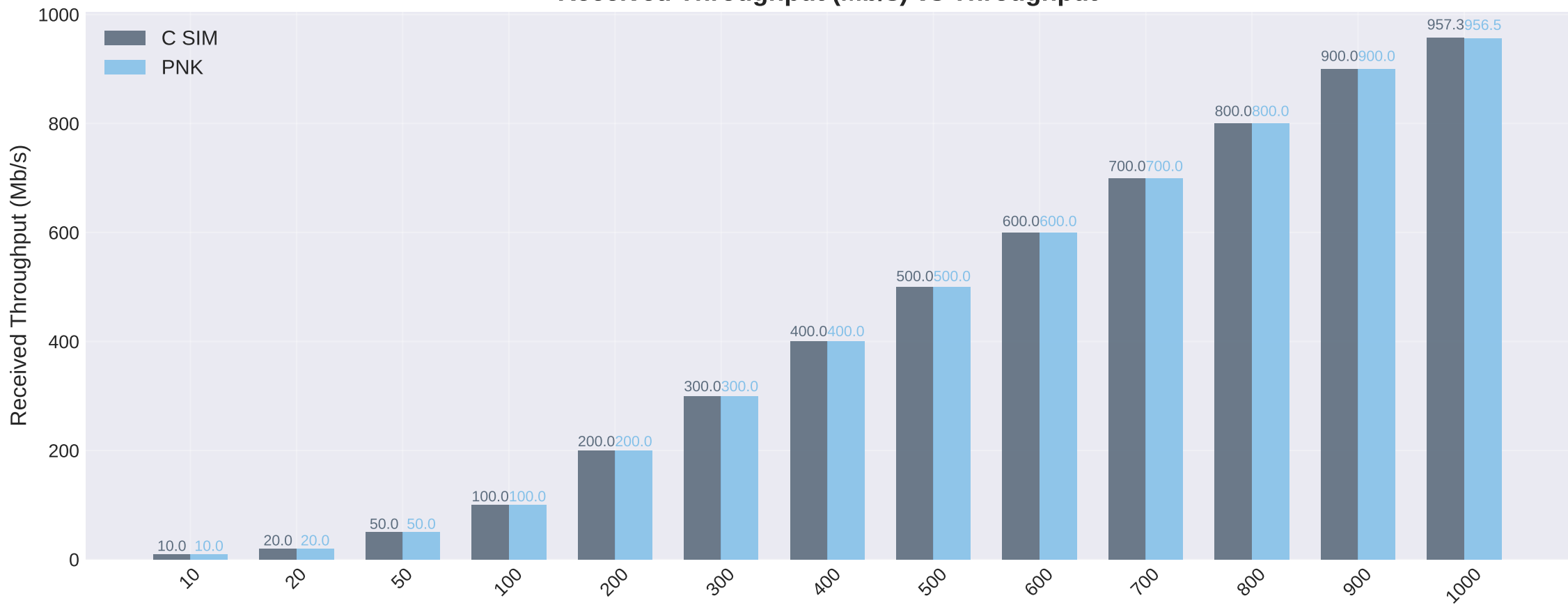
# Packet Rate (packets/s) vs Throughput



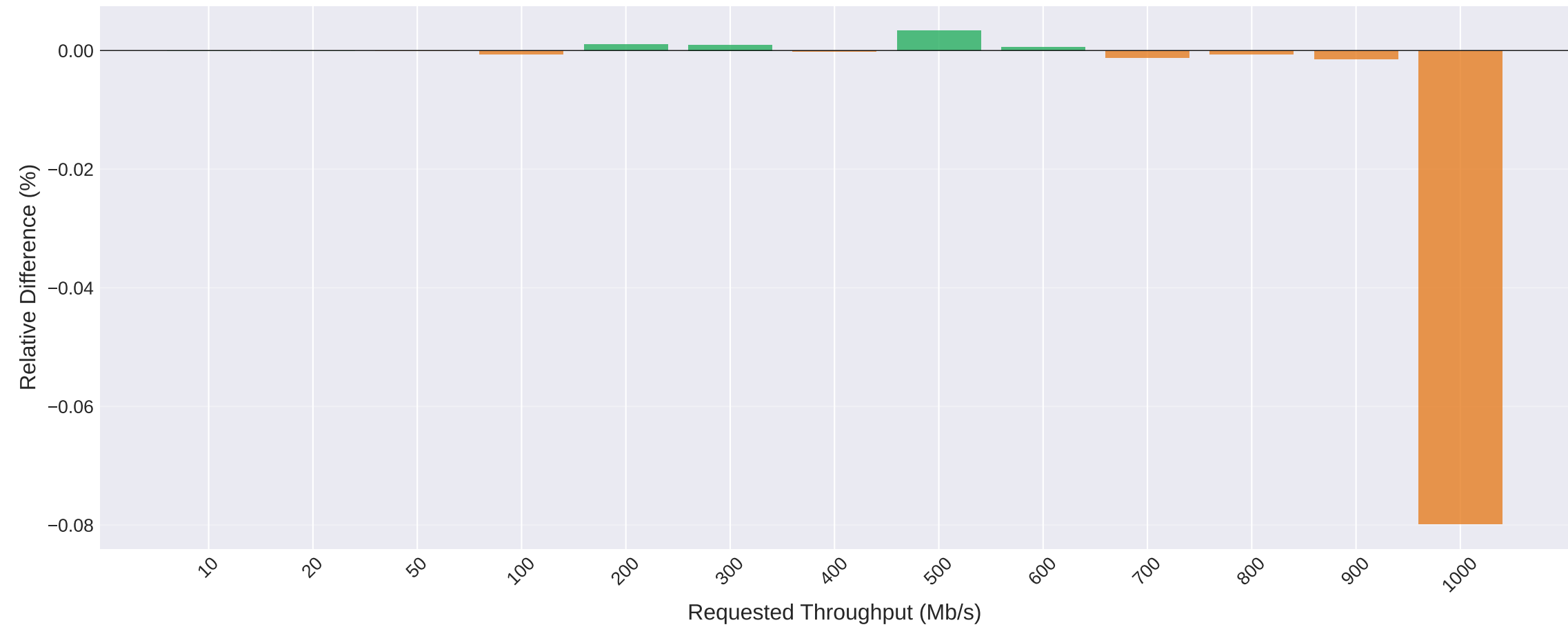
## Relative Difference (PNK vs C SIM)



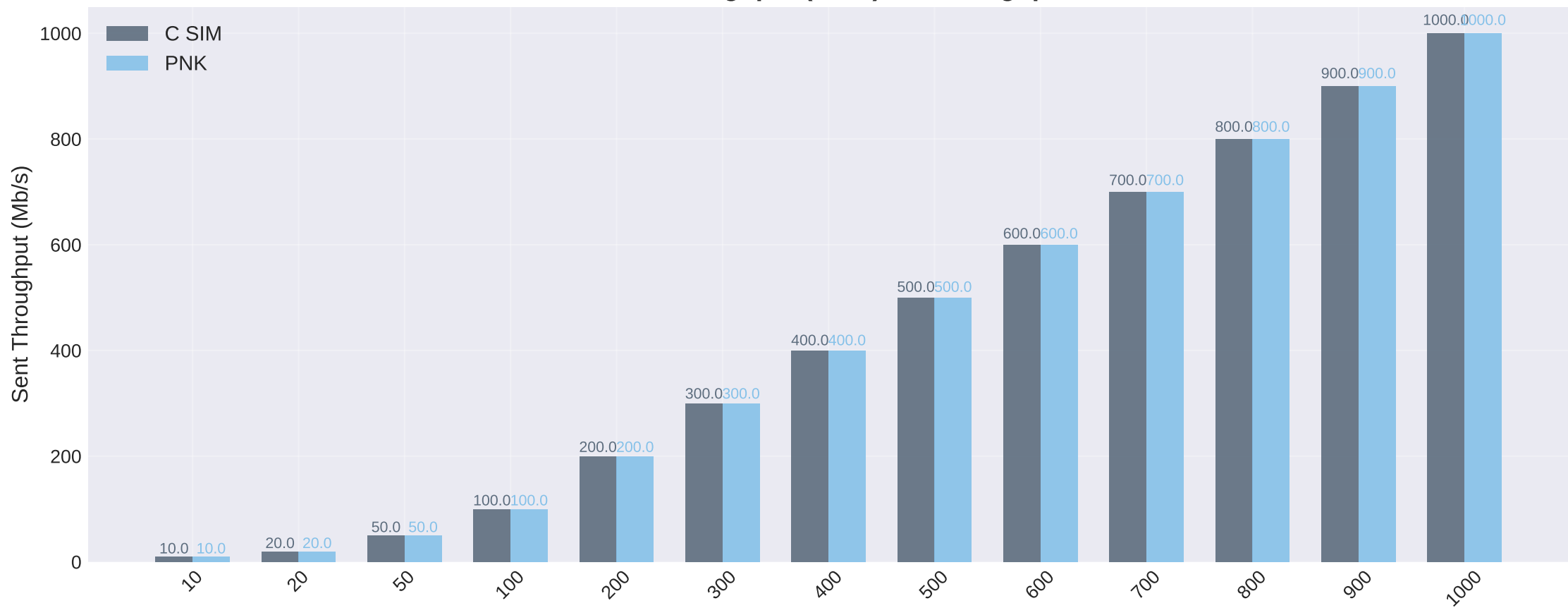
Received Throughput (Mb/s) vs Throughput



Relative Difference (PNK vs C SIM)



Sent Throughput (Mb/s) vs Throughput



Relative Difference (PNK vs C SIM)

