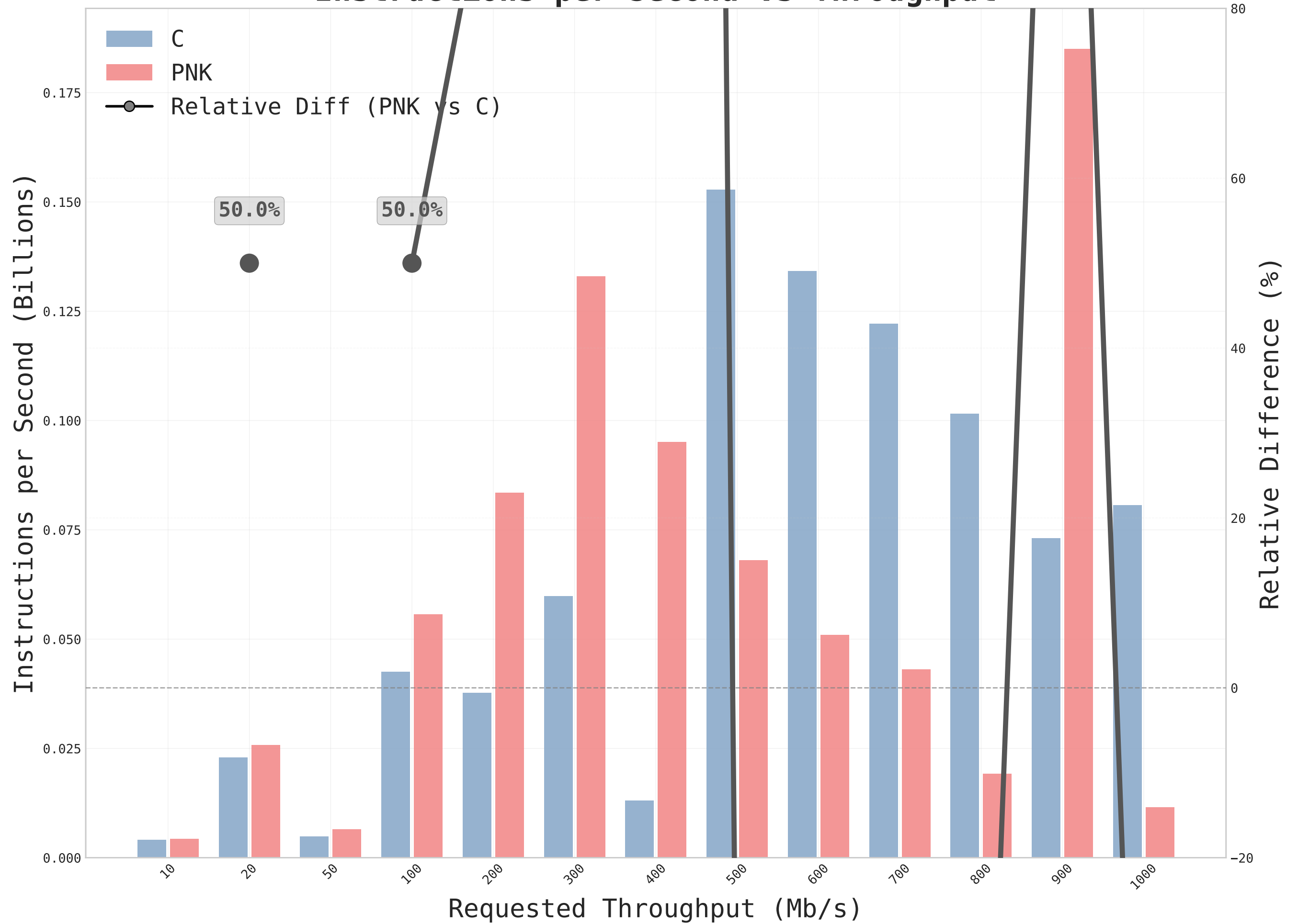
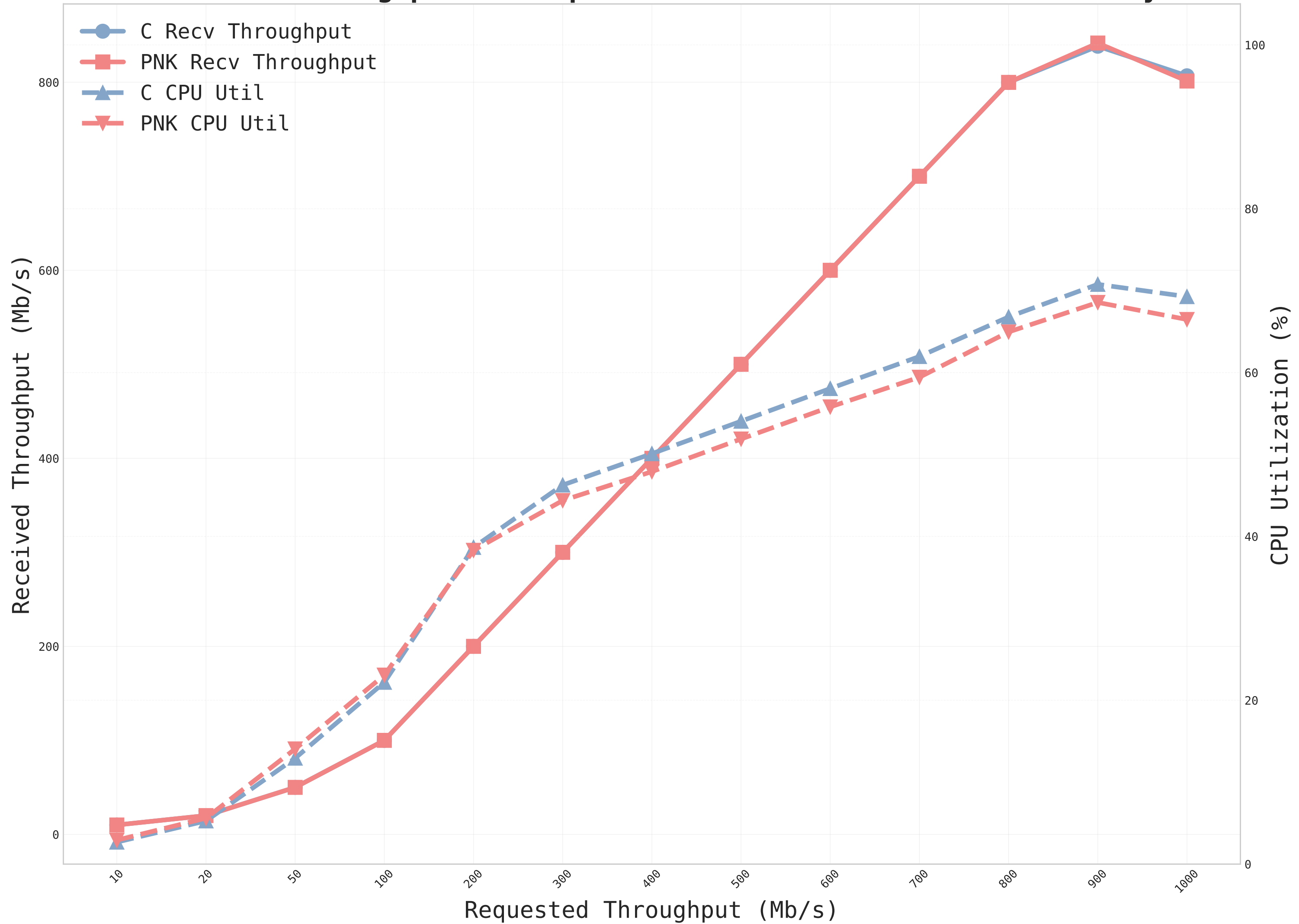


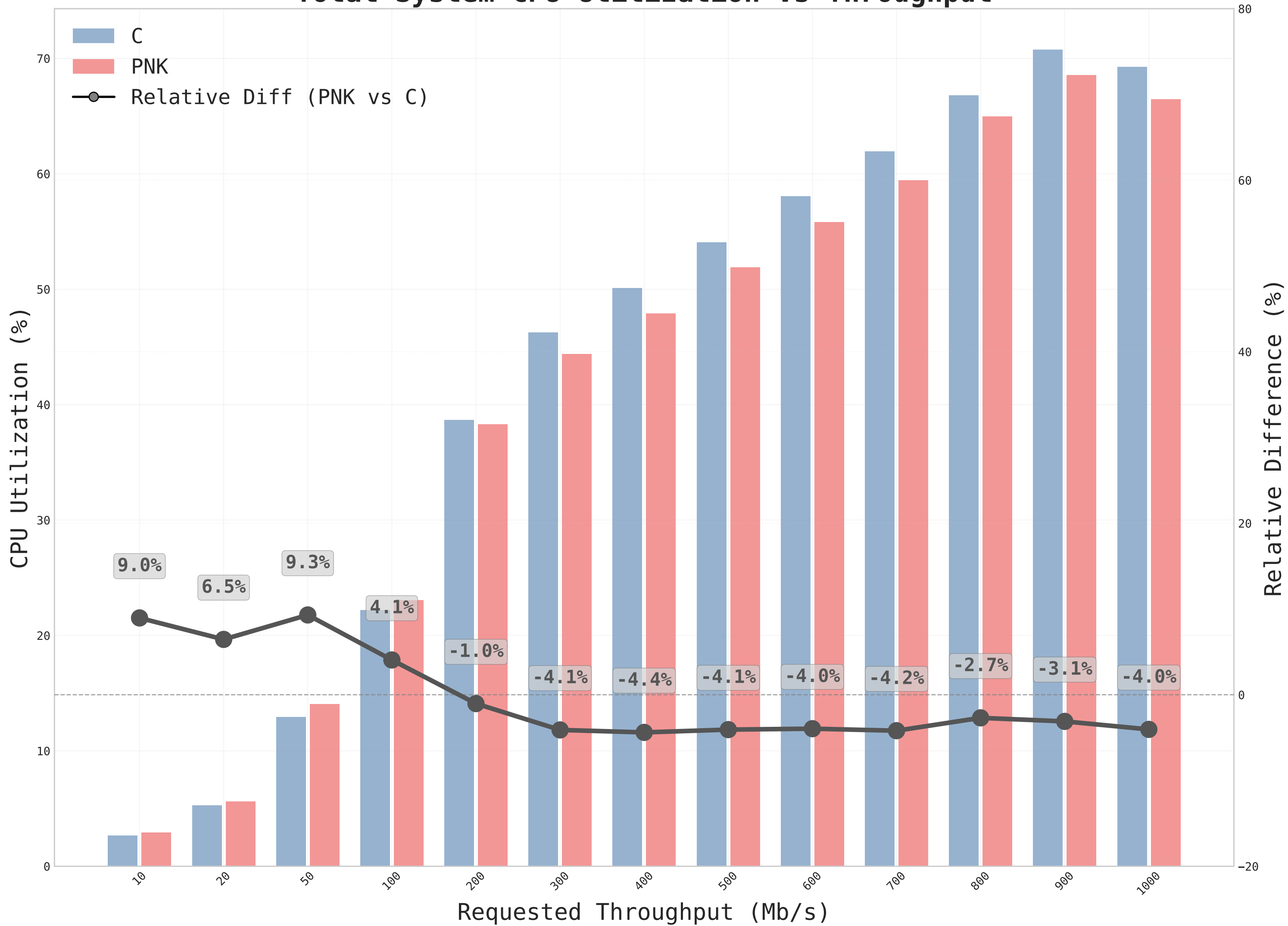
Instructions per Second vs Throughput



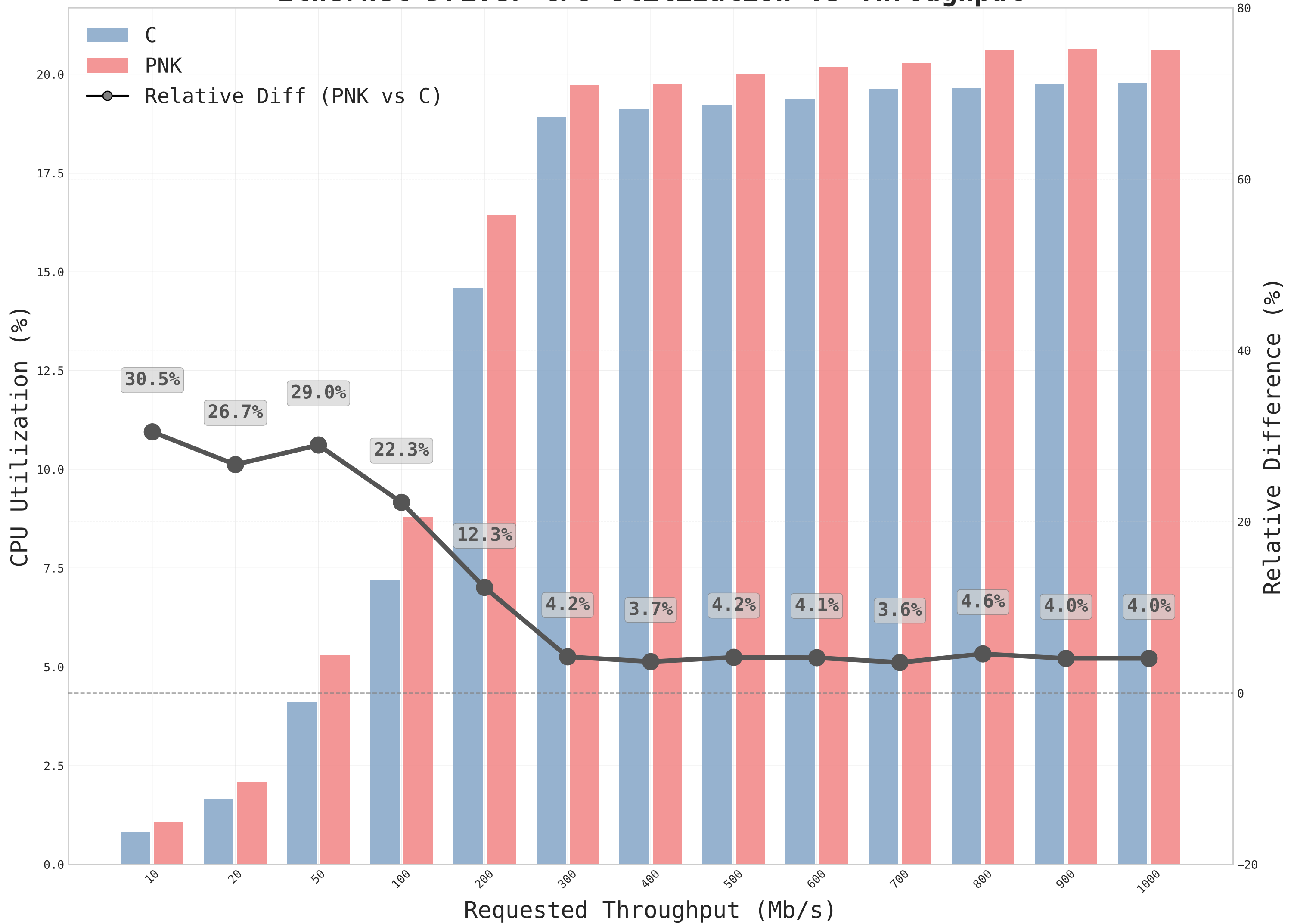
Received Throughput vs Requested with CPU Utilization Overlay



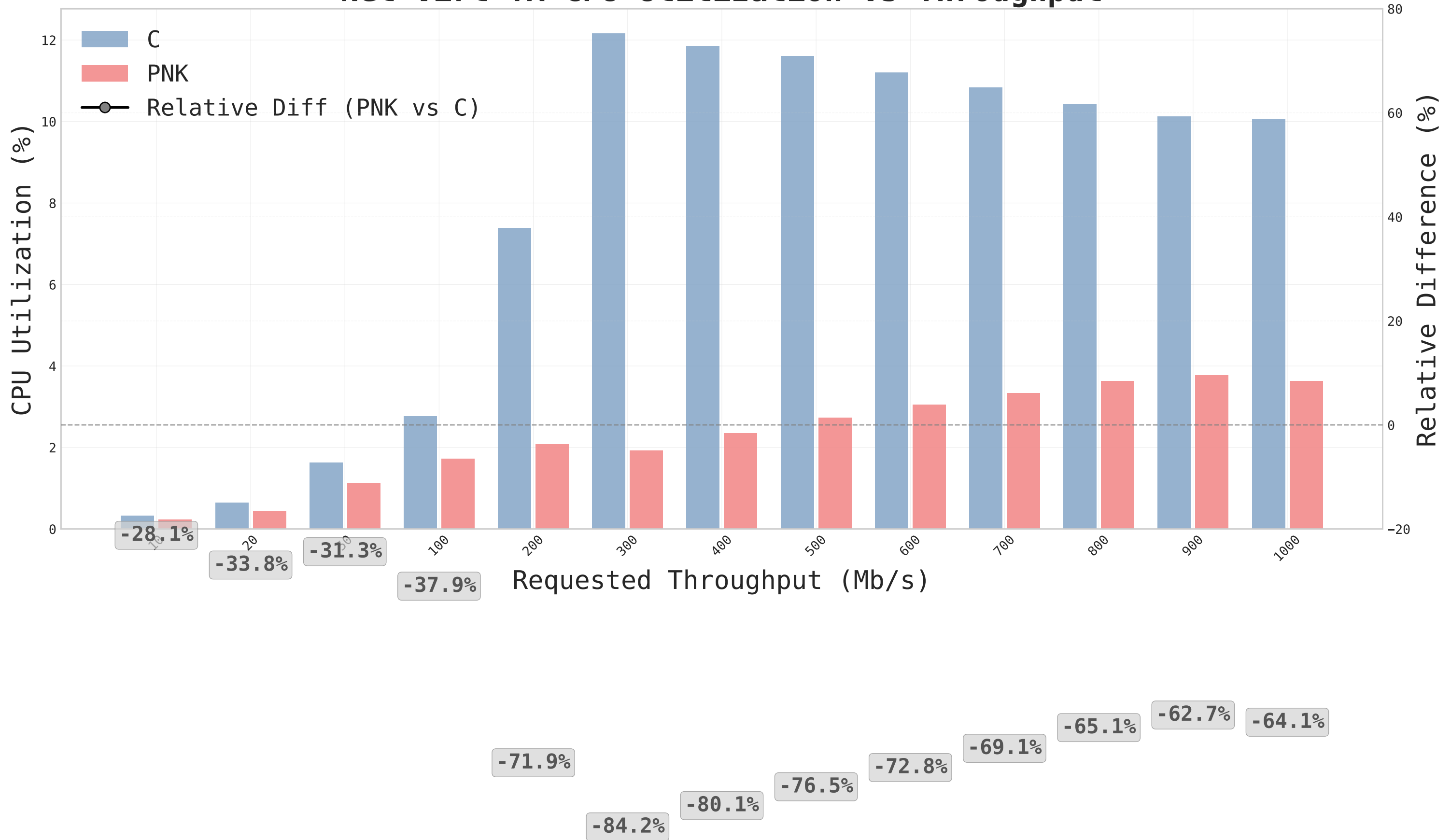
Total System CPU Utilization vs Throughput



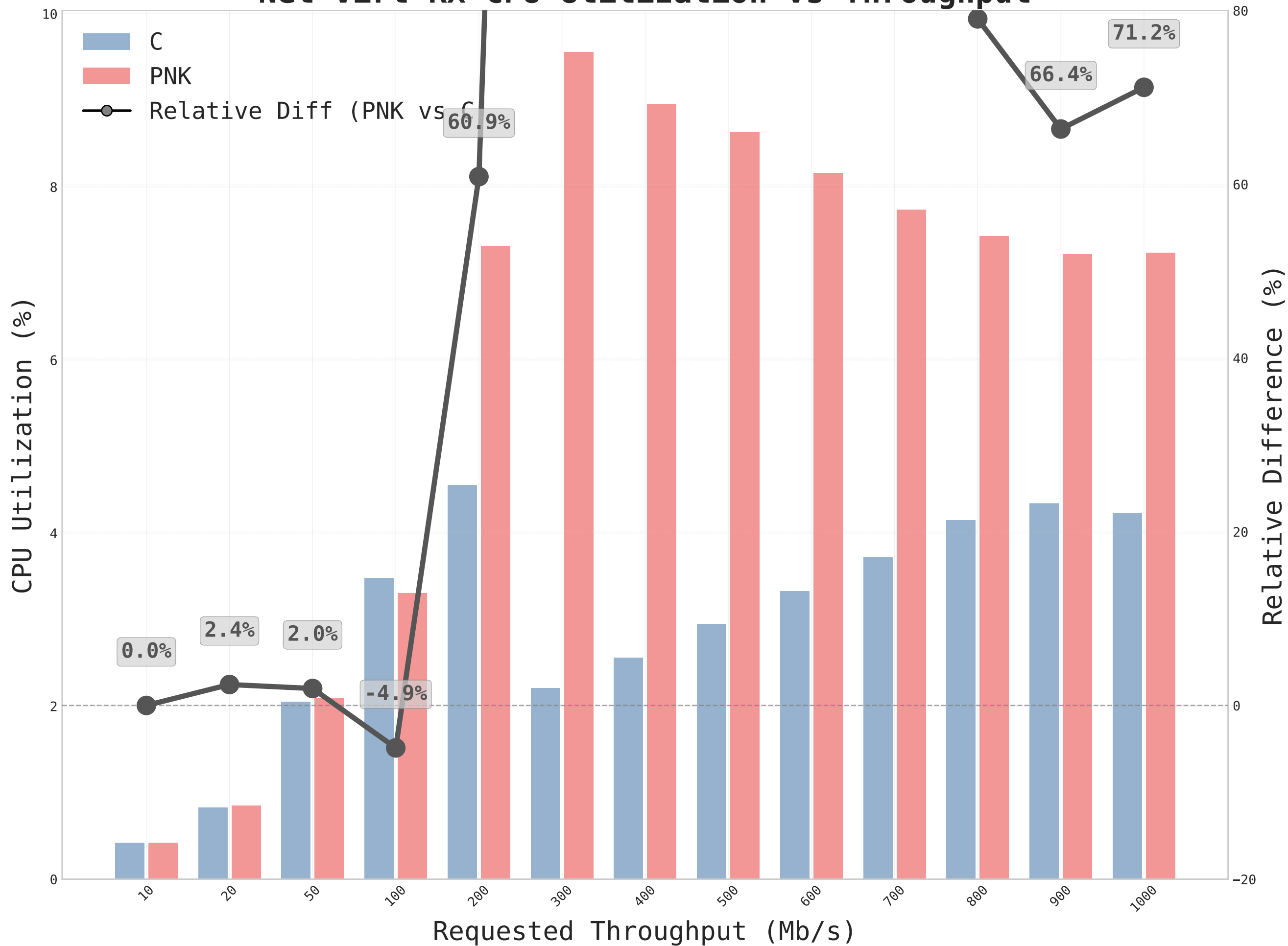
Ethernet Driver CPU Utilization vs Throughput



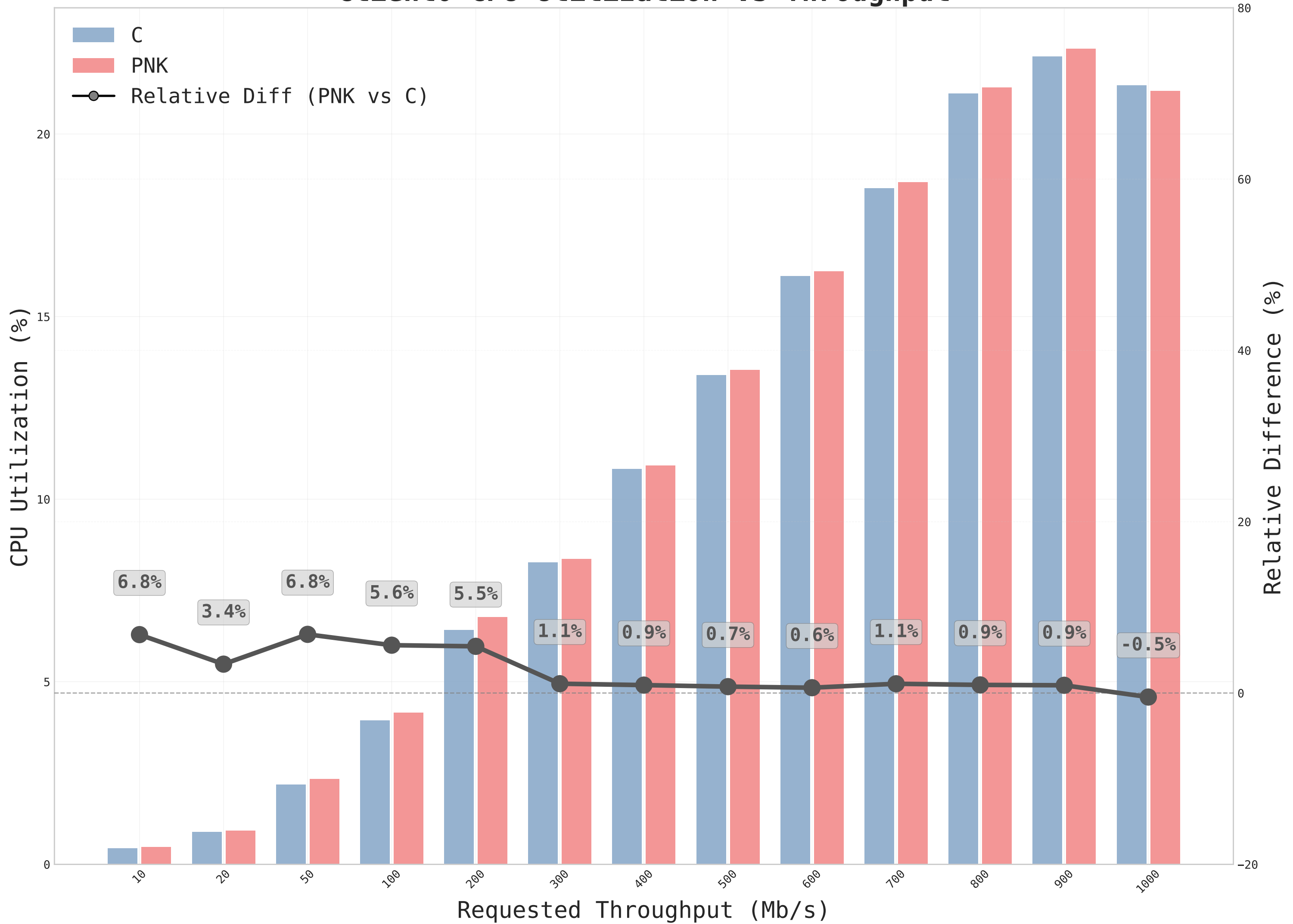
Net Virt TX CPU Utilization vs Throughput



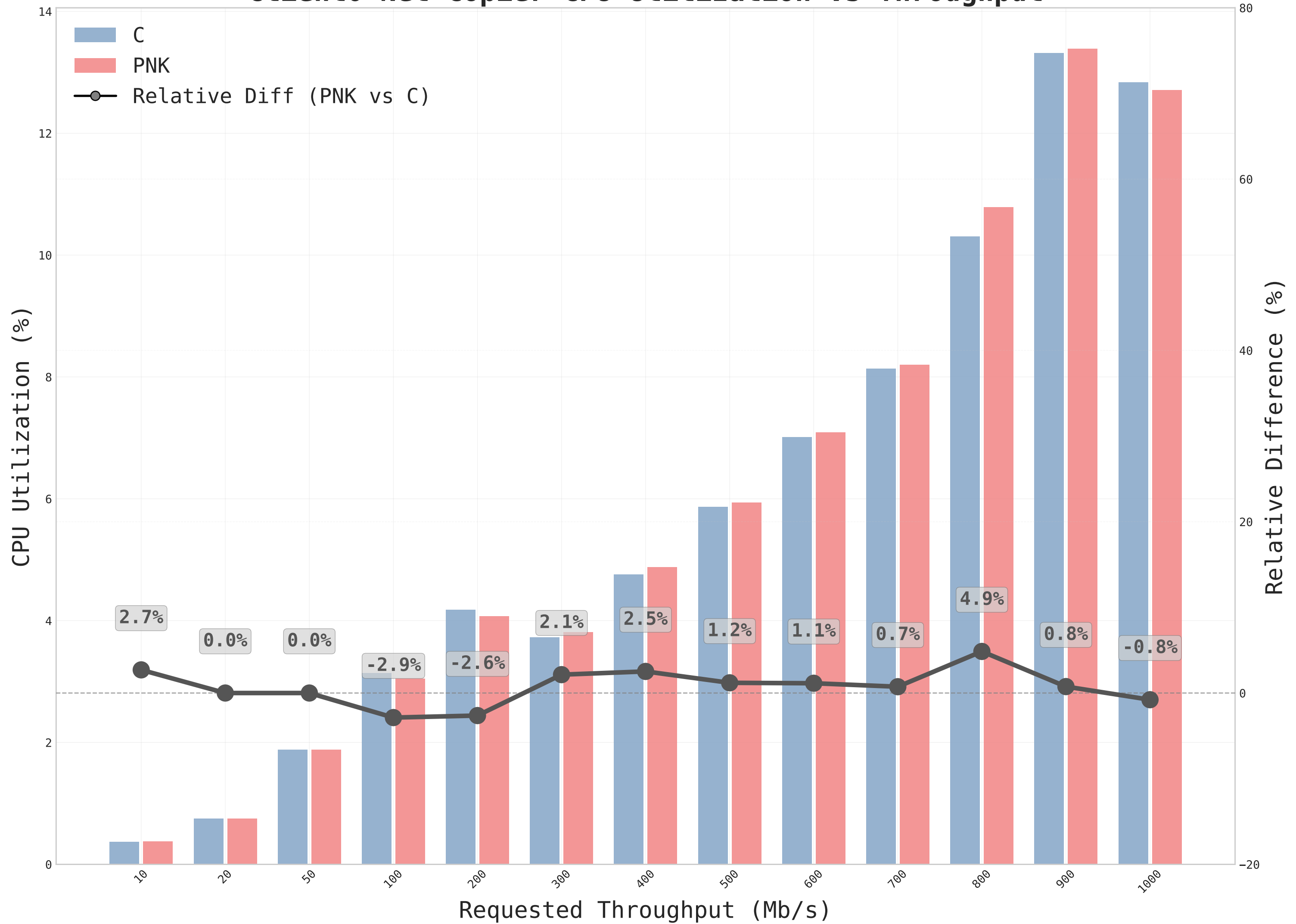
Net Virt RX CPU Utilization vs Throughput



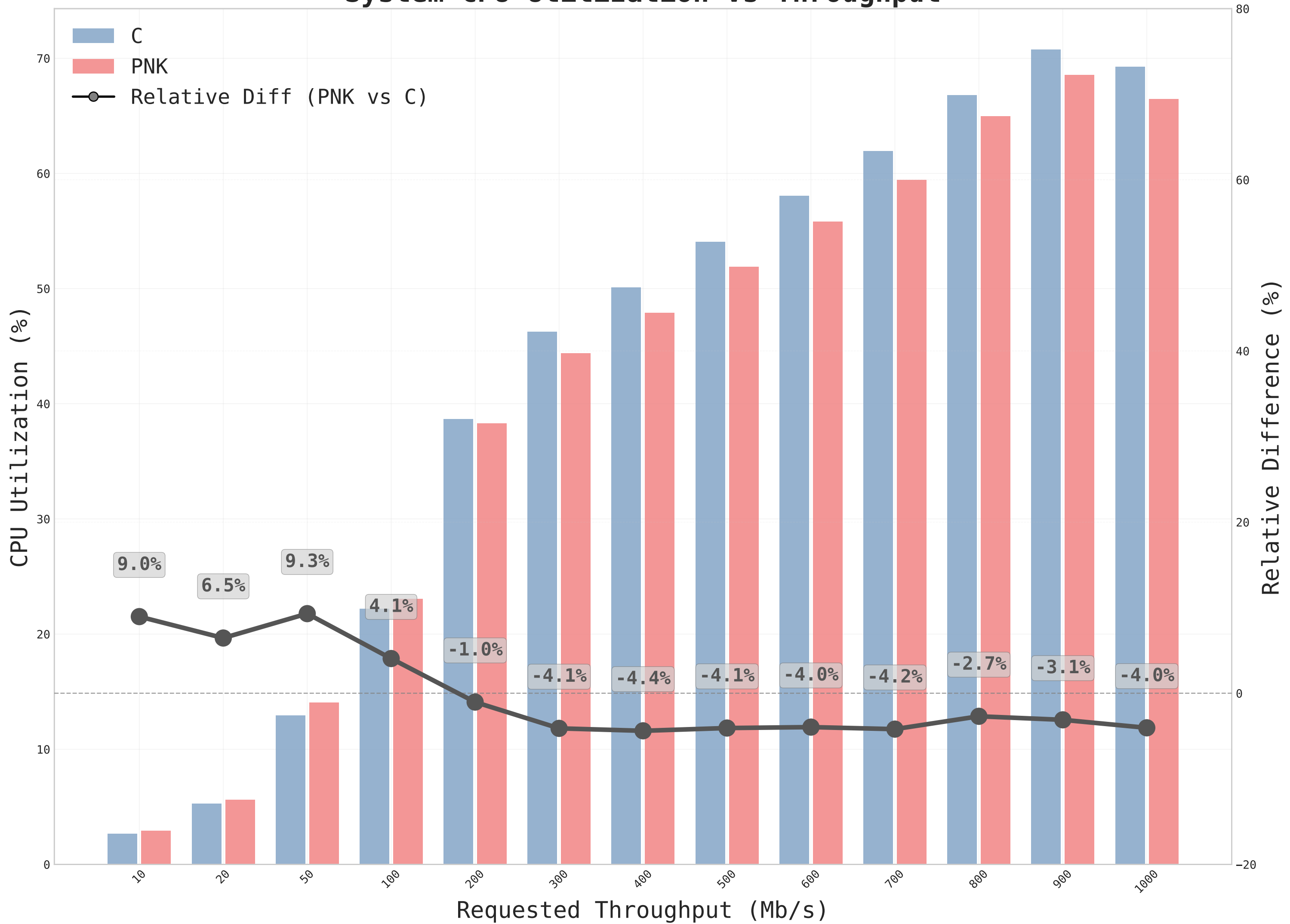
Client0 CPU Utilization vs Throughput



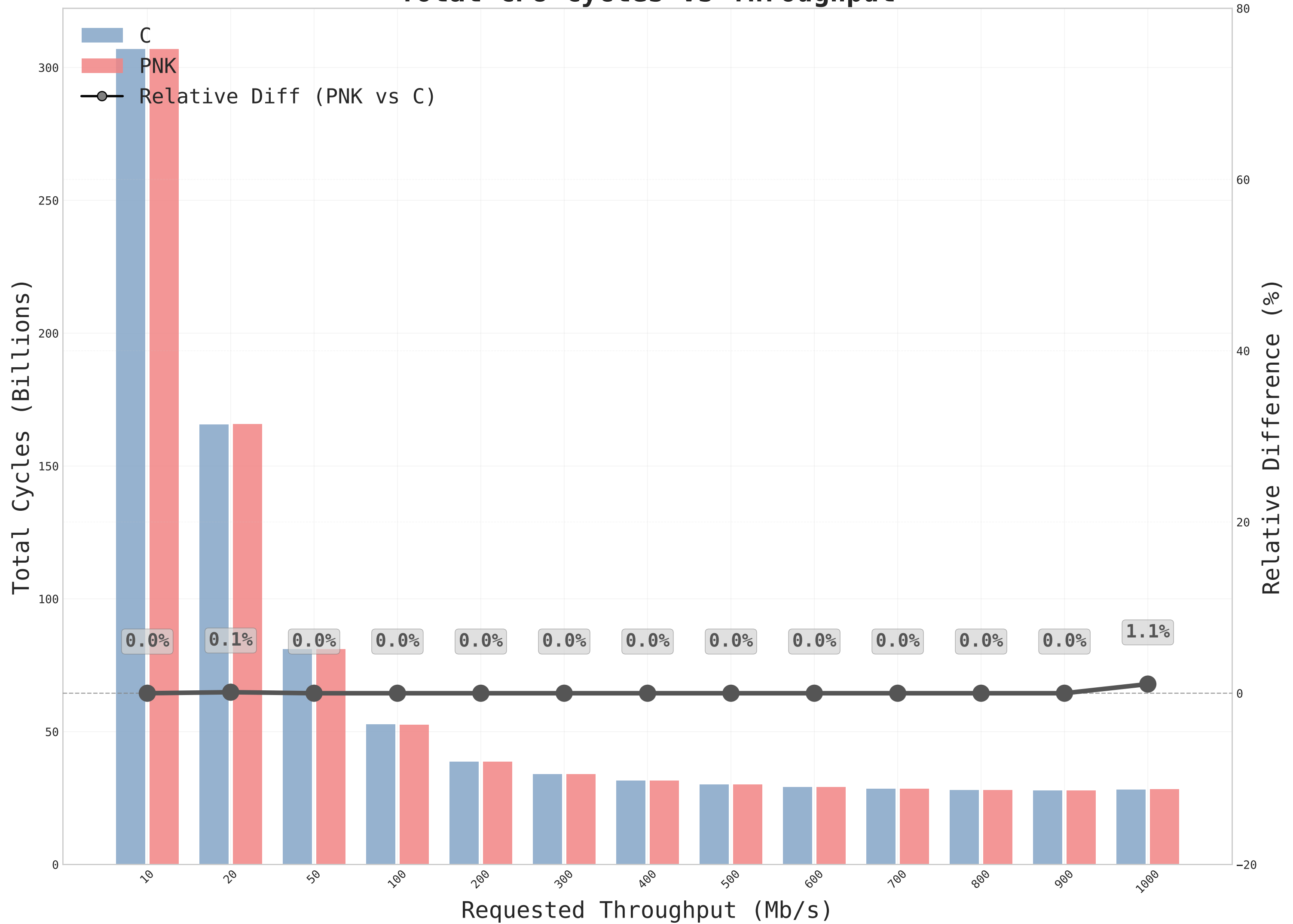
Client0 Net Copier CPU Utilization vs Throughput



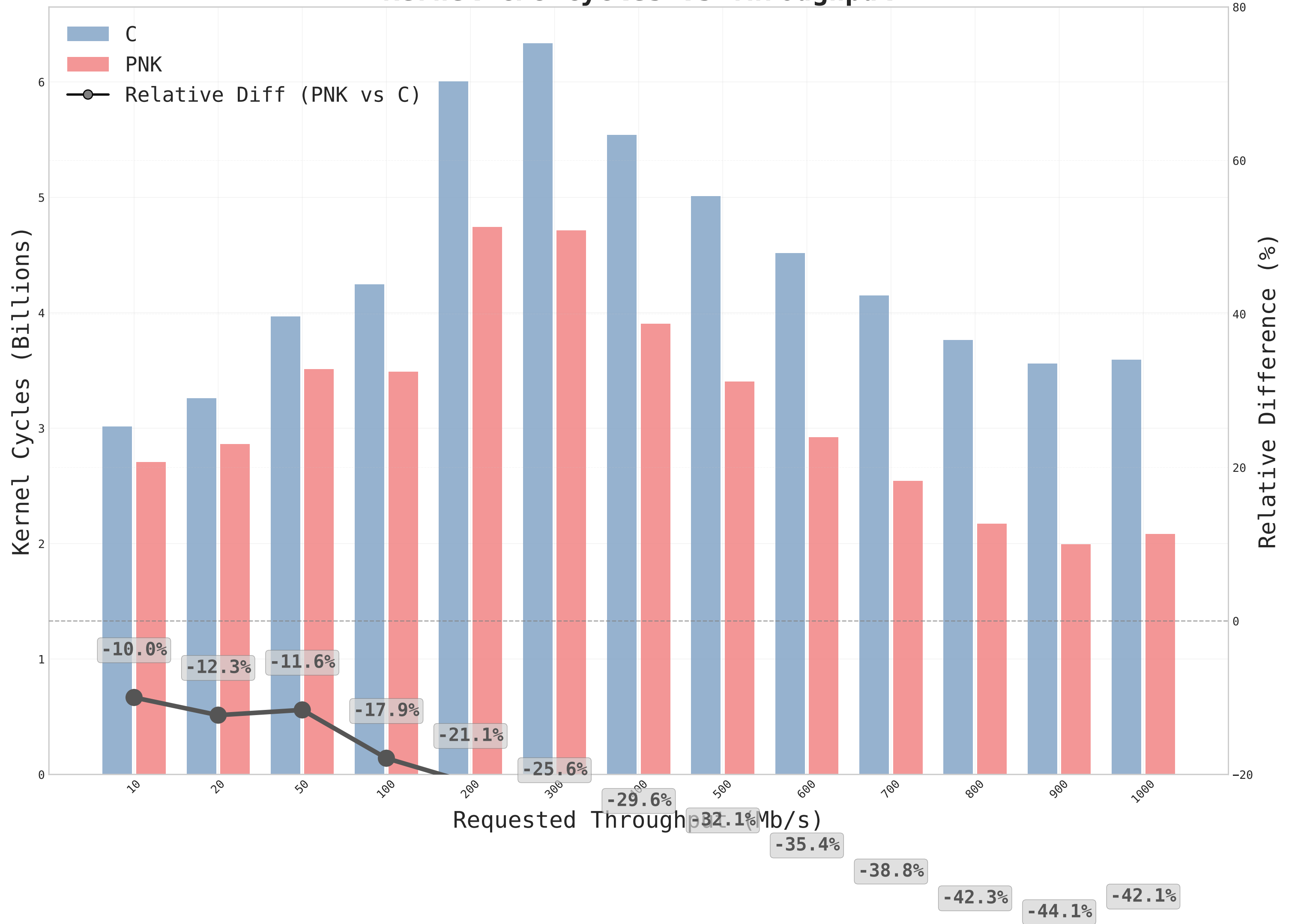
System CPU Utilization vs Throughput



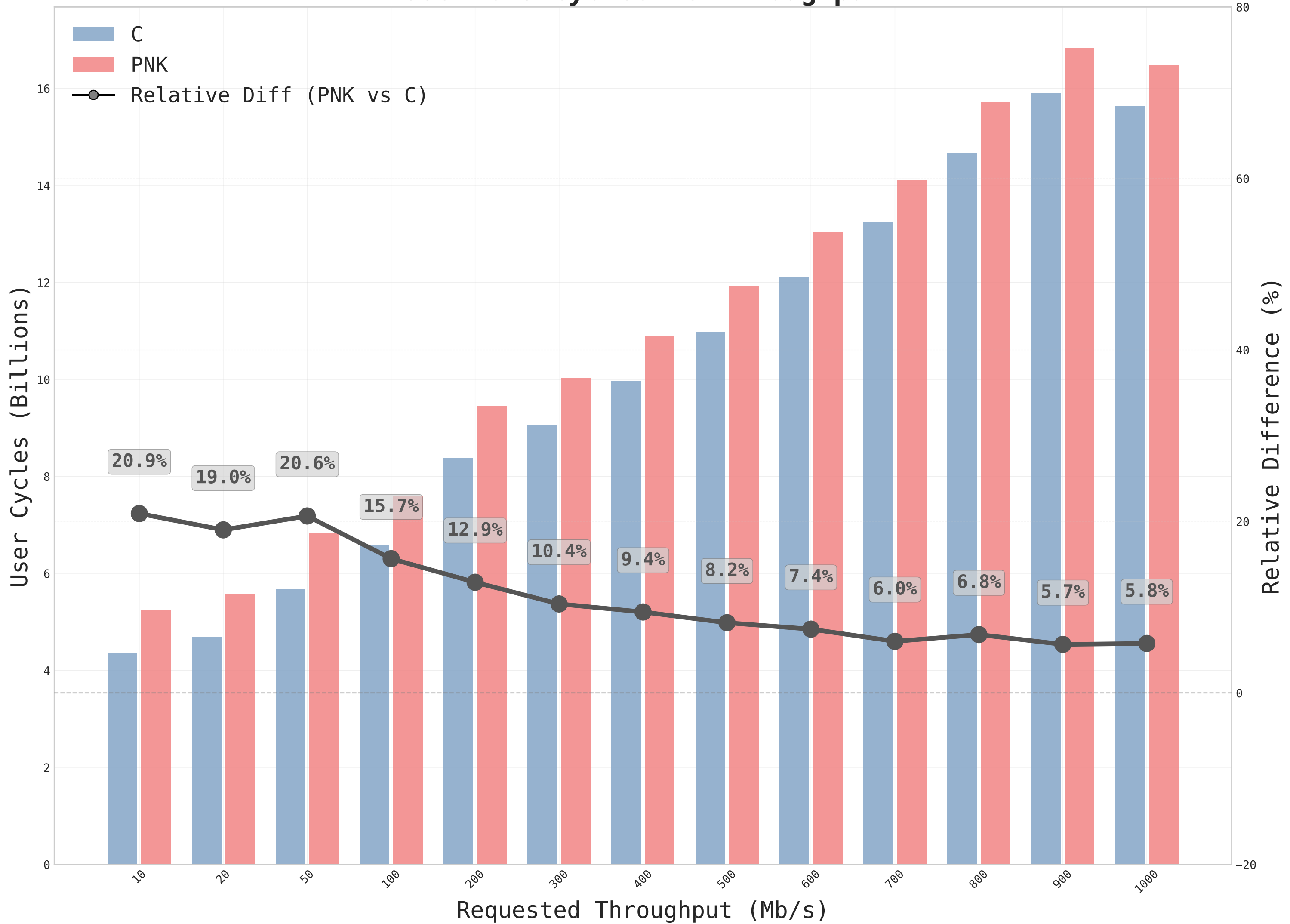
Total CPU Cycles vs Throughput



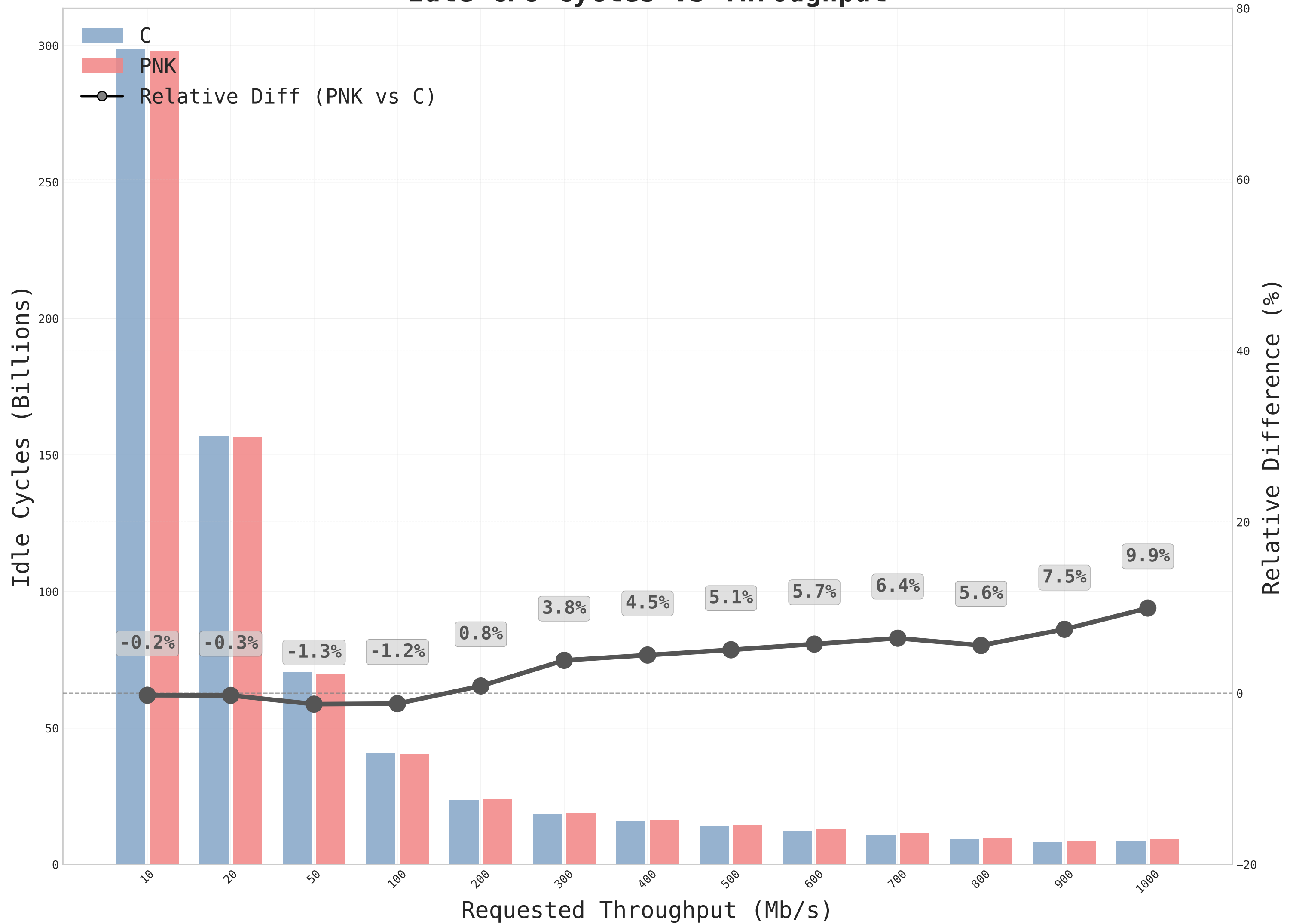
Kernel CPU Cycles vs Throughput

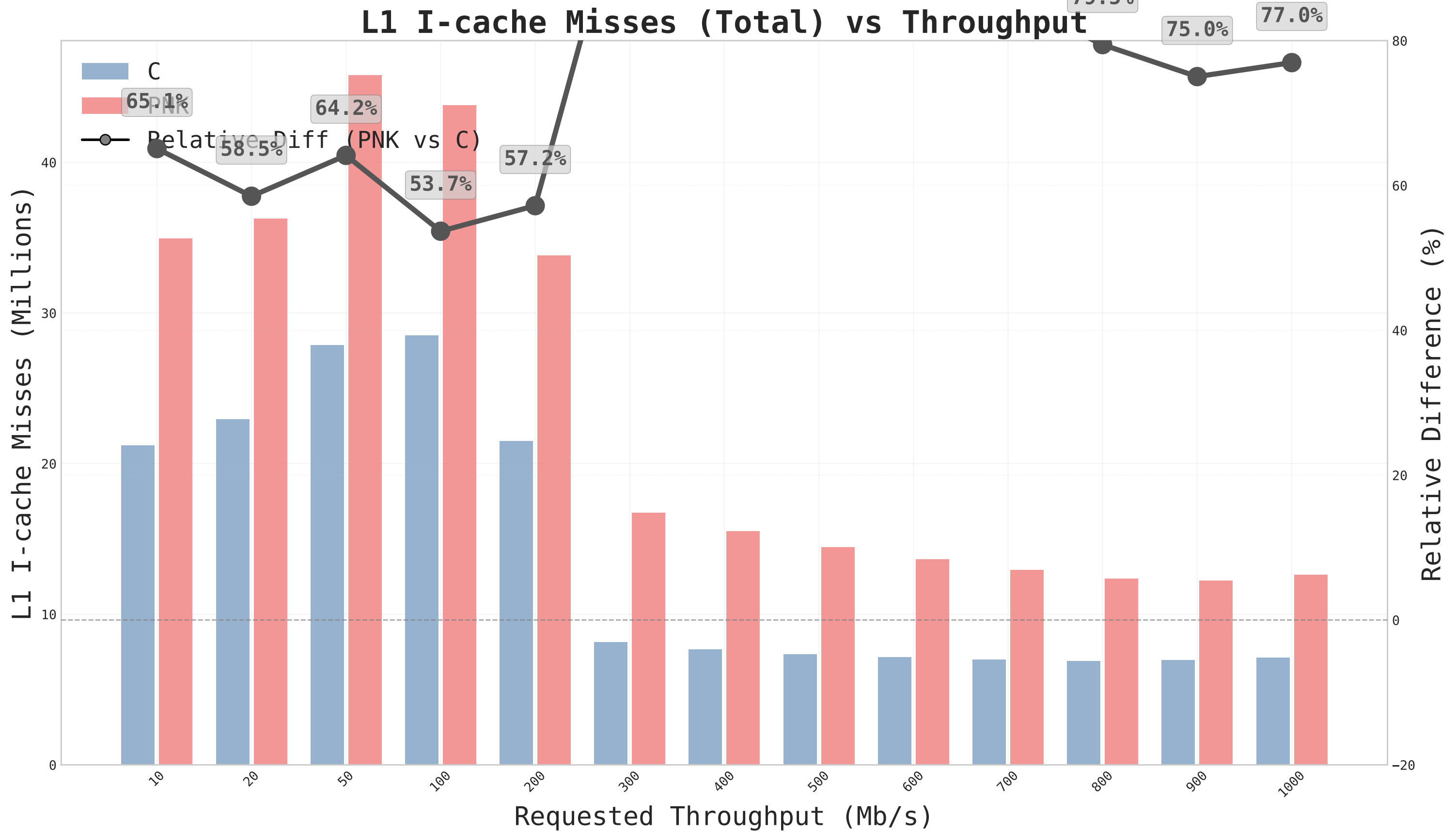


User CPU Cycles vs Throughput

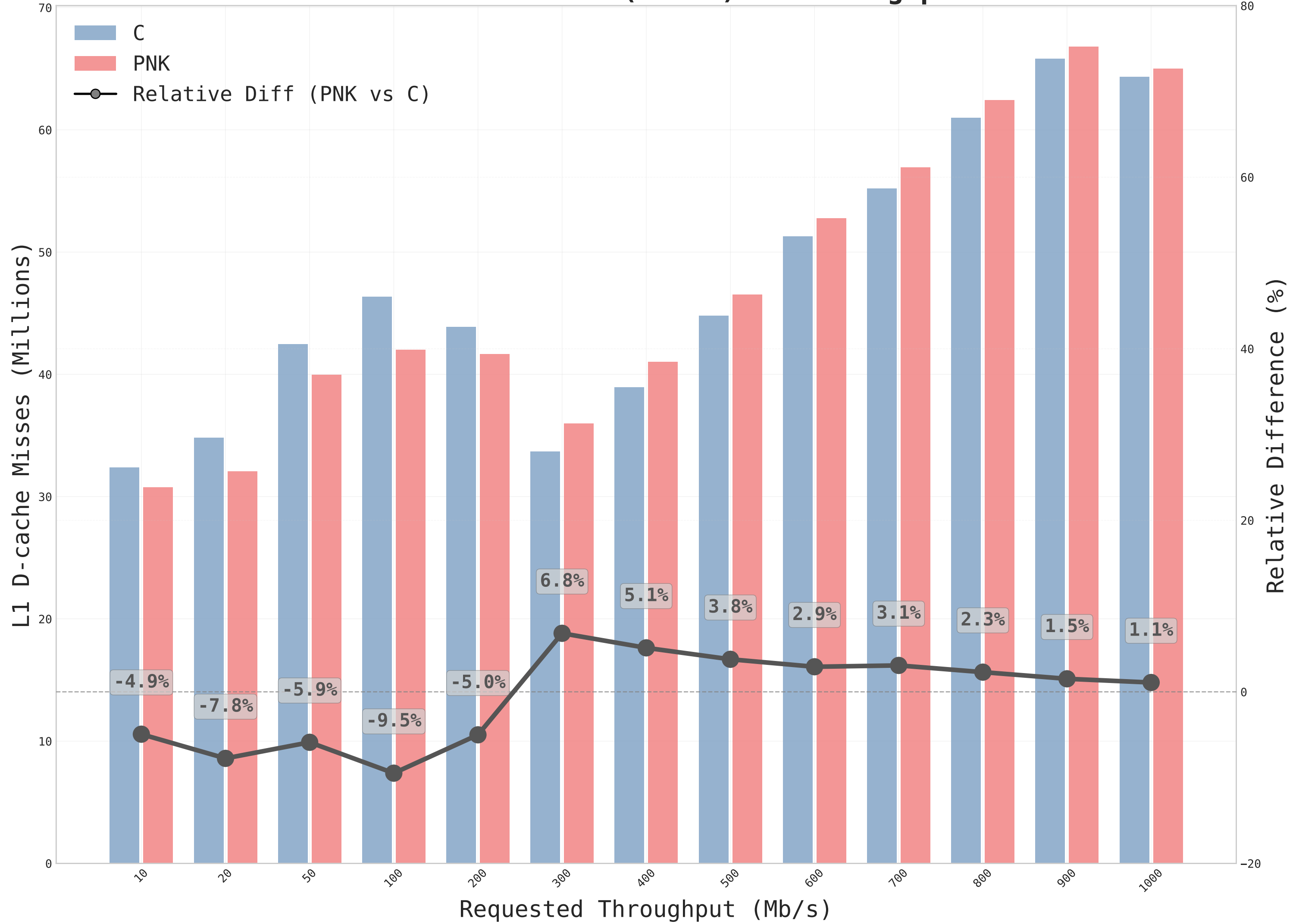


Idle CPU Cycles vs Throughput



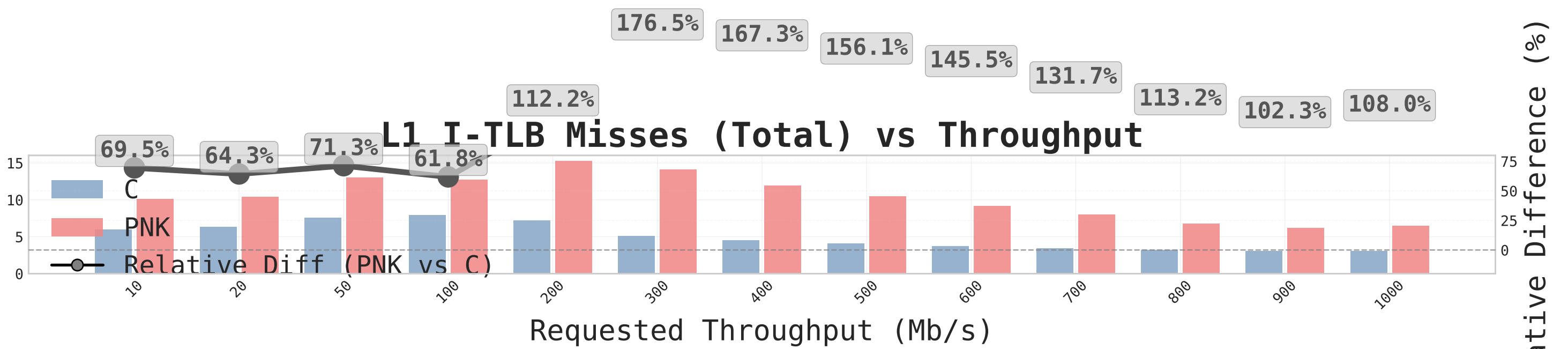


L1 D-cache Misses (Total) vs Throughput



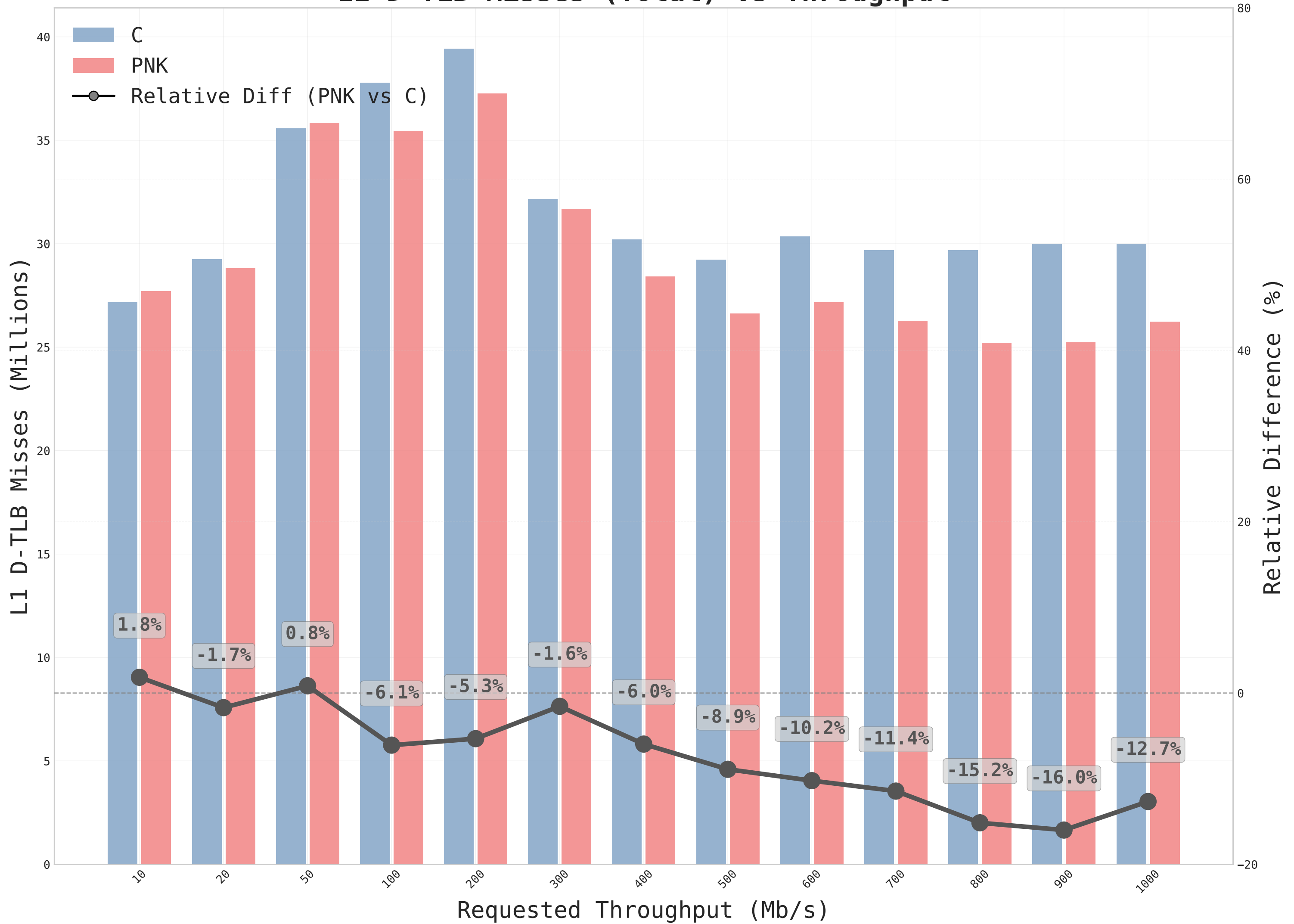
L1 I-TLB Misses (Millions)

L1 I-TLB Misses (Total) vs Throughput

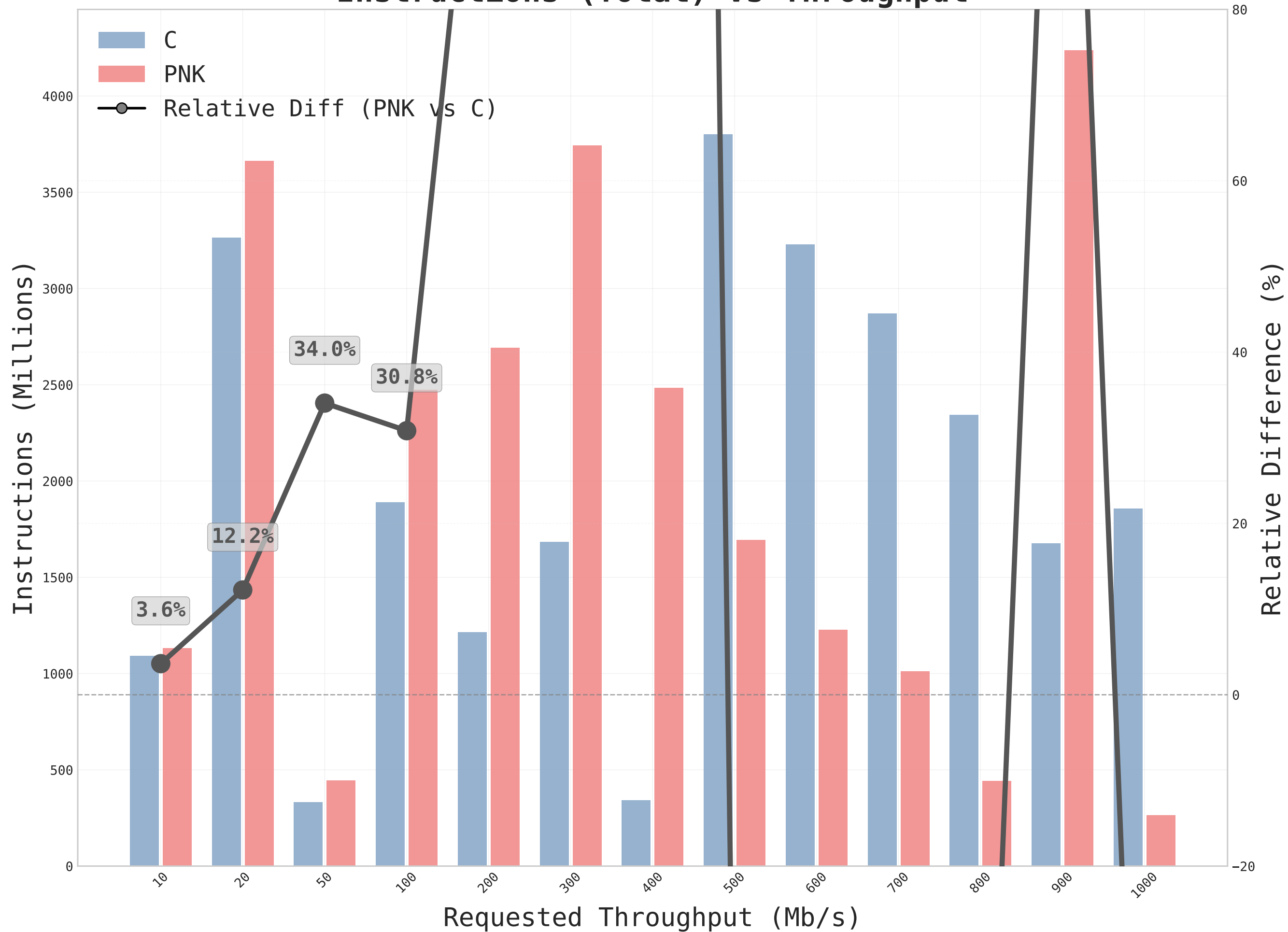


Relative Difference (%)

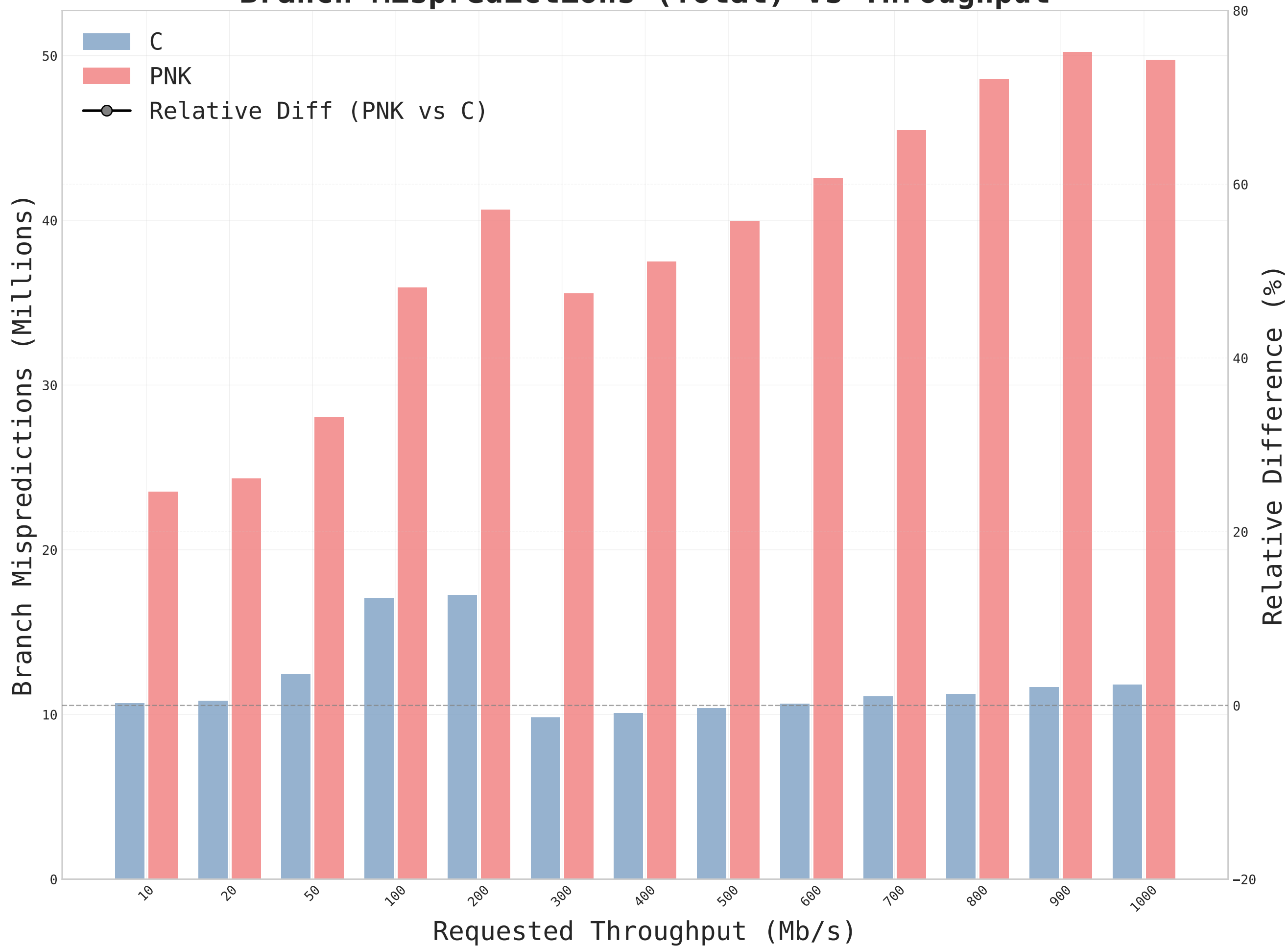
L1 D-TLB Misses (Total) vs Throughput

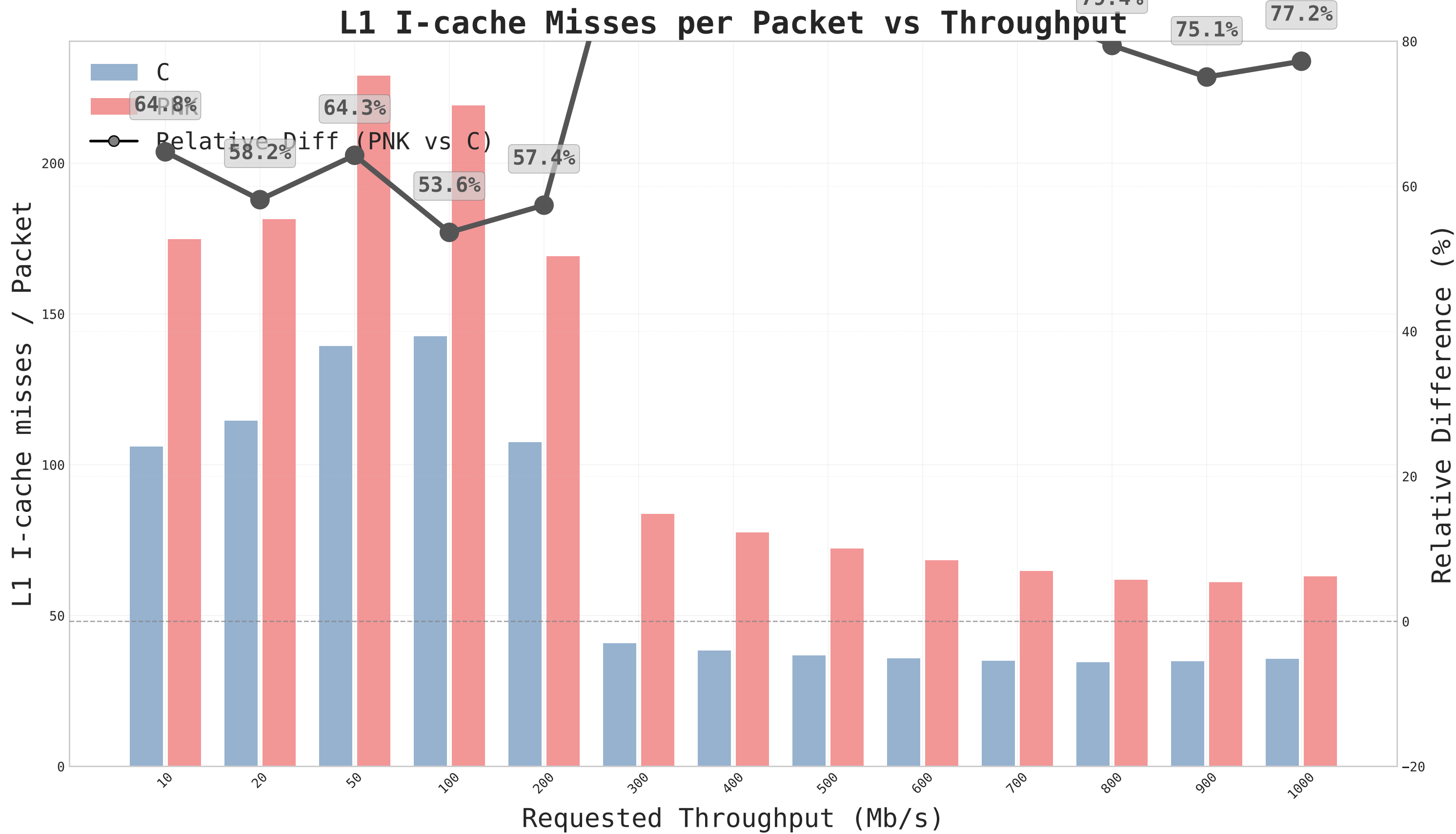


Instructions (Total) vs Throughput

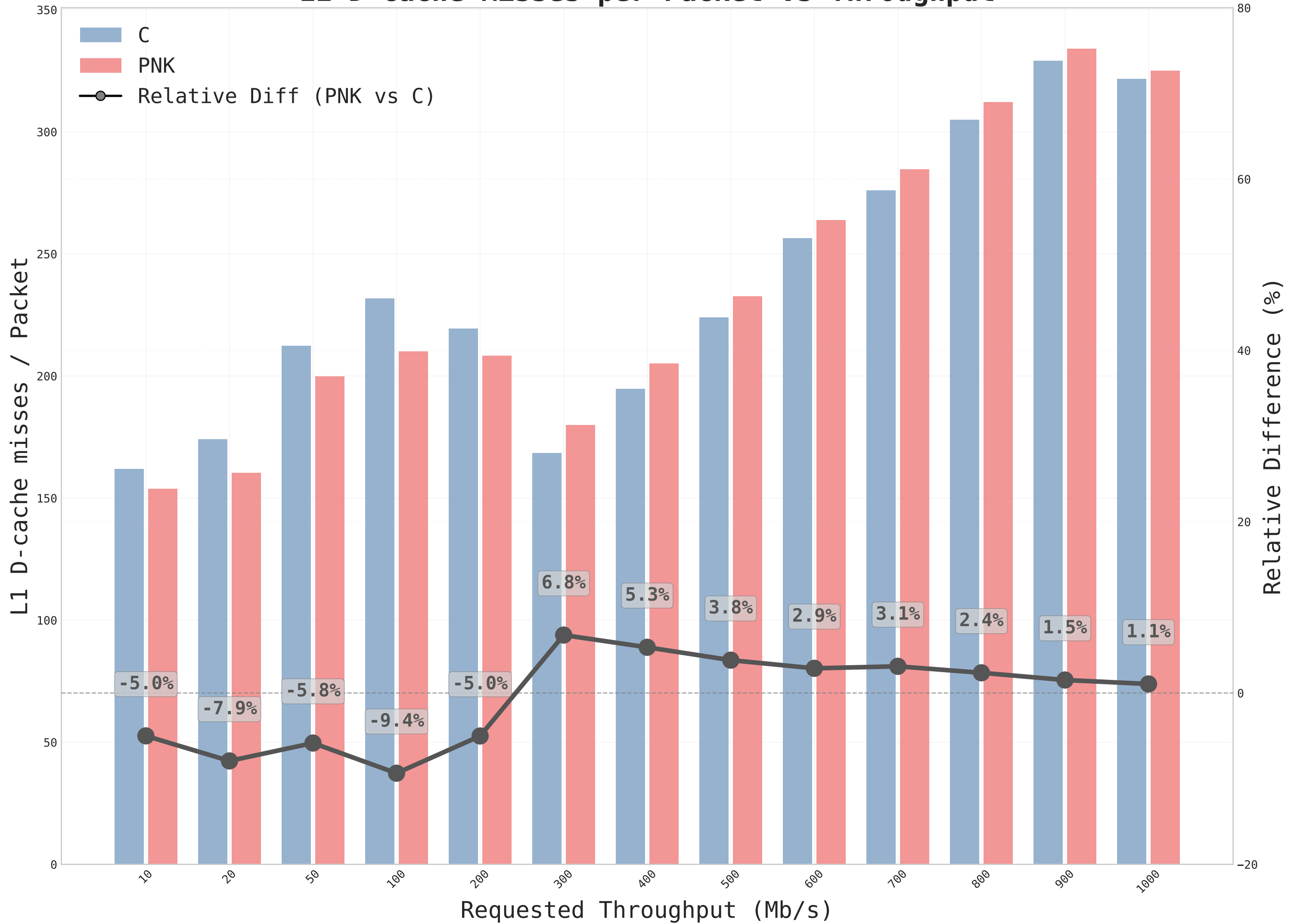


Branch Mispredictions (Total) vs Throughput



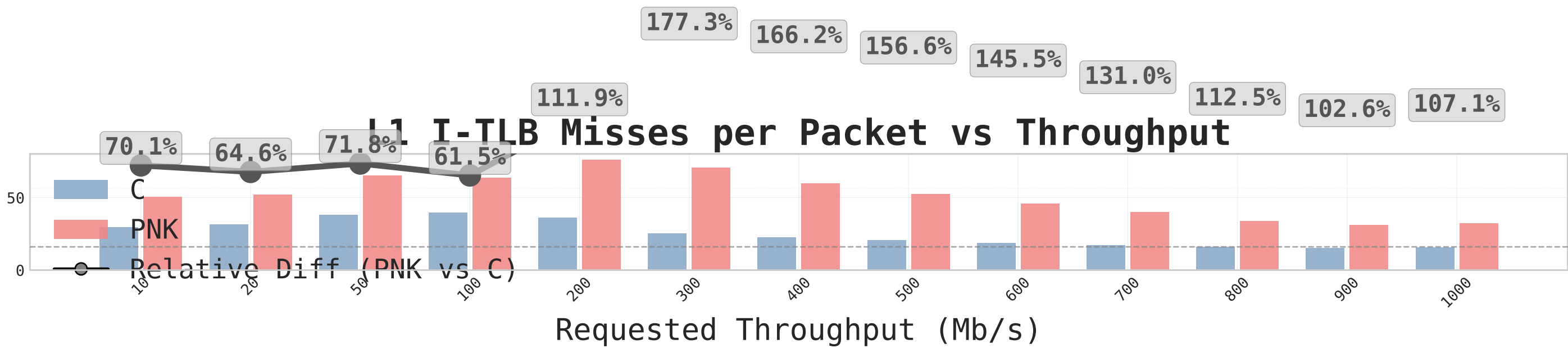


L1 D-cache Misses per Packet vs Throughput

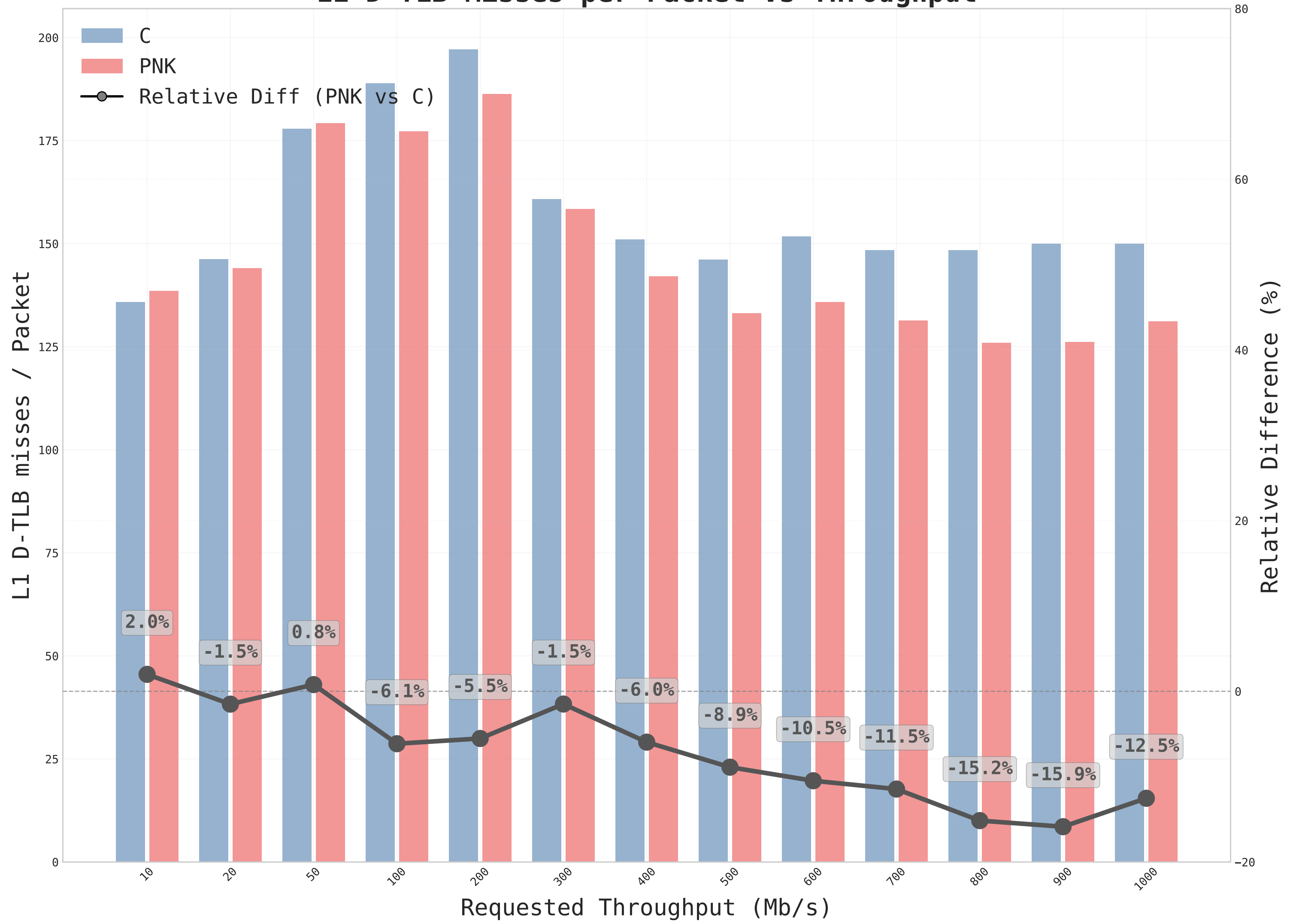


I-TLB misses / Packet

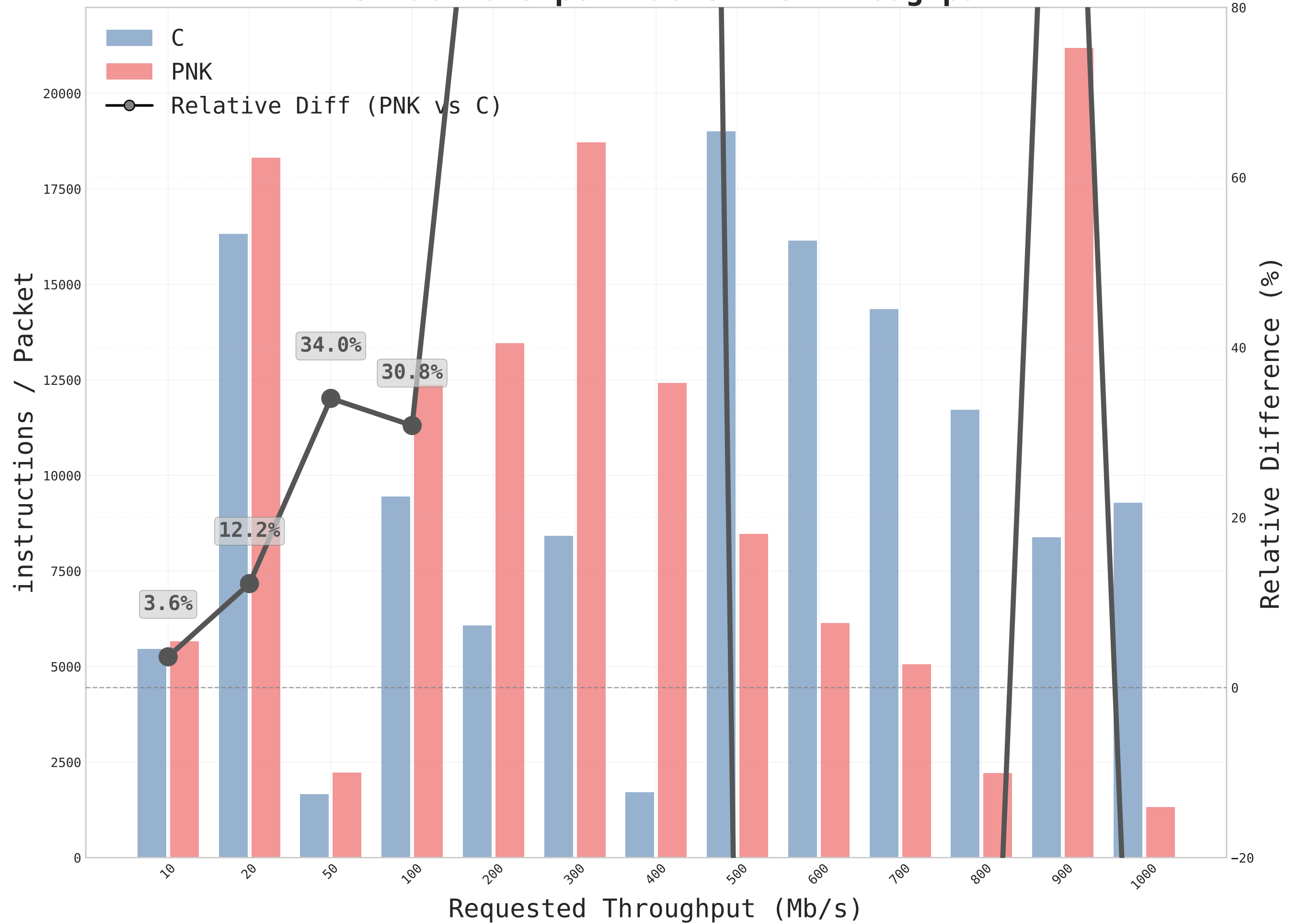
Relative Difference (%)



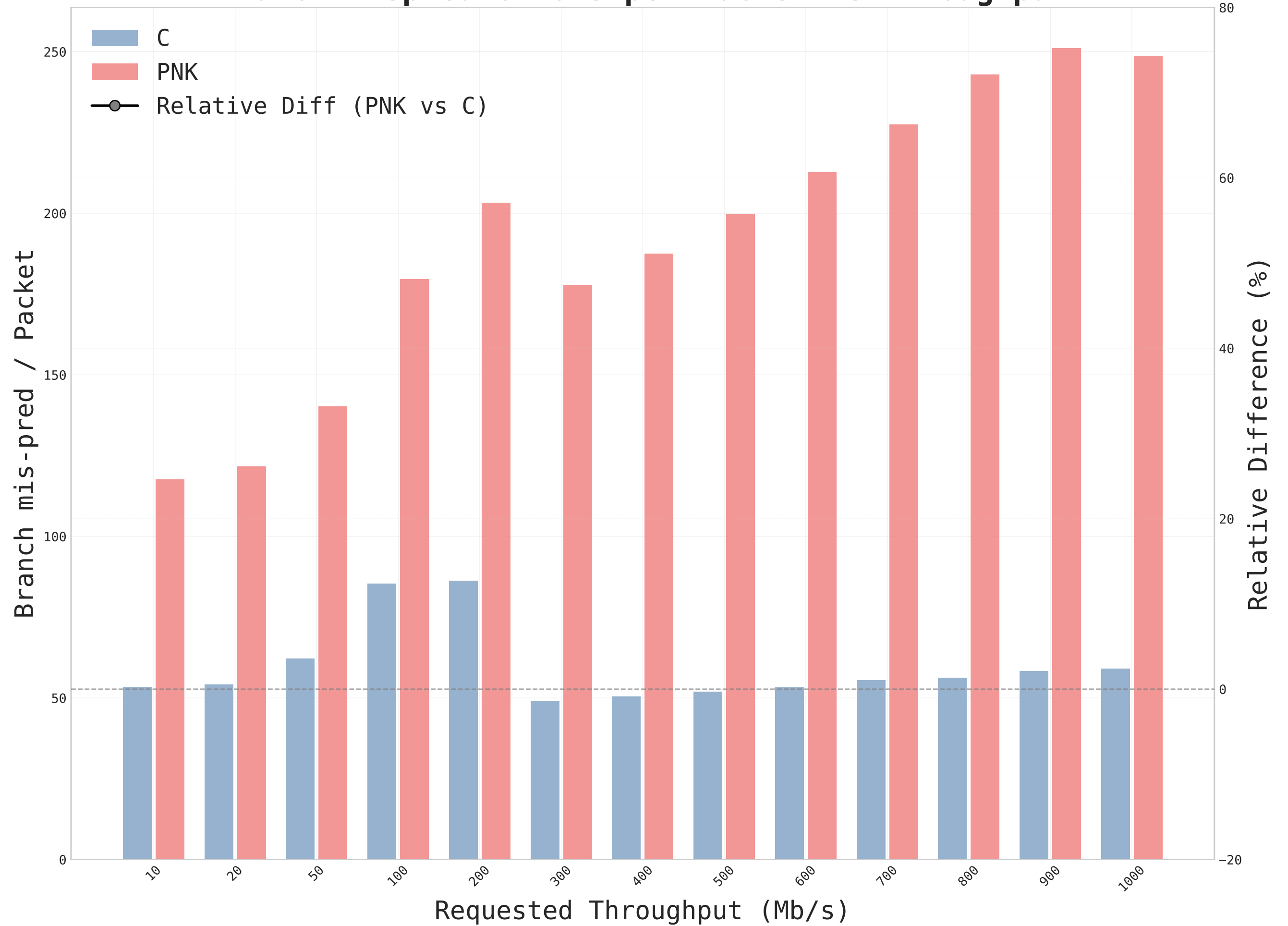
L1 D-TLB Misses per Packet vs Throughput



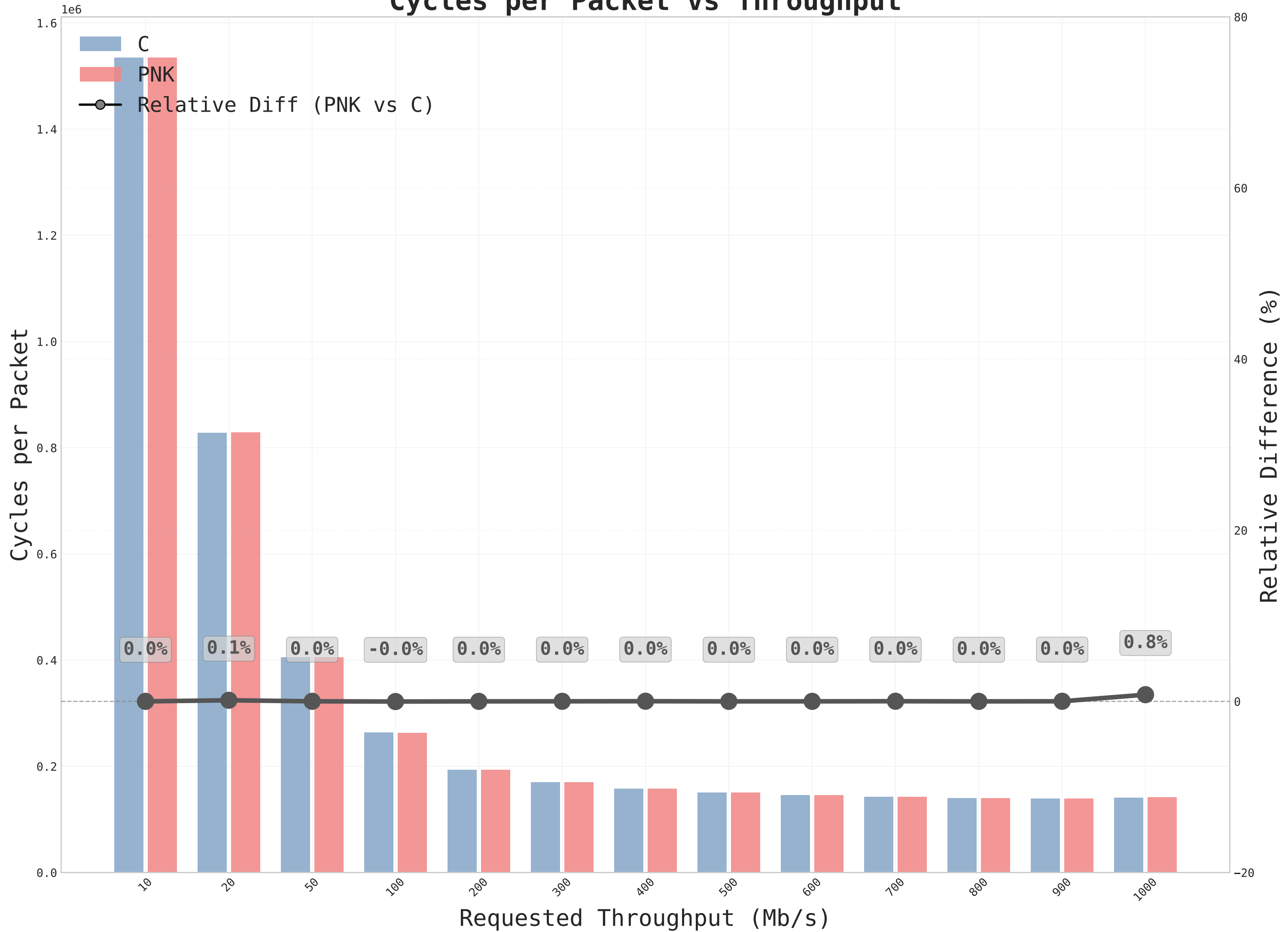
Instructions per Packet vs Throughput



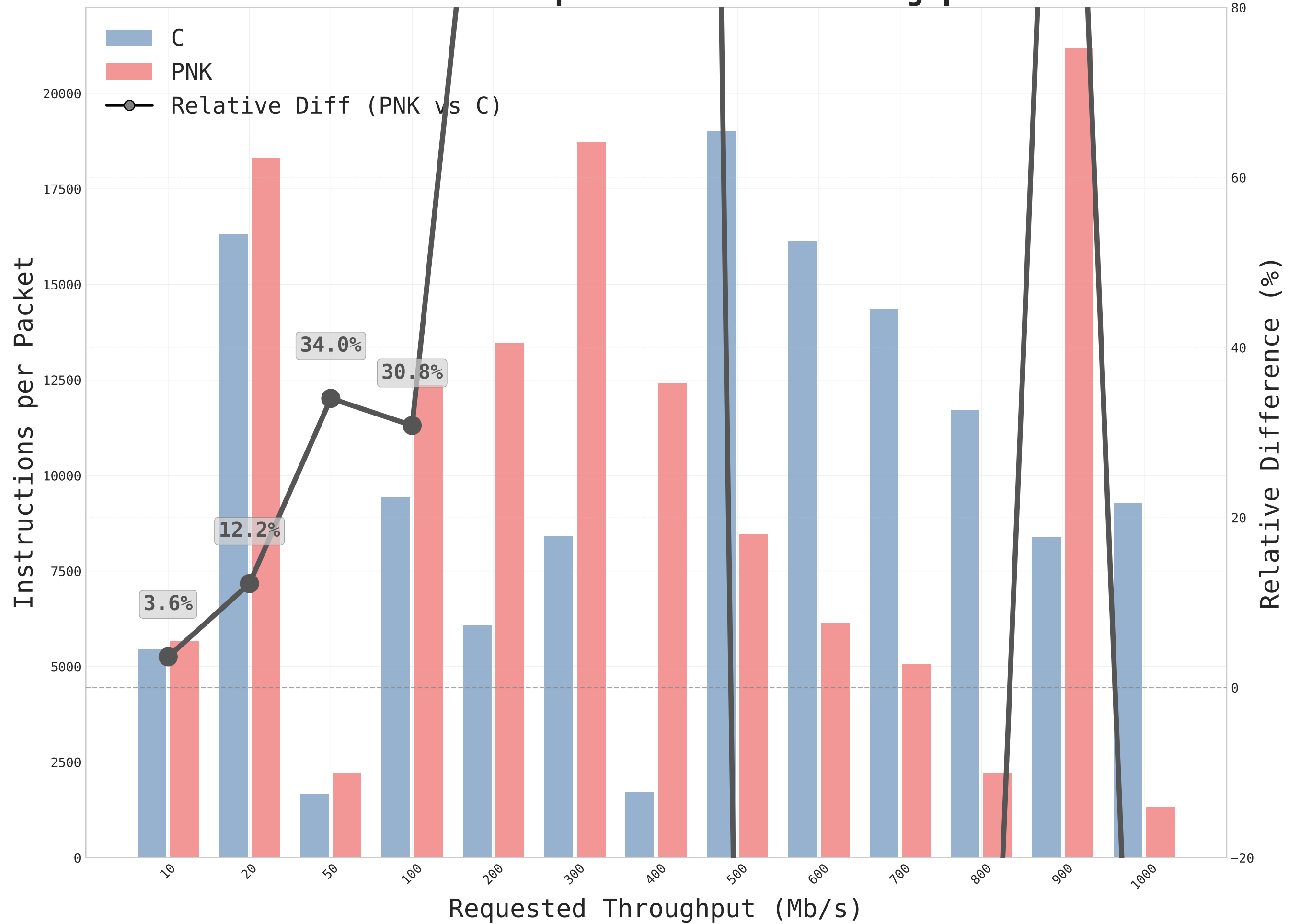
Branch Mispredictions per Packet vs Throughput



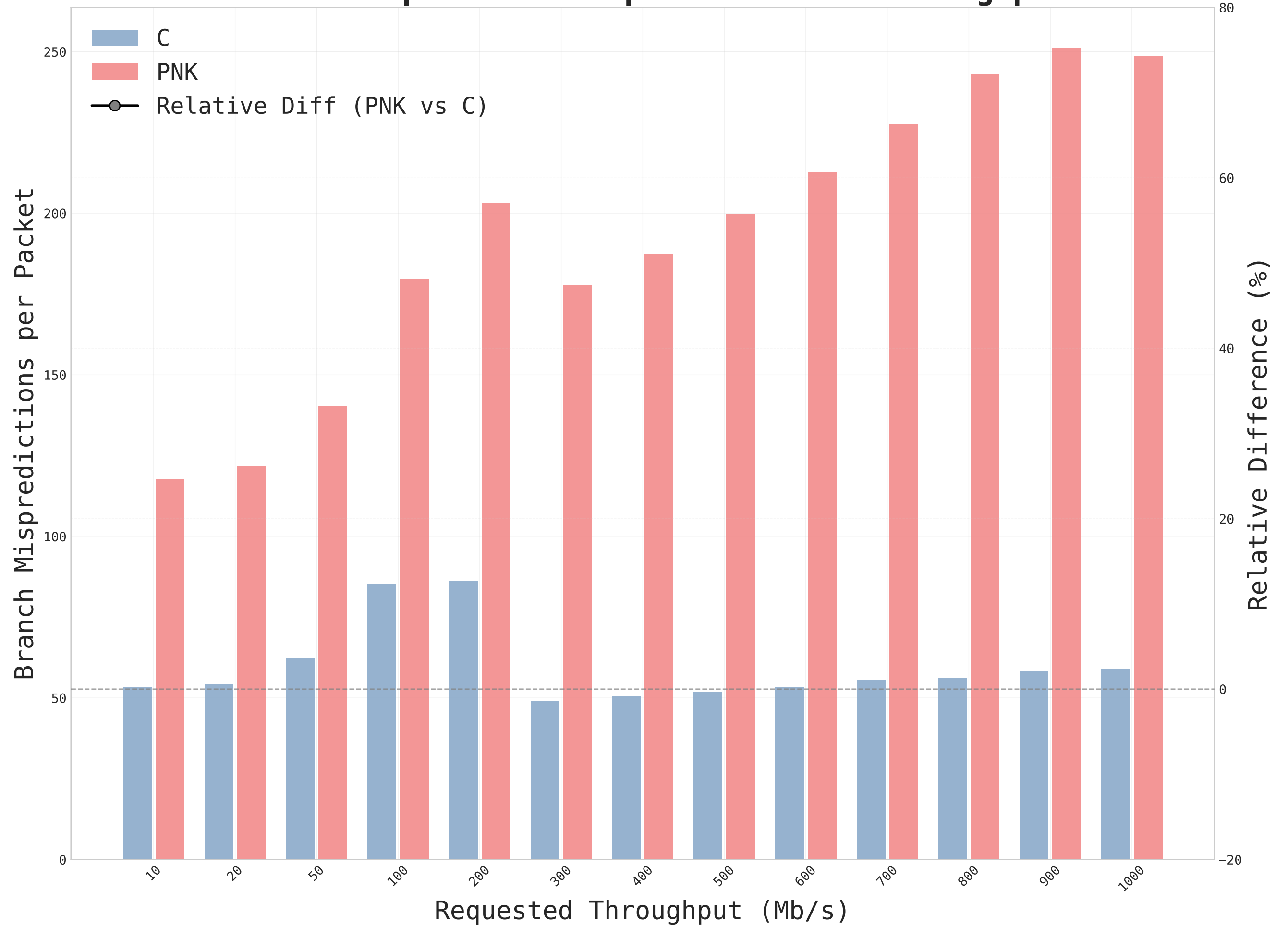
Cycles per Packet vs Throughput



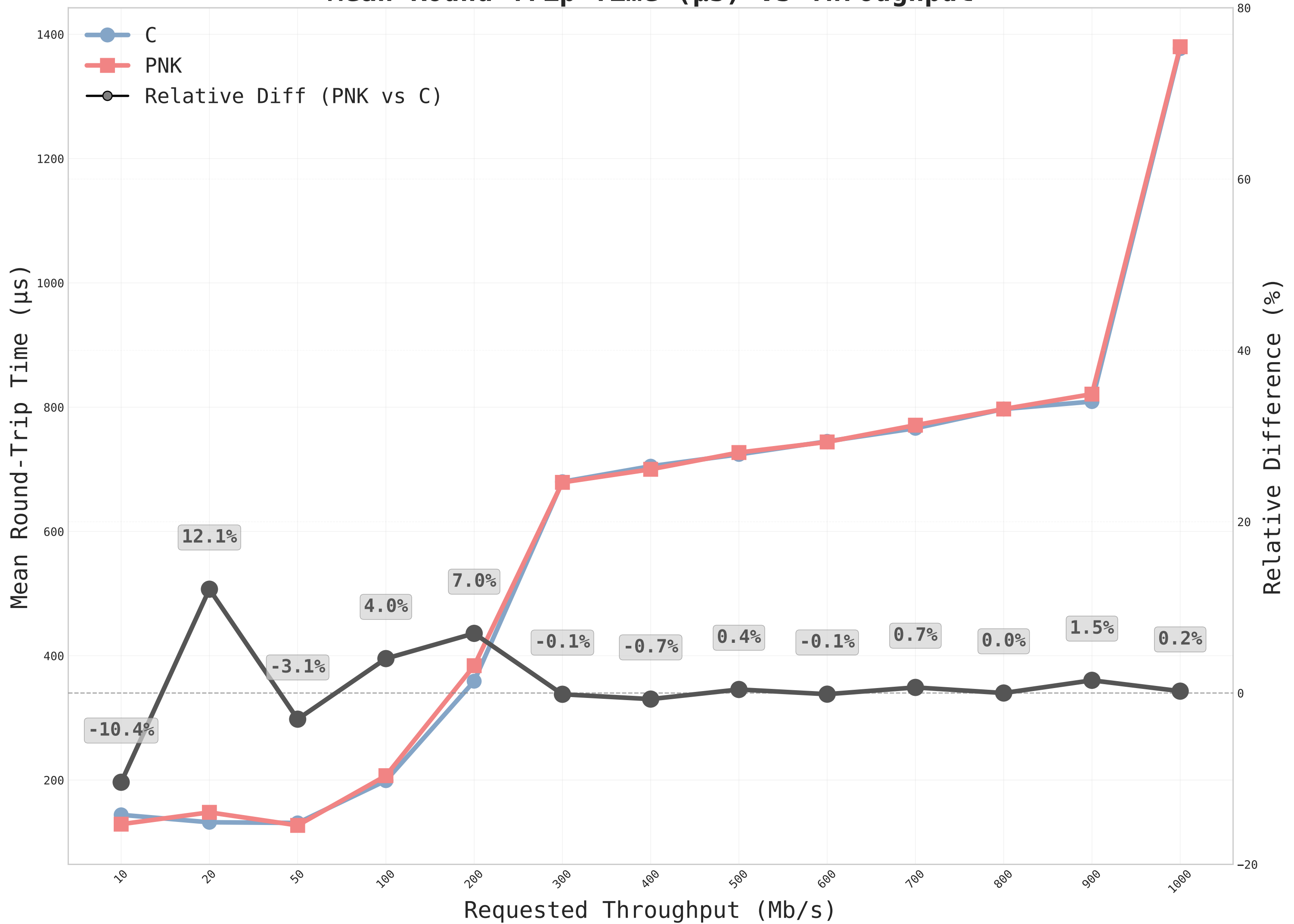
Instructions per Packet vs Throughput



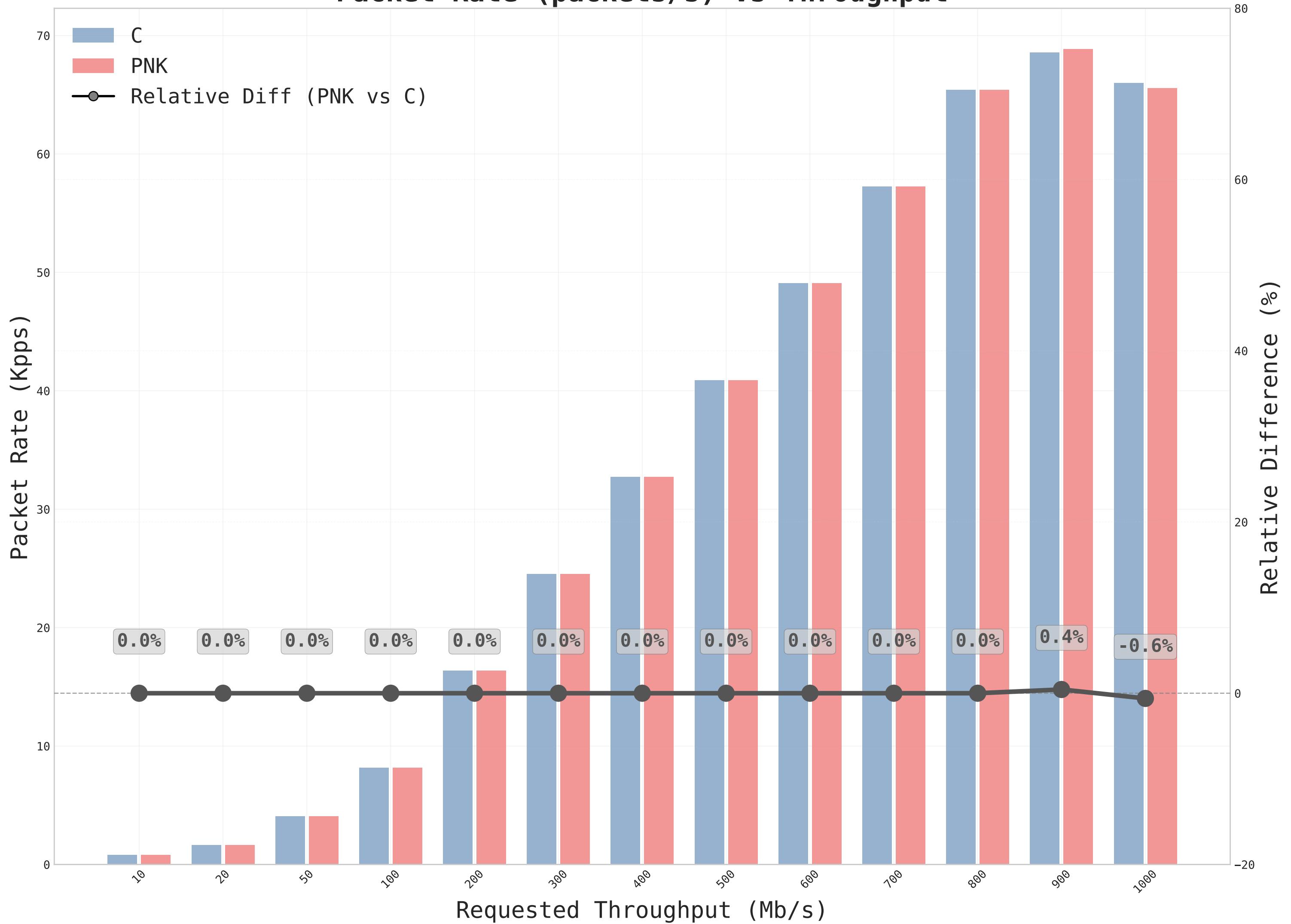
Branch Mispredictions per Packet vs Throughput



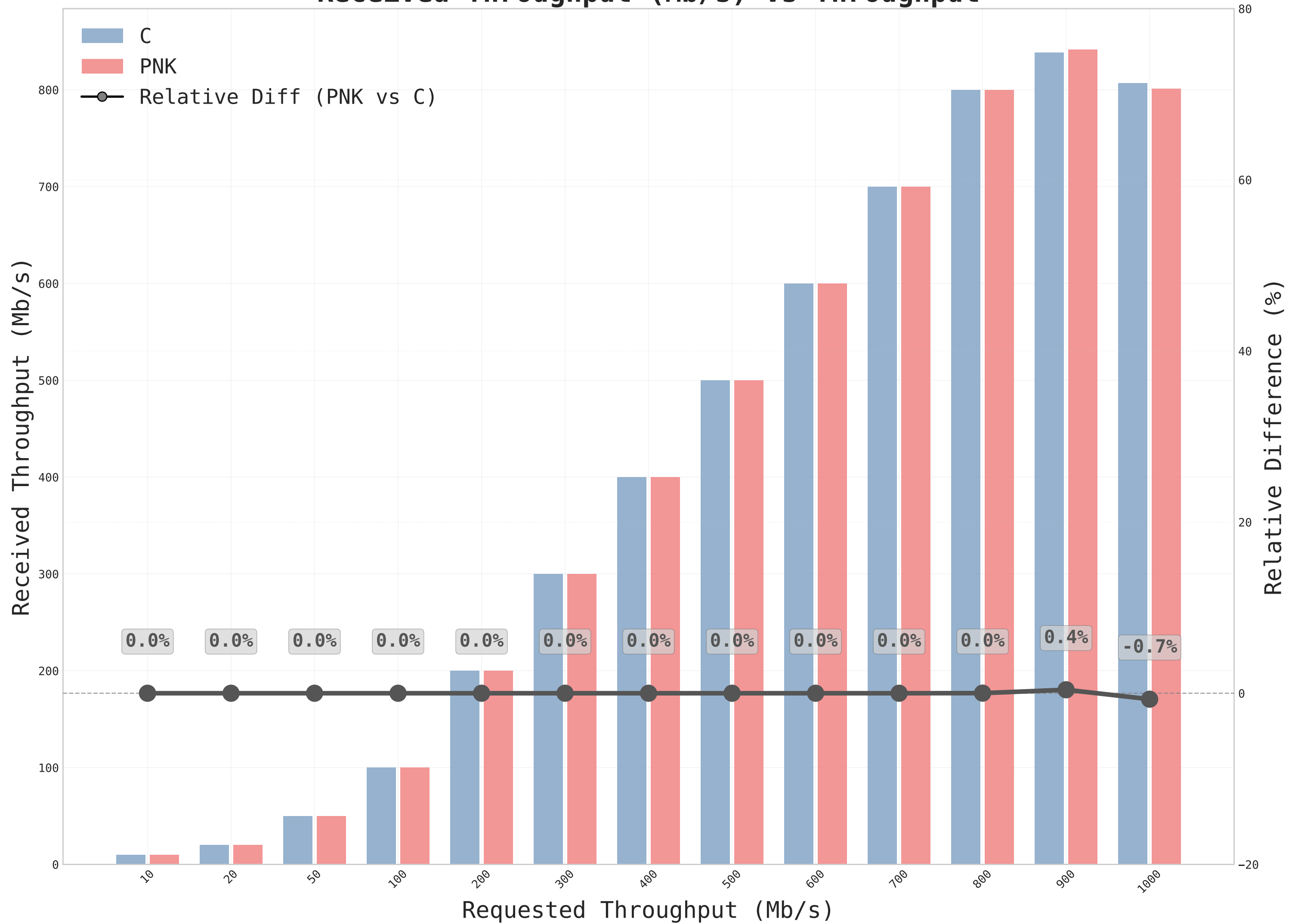
Mean Round-Trip Time (μ s) vs Throughput



Packet Rate (packets/s) vs Throughput



Received Throughput (Mb/s) vs Throughput



Sent Throughput (Mb/s) vs Throughput

