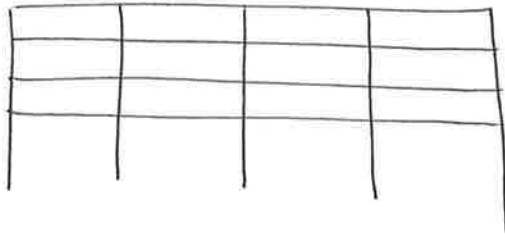
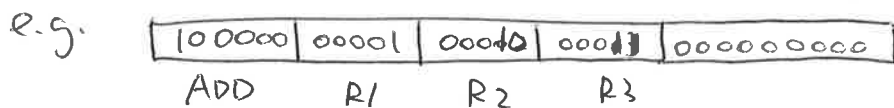


S9 ☐ The assembly language• The assembly language

- code



write instructions directly by checking OPCODE



- assembly provides an easier way to write code

just write `ADD(R2, R3, R1)`
in text file.

- how is it done

write byte by byte

1 2 0xC

0b101010

...

left to right
by spacetranslate

2A	0C	02	01
----	----	----	----

...

lower

- define ~~var~~ macro $X=123$ X

- specify location

\leftrightarrow current address
labels

text

1 2 0xc

0b101010

$X=123$ } macro
 X

$\cdot = 0x8$

1 2 3 4

$\cdot = \cdot + 3$

lab1

align \rightarrow to next word.
16 lab1

If some addr skipped, filled with 0.
The default start 0

translation (binary)

2A	0C	02	01	00000000
			7B	00000004
04	03	02	01	00000008
01		0F	10	0000000E
				00000010

- Allow expressions

$+$, $-$, $*$, $/$, $\%$, \ll , \gg

text

1 2 3+3

5x6

$R0=0$

$R1=1$

:

$R31=31$ \rightarrow special register, constant 0, talk in beta

$\cdot - 2$

$R1 \ll 2$

translation

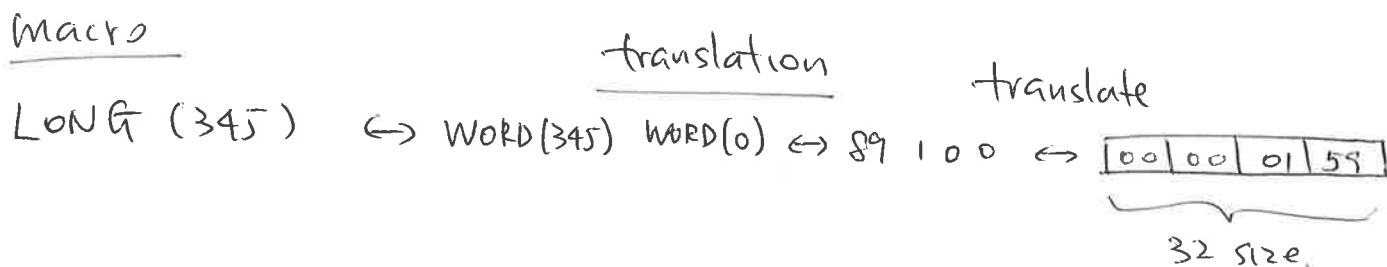
1E	06	02	01
		04	02

function macros

macro WORD(x) $x \% 256$ $(x / 256) \% 256$

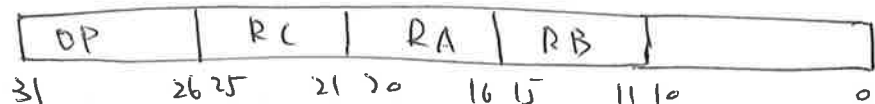


macro LONG(x) WORD(x) WORD(x >> 16)



macro betaop(OP, RA, RB, RC)

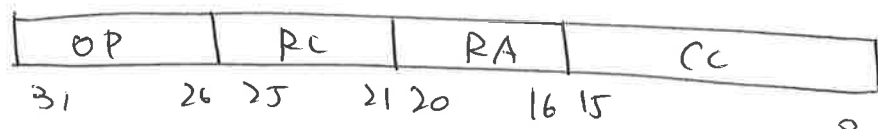
$\text{LONG}((\text{OP} \ll 26) + (\text{RC} \ll 21) + (\text{RA} \ll 16) + (\text{RB} \ll 11))$



ADD

macro betaopc(OP, RA, ^{CC}RC, ~~RC~~)

$\text{LONG}((\text{OP} \ll 26) + (\text{RC} \ll 21) + (\text{RA} \ll 16) + (\text{CC} \% 0x10000))$



ADDC ... LD, ST, JMP

macro beta br(OP, RA, RC, CC)

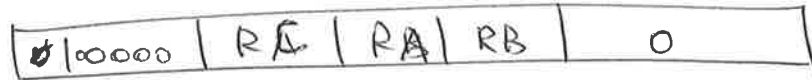
$\text{betaopc}(\text{OP}, \text{RA}, ((\text{CC} - 1) + 4) \gg 2, \text{RC})$

CC in betaopc X ^{why?}

$$X = (CC - (PC + 4)) \gg 2$$

$$CC = PC + 4 + 4 * X. \text{ (LABEL) of jump.}$$

. macro ADD(RA, RB, RC) betaop(0x20, RA, RB, RC)



. macro LD(RA, CC, RC) betaopc(0x18, RA, CC, RC)



. macro ST(RC, CC, RA) betaopc(0x19, RA, CC, RC)



. macro BEQ(RA, LABEL, RC) betabr(0x1D, RA, RC, LABEL)



all the instructions now can write.

• Extending the functionality

- macro MOVE (RA, RC) ADD (RA, R31, RC)
- macro CMOVE (CC, RC) ADDC (R31, C, RC)
- macro BF (RA, LABEL, RC) BEQ (RA, LABEL, RC)
 ↓
 false
- macro BT (RA, LABEL, RC) BNE (RA, LABEL, RC)
- macro BR (LABEL, RC) BEQ (R31, LABEL, RC)
- Macro LD (CC, RC)
- macro ST (RC, CC)
- macro COM (RA, RC) XORC (RA, -1, RC)
 ↓
 ~ RA
- Macro NEG (RA, RC) SUB (R31, RA, RC)
- macro NOP () ADD (R31, R31, R31)

HALT → LONG(0)

ADD 35 , 99

LD (X, R1)

LD (Y, R2)

ADD (R1, R2, R0)

ST (R0, Z)

HALT

X:
LONG (35)

Y:
LONG (99)

Z:
LONG (0)

LD
LD
ADD
ST
0
35
99
0