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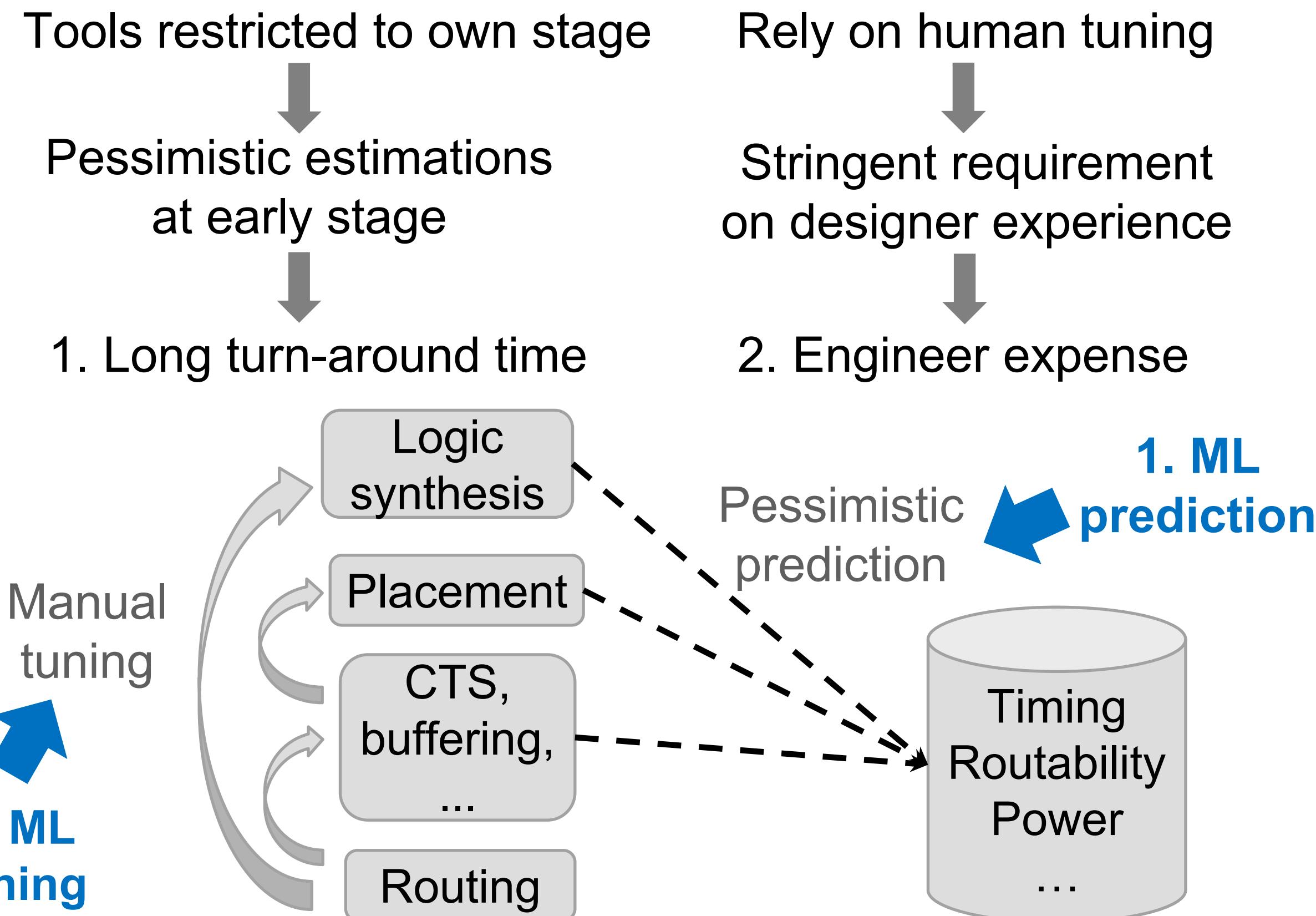
Machine Learning for High-Fidelity Design Prediction and Automatic Tuning



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Task Number: 2810.022

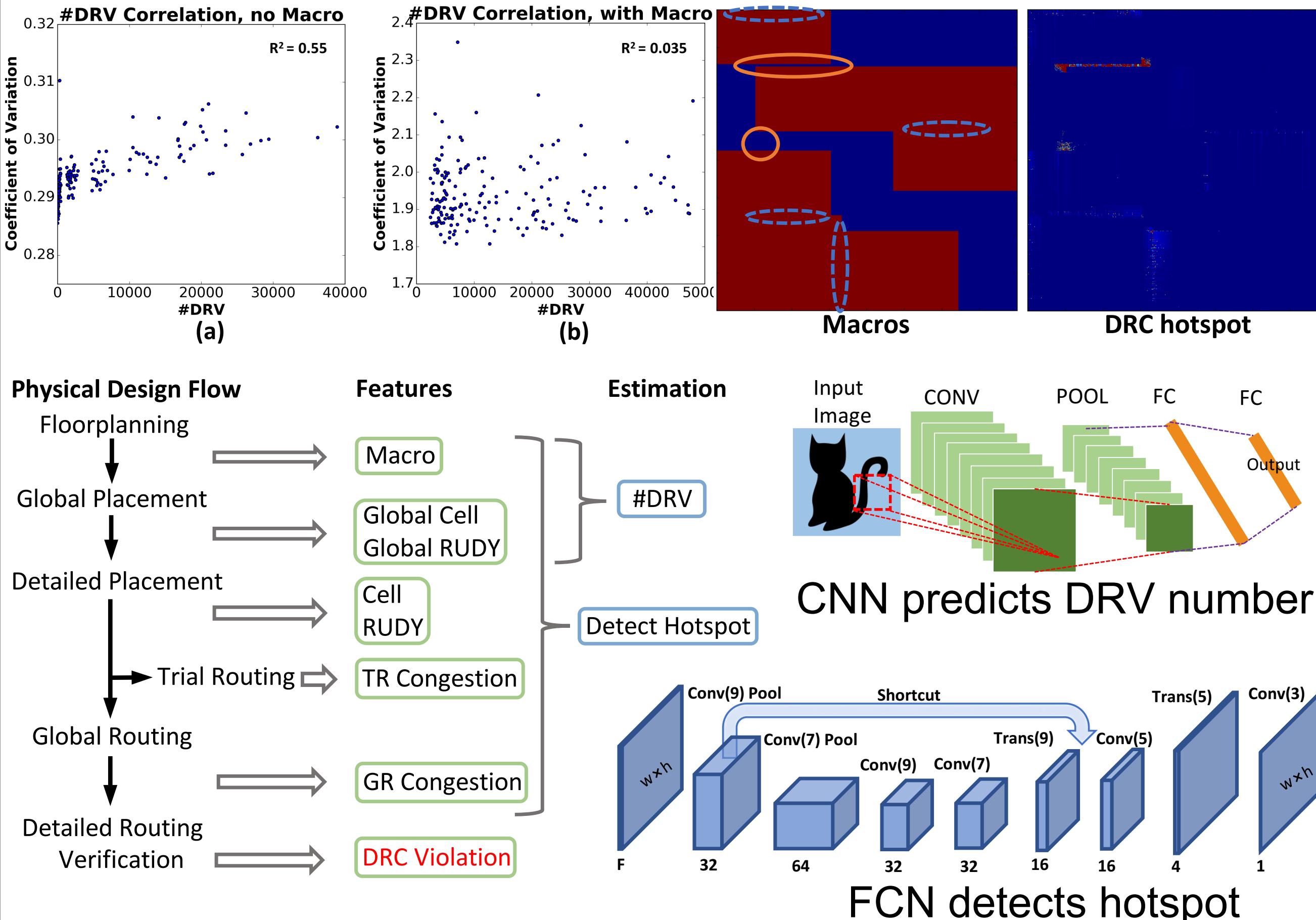
INTRODUCTION

Challenges in traditional EDA

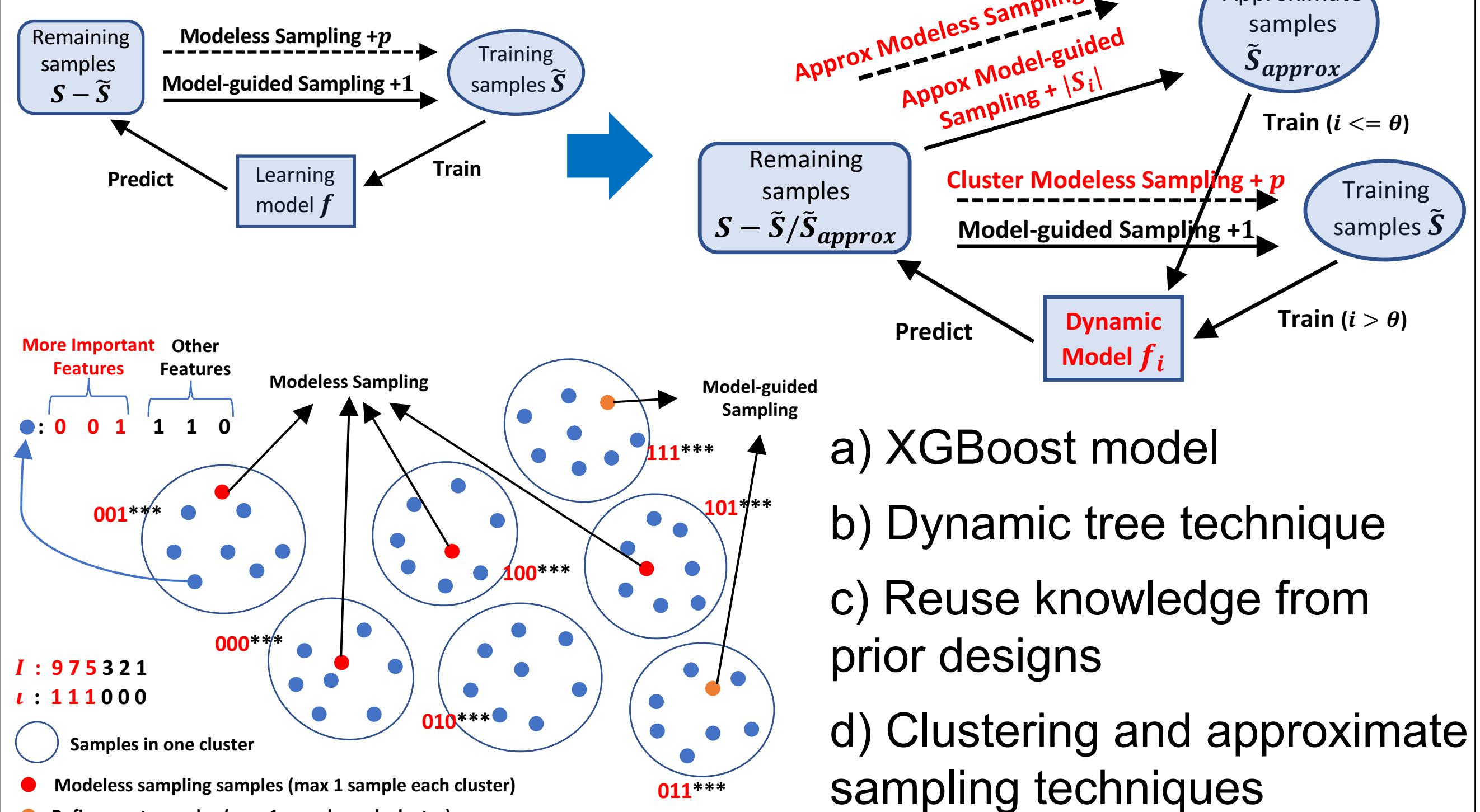


METHODS

1. RouteNet: Routability Prediction for Mixed-Size Designs Using Convolutional Neural Network



2. FIST: A Feature-Importance Sampling and Tree-Based Method for Automatic Design Flow Parameter Tuning

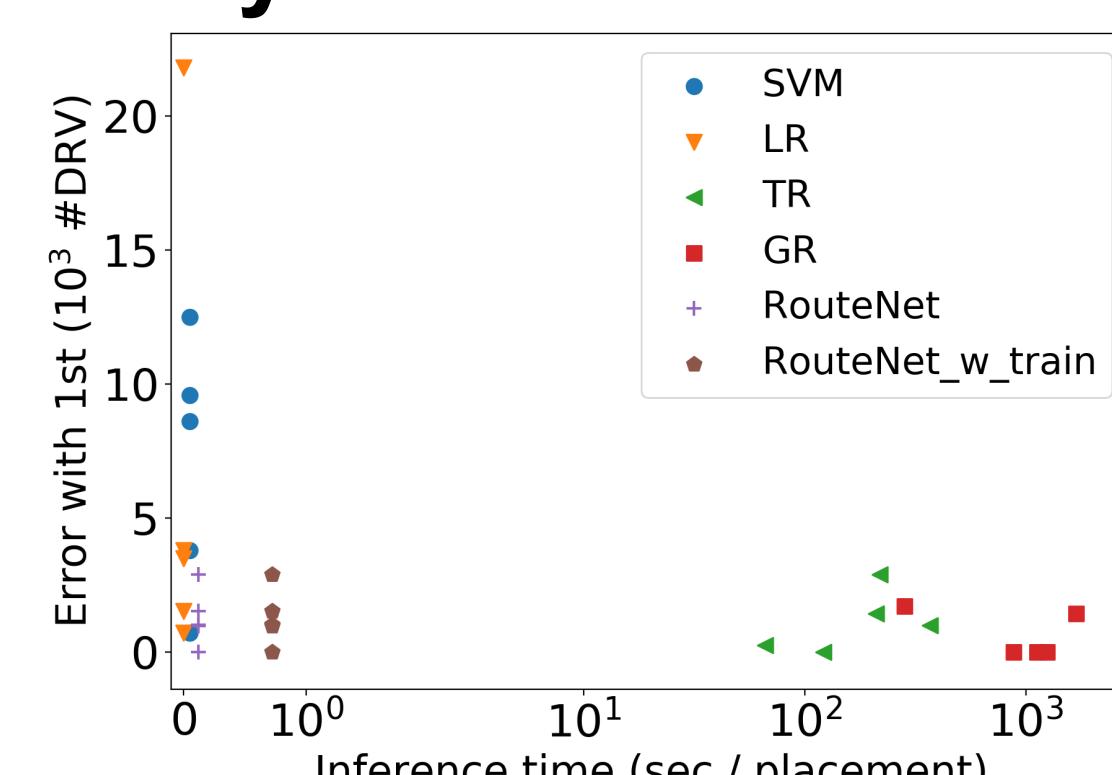


- a) XGBoost model
- b) Dynamic tree technique
- c) Reuse knowledge from prior designs
- d) Clustering and approximate sampling techniques

ROUTABILITY PREDICTION RESULT

Routability prediction by DRV number by CNN

Circuit Name	$c_0/c_1+c_2+c_3$ accuracy (%)					Best rank in top 10				
	SVM	LR	TR	GR	Route Net	SVM	LR	TR	GR	Route Net
des_perf	63	74	80	77	80	87th	15th	2nd	1st	2nd
edit_dist	69	68	78	77	76	17th	17th	3rd	3rd	2nd
fft	66	62	73	70	75	6th	6th	2nd	33rd	1st
matrix_mult_a	66	65	78	74	72	30th	5th	1st	1st	5th
matrix_mult_b	63	62	76	73	76	22nd	93rd	4th	1st	4th
Average	65	66	77	74	76	32nd	27th	2nd	8th	3rd



RouteNet is the only fast and accuracy method that predicts DRV num.

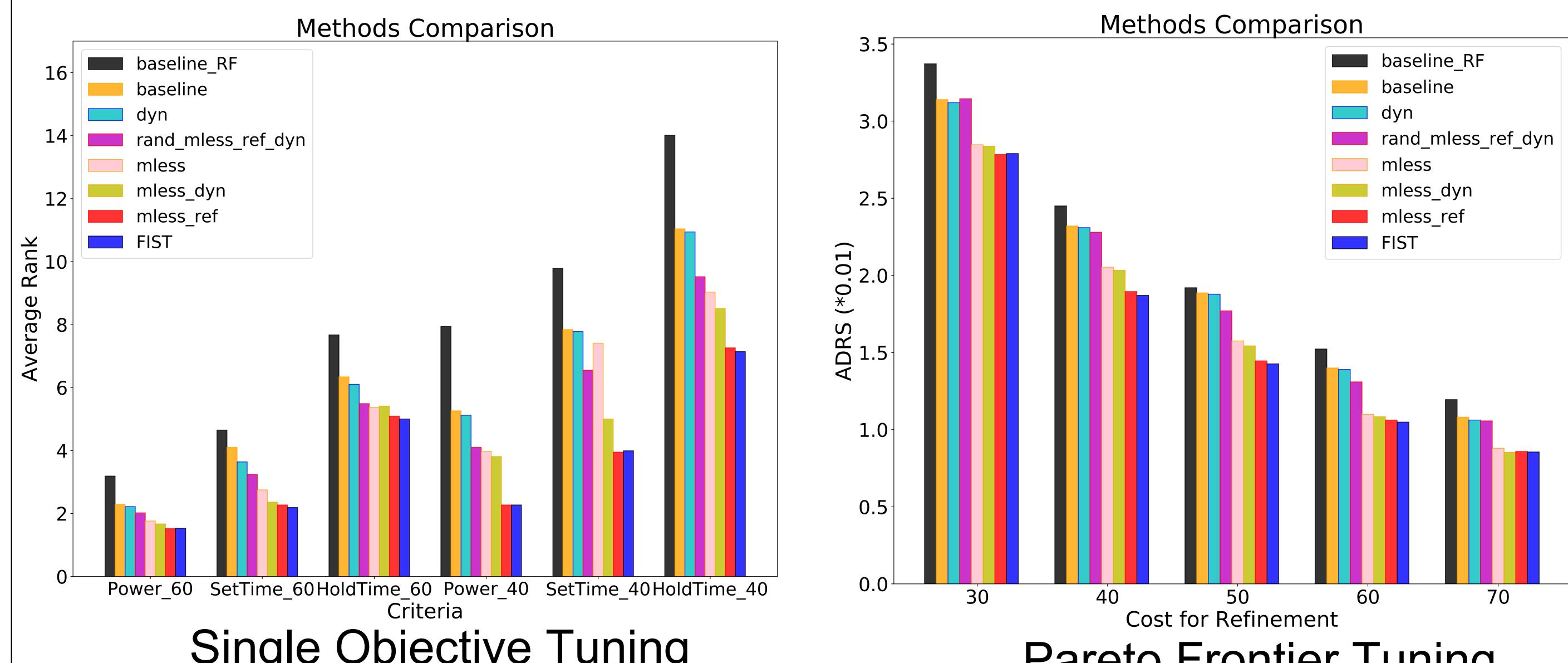
DRV hotspot detection by FCN

Label	Prediction Result		Evaluation		LR	Ground Truth	RouteNet
	Positive	Negative	TP	FN			
Positive	TP	FN	$TPR = \frac{TP}{TP+FN}$		0.54	77	71
Negative	FP	TN	$FPR = \frac{FP}{FP+TN}$				
des_perf	0.54	17	56	54	42	74	74
edit_dist	1.00	25	36	38	28	64	64
fft	0.30	21	45	54	31	71	71
matrix_mult_a	0.21	13	30	34	12	49	49
matrix_mult_b	0.24	13	37	41	20	53	53
Average	0.46	18	41	44	27	62	62

Importance of large receptive region and global information.

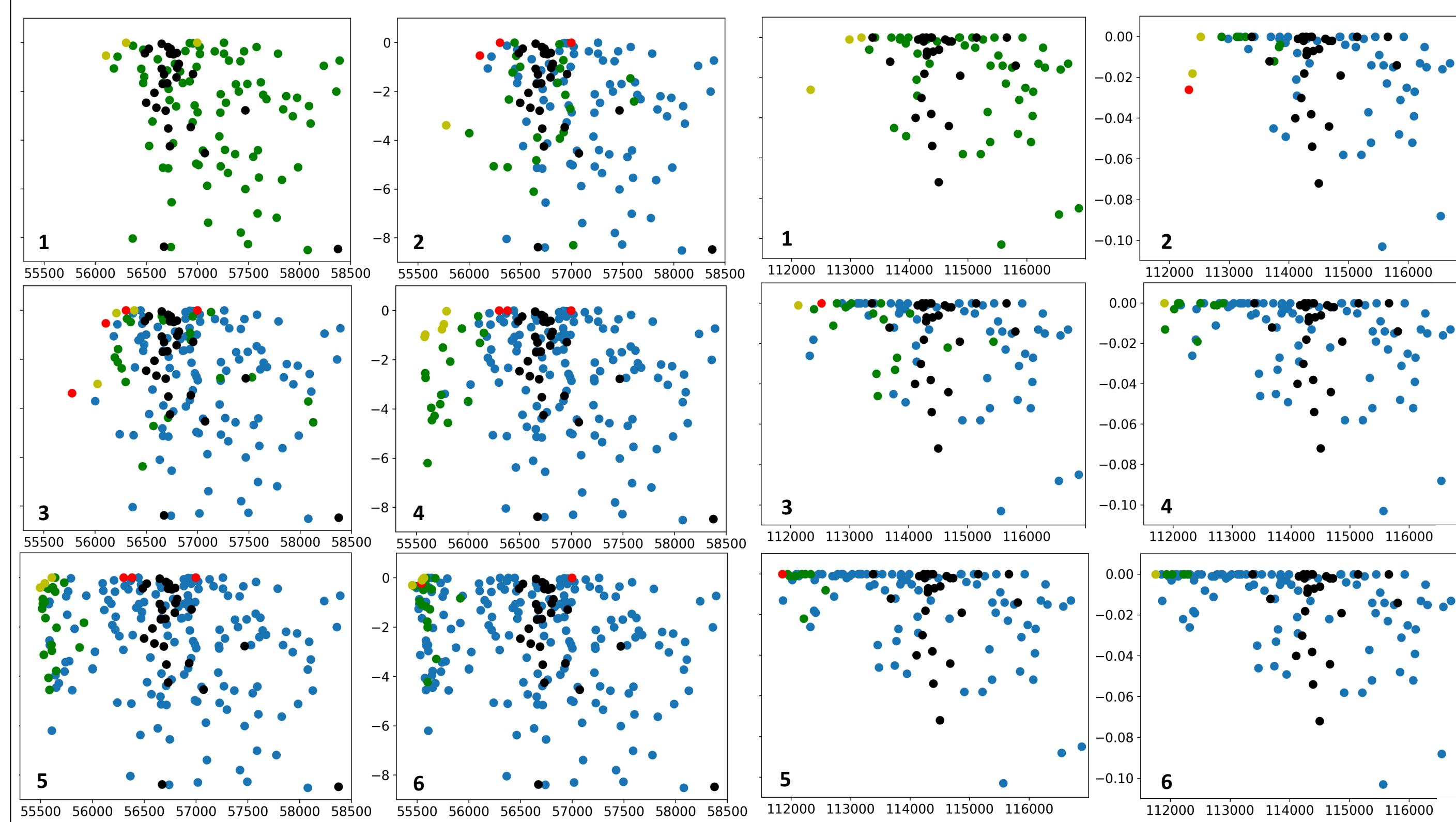
AUTOMATIC TUNING RESULT

The design quality of best explored parameter sets



Single Objective Tuning

Pareto Frontier Tuning



Parameter tuning process in six stages on two industrial designs. Area (μm^2) vs. setup TNS (ns). Red and yellow are Pareto points. Black are baselines from designer.