D3 Design Completion Form.

This form should be filled in during the laboratory session. You must get a supervisor to initial each entry and then sign off the completed form at the end. Don't leave all the validation till the end.

You must keep this form safe and make copies of it. The original or a copy must be bound into your report when you submit it[1]. You should also keep a copy each for your own records. Unless the original form is included in the submissions the group will not get any marks for technical achievement.

Preparation.

Derivation of equations for gain for each stage and the combined circuit (logbook).	AG
A paper design with values for all components (logbook).	AG
Simulation of the above design.	AG

In the laboratory.

First stage

Input impedance in Ohms.	60.1K	AG
Output impedance in Ohms	3.84K	AG
Voltage Gain	6.47	AG
Advanced		
Voltage gain with C_e removed.	3.67	AG
Voltage gain with C_e bridging R_{e2} and R_{e1}.	114.0	AG
Frequency response measured, recorded in logbook and graph drawn.		AG

Second Stage.

Input impedance in Ohms.	44.7K	AG
Output impedance in Ohms	152	AG
Voltage Gain	0.99	AG

Multi-Stage.

Input impedance in Ohms.	60.0K	AG
Output impedance in Ohms	180.4	AG
Voltage Gain	5.92	AG

Conclusion.

		Name	Signature	e-mail
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Handin date[2].	23/2/15	ALEX WOODEN		**Signed Off By.**

You should handin a paper copy of the report plus a zip file containing an electronic copy of your report, all the simulation files and results. Aside from the report each partner should submit the same files.

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[1] One partner has the original the other a copy.
[2] One working week after laboratory date.