Module:	ELEC2205 - Electronic Design
Assignment:	D3
Handin Due:	Mon Feb 23 2015 16:00
Student Name:	ZHI, Yubo
Handin Time:	On time

Learning Outcomes

1. Design and simulate a multistage amplifier
2. Write a Scientific report
3. Implement and debug the circuit
4. Perform scientific measurements
5. Compare theoretical results with theory

Marking Scheme

Criterion	Description	LOs	Mark
1	Writing report	2	20
2	Theoretical Design	1	35
3	Simulation	1	15
4	Circuit Implementation and Debugging	3,4	10
5	Achieving Goals	5	20

Remarks and Feedback

Overall a very good piece of work. All of the main objectives have been met, including the advanced work. The report is very good, with a sensible format and a good distinction between simulation and practical work.

Available component values were chosen and used for simulation. Impedance values have been calculated, but could also have been simulated for completeness. It was good to see that the operating point has been tested in the circuit and compared against the simulation. It would have been good to see some consideration of the likely cause of differences between simulated and actual performance.

Marks Breakdown

1	Writing report	14 / 20
2	Theoretical Design	23 / 35
3	Simulation	12 / 15
4	Circuit Implementation and Debugging	8 / 10
5	Achieving Goals	18 / 20
Days Late:	**Final mark:**	**75 / 100**

Note that marks are provisional until the June exam board.
Please retain all assignments and associated paperwork until then.