### **General Features**

- Advanced Trench Technology
- Lead free product is acquired
- Provide Excellent RDS(ON) and Low Gate Charge

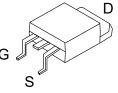
## **Product Summary**



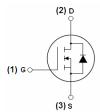
V <sub>DSS</sub>	100	V
R <sub>DS</sub> (ON)-Typ	24	mΩ
ID	34	А

### **Application**

- Load Switch
- PWM Application
- Power management







## **Absolute Maximum Ratings** (Tc=25°C unless otherwise specified)

Symbol	Parameter		Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage		100	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 25°C	34	Α
		T <sub>C</sub> = 100°C	20	Α
I <sub>DM</sub>	Pulsed Drain Current note1		120	Α
EAS	Single Pulsed Avalanche Energy note2		110	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	63	W
R <sub>θJC</sub>	Thermal Resistance, Junction to Case		2.5	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	$^{\circ}$



## **Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Charac	cteristic					
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	100	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V,	-	-	1.0	μΑ
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
On Charac	cteristics					
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0	1.5	2.5	V
	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	24	32	mΩ
$R_{DS(on)}$	note2	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	26	36	mΩ
Dynamic (	Characteristics					
$C_{iss}$	Input Capacitance	\\ -05\\\\ -0\\	-	1500	-	pF
Coss	Output Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	380	-	pF
$C_{rss}$	Reverse Transfer Capacitance	f=1.0MHz	-	252	-	pF
Qg	Total Gate Charge	\/ -20\/ I -45A	-	23	-	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =30V, $I_{D}$ =15A, $V_{GS}$ =10V	-	5	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge	VGS-1UV	-	4	-	nC
Switching	Characteristics					
$t_{d(on)}$	Turn-on Delay Time		-	12.6	-	ns
t <sub>r</sub>	Turn-on Rise Time	V <sub>DS</sub> =30V, I <sub>D</sub> =15A,	-	6	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =1.8Ω, V <sub>GS</sub> =10V	-	22	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	5.3	-	ns
Drain-Sou	rce Diode Characteristics and Maxim	um Ratings				
Is	Maximum Continuous Drain to Source Diode Forward Current		_	_	30	Α
15			_	_	30	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	120	Α
$V_{\text{SD}}$	Drain to Source Diode Forward Voltage  V <sub>GS</sub> =0V, I <sub>S</sub> =30A		-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	71	-	ns
Qrr	Body Diode Reverse Recovery IF=15A,dI/dt=100A/μs Charge		-	145	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : TJ=25  $^{\circ}\text{C}$  ,VDD=50V,VG=10V,L=0.5mH,Rg=25 $\Omega$ 

3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



## **Test Circuit**

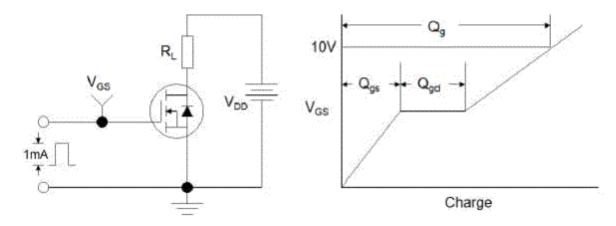


Figure1:Gate Charge Test Circuit & Waveform

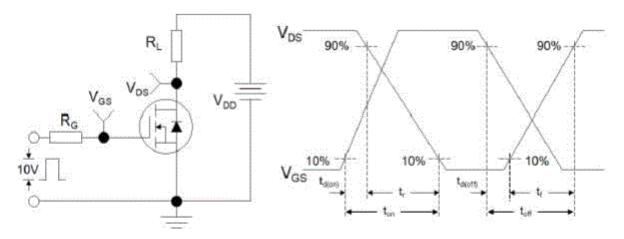


Figure 2: Resistive Switching Test Circuit & Waveforms

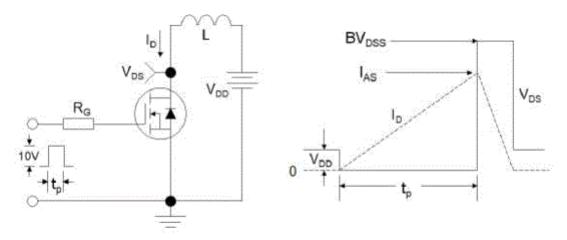


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## **Typical Electrical and Thermal Characteristics (Curves)**

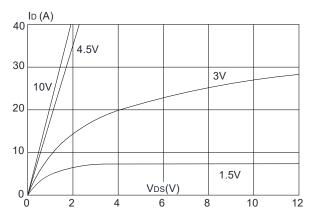


Figure1: Output Characteristics

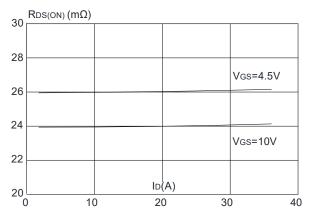


Figure 3:On-resistance vs. Drain Current

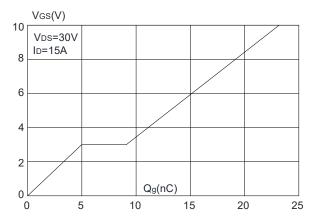


Figure 5: Gate Charge Characteristics

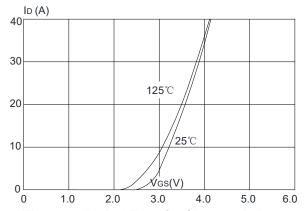


Figure 2: Typical Transfer Characteristics

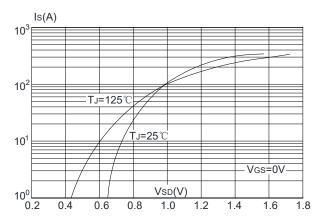


Figure 4: Body Diode Characteristics

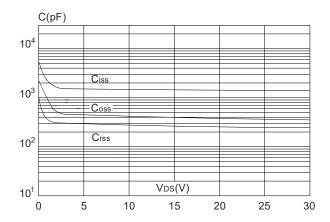
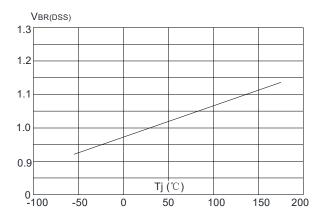


Figure 6: Capacitance Characteristics





**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

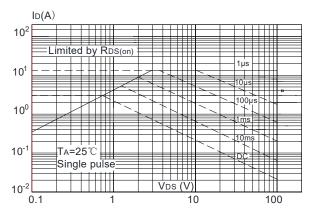
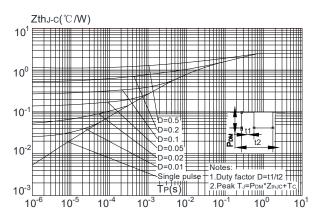
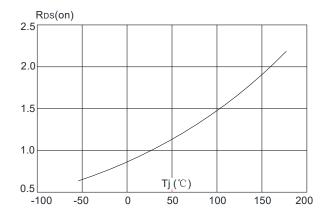


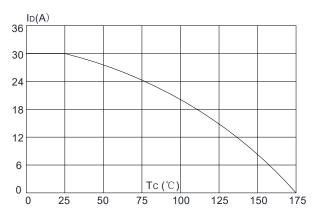
Figure 9: Maximum Safe Operating Area



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature



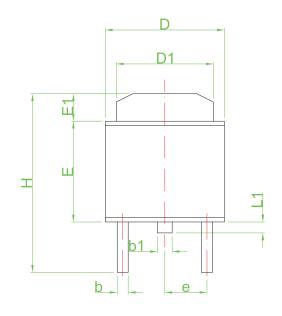
**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

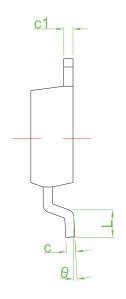
# **Ordering and Marking Information**

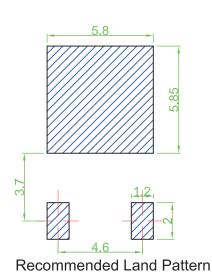
Ordering Device No.	Marking	Package	Packing	Quantity
ASDM100N34KQ-R	100N34	TO-252	Tape&Reel	2500

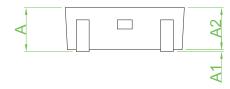
PACKAGE	MARKING
TO-252	Lot Number 100N34 □□□□□ Date Code

## **TO-252 PACKAGE IN FORMATION**









Symbol	Dimensions in Millimeters		Dimensions in Inches		
	Min	Max	Min	Max	
Α	2.25	2.65	0.089	0.104	
A1	0.00	0.15	0.000	0.006	
A2	2.20	2.40	0.087	0.094	
b	0.50	0.70	0.020	0.028	
b1	0.70	0.90	0.028	0.035	
С	0.46	0.66	0.018	0.026	
c1	0.46	0.66	0.018	0.026	
D	6.30	6.70	0.248	0.264	
D1	5.20	5.40	0.205	0.213	
Е	5.30	5.70	0.209	0.224	
E1	1.40	1.60	0.055	0.063	
Н	9.40	9.90	0.370	0.390	
е	2.30 TYP		0.09	TYP	
L	1.40	1.77	0.055	0.070	
L1	0.50	0.70	0.020	0.028	
θ	0°	8°	0°	8°	



## ASDM100N34KQ

#### 100V N-Channel MOSFET

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