



zhongzero

记录数据:

ins-queue: <sup>指令</sup> inst, <sup>指令类型</sup> ordertype, pc, jumppc

RS: inst, ordertype, pc, jumppc, vj, vk, qj, qk, A, <sup>(immediate)</sup> reorder, <sup>是否被占用</sup> busy

SLB: inst, ordertype, pc, vj, vk, qj, qk, A, reorder, <sup>是否可以开始 store</sup> ready

ROB: inst, ordertype, pc, jumppc, <sup>需要修改的寄存器位置与值</sup> dest, value, <sup>是否为分支预测跳转</sup> isjump

register: <sup>值</sup> reg, <sup>最新寄存器位置, 与是否为最新状态</sup> reorder, busy

BHT<sub>[inst]</sub> <sup>SC2</sup> 用于分支预测, 强不跳/弱不跳/弱跳/弱不跳

五大模块

Get-ins-to-queue(): 读取指令到 ins-queue

do-ins-queue():  $\rightarrow$  ROB,  $\begin{cases} \rightarrow RS \\ \rightarrow SLB \end{cases}$

do-RS(): 查看是否有更新 (reg/commit 了的 ROB),  $\rightarrow$  ROB

do-SLB():  $\begin{cases} \text{查看是否有更新 (load data from memory), } \rightarrow \text{ROB} \\ \text{查看是否有更新 (store data from register/store-ready 状态 from ROB), } \rightarrow \text{memory} \end{cases}$

ROB(): 查看队头  $\begin{cases} \text{register} \\ \text{SLB} \end{cases}$  all (remake)

一般计算: LUI, AUUIPC, ADD, SUB, SLL, SLT, SLTU, XOR, SRL, SRA, OR, AND, ADDI, SLTI, SLTIU, XORI, ORI, ANDI, SLLI, SRLI, SRAI

branch:  $\begin{cases} \text{有条件: BEQ, BNE, BLT, BGE, BLEQ, BGEU} \\ \text{无条件: JAL} \\ \text{特殊: JALR} \end{cases}$

load: LB, LH, LW, LBH, LHW

store: SB, SH, SW

