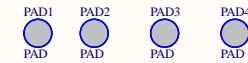
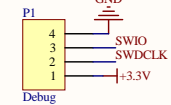
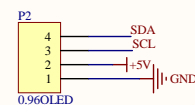
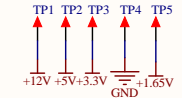


[illegible]

The diagram shows a two-stage DC-DC converter. The first stage, labeled VR1 (L78M05ABDT-TR), is a buck converter that takes a +12V input and outputs +5V. It includes an input capacitor C32 (100nF/50V), an output capacitor C31 (100nF/50V), and a load capacitor C34 (0.1uF/100V). The second stage, labeled VR2 (AMS1117-3.3), is another buck converter that takes the +5V output from the first stage and outputs +3.3V. It includes an input capacitor C36 (100nF/50V), an output capacitor C37 (100nF/50V), and a load capacitor C38 (1uF/50V). A yellow box on the right contains the text: DC12V---->DC5V, ----->DC3.3V.

[illegible]

A

B

C

D

A

B

C

D

