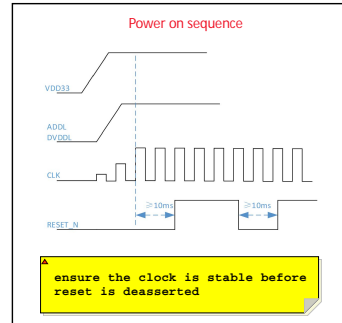
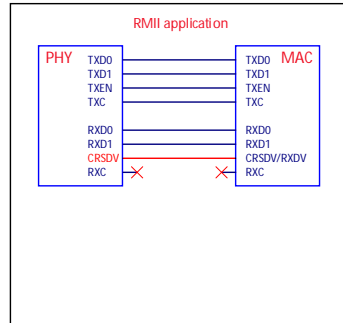
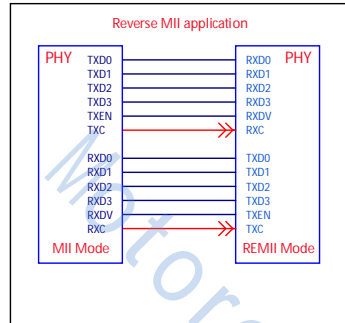
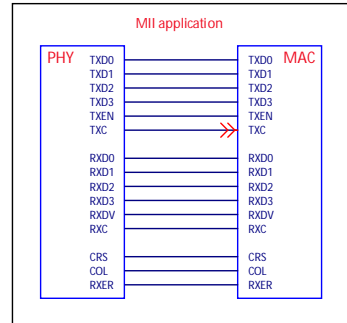


YT8512 Reference Design V1.2

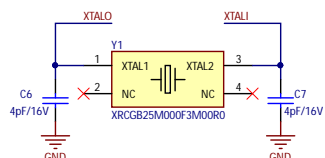
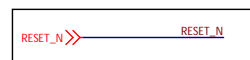
REVISION	DATE	COMMENTS
1.0	07/29/2019	initial version
1.1	10/18/2019	Change R2 from 2.4K to 2.49K Update power on strapping configuration
1.2	06/10/2020	Add reserved pull down resistor for CRS/CRS_DV Update PHY address configuration Update PHY symbol and application notes

Sheet Title: <i>TITLE</i>	Drawn By: <i>OWB</i>
Project Title: <i>YT8512 reference design</i>	Revision: 1.2 Size: A3
Designed for:	Sheet 1 of 2
Number:	Mod. Date: 2020/6/10

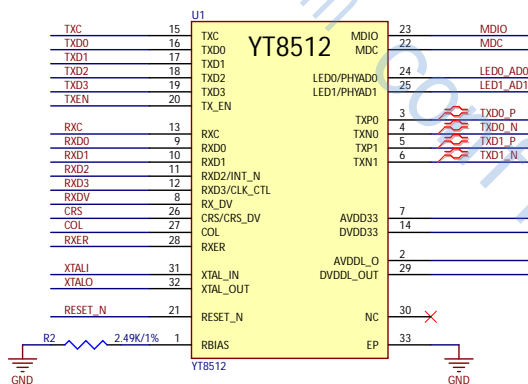




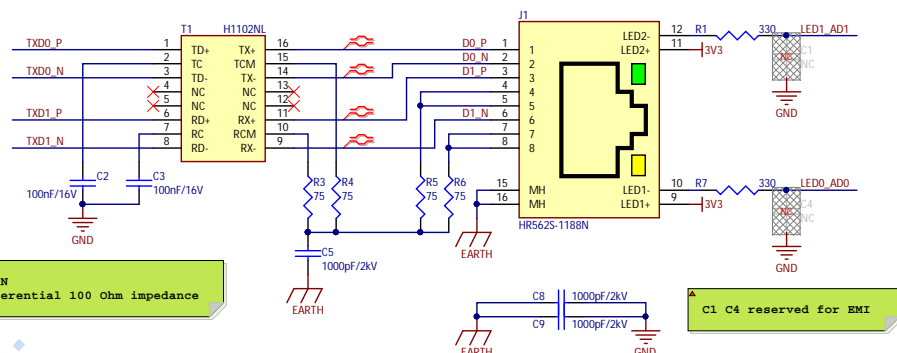
reset from MAC or MCU



25M crystal Calculate the value of external load capacitor according to the crystal load capacitance



place R2 close to PHY



Power on Strapping Pins

RX_DV (IPD)	0 = MII mode 1 = RMII mode
RXD3 (IPD)	0 = TXC output mode 1 = TXC input mode
RXD1 (IPD)	0 = LED mode 1 = WOL mode

LED1/PHYAD1 (IPD)	LED0/PHYAD0 (IPD)	PHY address
0	0	00000
0	1	00001
1	0	00010
1	1	00011

