i.MX28 10.05.02 Linux Standard Release Notes

This document contains important information about the package contents, supported features, and known issues/limitations.

Contents

1	Release Contents			
	1.1	Contents	2	
		License		
2		stem Requirements		
	2.1	Linux Host server	3	
	2.2	Manufacturing tool	3	
	2.3	i.MX28 EVK Components	3	
3	Wh	nat's New	3	
4	LTI	B profile	2	
5		P Supported Features		
6		pported NAND Flash		
7		own Issues/Limitations		



1 Release Contents

1.1 Contents

This release consists of three ZIP files: L2.6.31_10.05.02_ER_source.tar.gz, L2.6.31_10.05.02_ER_images_MX28.tar.gz, and L2.6.31_10.05.02_ER_docs.tar.gz. Tools binary are in mfg.zip

L2.6.31 10.05.02 ER source.tar.gz contains the following files:

File	Description
EULA	Freescale End User License Agreement
install	install script for LTIB
Itib.tar.gz	Itib (Linux Target Image Builder)
package_manifest.txt	Freescale LTIB package lists
Pkgs	Source and patches for root file system
pkgs/imx-test-10.05.02.tar.gz	Source of unit tests
pkgs/imx-lib-10.05.02.tar.gz	Source of libraries
pkgs/imx-bootlets-src-10.05.02.tar.gz	Source of bootlets
pkgs/linux-2.6.31-imx_10.05.02.bz2	Freescale 2.6.31-10.05.02 kernel patches
pkgs/tc-fsl-x86lnx-armeabi-nptl-4.1.2-3.i386.rpm	Open source tool chain for ARM9 and ARM11
pkgs/u-boot-v2009.08-imx_10.05.02.tar.bz2	Patches of u-boot
tftp.zip	A Windows TFTP server program

L2.6.31 10.05.02 ER images MX28.tar.gz contains the following files:

File	Description	
imx28/rootfs.ext2.gz	Root file system ext2 image for the Linux 2.6.31 kernel	
imx28/imx28_linux.sb	SB format file combined bootlets and zImage	
imx28/imx28_uboot.sb	SB format file combined bootlets and uboot image	
imx283/ulmage	Binary kernel image for uBoot	

L2.6.31_10.05.02_ER_docs.tar.gz contains the following files:

File	Description	
EULA	Freescale End User License Agreement	
readme.html	Readme	
doc/mx28	i.MX28 Linux BSP release notes and User Guide.	

mfg.zip Manufacturing tool contains the following files

File	Description
sb_loader.exe	Windows tools. Load sb image from USB to RAM and run
cfimager.exe	Windows tools. Writes a boot stream and rootfs to an SD
	card

File	Description	
sdimage	Linux host tools. Write boot steam to SD card	
MfgTool.exe	Mfgtool host tools	

1.2 License

- All BSP source-code files are GPL or LGPL or another open source license.
- Some binary files contained in the included root file systems are built from proprietary source code (source not included in the BSP):
 - o Files in package csr-bt-bin-1.2.0.tar.gz
 - o Files in package gl-gps-1.2.3.tar.gz

2 System Requirements

2.1 Linux Host server

A Linux host running Ubuntu 9.04 is recommended to install and run LTIB.

2.2 Manufacturing tool

Windows XP SP3

2.3 i.MX28 EVK Components

This EVK contains the following items:

Table 2.1 Kit Components

Hardware Modules	Comments
iMX28 EVK Main board	
iMX28 EVK WVGA LCD Panel	
MMC/SD card	
Power Supply (5V)	
USB Cable	

3 What's New

The section describes the new changes in this release, including new features and defect fixes. This is first release of mx28 Linux BSP. Here just lists new features.

3.1. New Features

Here just lists the main new features:

- o Ethernet IEEE 1588 (Driver only with IXAAT API)
- o Ethernet Switch
- o Support Suspend
- Support CPU Frequency
- o Manufacturing Tool
- o Dual Ethernet support
- o Merge dcp-bootstream driver to DCP driver

3.2. Highlighed Defect Fixes

- o ENGR00122915 Fix iMX28 SDIO INT Handle problem
- o ENGR00122687: [MX28_EVK_BSP] WIFI: Can not download files with large size
- o ENGR00122127 iMX28 turn off usb0 vbus power by default.
- o ENGR00122136 MX28 ALSA: Fail to playback wayfiles with sample rate over 48 KHz
- ENGR00122385 [MX28_BSP_EVK] SD: There are I/O errors when insert an SD card in SD1.
- o ENGR00122127 [MX28_BSP_EVK] Battery: Battery status is Charging even if no usb cable connected.
- o bootlets fails to boot kernel when using nand mtdblock1 for rootfs in bootlets command line

4 LTIB profile

Only the FSL gnome release package profile of LTIB profile was tested in this release.

5 BSP Supported Features

The following table describes the features that are supported in this BSP.

Feature Supported?		Comments
Kernel		
Kernel	Υ	Kernel version: 2.6.31
File System	Υ	EXT2 used as the file system in SD/MMC
Bootloader		
uBoot	Υ	Support Ethernet download and SD/MMC boot
Machine Specific Layer		
ARM Core	Υ	Support ARM9
Interrupt	Υ	
Timer (GPT)	Υ	System timer tick support
GPIO/EDIO	Υ	
IOMUX	Υ	Provides the interfaces for IO configuration
DMA	Υ	
Character Device Drivers		
Debug UART	Y	Console support via internal Debug UART

Feature	Supported?	Comments	
Application UART	Y		
Graphic Drivers			
Frame Buffer Driver	Y	MXC Frame buffer driver	
WVGA	Y	Support WVGA panel.	
Backlight	Y		
MultiMedia Drivers			
V4L2 Output (PXP)	Y	Provide V4L2 implementations	
Camera	N		
Power Management Drivers			
Power Management Unit	Y		
Lower Power mode	Y	Support stop mode in "standby" state.	
CPUFreq	Y	CPUFreq can be used for CPU frequency adjustment and bus scaling.	
Sound Drivers			
SAIF and external audio codec	Υ		
SPDIF	Y		
Input Device Drivers			
LRADC	Y		
Keypad	Y	Support keypad driver	
Touch panel	Υ		
USB devices	Υ	Support USB mouse and USB keypad via USB ports	
MTD driver			
SPINOR	N		
NAND	Y		
Networking Drivers			
Single ENET	Υ		
USB Drivers			
USB Host	Υ		
USB Device	Υ	Support USB OTG device mode	
Security Drivers			
Security drivers(DCP)	Υ		
General drivers			
MMC/SD/SDIO	Y		
WatchDog	Y	Support Watchdog reset	
RTC	Y		
I2C	Y	Support I2C master. Support I2C1, I2C2	
ERP	Y		
PWM	Y	Support backlight driver via PWM	
LED	Υ	0~255 level brightness	
flexCAN	Y		
Battery	Y		

6 Supported NAND Flash

Tested NAND Flash

Manufacturer	Part Number	Chip Count	Medium Size	Page Geometry
--------------	-------------	---------------	----------------	---------------

Manufacturer Part Number		Chip Count	Medium Size	Page Geometry
SAMSUNG K9LBG08U0D		1	1GB	4KiB+128

7 Known Issues/Limitations

Here list some key known issues and workaround:

Features	Category	Description	Workaround
CPU Frequency	BSP	Default CPU frequency is 261MHz	echo -n 454736 > /sys/devices/system/cpu/cpu0/cpufreq/scali ng_setspeed

How to Reach Us:

Home Page:

www.freescale.com

Web Support:

http://www.freescale.com/support

USA/Europe or Locations Not Listed:

Freescale Semiconductor Technical Information Center, EL516 2100 East Elliot Road Tempe, Arizona 85284 +1-800-521-6274 or +1-480-768-2130 www.freescale.com/support

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) www.freescale.com/support

Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064, Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor China Ltd. Exchange Building 23F No. 118 Jianguo Road Chaoyang District Beijing 100022 China +86 010 5879 8000 support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405 Denver, Colorado 80217 1-800-441-2447 or 303-675-2140 Fax: 303-675-2150 LDCForFreescaleSemiconductor@hibbertgroup.com Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the

Freescale and the Freescale logo are trademarks or registered trademarks of Freescale Semiconductor, Inc. in the U.S. and other countries. All other product or service names are the property of their respective owners. Microsoft and Windows are registered trademarks of Microsoft Corporation.

© Freescale Semiconductor, Inc. 2007-2008. All rights reserved.

