



R-Car StarterKit CoM Express Interfaces

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General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual

The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.
- 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

The reserved addresses are provided for the possible future expansion of functions. Do not access
these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal.
 Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product. "R-Car Starter Kit Premier (H3) / Pro (M3) " ComExpress Pin List

Pin	Direction		Function	This parts list corresponds to Sch Signal			estination
A001 A002	- IN	0V (CSI2)	Power CSI2_2	GND CSI2_DATAP0			- SiP(H3_Only)
A003	IN .	(CSI2)	CSI2_2 SYSTEM	CSI2_DATAN0			SiP(H3_Only)
A004 A005	IN :	3.3V 3.3V	SYSTEM	BKUP_TRG BKUP_REQB	-		PMIC,CPLD PMIC,CPLD
A006	OUT	3 3V	SYSTEM SYSTEM/POWER	BKUP_REQB DVFS_PGD	-		PMIC.CPLD
A007 A008	IN IN	(CSI2) (CSI2) (CSI2) (CSI2) (CSI2)	CSI2_2 CSI2_2 CSI2_2 CSI2_2	CSI2 CLKP CSI2 DATAP1 CSI2 DATAP1			SIP(H3_Only) SIP(H3_Only) SIP(H3_Only)
A009	IN	(CSI2)	CSI2_2	CSI2_DATAP1	-	-	SiP(H3_Only)
A010 A011	IN -	(CSI2) 0V	CSI2_2 Power	CSI2 DATAN1 GND	-		SiP(H3_Only)
A012	IN	(CSI2)	Power CSI2_3	CSI3_DATAN0			SiP
A013 A014		(CSI2) 3.3V	CSI2_3 PWM	CSI3_DATAP0 PWM2	- GP2_08		SiP PMIC,CPLD
A015	IN	3.3V	SYSTEM/POWER	EX PWRONn			PMIC
A016 A017		(CSI2) (CSI2)	CSI2_3 CSI2_3	CSI3_CLKN CSI3_CLKP	-		SiP SiP
A018	IN :	3.3V	SYSTEM/POWER	RSTMODE SW	-	-	PMIC
A019 A020		1.8V? 1.8V?	LVDS0 LVDS0	LVDS0_CLK_P LVDS0_CLK_N	-		SiP SiP
A020 A021	- 1	0V	Dower	GND	-	-	- SiP
A022 A023	IN IN	(CSI2) (CSI2) (CSI2)	CSI2 1 C	CSI1 DATAN1 CSI1 DATAP1 CSI1 CLKP			SiP SiP
A024	IN	(CSI2)	CSI2_1	CSI1_CLKP	-	-	SiP
A025 A026	IN IN	(CSI2) (CSI2)	CSI2_1 CSI2_1	CSI1_CLKN CSI1_DATAN0	-		SiP SiP
A026 A027	IN	(CSI2)	CSI2_1	CSI1 DATAP0			SiP
A028 A029	IN OUT	3.3V (LVDS)	SCIF0 LVDS0	SCIFO RX/SD0 VL LVDS0 CH1 P	GP5_01	with on-board PMIC	PMIC SiP
A030	OUT	(LVDS)	LVDS0	LVDS0_CH1_N	-	-	SiP
A031 A032	- OUT	0V (LVDS)	Power LVDS0	CND LVDS0 CH3 P LVDS0 CH3 N LVDS0 CH0 P			- SiP
A032 A033 A034	OUT	(LVDS)	LVDS0	LVDS0_CH3_N	-		SiP
A034	OUT OUT	(LVDS) (LVDS)	LVDS0 LVDS0	LVDS0_CH0_P			SiP SiP
4035 4036	OUT	(LVDS)	LVDS0	LVDS0_CH0_N LVDS0_CH2_P	-		SiP
4037	OUT	(LVDS)	LVDS0 HDMI OUT	LVDS0_CH2_P LVDS0_CH2_N HDM1_CEC	-	-	SiP SiP(H3_Only)
A038 A039	OUT	3.3V LVDS	HDMI_OUT	HDMI1_CEC HDMI1_HPD	GP7_02 -		SiP(H3_Only)
A040 A041	BIDIR	3.3V 0V	GPIO Power	AVS2/GP7_01/PMIC GND	GP7_01		SÎP,PMIC
\041 \042		1.8V	JTAG	TDO 18		with on-board CN	
\043		1.8V	SYSTEM/POWER	TDO_18 PRESETn_18			SiP,CN2,CN3,U22,CPLD,PMIC
A044 A045		1.8V 1.8V	INTC JTAG	NMIn_18 TDI_18		with on-board CN	SIP CN3 CN3
\046	IN	1.8V	JTAG	TDI 18 TRSTn_18	-	with on-board CN with on-board CN	CN3
4047 4048	IN	- 1.8V	nc JTAG	TCK 18		with on-board CN	- CN3
\049	BIDIR	3.3V	GPIO	AVS1/GP7_00/PMIC MSIOF1_TXD/LVDS_BLEN	GP7_00	-	SiP,PMIC
4050 4051	OUT	3.3V 0V	MSIOF1 Power	MSIOF1_TXD/LVDS_BLEN	GP6_07		SiP,CPLD -
A051 A052	IN or OUT	3.3V	MSIOF0	GND MSIOF0_SCK USB22_ID2	GP5_17	with on-board SW	- SiP
4053 4054	BIDIR IN	3.3V 1.8V	USB 2.0 ch2 JTAG	USB22_ID2 TMS_18		with on-board CN	SiP CN3
A055	BIDIR	(5V via 30k)	USB 3.0 ch1	USB31_VBUS	-		SiP
A056 A057		(5V via 30k) 0V	USB 3.0 ch0 Power	USB30_VBUS GND			SiP -
A057 A058	OUT	3.3V	MSIOF0 MSIOF0	MSIOF0_TXD MSIOF0_RXD	GP5_20	with on-board SW	SiP
A059 A060		3.3V 0V	MSIOF0 Power	MSIOF0_RXD GND	GP5_22	with on-board SW	SiP
A061	BIDIR	3.3V	USB 3.0 ch1	USB31_ID	-	-	SiP
A062 A063	BIDIR IN or OUT	3.3V 3.3V 3.3V	USB 3.0 ch0 SCIF1	USB30_ID SCIF1_SCK	- GP6_21		SiP SiP
A064	OUT	(PCle)	PCle ch1	PCIE1_TX_P	-		SiP
4065 4066		(PCle) 0V	PCle ch1 Power	PCIE1 TX M GND			SiP -
1067	OUT	3.3V	MSIOF1	GP6_06/OTG_STAT2/MSIOF1_SS2	GP6_06		SiP
A068		(PCIe)	PCIe ch0 PCIe ch0	PCIE0_TX_P PCIE0_TX_M			SiP SiP
A069 A070	OUT -	(PCIe) 0V	Power	GND	-		-
\071 \072	IN or OUT	3.3V	HSCIF2 HSCIF2	HSCIF2 HRTSZ/SSI SDATA8	GP6_20		SiP SiP
\072 \073	IN or OUT	3.3V 3.3V	HSCIF2	HSCIF2 HRX/SSI SCK78 HSCIF2 HTX/SSI WS78 HSCIF2 HCTSZ/SSI SDATA7	GP6_17 GP6_18		SiP
\074	IN or OUT	3.3V 3.3V	HSCIF2	HSCIF2_HCTSZ/SSI_SDATA7 USB31_OVC	GP6_19		SiP SiP
A075 A076	IN IN or OUT	3.3V	USB 3.0 ch1 HSCIF0	HSCIF0_HRTSZ	GP6_31 GP5_16		SiP
1077	OUT . BIDIR	3.3V 3.3V	USB 2.0 ch2 I2C0	USB22_PWEN SDA0/SD3_PWEN	GP6_14		SiP SiP
\078 \079	IN or OUT	3.3V 3.3V	MSIOF1	MSIOF1_SCK/SSI_SCK4	GP3_15 GP6_08		SIP SIP,CPLD
\080	- 1	0V	Power	GND	-		- SiP
.081 .082	BIDIR	USB2.0 USB2.0	USB 3.0 ch1 USB 3.0 ch1	USB31_DP USB31_DM			SiP
1083	BIDIR	USB2.0	USB 3.0 ch0	USB30_DM			SiP
N084 N085	BIDIR IN or OUT	USB2.0 5V	USB 3.0 ch0 Power	USB30_DP D5.0V			SiP -
.086	IN or OUT	5V 5V	Power	D5.0V		-	
.087 .088	OUT	3.3V LP-HCSL	AUDIO PCIe ch0	EX_AUDIO_CLKA PCIEO_CN_CLK_P			X11 U87
.089	OUT	LP-HCSL	PCIe ch0	PCIE0_CN_CLK_M			U87
.090 .091		0V 5V	Power Power	GND D5.0V			- - -
092	IN or OUT	5V	Power	D5.0V	-	-	
.093 .094	IN or OUT	5V 5V	Power Power	D5.0V D5.0V			
095	IN or OUT	5V 5V 3.3V	Power	D5.0V	-		
.096 .097	OUT	3.3V 3.3V	USB 3.0 ch1 USB 3.0 ch0	USB31 PWEN USB30 OVC	GP6_30 GP6_29		SiP SiP
098	OUT	3.3V	USB 3.0 ch0	USB30_OVC USB30_PWEN	GP6_28		SiP
1099 1100	BIDIR	USB2.0 0V	USB 2.0 ch1 Power	USB20_VBUS0 GND			SiP
A100 A101	BIDIR	3.3V	USB 2.0 ch0	USB20_ID0			- SiP
102	BIDIR	USB2.0 USB2.0	USB 2.0 ch0 USB 2.0 ch0	USB20 DM0 USB20 DP0	-		SiP SiP
\103 \104	אוטום -	USDZ.U -	NC(Power)	NC(VCC12V0)	-		VCC12V0 Starter Kit Not Used (Option FA
\105		-	NC(Power)	NC(VCC12V0)	-		VCC12V0 Starter Kit Not Used (Option FA)
106 107		- -	NC(Power) NC(Power)	NC(VCC12V0) NC(VCC12V0)			VCC12V0 Starter Kit Not Used (Option FA VCC12V0 Starter Kit Not Used (Option FA
		-	NC(Power) NC(Power)	NC(VCC12V0) NC(VCC12V0)	-		VCC12V0 Starter Kit Not Used (Option FA)
108 109							VCC12V0 Starter Kit Not Used (Option FA

Pin	Direction Level/Group	Function	Signal	GPIO	Shared D	Destination
B001 B002	- 0V IN 2.5V	Power RGMII	GND AVB_PHY_INT_25		- with on board PHY	- U34
B003 B004	IN 2.5V	RGMII RGMII	AVB_RD0 AVB_RD1		with on board PHY with on board PHY	SiP(Option:when Resistance mounted) SiP(Option:when Resistance mounted)
B005	IN 2.5V	RGMII	AVB_RD2		with on board PHY	SiP(Option:when Resistance mounted)
B006 B007	IN 2.5V IN 2.5V	RGMII RGMII	AVB_RD3 AVB_RXC		with on board PHY with on board PHY	SiP(Option:when Resistance mounted) SiP(Option:when Resistance mounted)
B008 B009	IN 2.5V OUT 2.5V	RGMII RGMII	AVB_RX_CTL AVB_TD0		with on board PHY with on board PHY	SiP(Option:when Resistance mounted) SiP(Option:when Resistance mounted)
B010	OUT 2.5V	RGMII	AVB_TD1		with on board PHY	SiP(Option:when Resistance mounted)
B011 B012	- 0V OUT 2.5V	Power RGMII	GND AVB_TD2		with on board PHY	SiP(Option:when Resistance mounted)
B013 B014	OUT 2.5V OUT 2.5V	RGMII RGMII	AVB_TD3 AVB_TXC		with on board PHY with on board PHY	SiP(Option:when Resistance mounted) SiP(Option:when Resistance mounted)
B015 B016	OUT 2.5V BIDIR 2.5V	RGMII RGMII	AVB_TX_CTL AVB_MDIO_25		with on board PHY with on board PHY	SiP(Option:when Resistance mounted) U33
B017	OUT 2.5V	RGMII	AVB_MDC_25		with on board PHY	U33
B018 B019	OUT 2.5V OUT 3.3V	RGMII CANFD1	AVB_PHY_RESETn_25 MDT0/BSn/GP1_22/CAN1_TX	- GP1_22	with on board PHY MDT0	U35 SiP,CPLD
B020 B021	IN 3.3V - 0V	CANFD1 Power	WE1n/GP1_26/CAN1_RX GND	GP1_26 -	WE1n	SiP,CPLD -
B022 B023	IN 3.3V IN 3.3V	VIN5 VIN5	VI5_DATA7(ExD7) VI5_DATA4(ExD4) VI5_DATA5(ExD5)	GP0_07 GP0_04	MD05 MD11	SiP,CPLD SiP,CPLD
B024	IN 3.3V	VIN5	VIS_DATA5(ExD5)	GP0_05	MD03	SiP,CPLD
B025 B026 B027	IN 3.3V IN 3.3V	VIN5 VIN5	VI5_DATA0(ExD0) VI5_DATA3(ExD3)	GP0_00 GP0_03	- MD02	SiP,CPLD SiP,CPLD
B027 B028	IN 3.3V IN 3.3V	VIN5 VIN5	VI5_DATA2(ExD2) VI5_DATA1(ExD1)	GP0_02 GP0_01	MD01 MD00	SiP,CPLD SiP,CPLD
B029 B030	BIDIR 1.8/3.3V	GPIO	GP4_14	GP4_14		SiP
B031	- 0V	nc Power	GND			
B032 B033	OUT 1.8V OUT 1.8V	POWER POWER	D1.8V (current tbd) D1.8V (current tbd)			
B034 B035	OUT 1.8V OUT 1.8V	POWER POWER	D1.8V (current tbd) D1.8V (current tbd)			
B036		nc	-	- CDe no		SID CDI D
B037 B038	IN or OUT 3.3V	MSIOF1 nc	MSIOF1_SYNC/SSI_WS4	GP6_09 -		SiP,CPLD
B039 B040	IN or OUT 3.3V OUT 3.3V	SCIF0 PWM	SCIF0_SCK/I2C2_SDA PWM3/IRQ2n/DU_DISP	GP5_00 GP2_02	with on-board I2C	SiP,U38,U41 SiP
B041 B042	- 0V BIDIR 3.3V	Power GPIO	GND WE0n/GP1_25/CAN_CLK	- GP1 25		SiP
B043 B044	OUT 1.8/3.3V OUT 3.3V	SDHC3	SD3_PWR_SEL ExA20/PWM2	GP4_13		SiP SiP,CPLD
B045	BIDIR 1.8/3.3V	SDHC3 VIN5	SD3 DAT1 V	GP4_10		SiP,CPLD
B046 B047	IN 3.3V OUT 1.8/3.3V	SDHC3 PWM	VI5_CLKENB(CS0n) SD3_PWR_EN	GP1_20 GP4_17 GP2_06		SIP,CPLD SIP SIP,CPLD
B048 B049	OUT 3.3V IN 3.3V	PWM SYSTEM	SD3_PWR_EN ExA22/PWM0 PRESET_SYSZ	GP2_06 -		SiP,CPLD CPLD
B050 B051	IN 3.3V OUT 3.3V - 0V	SYSTEM SYSTEM Power	PRESET_CBZ(PRESETOUTn) GND			CPLD SiP,U35,U38,CPLD
B052	OUT 1.8/3.3V	SDHC3	SD3_CLK_V	GP4_07		SiP
B053 B054	BIDIR 3.3V BIDIR 3.3V	GPIO 12C5 12C5 12C1	GP2_00/IRQ0n #SDA5	GP2_00 -		SiP,PMIC SiP
B055 B056	BIDIR 3.3V BIDIR 3.3V	12C5 12C1	#SCL5 SDA1	- GP5_24		SiP SiP
B057 B058	BIDIR 3.3V BIDIR 3.3V IN or OUT 2.5V	GPIO Power	GP5_25/MLB/IOEX_INTn VLDO_2.5V	GP5_25		SiP EtherAVB(GbPHY)
B059	IN or OUT 2.5V	Power	VLDO_2.5V			EtherAVB(GbPHY)
B060 B061	- 0V 	Power	GND -			
B062 B063	OUT 3.3V IN or OUT 3.3V	PWM MSIOF0	ExA25/PWM4 MSIOF0_SYNC	GP2_03 GP5_18		SiP,CPLD SiP
B064 B065	IN (PCIe) IN (PCIe)	PCle ch1 PCle ch1	PCIE1_RX_P PCIE1_RX_M			SiP SiP
B066	IN 3.3V	VIN5	VI5_CLK(CS1n/A26)	- GP1_21		SiP
B067 B068	OUT 3.3V IN (PCIe)	MSIOF0 PCle ch0	MSIOF0_SS2/AUDIO_CLKC PCIE0_RX_P	GP5_21 -		SiP SiP
B069 B070	IN (PCIe) - 0V	PCIe ch0 Power	PCIE0_RX_M GND			SiP -
B071	IN (SATA) IN (SATA)	SATA SATA	SATA RX M SATA RX P			9 SiP
B072 B073 B074	IN or OUT 3.3V	COIDS	ecita eciziena (ii	GP5_09 GP5_06		SiP SiP
B075	OUT (SATA) OUT (SATA) OUT (3.3V	SCIP1 SCIP1 SATA SATA MSIOF1 MSIOF1 SCIP1	GPS 06/1X/IHTXI_CN SATA_TX_P SATA_TX_M SATA_TX_M GP6_05/OTG_STATI/MSIOF1_SS1 MSIOF1_RXD/SSI_SDATA4			SIP SIP
B076 B077 B078	OUT 3.3V	MSIOF1	GP6_05/OTG_STAT1/MSIOF1_SS1 MSIOF1_RXN/SSI_SDATA/	GP6_05 GP6_10 GP5_05		SiP SiP.CPLD
B079 B080	IN 3.3V IN 3.3V - OV	SCIF1 Power	GPS_05/RX1/HRXT_CN GND			ŚiP -
B081 B082	IN or OUT 3.3V	HSCIF0 nc	HSCIFO_HCTS2	GP5_15		SiP
B083	OUT 3.3V OUT 3.3V	Power	D3.3V D3.3V	-		
B084 B085	OUT 3.3V	Power Power	D3.3V			
B086 B087	OUT 3.3V OUT 3.3V	Power Power	D3.3V D3.3V	-		
B088 B089	OUT 3.3V IN 3.3V	HSCIF0 HSCIF0	HSCIFO_HTX HSCIFO_HRX	GP5_14 GP5_13		SiP SiP
B090	- 0V	Power	GND	-		
B091 B092	IN or OUT 5V IN or OUT 5V	Power Power	D5.0V D5.0V	-		
B093 B094	IN or OUT 5V IN or OUT 5V	Power Power	D5.0V D5.0V	-		
B095 B096	IN or OUT 5V IN or OUT 5V	Power Power	D5.0V D5.0V		-	
B097	IN or OUT 3.3V	SCIF0	SCIF0_CTSZ/SD1_VL	GP5_03 GP1_23	MDT:	SiP
B098 B099	OUT 3.3V IN 3.3V	CANFD0 CANFD0	MDT1/RDn/GP1_23/CAN0_TX RD/WR#/GP1_24/CAN0_RX	GP1_23 GP1_24	MDT1 RD/WR	SiP,CPLD SiP,CPLD
B100 B101	- 0V IN or OUT 5V	Power Power	GND D5.0V		-	
B102 B103	IN or OUT 5V IN or OUT 5V	Power Power	D5.0V D5.0V	-		
B104		NC(Power)	NC(VCC12V0)			VCC12V0 Starter Kit Not Used (Option FAN)
B105 B106		NC(Power) NC(Power) NC(Power)	NC(VCC12V0) NC(VCC12V0) NC(VCC12V0)	-		VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN)
B107 B108		NC(Power) NC(Power)	NC(VCC12V0)			VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN)
B109 B110	OV	NC(Power) Power	NC(VCC12V0) GND	-		VCC12V0 Starter Kit Not Used (Option FAN)
2110	,=•				<u> </u>	

Pin C001	Direction	Level/Group 0V	Function Power	Signal GND	GPIO .	Shared	Destination
C001 C002 C003	- IN	0V (CSI2)	Power CSI2 2	GND CSI2 DATAN2			- SiP
C004 C005	İN	(CSI2) 0V	CSI2_2 Power	CSI2_DATAP2 GND			SiP
C006 C007	IN IN	(CSI2) (CSI2)	CSI2 2 CSI2 2	CSI2_DATAN3 CSI2_DATAP3	<u>-</u>		- SiP SiP
C008 C009	OUT	0V 3.3V	Power	GND PMIC GPI00			- PMIC
C010 C011	BIDIR	3.3V 0V	SYSTEM/POWER 12C4 Power	IZC4_SDA GND	-	with on-board I2C	SiP,U61,U87
C012	BIDIR	1.8V 3.3V	QSPI	QSPI0_IO3		with RPC	U5,CN2
C013 C014	BIDIR -	0V	I2C4 Power QSPI	I2C4_SCL GND QSPI0_IO2		with on-board I2C	SiP,U61,U87 U5,CN2
C015 C016	BIDIR OUT	1.8V 0.8V	POWER	DDR0_1.8V (probe only)		with RPC -	-
C017 C018	OUT IN or OUT	3.3V 3.3V	SCIF0	WE1n/GP1_26/CAN1_RX SCIF0_RTSZ/I2C2_SCL	GP1_26 GP5_04	with on-board I2C	SiP SiP,U38,U41
C019 C020 C021	BIDIR IN	3.3V 1.8V	I2C0 BSMODE	SCL0/SD3_VL BSMODE_18	GP3_14 -	with on-board PMIC	SiP,PMIC SiP
C021 C022 C023	OUT	0V 3.3V	Power USB_OTG	GND GP6_16/OTG_EXTLPn VI4_DATA4(ExD12)	GP6_16 GP0_12	- DU_RGB/MD09	SIP SIP,CPLD
C024	IN BIDIR	3.3V	VIN4 GPIO	GP6_04	-	-	: SiP
C025 C026 C027	IN IN	3.3V 3.3V	VIN4 VIN4	VI4_DATA5(ExD13) VI4_DATA2(ExD10)	GP0_13 GP0_10	DU_RGB/MD10 DU_RGB/MD07 DU_RGB/MD08	SIP,CPLD SIP,CPLD SIP,CPLD
C028	IN IN	3.3V 3.3V	VIN4 VIN4	VI4_DATA2(ExD10) VI4_DATA3(ExD11) VI4_DATA0(ExD8)	GP0_11 GP0_08	DU_RGB/MD12	SiP,CPLD
C029 C030	IN IN	3.3V 3.3V	VIN4 VIN5	VI4_DATA1(ExD9) VI5_DATA6(ExD6)	GP0_09 GP0_06	DU_RGB/MD06 MD04	SiP,CPLD SiP,CPLD
C031 C032 C033	-	0V -	Power inc	GND	- -	<u> </u>	<u>-</u>
C033 C034	OUT IN	3.3V 3.3V	- VIN4	EXT_LPO VI4_DATA6(ExD14) VI4_DATA7(ExD15)	- GP0_14	- DU_RGB	SiP SiP,CPLD
C034 C035 C036	IN IN	3.3V 3.3V	VIN4 VIN4	VI4_DA I A8(ExA0)	GP0_14 GP0_15 GP1_00	DU_RGB DU_RGB DU_RGB/MD28 DU_RGB/MD27	SIP,CPLD SIP,CPLD SIP,CPLD
C037 C038	IN IN	3.3V 3.3V	VIN4 VIN4	VI4_DATA9(ExA1) VI4_DATA10(ExA2)	GP1_01 GP1_02	DU_RGB	SiP,CPLD SiP,CPLD
C039 C040	IN -	3.3V -	VIN4 nc	VI4_DATA11(ExA3) -	GP1_03 -	DU_RGB/MD26 -	SiP,CPLD -
C041 C042	- IN	0V 3.3V	Power VIN4/5	GND Vix_DATA12(ExA4)	- GP1_04	- DU_RGB/MD25 DU_RGB/MD24	- SiP,CPLD
C043 C044	IN OUT	3.3V 3.3V	VIN4/5 PWM	VIx_DATA13(ExA5) PWM1	GP1_05 GP2_07	DU_RGB/MD24 -	SIP,CPLD CPLD
C045	OUT BIDIR	3.3V 3.3V	CPG GPIO	FSCLKST SD0 CD	- GP3_12	- with on-board SD	SiP SiP,CN6
C046 C047 C048	IN OUT	3.3V 3.3V	SYSTEM AUDIO	SYS_TRG EX_AUDIO_CLKB	- -	<u> </u>	CPLD
C049 C050	BIDIR BIDIR	3.3V 1.8/3.3V	GPIO SDHC3	SD0_WP SD3_DAT3_V	GP3_13 GP4_12	with on-board SD with on-board SD	SiP,CN6 SiP,CN6
C051 C052	- OUT	0V 1.8/3.3V	Power POWER	GND VLDO_SD3 (1.6A)	-	-	
C053 C054	OUT OUT	1.8/3.3V 1.8/3.3V	POWER POWER	VLDO_SD3 (1.6A) VLDO_SD3 (1.6A)	-	-	
C055	OUT OUT	1.8/3.3V 0.8V	POWER	VLDO_SD3 (1.6A) VLDO_SD0 (probe only)	-		
C056 C057 C058	BIDIR IN	1.8/3.3V 1.8/3.3V	SDHC3 SDHC3 SDHC3	SD3 DAT0 V	GP4_09 GP4_15	with on-board SD with on-board SD	SiP,CN6 SiP,CN6
C059 C060	BIDIR -	1.8/3.3V 0V	SDHC3 Power	SD3_CD_V SD3_DAT2_V GND	GP4_11 -	with on-board SD -	SiP,CN6
C061 C062	IN IN	(CSI2) (CSI2)	CSI2_0 CSI2_0	CSIO_CLKP CSIO_CLKN	-		SiP SiP
C063 C064	IN BIDIR	1.8/3.3V 1.8/3.3V	SDHC3 SDHC3	SD3_WP_V SD3_CMD_V	GP4_16 GP4_08	with on-board SD with on-board SD	SiP,CN6 SiP,CN6
C065 C066	IN IN	(CSI2) (CSI2)	CSI2_0 CSI2_0	CSIO DATAPO CSIO DATANO	- -	<u> </u>	SiP SiP
C067 C068	OUT IN	3.3V (CSI2)	MSIOF0 CSI2_0	MSIOF0_SS1 CSI0_DATAP1	GP5_19 -		SiP SiP
C069 C070	IN or OUT	3.3V 0V	HSCIF0 Power	HSCIFO_HSCK GND	GP5_12 -		SiP -
C071 C072	IN IN	(CSI2) (CSI2)	CSI2_0 CSI2_0	CSI0_DATAN1 CSI0_DATAP2		<u> </u>	SiP SiP
C073 C074	- IN	0V (CSI2)	Power	GND CSI0_DATAN2	-	-	SiP
C075 C076	IN -	(CSI2) 0V	CSI2_0 CSI2_0 Power	CSI0_DATAP3 GND		-	SiP -
C077 C078	IN IN	3.3V (CSI2)	USB 2.0 ch0 CSI2_0	USB20_OVC CSI0_DATAN3	GP6_25 -	-	SiP SiP
C079 C080	OUT -	3.3V 0V	USB 2.0 ch0 Power	USB20_PWEN GND	GP6_24	-	SiP
C081 C082	IN OUT OUT	3.3V 3.3V	VIN5 DU_RGB	VI5_FIELD(ExA11) DU_DR0(ExD8)	GP1_11 GP0_08 GP0_09	- MD12 MD06	SIP,CPLD SIP,CPLD SIP,CPLD
C083 C084	-	3.3V 0V	DU_RGB Power	DU_DR1(ExD9) GND			
C085 C086	OUT OUT	3.3V 3.3V	DU_RGB DU_RGB	DU_DR2(ExD10) DU_DR3(ExD11)	GP0_10 GP0_11	MD07 MD08	SiP,CPLD SiP,CPLD
C087 C088	_	0V 3.3V	Power DU RGB	GND DU_DR4(ExD12)	- GP0_12	- MD09	: - I
C089 C090	OUT -	3.3V 0V	DU_RGB Power	DU_DR5(ExD13) GND	GP0_13 -	MD10 -	SIP,CPLD SIP,CPLD -
C091 C092	OUT OUT	3.3V 3.3V	DU_RGB DU_RGB	DU_DR6(ExD14) DU_DR7(ExD15)	GP0_14 GP0_15	-	SiP,CPLD SiP,CPLD
C093	OUT	0V 3.3V	Power DU RGB	GND DU_DB0(ExA0)	- GP1 00	- MD28	-
C094 C095 C096	OUT -	3.3V 0V	DU_RGB Power	DLL DB1(ExA1)	GP1_01 -	MD28 MD27	SIP,CPLD SIP,CPLD
C096 C097 C098	OUT OUT	3.3V 3.3V	DU_RGB DU_RGB	GND DU_DB2(ExA2) DU_DB3(ExA3)	GP1_02 GP1_03	- MD26	SiP,CPLD SiP,CPLD
C099 C100	OUT -	3.3V	DU_RGB Power	DU_DB4(ExA4) GND	GP1_04 -	MD25	SiP,CPLD
C100 C101	OUT IN	0V 3.3V 3.3V	DU_RGB VIN5	DU_DB5(ExA5) VI5_HSYNCZ(ExA10)	GP1_05 GP1_10	- MD24 MD20	SiP,CPLD SiP,CPLD
C102 C103		0V -	Power NC(Power)	GND NC(VCC12V0)		-	- VCC12V0 Starter Kit Not Used (Option FAN)
C105		-	:NC(Power)	NC(VCC12V0) NC(VCC12V0)	- -	-	VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN)
C106 C107 C108		-	NC(Power) NC(Power) NC(Power)	NC(VCC12V0) NC(VCC12V0)	-	-	VCC12V0 Starter Kit Not Used (Option FAN) VCC12V0 Starter Kit Not Used (Option FAN)
C109 C110		- 0V	NC(Power) Power	NC(VCC12V0) GND	-	-	VCC12V0 Starter Kit Not Used (Option FAN)
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Pin D001	Direction Level/Group - 0V	Function Power	Signal GND	GPIO -	Shared De	estination -
D002 D003	- 0V OUT 3.3V	Power SYSTEM/POWER	GND PMIC_GPIO1	-		- PMIC
D004 D005	OUT 0.8V - 0V	POWER Power	DDR1_1.8V (probe only) GND	-	-	-
D006 D007	OUT 3.3V OUT 1.8V	SYSTEM QSPI	CLKOUT QSPIO_SPCLK	-	- with RPC	SiP U5,CN2
D008 D009	- 0V OUT 1.8V	Power QSPI	GND QSPI0 SSL	-	- with RPC	- U5,CN2
D010 D011	BIDIR 1.8V - 0V	QSPI Power	QSPI0_IO0 GND		with RPC	U5,CN2 -
D012 D013	BIDIR 1.8V BIDIR 3.3V	QSPI GPIO	QSPI0 IO1	- GP6_22	with RPC	U5,CN2 SiP
D014 D015	- 0V	Power	GP6_22 GND			-
D016 D017		nc		-		- -
D018 D019		nc				- -
D020 D021	OV	nc nc Power	- GND			
D022 D023	IN 3.3V IN 3.3V	VIN5 VIN5	VI5_DATA10(ExA14) VI5_DATA11(ExA15)	GP1_14 GP1_15	DU_RGB/MD18 DU_RGB/MD17	SiP,CPLD SiP,CPLD
D024 D025	OUT 3.3V BIDIR 3.3V	HDMI_OUT HDMI_OUT	HDMI1_SCL HDMI1_SDA	- GF1_15		SiP(H3_Only)
D026	OUT LVDS	HDMI_OUT HDMI_OUT	HDMI1_TMDSDATAP0			SiP(H3_Only) SiP(H3_Only)
D027 D028	OUT LVDS	nc	HDMI1_TMDSDATAN0			SiP(H3_Only)
D029 D030	OUT LVDS OUT LVDS	HDMI_OUT HDMI_OUT	HDMI1_TMDSDATAP1 HDMI1_TMDSDATAN1	-	-	SiP(H3_Only) SiP(H3_Only)
D031 D032	- 0V OUT LVDS	Power HDMI_OUT	GND HDMI1_TMDSDATAP2		-	- SiP(H3_Only)
D033 D034	OUT LVDS OUT 1.1V	HDMI_OUT PMIC	HDMI1_TMDSDATAN2 BKUP_CTRL_11		-	SP(H3, Only) SP(H3, Only) U72, SIP SIP, CPLD SIP(H3, Only) SIP(H3, Only)
D035 D036	IN 3.3V OUT LVDS	VIN4 HDMI OUT	BKUP_CTRL_11 VI4_HSYNCZ(ExA18) HDMI1_TMDSCLKP	GP1_18 -	DU_RGB/MD14 -	SiP,CPLD SiP(H3_Only)
D037 D038	OUT LVDS IN 3.3V	HDMI_OUT VIN4	HDMI1_TMDSCLKN VI4_VSYNCZ(ExA17)	- GP1_17	- DU_RGB/MD15	SiP(H3_Only) SiP,CPLD
D039 D040	IN 3.3V	VIN4 nc	VI4_CLK(EX_WAIT0n//DU_DOTCLKOUT0) -	GP1_27 -	DU_RGB -	SiP -
D041 D042	0V 	Power nc	GND -			-
D043 D044	IN 3.3V	VIN4 nc	VI4_CLKENB(ExA19) -	GP1_19 -	DU_RGB -	SiP,CPLD -
D045 D046	 OUT 3.3V	nc SYSTEM	- RD/WR#/GP1_24/CAN0_RX	- GP1_24		- SiP
D047 D048	IN or OUT 1.2V IN or OUT 1.2V	Power Power	D1.2V D1.2V	-	-	EtherAVB(GbPHY) EtherAVB(GbPHY)
D049 D050	IN 3.3V IN 3.3V	SCIF2 VIN5	SCIF2_RXD_CN VI5_VSYNCZ(ExA9)	GP5_11 GP1_09	with on-board USB	SiP SiP,CPLD
D051 D052	- 0V BIDIR 3.3V	Power	GND ExA8	- GP1_08		- SiP,CPLD
D053 D054	IN 3.3V OUT 3.3V	VIN4	VI4_FIELD(ExA16) GP2_10/MSIOF2_SS1/PHY_RESETn	GP1_16 GP2_10	DU_RGB/MD16 PHY RESET	SiP,CPLD U35,SiP
D055 D056	IN 3.3V IN 3.3V	MSIOF2 VIN5 USB 2.0 ch2	VI5_DATA9(ExA13) USB22_OVC	GP1_13 GP6_15	DU_RGB/MD19	SiP.CPLD
D057 D058	IN 3.3V IN 3.3V	VIN5	VI5_DATA8(ExA12) VIx_DATA14(ExA6)	GP1_12 GP1_06 GP5_02	DU_RGB/MD21 DU_RGB/MD23	SIP SIP,CPLD SIP,CPLD
D059 D060	OUT 3.3V - 0V	VIN4/5 SCIF0 Power	SCIF0 TX/SD0 PWEN		with on-board SD	SIP,U19
D061 D062	IN 3.3V IN (P)CML	VIN4/5 USB 3.0 ch1	GND Vix_DATA15(ExA7) USB31_TX_P	GP1_07	DU_RGB/MD22	SiP,CPLD SiP
D063	IN (P)CML OUT 3.3V	USB 3.0 ch1 SCIF2	USB31_TX_M SCIF2_TXD_CN	- - GP5_10	with on-board USB	SiP SiP
D064 D065	OUT (P)CML	USB 3.0 ch1	USB31_RX_P	- GF5_10		SiP
D066 D067	- 0V	USB 3.0 ch1 Power	USB31_RX_M GND			SiP - SiP
D068 D069	IN (P)CML IN (P)CML	USB 3.0 ch0 USB 3.0 ch0	USB30_TX_P USB30_TX_M			SiP
D070 D071	OUT (P)CML OUT (P)CML	USB 3.0 ch0	USB30_RX_P USB30_RX_M			SiP SiP
D072 D073	- 0V	USB 3.0 ch0 Power	GND			-
D074 D075	BIDIR USB2.0 BIDIR USB2.0	USB 2.0 ch2 USB 2.0 ch2	USB22_DP2 USB22_DM2			SiP SiP
D076 D077	- 0V IN or OUT 3.3V	Power SCIF1	GP5_07/CTS1n/HCTS1n	- GP5_07		SiP
D078 D079	OUT LP-HCSL OUT LP-HCSL	PCle ch1	PCIE1_CN_CLK_P PCIE1_CN_CLK_M			U87 U87
D080 D081	- 0V IN or OUT 3.3V	Power SCIF1	GND GP5_08/RTS1n/HRTS1n	GP5_08		SiP SiP,CPLD
D082 D083	OUT 3.3V OUT 3.3V	DU_RGB DU_RGB	DU_DB6 DU_DB7	GP5_08 GP1_06 GP1_07	MD23 MD22	SiP,CPLD
D084 D085	- 0V OUT 3.3V	Power DU_RGB	GND DU_DG0 DU_DG1	- GP1_16 GP1_17	MD16	SiP,CPLD
D086 D087	OUT 3.3V - 0V	DU_RGB Power	GND	- :	MD15 - MD14	SiP,CPLD - SiP,CPLD
D088 D089	OUT 3.3V OUT 3.3V	DU_RGB DU_RGB	DU_DG2 DU_DG3	GP1_18 GP1_19	MD13	SiP,CPLD SiP,CPLD
D090 D091	- 0V OUT 3.3V	Power DU_RGB	GND DU DG4	GP1_12	MD21	SiP,CPLD
D092 D093	OUT 3.3V - 0V	DU_RGB Power	DU_DG5 GND	GP1_13 -	MD19	SiP,CPLD
D094 D095	OUT 3.3V OUT 3.3V	DU_RGB DU_RGB	DU_DG6 DU_DG7	GP1_14 GP1_15	MD18 MD17	SiP,CPLD SiP,CPLD
D096 D097	- 0V OUT 3.3V OUT 3.3V	Power DU_RGB	GND ExA24/DU_EXHSYNC_DU_HSYNC	- GP2_04 GP2_05		- SiP,CPLD
D098 D099	OUT 3.3V	DU_RGB DU_RGB	ExA23/DU_EXVSYNC_DU_VSYNC DU_DISP/IRQ1n	GP2_05 GP2_01		SiP,CPLD SiP
D100 D101	- :0V OUT :3.3V	Power DU_RGB	GND EX_WAIT0n//DU_DOTCLKOUT0	- GP1_27	-	- SiP
D102 D103	BIDIR 3.3V - 0V	I2C1 Power	GP5_23/MLB/SCL1 GND	GP5_23 -	with on-board SW -	SiP -
D104 D105	IN or OUT 12V IN or OUT 12V	Power	VCC12V0 VCC12V0	-		Starter Kit Not Used (Option FAN) Starter Kit Not Used (Option FAN)
D106 D107	IN or OUT 12V IN or OUT 12V	Power Power Power	VCC12V0	-	-	Starter Kit Not Used (Option FAN) Starter Kit Not Used (Option FAN)
D108 D109	IN or OUT 12V IN or OUT 12V	Power Power	VCC12V0 VCC12V0 VCC12V0 VCC12V0	-		Starter Kit Not Used (Option FAN) Starter Kit Not Used (Option FAN)
D110	- 0V	Power	GND			

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