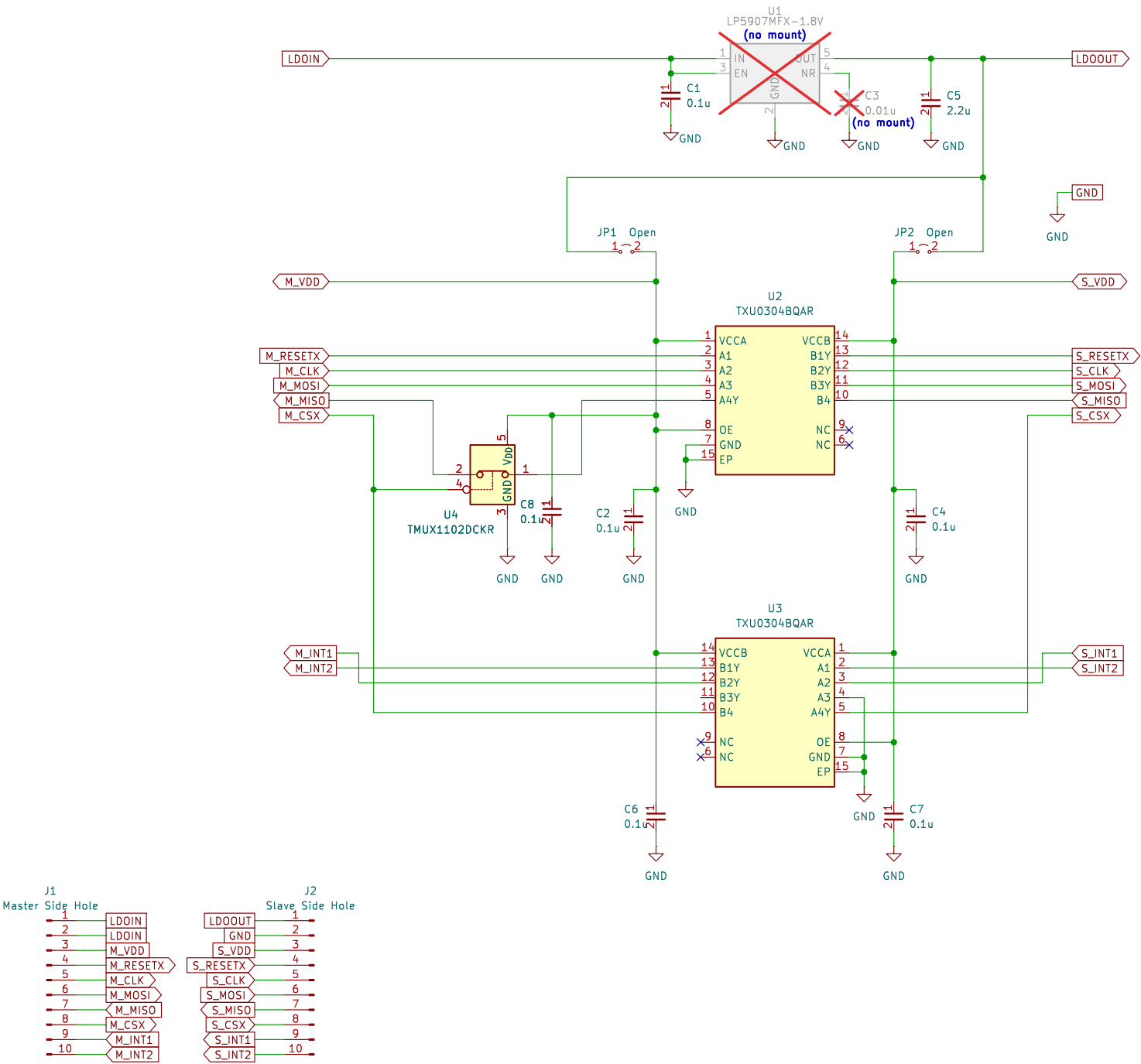


CONVSPI



### JP settings

1. M_VDD > S_VDD(no ext supply):	JP1	JP2	LDOIN	LDOOUT	Note
2. M_VDD(no ext supply) < S_VDD:	mnt	mnt	M_VDD	S_VDD	S_VDD is pwr supply from this board
3. M_VDD, S_VDD:	mnt	mnt	S_VDD	M_VDD	M_VDD is pwr supply from this board
					the LDO is not needed
					M_VDD and S_VDD have the extenal power supply

see the following table, if the "internal LDO" on the board is placed.  
the max rate (bps) is referred by TXU0304 spec.  
the MISO line has MUX switch, so the max rate must be measured on the actual circuits.

Down translation

Up translation

M_VCC	S_VCC	LDO	JP1	JP2	Max_bps
5.0V	3.3V	LD03.3V		close	125M
5.0V	1.8V	LD01.8V		close	50M
5.0V	1.2V	LD01.2V		close	10M
3.3V	1.8V	LD01.8V		close	50M
3.3V	1.2V	LD01.2V		close	10M
1.8V	1.2V	LD01.2V		close	10M
1.2V	1.8V	LD01.2V	close		10M
1.2V	3.3V	LD01.2V	close		10M
1.2V	5.0V	LD01.2V	close		10M
1.8V	3.3V	LD01.8V	close		50M
1.8V	5.0V	LD01.8V	close		50M
3.3V	5.0V	LD03.3V	close		125M

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