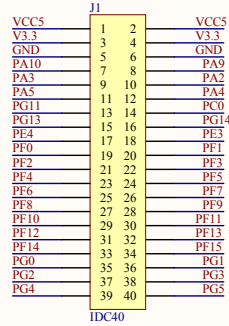


# IO



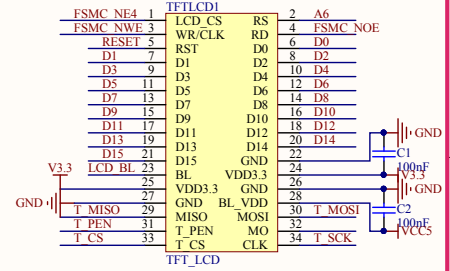
IO pin header J1 connector diagram

# CPU



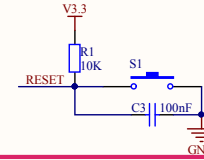
CPU pin header U1 connector diagram

# TFTLCD

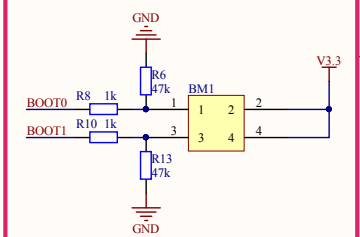


TFTLCD pin header C1 connector diagram

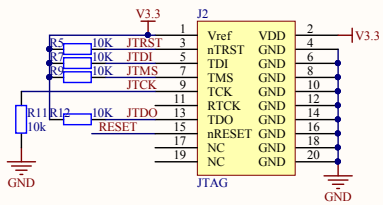
# RESET



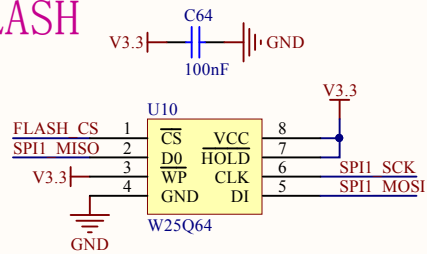
# BOOT



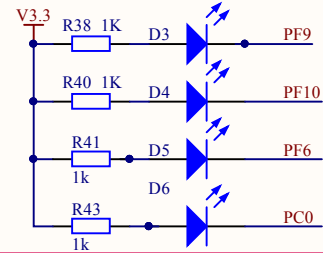
# JTAG



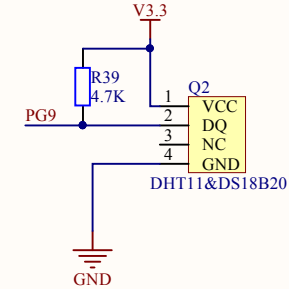
## FLASH



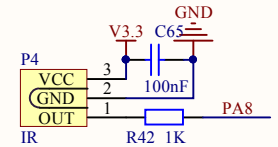
## LED



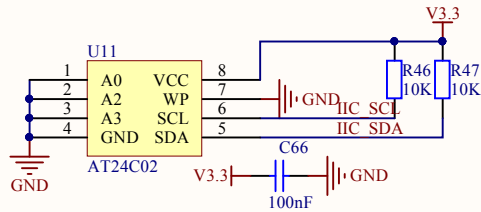
## 温度



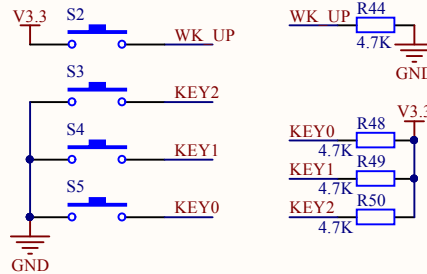
## IR



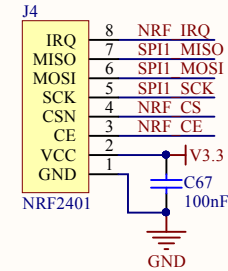
## EEPROM



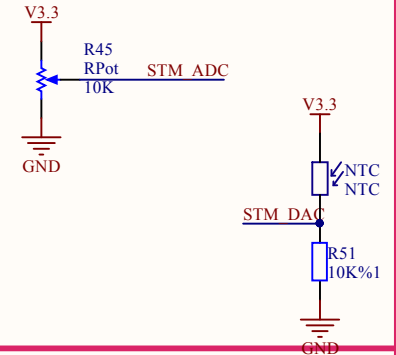
## KEY



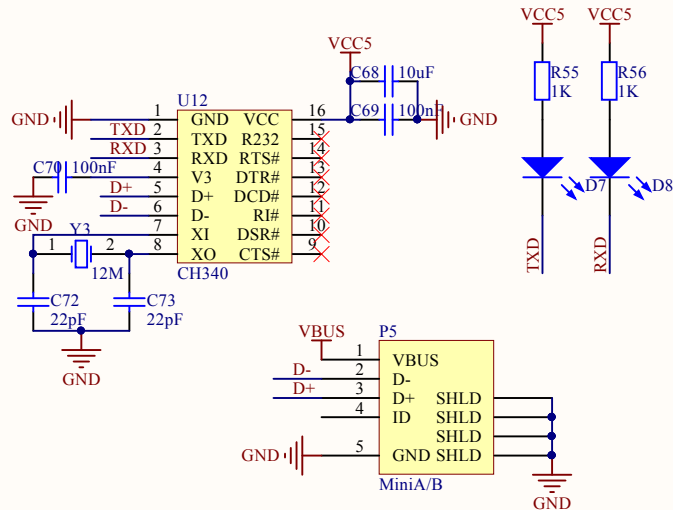
## WIRELESS



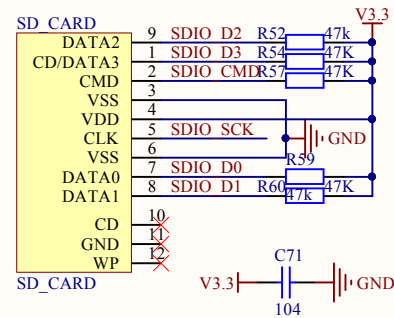
## ADC&amp;DAC



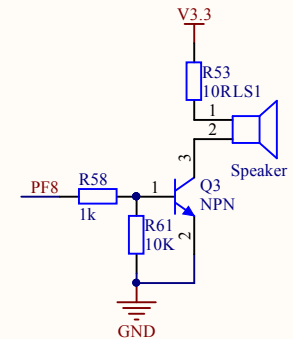
## USB\_232



## SD



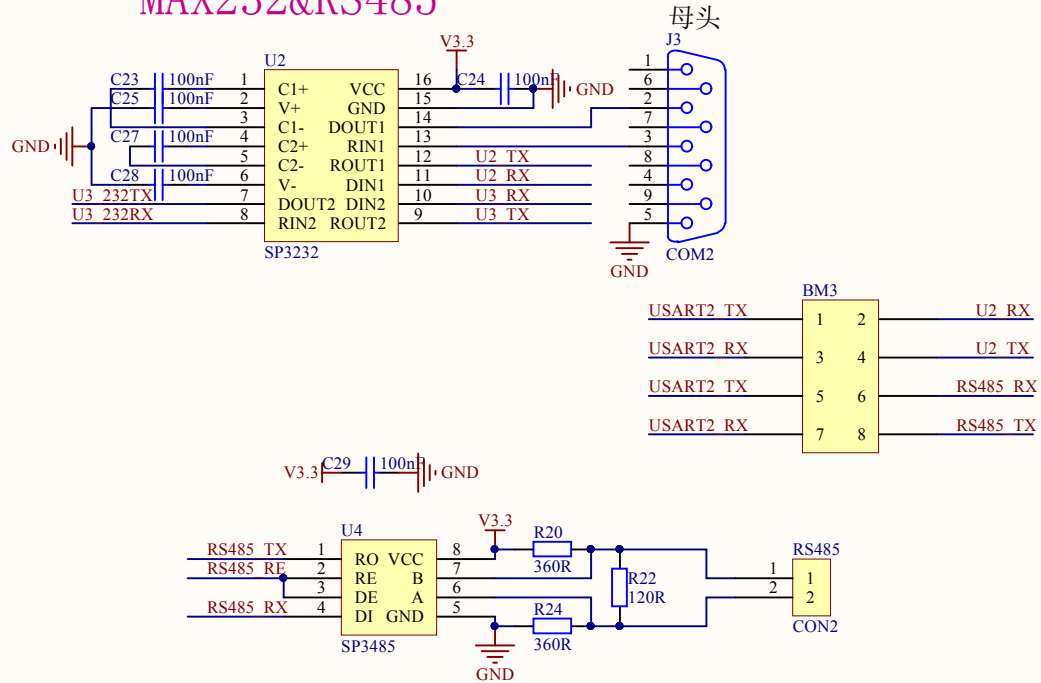
## BEEP



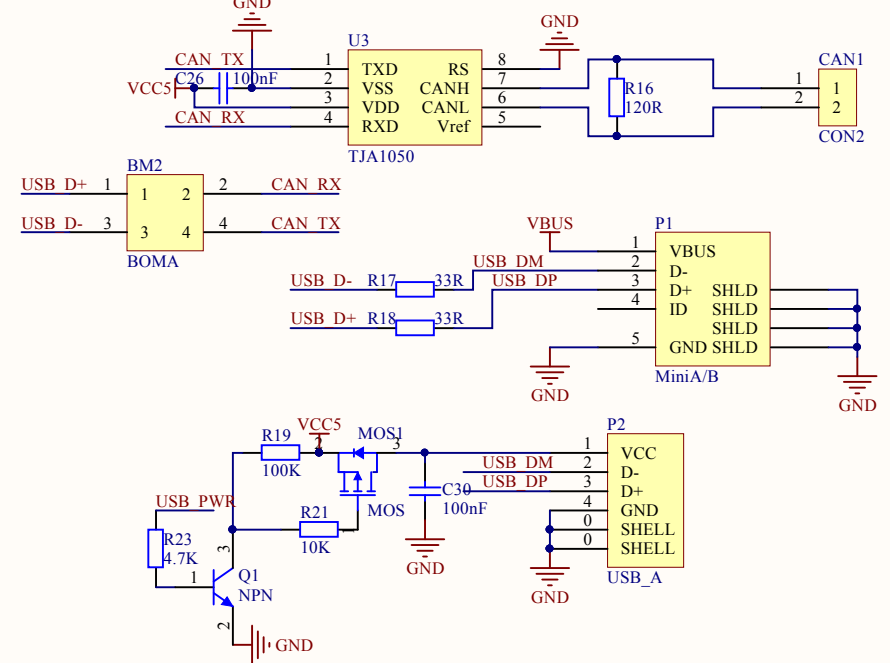
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Size: A4	Number:	Revision:	
Date: 2016/6/23	Time: 18:56:50	Sheet of	
File: F:\20160323桌面\STM32M4V1\DEVICE.SchDoc			



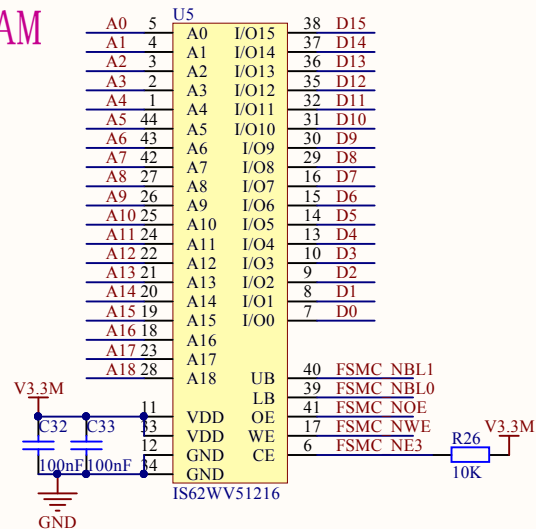
## MAX232&RS485



## CAN&USB

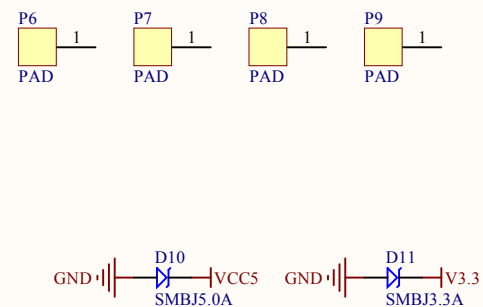
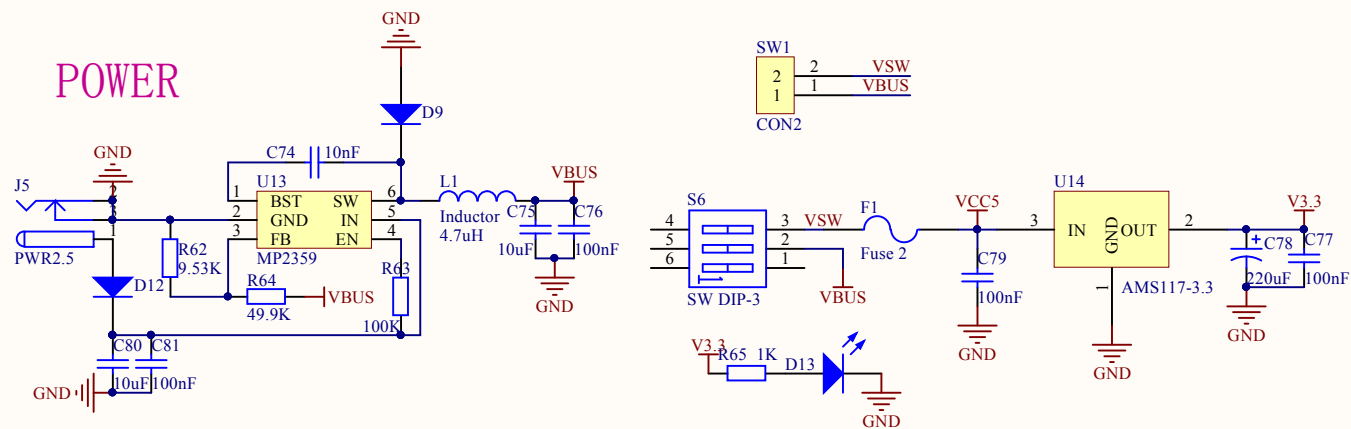


## SRAM

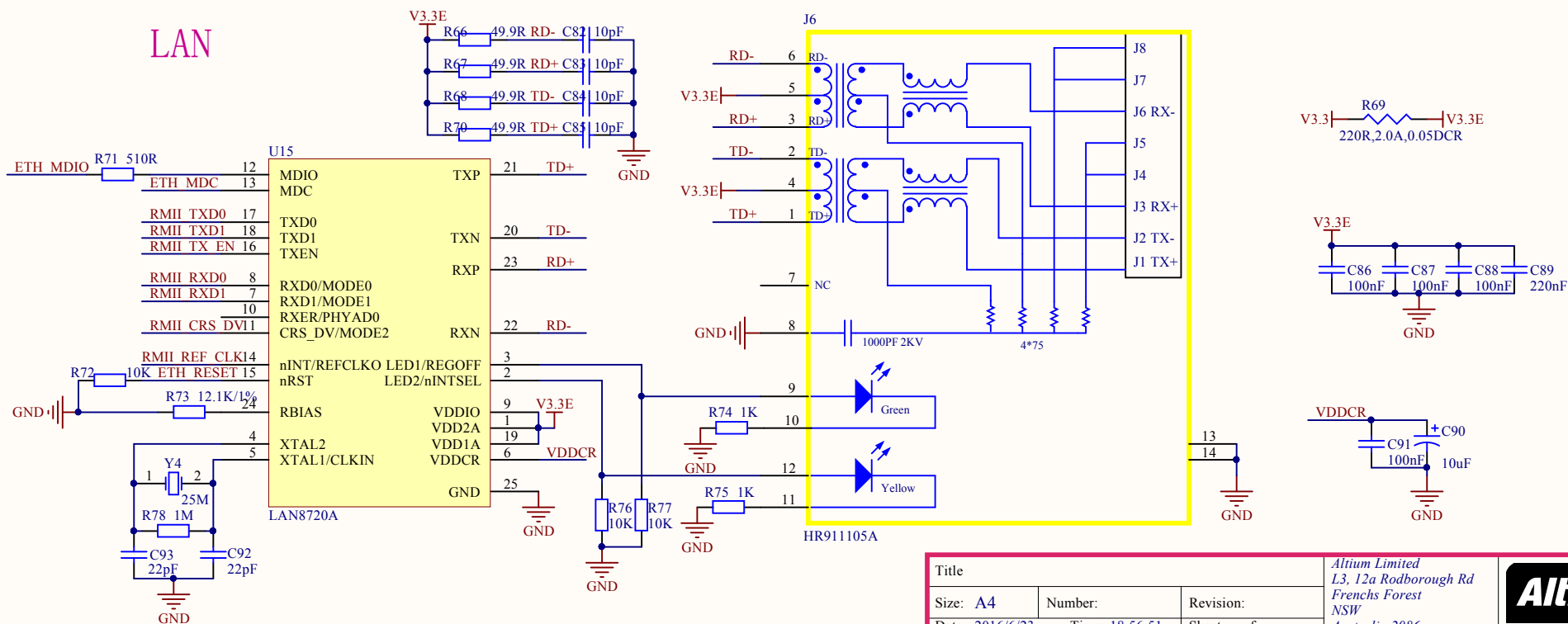




# POWER



# LAN



Title			Altium Limited
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Date: 2016/6/23	Time: 18:56:51	Sheet of	Frenchs Forest
File: F:\20160323桌面\STM32M4V1.1\POWER.SchDoc			NSW
			Australia 2086



Board S.