

Recent Advances in Avalanche Photodiodes

Joe C. Campbell, *Fellow, IEEE*

(Invited Paper)

Abstract—Until the early 2000's, the avalanche photodiode (APD) was widely deployed in high-performance optical receivers that operated up to 10 Gb/s. In subsequent years, the use of APDs for high-capacity systems declined as a result of their limited gain bandwidth, the transition to coherent detection, and the development of high-efficiency modulation techniques. Recently, the rapid growth of optical-fiber communications systems that utilize baud rates up to 25 Gb/s represented by a 100-Gb/s Ethernet has led to a resurgence of research on APDs and the development of low-noise APDs with enhanced gain bandwidth. This paper presents a brief review of APD fundamentals and describes some of the recent advances.

Index Terms—Avalanche photodiode, photodetector, photodiode.

I. INTRODUCTION

OVER the past five decades, avalanche photodiodes (APDs) have been utilized for a wide range of commercial, military, and research applications. From the mid 1970's to the present, optical communications, [1] imaging, [2], [3] and single photon detection [4], [5] were the primary driving forces for research and development of APDs. In the communication area, relative to PIN photodiodes, the internal gain of APDs provides higher receiver sensitivity and dynamic range with concomitant increases in loss margins [6]–[9]. The origin of the APD gain is impact ionization, a stochastic process that results in excess noise, relative to shot noise, and limits the gain-bandwidth [10]–[12]. The noise power spectral density of an APD, ϕ , is given by the expression $\phi = 2qIM^2F(M)R(\omega)$ where q is the charge on an electron, I is the current, M is the avalanche gain, $F(M)$ is the excess noise factor caused by the random nature of the multiplication process, and $R(\omega)$ is the device impedance [10], [11]. The benefit of an APD is strongly dependent on whether it has sufficient gain-bandwidth, which is closely tied to the excess noise [12]. The factors that establish low noise tend to also yield high gain-bandwidth. Consequently, increasing the gain-bandwidth while reducing the excess noise has been a focus for APD research and development. It should also be noted that while the incremental cost of utilizing APDs is modest, APDs do require more complex epitaxial wafer structures and bias circuits. The most important performance

parameters for an APD are gain, bandwidth, gain-bandwidth product, and excess noise factor.

There are three ways to achieve low noise and high gain bandwidth: selecting a semiconductor with favorable impact ionization coefficients, scaling the multiplication region to exploit the non-local aspect of impact ionization, [13]–[27] or impact ionization engineering using appropriately designed heterojunctions [28]–[34]. For bulk multiplication regions, the highest performance is achieved with materials such as Si [35]–[38], $\text{Hg}_{0.7}\text{Cd}_{0.3}\text{Te}$, [39], [40] or InAs [41]–[45] in which k , the ratio of the hole ionization coefficient, β , to that of the electron, α , approaches zero. This is due to the fact that the excess noise factor, $F(M)$, is low when $k \ll 1$. For other materials systems, enhanced performance can be achieved through designs optimized for low noise. This paper will review recent advances in materials and structures that have enabled improved performance for fiber optic communications.

II. SEPARATE ABSORPTION, CHARGE, AND MULTIPLICATION (SACM) APDS

Since the first generation of optical fiber communication systems operated in the wavelength range 800 to 900 nm owing to the operating wavelength of the available $\text{Al}_x\text{Ga}_{1-x}\text{As}$ lasers, the optical receivers utilized Si PIN and APDs [46]. However, the attenuation and dispersion characteristics of optical fibers favor operation in the now standard telecommunication wavelength range of 1.3 to 1.6 μm . Consequently, subsequent generations of fiber optic systems employed materials appropriate for longer wavelength operation, primarily those lattice-matched to InP. One such material, $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ (referred to in the following as InGaAs), which has bandgap energy of 0.75 eV, is widely used as the absorbing layer in a wide range of telecommunication detectors. However, the narrow bandgap, which enables high responsivity in the short wavelength infrared spectrum also results in excessive dark current due to tunneling at the high electric fields required for impact ionization [47], [48]. This led to the development of SACM APD structures. An important design consideration is to maintain low electric field in the InGaAs absorption region in order to minimize tunneling, which causes high dark current if the electric field strength exceeds ~ 200 kV/cm. This is accomplished by placing the p-n junction and thus the high-field multiplication region in a wide bandgap semiconductor such as InP or $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$, which is lattice-matched to InP, where tunneling is inconsequential. The relative field strength between the absorber and multiplication regions is controlled with a high-low doping profile in the multiplication region [49]–[51] similar to the reach-through structure that has been widely used for Si APDs [52]. In this structure the

Manuscript received May 29, 2015; revised June 30, 2015; accepted July 1, 2015. Date of publication July 5, 2015; date of current version February 5, 2016.

The author is with the Department of Electrical and Computer Engineering, University of Virginia, Charlottesville, VA 22904 USA (e-mail: jccuva@virginia.edu).

Color versions of one or more of the figures in this paper are available online at <http://ieeexplore.ieee.org>.

Digital Object Identifier 10.1109/JLT.2015.2453092

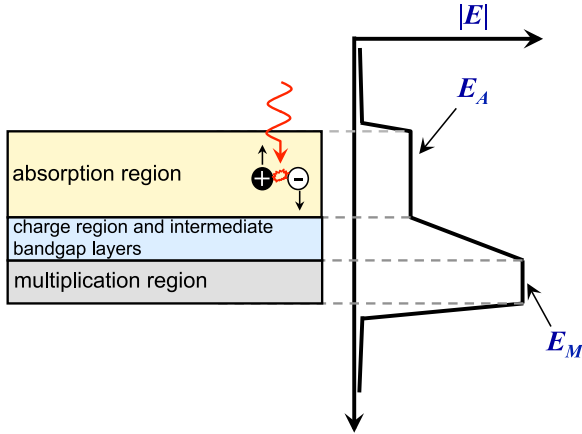


Fig. 1. Layer structure of an SACM APD with internal electric field profile. E_A and E_M are the electric fields in the absorber and multiplication regions, respectively.

wide-bandgap multiplication region consists of a lightly-doped (usually unintentionally doped) layer where the field is high and an adjacent, doped charge layer or field control region. In order to prevent charge accumulation at the heterojunction interface the absorber and multiplication regions are separated by a transition region consisting of one or more lattice-matched, intermediate-bandgap $\text{In}_x\text{Ga}_{1-x}\text{As}_{1-y}\text{P}_y$ [53], [54] or InAlGaAs [55]–[58] layers. A schematic cross section of the SACM APD and the internal electric field are illustrated in Fig. 1.

The $\text{InP}/\text{InGaAsP}/\text{InGaAs}$ SACM APDs have been the most widely commercialized for telecom receivers. While these APDs have achieved excellent receiver sensitivities up to 10 Gb/s [59]–[61] there are three factors that limit their performance at higher bit rates. Since they operate under normal incidence and since the absorption coefficient of InGaAs at telecom wavelengths is $\sim 10^4 \text{ cm}^{-1}$, [62], [63] the absorption region must be approximately $2.5 \mu\text{m}$ thick in order to obtain external quantum efficiency $> 90\%$. The associated transit time limits the bandwidth to $\sim 10 \text{ GHz}$ at low gain. At higher gains, the relatively low gain-bandwidth product ($< 100 \text{ GHz}$) restricts the frequency response. In addition, the ionization coefficients yield a k factor of 0.4 to 0.5, [64], [65] which results in significantly higher excess noise than Si. Much of the recent work on APDs has focused on developing new structures and incorporating alternative materials that will yield lower noise and higher speed while maintaining adequate gain levels.

III. GE ON SI APDS

The SACM structure has been successfully applied to a monolithically grown Ge/Si APD in which light absorption and carrier multiplication occur inside Ge and Si, respectively [66]–[69]. A schematic cross-section of a Ge on Si APD is shown in Fig. 2 [66]. A $1 \mu\text{m}$ -thick Ge absorber was grown on the Si multiplication layer ($0.5 \mu\text{m}$) by chemical vapor deposition. A Si charge layer was used to maintain low electric field at the Si-Ge interface. Of particular note, these APDs achieved gain-bandwidth product of 340 GHz, which is two to three times higher than InP/InGaAs APDs. The effective noise factor, k , was only 0.09.

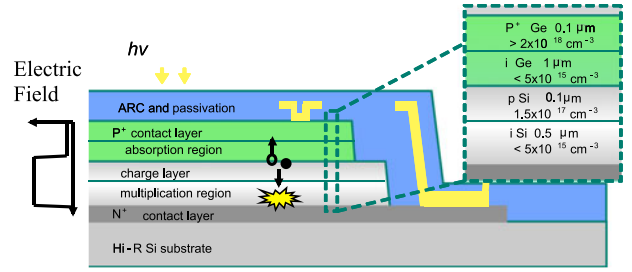


Fig. 2. Schematic cross section of Ge on Si SACM APD [68].

Optical receivers built with these APDs demonstrated sensitivity of -28 dBm at 10 Gb/s and bit error rate (BER) of 10^{-12} [66]. Using a similar structure, Huang *et al.*, reported 18 GHz at a gain of 8, responsivity = 0.55 A/W at 1310 nm, and successful operation at 25 Gb/s; the receiver sensitivity was -20 dBm [68]. Two issues for this type of APD are (1) the lattice mismatch between Si and Ge creates deep-level traps that give rise to high dark current and (2) the absorption coefficient of Ge drops rapidly with increasing wavelength beyond $\sim 1.55 \mu\text{m}$. Using a waveguide structure enables the thickness of the Ge layer to be reduced, which will reduce the number of traps and decrease carrier transit times yielding higher bandwidths. This approach also increases the optical absorption path length in order to maintain high responsivity at longer wavelengths. Kang *et al.* [67] reported waveguide Ge on Si SACM APDs with 400 nm-thick Ge absorbing layer have been reported. The internal responsivity and bandwidth were 0.8 A/W and 29 GHz, respectively. A 10 Gb/s optical receiver achieved -30.4 dBm sensitivity at BER of 10^{-12} . An open eye diagram was obtained at 40 Gb/s.

IV. SUBMICRON SCALING OF THE MULTIPLICATION REGION

It has been shown for a wide range of materials including InP , [20]–[23] GaAs , [22], [28], [70]–[73] $\text{In}_{1-x}\text{Al}_x\text{As}$, [22], [23], [74] Si , [75], [76] $\text{Al}_x\text{Ga}_{1-x}\text{As}$, [22], [23], [77]–[80] SiC , [81] GaP , [82] and GaInP [83] that submicron scaling of the multiplication thickness can significantly reduce the excess noise factor. This is due to the non-local nature of impact ionization, as manifest in the “dead space” effect. The dead space refers to the distance that a carrier travels before gaining enough energy to impact ionize. When the dead space becomes a significant fraction of the multiplication layer thickness, the probability distribution function, which is the probability per unit length that a carrier ionizes at a specific distance from the injection point or the point where it was created by another impact ionization event, narrows. The result is that the gain process becomes more deterministic and the excess noise decreases. It should be noted that there are two competing effects associated with decreasing the multiplication layer thickness. The thinner layers decrease carrier transit times, however, the dead space effect counterbalances this to a small degree.

While shrinking the multiplication region thickness is an effective approach to noise reduction, it should be noted that this is relative to the characteristic noise of the bulk (thick) material. Thus, lower noise can be achieved by beginning with

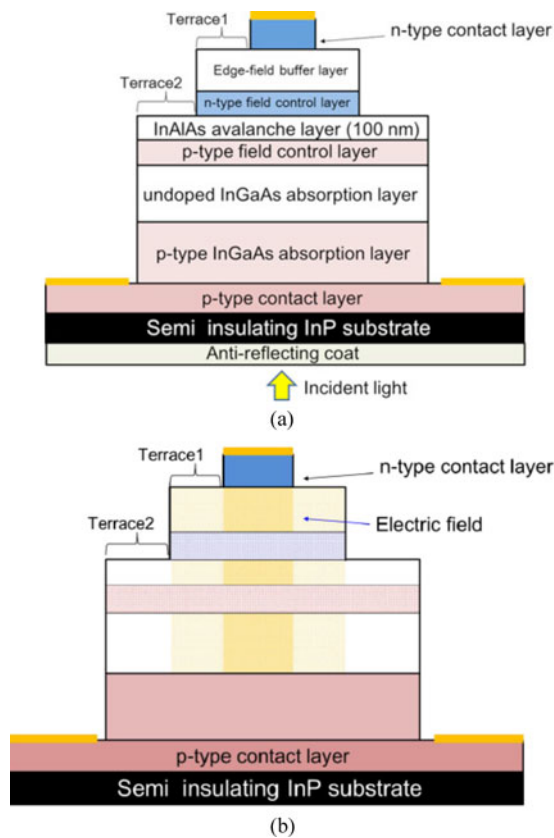


Fig. 3. (a) Schematic cross section and (b) electric field profile of triple mesa p-down InAlAs/InGaAs APD [57].

“low-noise” semiconductors. For this reason, $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$ (referred to below as InAlAs), which has lower intrinsic noise than InP has become an attractive candidate to replace InP in telecommunications APDs. Thin layers of InAlAs have been incorporated into the multiplication region of SACM APDs. Li *et al.* [84] reported that mesa-structure undepleted-absorber InAlAs APDs with 180 nm-thick multiplication regions exhibited excess noise equivalent to $k = 0.15$ and a gain-bandwidth product of 160 GHz. Several planar InAlAs/InGaAs SACM APDs have also been developed. An AlInAs/InGaAs planar SACM APD without a guard ring has achieved gain > 40 , high external quantum efficiency (88%), 10 GHz low-gain bandwidth, and a gain-bandwidth product of 120 GHz [56]. Higher operating bandwidth and gain-bandwidth product have been achieved with a triple mesa p-down InAlAs/InGaAs APD [57], [85]. A schematic cross section of the structure is shown in Fig. 3(a). Light is incident through an InGaAs absorber. Above the absorber are a p-doped InAlAs field control layer, an undoped 100-nm InAlAs avalanche layer, n-doped InP field control layer, lightly n-doped InP edge-field buffer layer, and n-doped InP contact layer. The top mesa contains the n-type contact layer; the second mesa contains the edge-field buffer and n-type field control layers; and the other layers are within the third mesa. As illustrated in Fig. 3(b), the triple mesa structure restricts the high electric field regions to the space directly underneath the top mesa thus obviating the need for a guard ring. The electric field is low at the outside edges of the regions labeled

terrace 1 and terrace 2, which not only prevents edge breakdown but also reduces surface leakage, a common issue for mesa-structure APDs. In order to optimize the transit time, the InGaAs absorber consists of two regions, an undepleted p-type layer and an undoped depleted layer; this absorber doping profile is similar to that in high-power modified uni-traveling carrier photodiodes [86] for which this type of absorber has been employed to suppress the space-charge effect, a primary limitation on output power and saturation current. For APDs space charge can reduce the internal field, which will result in lower gain and a corresponding decrease in dynamic range. The partially depleted absorber in the triple mesa APD has enabled linear response up to 1.5 dBm. The 100 nm InAlAs multiplication layer enabled a gain-bandwidth of 235 GHz. Devices designed for 25 Gb/s have achieved 72% external quantum efficiency, bandwidths of 23 GHz and 18.5 GHz at gain of $M = 4.47$ and 10, respectively, and excess noise factor corresponding to $k = 0.2$. A four channel receiver utilizing these APDs demonstrated 100-GbE over 50 km; all channels achieved receiver sensitivities better than -20 dBm at 10^{-12} BER. By decreasing the thicknesses of the absorbing and multiplication regions to reduce the transit time component of the bandwidth, operation at 50 Gb/s was achieved with a gain of 4.6 [87].

V. IMPACT IONIZATION ENGINEERING

A third method that has been employed to achieve higher performance utilizes impact ionization engineering (I^2E) [28]–[34]. Structurally, I^2E is similar to a truncated multiple quantum well (frequently mislabeled as “superlattice”) APD, [88], [89] however, operationally there is a fundamental difference in that these APDs do not invoke heterojunction band discontinuities. While the distinction is somewhat subtle, the I^2E devices achieve low noise through abrupt changes in the threshold energy for impact ionization as carriers transit from materials with wide bandgaps to those with much narrower bandgaps. Carriers gain energy from the electric field in the wide bandgap material and quickly impact ionize when they enter the narrow bandgap layer. Initial work that demonstrated the efficacy of this approach utilized the GaAs/ $\text{Al}_x\text{Ga}_{1-x}\text{As}$ material system [28]–[30]. Excess noise equivalent to $k < 0.1$ has been demonstrated [90]–[92]. Duan *et al.*, reported an MBE-grown $\text{In}_{0.52}\text{Al}_{0.48}\text{As}/\text{In}_{0.53}\text{Al}_{0.3}\text{Ga}_{0.17}\text{As}$ (InAlAs/InAlGaAs below) I^2E SACM APD [33]. A cross section of the epitaxial layer structure of an InAlAs/InAlGaAs I^2E SACM APD is shown in Fig. 4. The absorber is a $1.5\text{ }\mu\text{m}$ -thick layer of InGaAs. The I^2E multiplication region consists of unintentionally-doped layers of InAlAs (130 or 180 nm) and InAlGaAs (100 nm). Two 50 nm-thick InAlAs and InAlGaAs p-type layers are used for the charge region. Monte Carlo simulations show that there are relatively few ionization events in the InAlAs multiplication layer, owing to the combined effects of “dead space” and the relatively high threshold energy in InAlAs. Fig. 5 shows the excess noise factor, $F(M)$, versus gain for two wafers, one having an InAlAs layer 130 nm (\blacktriangle) thick and the other 180 nm (\blacklozenge). The solid lines in Fig. 5 are plots of $F(M)$ for $k = 0$ to 1 using the local field model [10], [11]. For low gain values, it appears

| | | |
|----------------------------------|-----------------|---------------------------------|
| Zn: InGaAs(7×10^{18}) | 50nm | Absorber |
| InAlGaAs | 50nm | |
| Zn: InAlAs(7×10^{18}) | 300nm | |
| InAlGaAs | 50nm | Charge |
| InGaAs | 1500nm | |
| InAlGaAs | 50nm | |
| Zn: InAlAs | 50nm | I ² E Multiplication |
| InAlAs | 130nm or 180 nm | |
| InGaAlAs | 100 nm | |
| Si: InAlAs(5×10^{18}) | 500nm | |
| Si: InP(5×10^{18}) | 100 nm | |

Fig. 4. Schematic cross section of $\text{In}_{0.52}\text{Al}_{0.48}\text{As}/\text{In}_{0.53}\text{Al}_{0.3}\text{Ga}_{0.17}\text{As}$ I²E SACM APD.

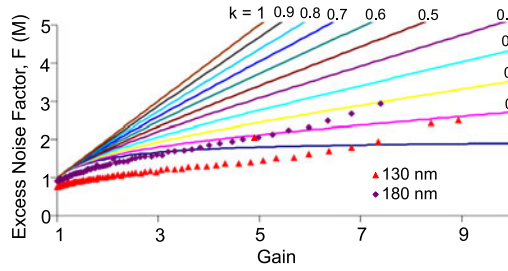


Fig. 5. Excess noise factor, $F(M)$, versus gain, M , for an $\text{In}_{0.52}\text{Al}_{0.48}\text{As}/\text{In}_{0.53}\text{Al}_{0.3}\text{Ga}_{0.17}\text{As}$ I²E SACM APD.

that $k < 0$, which is unphysical and simply reflects the inapplicability of the local field model for this type of multiplication region. A photoreceiver for LIDAR applications based on the InAlAs/InAlGaAs I²E SACM APD exhibited a noise equivalent power of $150 \text{ fW}/\sqrt{\text{Hz}}$ over 1 GHz bandwidth at $1.06 \mu\text{m}$ [93]. This type of APD has also been incorporated into free space laser communication terminals for small, unmanned airborne platforms. Receiver sensitivity of -47 dBm was achieved at 155 Mb/s .

A technique that has been proposed to further reduce the excess noise of APDs is to cascade low-noise gain regions within an APD [94], [95]. Recently this approach has been applied to I²E APDs [34], [96]. The connection between two adjacent multiplication cells is critical to the performance of this type of APD. A well-designed structure can significantly enhance impact ionization of the carrier type with higher ionization rate, and suppress impact ionization of the carrier with lower ionization rate. Williams *et al.*, have reported a five-stage I²E InAlAs/InGaAs tandem APD that achieved $k \leq 0.1$ for gain, $M < 20$ [97]. Clark *et al.* have successfully demonstrated a three-stage tandem I²E InAlAs/InGaAs APD [34]. Both structures have been designed to enhance electron ionization while suppressing that of holes. The structure of each I²E multiplication cell in Ref. [34] contains a 100 nm InAlAs hole relaxation layer, a 100 nm p-type InAlAs layer in which the electric field increases, a 200 nm InAlAs “acceleration” layer, a 10 nm InAlAs layer with decreasing electric

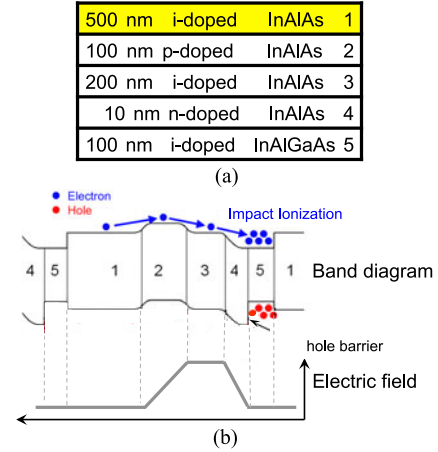


Fig. 6. (a) Epitaxial layer structure and (b) band diagram of three cell of a tandem I²E InAlAs/InGaAs APD.

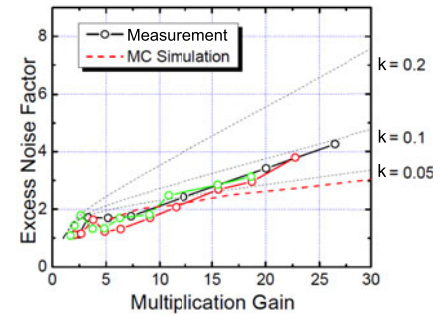


Fig. 7. Measured and simulated noise of the tandem I²E InAlAs/InGaAs APD in Fig. 6.

field, and a 100 nm InAlGaAs (band-gap = 0.92 eV) impact ionization layer. At high gain, the excess noise of this structure degrades due to increased hole ionization as holes surmount the hole barrier. To suppress hole-initiated ionization, Sun *et al.*, have used Monte Carlo simulations to show that increasing the thickness of the relaxation layer in the multiplication cells from 100 to 500 nm has the benefit of allowing the holes to cool before they enter the InAlGaAs multiplication layer [98]. The epitaxial layer structure and band diagram of a single cell are illustrated in Fig. 6(a) and (b), respectively. Fig. 7 shows the simulated [98] and measured excess noise for this structure. The simulated k factor for the thicker hole relaxation layer is less than 0.05 for gain values up to ~ 30 . For gain less than 15 the measured excess noise agrees with the simulation. At $M = 25$ the equivalent k value increases to 0.08. Simulations that address variations in the thickness and doping of the p-type relaxation layer show that 20% variation in the thickness-doping product can account for the slight increase in excess noise. It should be noted that the compound multiplication region of the tandem APD, while yielding very low excess noise, is also characterized by reduced gain-bandwidth product. Fig. 8 shows the simulated bandwidth versus gain for a single stage I²E APD and a three-stage tandem APD. The gain-bandwidth product decreases by approximately a factor of N , where N is the number of stages in the tandem APD.

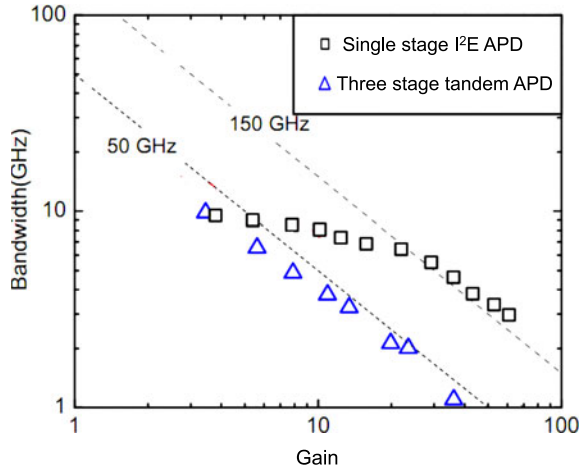


Fig. 8. Bandwidth versus avalanche gain for single- and three-cell tandem APDs [98].

VI. 2 μm APDs

Interest in extending the transmission of optical communications into the 2- μm band has been stimulated by the expected “capacity crunch” by 2020. The development of hollow core photonic bandgap fibers and thulium-doped fiber amplifiers are timely enabling components. Detectors that operate at wavelength longer than 2 μm are not as advanced as those in the standard telecommunications spectrum. One approach involves utilizing $\text{In}_x\text{Ga}_{1-x}\text{As}$ epitaxial layers on InP to extend the operating wavelength into the MWIR. Using highly strained $\text{In}_{0.83}\text{Ga}_{0.17}\text{As}$ quantum wells on InP Dries *et al.* [99] extended the operating wavelength to 2.01 μm , however, the quantum efficiency was $< 30\%$. Thick (2.5 μm) mismatched $\text{In}_{0.71}\text{Ga}_{0.29}\text{As}$ absorption layers on relaxed buffer layers yielded external quantum efficiency $> 80\%$ from 1 to 2 μm with cutoff wavelength of 2.6 μm [100]. However, these devices exhibited high dark current owing to defects related to the lattice-mismatched layers. A more promising approach has been to use type II quantum wells in the absorption region of an SACM APD. Type-II quantum wells are formed between materials with staggered band offsets. When thin layers of these materials are grown adjacent to each other, the electrons and holes are confined in adjoining layers. This allows spatially indirect tunneling-assisted emission or absorption. The type-II band line-up leads to a narrower effective bandgap and allows longer wavelength operation. Sidhu *et al.*, reported an SACM APD with InP multiplication region and an absorbing region consisting of 150 pairs of type-II $\text{Ga}_{0.47}\text{In}_{0.53}\text{As}$ - $\text{GaAs}_{0.51}\text{Sb}_{0.49}$ quantum wells [101]. A schematic cross section of the epitaxial layer structure and a band diagram for the type II superlattice absorption region are shown in Fig. 9. These APDs exhibited cutoff wavelength of 2.4 μm , quantum efficiency $> 40\%$ for wavelengths $< 2.1 \mu\text{m}$, dark current density near breakdown of less than 0.66 mA/cm^2 , and gain > 35 at room temperature. Ong *et al.*, have reported a similar SACM APD with type II $\text{Ga}_{0.47}\text{In}_{0.53}\text{As}$ - $\text{GaAs}_{0.51}\text{Sb}_{0.49}$ quantum well absorber except the multiplication region is InAlAs instead of InP [102]. These APDs had the following characteristics: 2.5 μm cutoff wavelength, 32% exter-

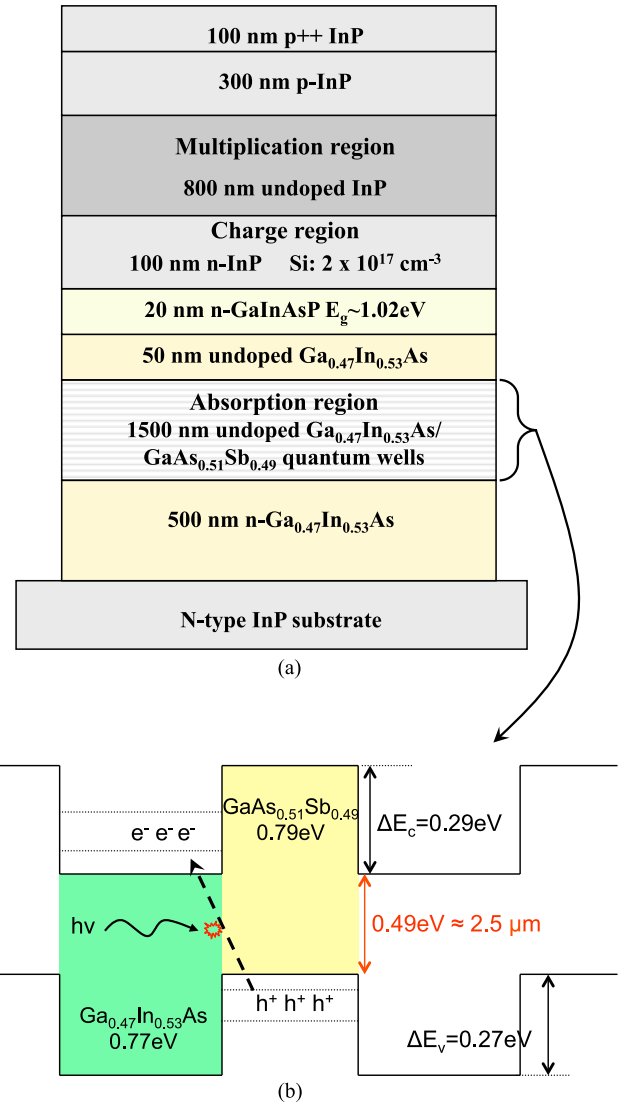


Fig. 9. (a) A schematic cross section of the epitaxial layer structure and (b) a band diagram for the type II superlattice absorption region.

nal quantum efficiency at 2.0 μm , gain > 60 , and 30 mA/cm^2 dark current near breakdown. Another approach for 2 μm APDs utilizes InAs as the gain material. InAs APDs have demonstrated $k \sim 0$ with moderate dark current at room temperature [41], [43]. InAs APDs employing 10 μm -thick intrinsic regions and AlAsSb blocking layer to suppress electron diffusion current achieved gain as high as ~ 300 at 15 V bias. The gain-bandwidth product was $> 500 \text{ GHz}$, however, the bandwidth was transit-time limited in the range 2 to 3 GHz independent of gain owing to the thick depletion width required to minimize the tunneling component of the dark current [43].

VII. SUMMARY

In the past decade the performance of APDs for optical fiber communication systems has improved as a result of improvements in materials and the development of advanced device structures. Techniques to reduce the excess noise and increase

the gain-bandwidth product have enabled operation at higher bit rates and set the stage for future systems that will benefit from the compact size, low cost, and high sensitivity that APDs provide.

REFERENCES

- [1] J. C. Campbell, "Advances in photodetectors," in *Optical Fiber Telecommunications, Part A: Components and Subsystems*, vol. 5, I. Kaminow, Tingye Li, and A. E. Wilner, Eds., 5th ed. San Francisco, CA, USA: Academic, Feb. 22, 2008.
- [2] N. Bertone and W. R. Clark, "APD ARRAYS—Avalanche photodiode arrays provide versatility in ultrasensitive applications," *Laser Focus World*, vol. 43, no. 9, pp. 69–73, Sep. 2007.
- [3] P. Mitra, J. D. Beck, M. R. Skokan, J. E. Robinson, J. Antoszewski, K. J. Winchester, A. J. Keating, T. Nguyen, K. K. M. Silva, C. A. Musca, J. M. Dell, and L. Farone, "Adaptive focal plane array (AFPA) technologies for integrated infrared microsystems," *Proc. SPIE*, vol. 6232, pp. 62320G-1–62320G-11, 2006.
- [4] A. Tosi, N. Calandri, M. Sanzaro, and F. Acerbi, "Low-noise, low-jitter, high detection efficiency InGaAs/InP single-photon avalanche diode," *IEEE J. Sel. Topics Quantum Electron.*, vol. 20, no. 6, 3803406 (6 pages), Nov./Dec. 2014.
- [5] X. Jiang, M. Itzler, K. O'Donnell, M. Entwistle, M. Owens, K. Slomkowski, and S. Rangwala, "InP-based single-photon detectors and geiger-mode APD arrays for quantum communications applications," *IEEE J. Sel. Topics Quantum Electron.*, vol. 21, no. 3, 3800112 (12 pages), May/June 2015.
- [6] S. D. Personick, "Receiver design for digital fiber-optic communication systems, Parts I and II," *Bell Syst. Tech. J.*, vol. 52, pp. 843–886, 1973.
- [7] R. G. Smith and S. D. Personick, "Receiver design for optical fiber communications systems," in *Semiconductor Devices for Optical Communication*. New York, NY, USA: Springer-Verlag, 1980, ch.4.
- [8] S. R. Forrest, "Sensitivity of avalanche photodiode receivers for high-bit-rate long-wavelength optical communication systems," in *Semiconductors and Semimetals: Lightwave Communications Technology*, vol. 22. Orlando, FL, USA: Academic, 1985, ch. 4.
- [9] B. L. Kasper and J. C. Campbell, "Multigigabit-per-second avalanche photodiode lightwave receivers," *J. Lightw. Technol.*, vol. LT-5, no. 10, pp. 1351–1364, Oct. 1987.
- [10] R. J. McIntyre, "Multiplication noise in uniform avalanche diodes," *IEEE Trans. Electron Devices*, vol. ED-13, no. 1, pp. 154–158, Jan. 1966.
- [11] R. J. McIntyre, "Factors affecting the ultimate capabilities of high speed avalanche photodiodes and a review of the state-of-the-art," in *Proc. Int. Electron Devices Meet.*, 1973, vol. 19, pp. 213–216.
- [12] R. B. Emmons, "Avalanche-photodiode frequency response," *J. Appl. Phys.*, vol. 38, pp. 3705–3714, 1967.
- [13] M. M. Hayat, B. E. A. Saleh, and M. C. Teich, "Effect of dead space on gain and noise of double-carrier multiplication avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 39, no. 3, pp. 546–552, Mar. 1992.
- [14] R. J. McIntyre, "A new look at impact ionization—Part I: A theory of gain, noise, breakdown probability and frequency response," *IEEE Trans. Electron Devices*, vol. 48, no. 8, pp. 1623–1631, Aug. 1999.
- [15] X. Li, X. Zheng, S. Wang, F. Ma, and J. C. Campbell, "Calculation of gain and noise with dead space for GaAs and $\text{Al}_x\text{Ga}_{1-x}\text{As}$ avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 49, no. 7, pp. 1112–1117, Jul. 2002.
- [16] B. Jacob, P. N. Robson, J. P. R. David, and G. J. Rees, "Fokker-Planck model for nonlocal impact ionization in semiconductors," *J. Appl. Phys.*, vol. 90, no. 3, pp. 1314–1317, 2001.
- [17] A. Spinelli and A. L. Lacaita, "Mean gain of avalanche photodiodes in a dead space model," *IEEE Trans. Electron Devices*, vol. 43, no. 1, pp. 23–30, Jan. 1996.
- [18] D. S. Ong, K. F. Li, G. J. Rees, G. M. Dunn, J. P. R. David, and P. N. Robson, "A Monte Carlo investigation of multiplication noise in thin $\text{p}^+\text{-i-n}^+$ avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 45, no. 8, pp. 1804–1810, Aug. 1998.
- [19] S. A. Plimmer, J. P. R. David, D. S. Ong, and K. F. Li, "A simple model including the effects of dead space," *IEEE Trans. Electron Devices*, vol. 46, no. 4, pp. 769–775, Apr. 1999.
- [20] K. F. Li, S. A. Plimmer, J. P. R. David, R. C. Tozer, G. J. Rees, P. N. Robson, C. C. Button, and J. C. Clark, "Low avalanche noise characteristics in thin $\text{InP p}^+\text{-i-n}^+$ diodes with electron initiated multiplication," *IEEE Photon. Technol. Lett.*, vol. 11, no. 3, pp. 364–366, Mar. 1999.
- [21] J. C. Campbell, S. Chandrasekhar, W. T. Tsang, G. J. Qua, and B. C. Johnson, "Multiplication noise of wide-bandwidth InP/InGaAsP/InGaAs avalanche photodiodes," *J. Lightw. Technol.*, vol. 7, no. 3, pp. 473–477, Mar. 1989.
- [22] P. Yuan, C. C. Hansing, K. A. Anselm, C. V. Lenox, H. Nie, A. L. Holmes, Jr., B. G. Streetman, and J. C. Campbell, "Impact ionization characteristics of III–V semiconductors for a wide range of multiplication region thicknesses," *IEEE J. Quantum Electron.*, vol. 36, no. 2, pp. 198–204, Feb. 2000.
- [23] M. A. Saleh, M. M. Hayat, P. O. Sotirelis, A. L. Holmes, J. C. Campbell, B. Saleh, and M. Teich, "Impact-ionization and noise characteristics of thin III–V avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 48, no. 12, pp. 2722–2731, Dec. 2001.
- [24] K. F. Li, D. S. Ong, J. P. R. David, R. C. Tozer, G. J. Rees, S. A. Plimmer, K. Y. Chang, and J. S. Roberts, "Avalanche noise characteristics of thin GaAs structures with distributed carrier generation," *IEEE Trans. Electron Devices*, vol. 47, no. 5, pp. 910–914, May 2000.
- [25] C. H. Tan, J. C. Clark, J. P. R. David, G. J. Rees, S. A. Plimmer, R. C. Tozer, D. C. Herbert, D. J. Robbins, W. Y. Leong, and J. Newey, "Avalanche noise measurements in thin $\text{Si p}^+\text{-i-n}^+$ diodes," *Appl. Phys. Lett.*, vol. 76, no. 26, pp. 3926–3928, 2000.
- [26] B. K. Ng, J. P. R. David, G. J. Rees, R. C. Tozer, M. Hopkinson, and R. J. Riley, "Avalanche multiplication and breakdown in $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x < 0.9$)," *IEEE Trans. Electron Devices*, vol. 49, no. 12, pp. 2349–2351, Dec. 2002.
- [27] P. Yuan, K. A. Anselm, C. Hu, H. Nie, C. Lenox, A. L. Holmes, B. G. Streetman, J. C. Campbell and R. J. McIntyre, "A new look at impact ionization—Part II: Gain and noise in short avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 46, no. 8, pp. 1632–1639, Aug. 1999.
- [28] P. Yuan, S. Wang, X. Sun, X. G. Zheng, A. L. Holmes, Jr., and J. C. Campbell, "Avalanche photodiodes with an impact-ionization-engineered multiplication region," *IEEE Photon. Technol. Lett.*, vol. 12, no. 10, pp. 1370–1372, Oct. 2000.
- [29] O.-H. Kwon, M. M. Hayat, S. Wang, J. C. Campbell, A. L. Holmes, Jr., B. E. A. Saleh, and M. C. Teich, "Optimal excess noise reduction in thin heterojunction $\text{Al}_{0.6}\text{Ga}_{0.4}\text{As}$ -GaAs avalanche photodiodes," *IEEE J. Quantum Electron.*, vol. 39, no. 10, pp. 1287–1296, Oct. 2003.
- [30] C. Groves, C. K. Chia, R. C. Tozer, J. P. R. David, and G. J. Rees, "Avalanche noise characteristics of single $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($0.3 < x < 0.6$)-GaAs heterojunction APDs," *IEEE J. Quantum Electron.*, vol. 41, no. 1, pp. 70–75, Jan. 2005.
- [31] M. M. Hayat, O.-H. Kwon, S. Wang, J. C. Campbell, B. E. A. Saleh, and M. C. Teich, "Boundary effects on multiplication noise in thin heterostructure avalanche photodiodes: Theory and experiment," *IEEE Trans. Electron Devices*, vol. 49, no. 12, pp. 2114–2123, Dec. 2002.
- [32] S. Wang, J. B. Hurst, F. Ma, R. Sidhu, X. Sun, X. G. Zheng, A. L. Holmes, Jr., J. C. Campbell, A. Huntington, and L. A. Coldren, "Low-noise impact-ionization-engineered avalanche photodiodes grown on InP substrates," *IEEE Photon. Technol. Lett.*, vol. 14, no. 12, pp. 1722–1724, Dec. 2002.
- [33] N. Duan, S. Wang, F. Ma, N. Li, J. C. Campbell, C. Wang, and L. A. Coldren, "High-speed and low-noise SACM avalanche photodiodes with an impact-ionization engineered multiplication region," *IEEE Photon. Technol. Lett.*, vol. 17, no. 8, pp. 1719–1721, Aug. 2005.
- [34] W. R. Clark, K. Vaccaro, and W. D. Waters, "InAlAs-InGaAs based avalanche photodiodes for next generation eye-safe optical receivers," *Proc. SPIE*, vol. 6796, p. 67962H, 2007.
- [35] C. A. Lee, R. A. Logan, R. L. Batdorf, J. J. Kleimack, and W. Weigmann, "Ionization rates of holes and electrons in silicon," *Phys. Rev.*, vol. 134, pp. A761–A773, 1964.
- [36] J. Conradi, "The distributions of gains in uniformly multiplying avalanche photodiodes: Experimental," *IEEE Trans. Electron Devices*, vol. ED-19, no. 6, pp. 713–718, Jun. 1972.
- [37] W. N. Grant, "Electron and hole ionization rates in epitaxial silicon at high electric fields," *Solid-State Electron.*, vol. 16, pp. 1189–1203, 1973.
- [38] T. Kaneda, H. Matsumoto, and T. Yamaoka, "A model for reach-through avalanche photodiodes (RAPD's)," *J. Appl. Phys.*, vol. 47, no. 7, pp. 3135–3139, 1976.
- [39] J. D. Beck, C. F. Wan, M. A. Kinch, and J. E. Robinson, "MWIR HgCdTe avalanche photodiodes," *Proc. SPIE, Mater. Infrared Detect.*, vol. 4454, pp. 188–197, 2001.
- [40] J. D. Beck, C.-F. Wan, M. A. Kinch, J. E. Robinson, F. Ma, and J. C. Campbell, "The HgCdTe electron avalanche photodiode," in *Proc. IEEE LEOS Annu. Meet.*, 2003, vol. 2, pp. 849–50.
- [41] A. R. J. Marshall, C. H. Tan, M. J. Steer, and J. P. R. David, "Electron dominated impact ionization and avalanche gain characteristics in InAs

- photodiodes," *Appl. Phys. Lett.*, vol. 93, no. 11, pp. 111107-1-111107-3, 2008.
- [42] A. R. J. Marshall, P. J. Ker, A. Krysa, J. P. R. David, and C. H. Tan, "High speed InAs electron avalanche photodiodes overcome the conventional gain-bandwidth product limit," *Opt. Express*, vol. 19, no. 23, pp. 23341-23349, 2011.
- [43] W. Sun, S. J. Maddox, S. R. Bank, and J. C. Campbell, "Record high gain from InAs avalanche photodiodes at room temperature," in *Proc. 72nd Device Res. Conf.*, Santa Barbara, CA, USA, 2014, pp. 47-48.
- [44] W. Sun, Z. Lu, X. Zheng, J. C. Campbell, S. J. Maddox, H. P. Nair, and S. R. Bank, "High-Gain InAs avalanche photodiodes," *IEEE J. Quantum Electron.*, vol. 49, no. 2, pp. 154-161, Feb. 2013.
- [45] P. J. Ker, A. R. J. Marshall, A. B. Krysa, J. P. R. David, and C. H. Tan, "InAs electron avalanche photodiodes with 580 GHz gain-bandwidth product," in *Proc. Opto-Electron. Commun. Conf.*, Busan, Korea, 2012, pp. 220-221.
- [46] H. Melchior, A. R. Hartman, D. P. Schinke, and T. E. Seidel, "Planar epitaxial silicon avalanche photodiode," *Bell Syst. Tech. J.*, vol. 57, pp. 1791-1807, 1978.
- [47] S. R. Forrest, M. DiDomenico, Jr., R. G. Smith, and H. J. Stocker, "Evidence of tunneling in reverse-bias III-V photodetector diodes," *Appl. Phys. Lett.*, vol. 36, pp. 580-582, 1980.
- [48] H. Ando, H. Kaaba, M. Ito, and T. Kaneda, "Tunneling current in InGaAsP and optimum design for InGaAs/InP avalanche photo-diodes," *Jpn. J. Appl. Phys.*, vol. 19, pp. 1277-1280, 1980.
- [49] F. Capasso, A. Y. Cho, and P. W. Foy, "Low-dark-current low-voltage 1.3-1.6 μm avalanche photodiode with high-low electric field profile and separate absorption and multiplication regions by molecular beam epitaxy," *Electron. Lett.*, vol. 20, no. 15, pp. 635-637, 1984.
- [50] P. Webb, R. McIntyre, J. Scheibling, and M. Holunga, "A planar InGaAs APD fabricated using Si implantation and regrowth techniques," presented at the Tech. Digest Optics Fiber Conf., New Orleans, LA, USA, 1988.
- [51] L. E. Tarof, "Planar InP-InGaAs avalanche photodetectors with n-multiplication layer exhibiting a very high gain-bandwidth product," *IEEE Photon. Technol. Lett.*, vol. 2, no. 9, pp. 643-645, Sep. 1990.
- [52] H. W. Rugg, "An optimized avalanche photodiode," *IEEE Trans. Electron Devices* vol. ED-14, no. 5, pp. 239-251, May 1967.
- [53] J. C. Campbell, A. G. Dentai, W. S. Holden, and B. L. Kasper, "High-performance avalanche photodiode with separate absorption, grading, and multiplication regions," *Electron. Lett.*, vol. 18, pp. 818-820, 1983.
- [54] Y. Matsushima, A. Akiba, K. Sakai, K. Kushirn, Y. Node, and K. Utaka, "High-speed response InGaAs/InP heterostructure avalanche photodiode with InGaAsP buffer layers," *Electron. Lett.*, vol. 18, pp. 945-946, 1982.
- [55] N. Li, R. Sidhu, X. Li, F. Ma, X. Zheng, S. Wang, G. Karve, S. Demiguel, A. L. Holmes, Jr., and J. C. Campbell, "InGaAs/InAlAs avalanche photodiode with undepleted absorber," *Appl. Phys. Lett.*, vol. 82, pp. 2175-2177, 2003.
- [56] E. Yagyu, E. Ishimura, M. Nakaji, T. Aoyagi, K. Yoshiara, and Y. Tokuda, "Investigation of guardring-free planar AlInAs avalanche photodiodes," *IEEE Photon. Technol. Lett.*, vol. 18, no. 11, pp. 1264-1266, Jun. 2006.
- [57] M. Nada, Y. Muramoto, H. Yokiyama, N. Ishibashi, and S. Kodama, "Inverted InAlAs/InGaAs Avalanche photodiode with low-high-low electric field profile," *Jpn. J. Appl. Phys.*, vol. 51, no. 2, p. 02BG03, Feb. 2012.
- [58] B. F. Levine, R. N. Sacks, J. Ko, M. Jazwiecki, J. A. Valdmanis, D. Gunther, and J. H. Meier, "A new planar InGaAs-InAlAs avalanche photodiode," *IEEE Photon. Technol. Lett.*, vol. 18, no. 18, pp. 1898-1900, Oct. 2006.
- [59] C. Y. Park, K. S. Hyun, S. K. Kang, M. K. Song, T. Y. Yoon, H. M. Kim, H. M. Park, S.-C. Park, Y. H. Lee, C. Lee, and J. B. Yoo, "High-performance InGaAs/InP photodiode for 2.5 Gb/s optical receiver," *Opt. Quantum Electron.*, vol. 24, no. 5, pp. 553-559, 1995.
- [60] G. Hasnain, W. G. Bi, S. Song, J. T. Anderson, N. Moll, C.-Yi Su, J. N. Hollenhorst, N. D. Baynes, I. Athroll, S. Amos, and R. M. Ash, "Buried-mesa avalanche photodiodes," *IEEE J. Quantum Electron.*, vol. 34, no. 12, pp. 2321-2326, Dec. 1998.
- [61] M. A. Itzler, C. S. Wang, S. McCoy, N. Codd, and N. Komaba, "Planar bulk InP avalanche photodiode design for 2.5 and 10 Gb/s applications," in *Proc. 24th Eur. Conf. Opt. Commun.*, Madrid, Spain, Sep. 20-24, 1998, vol. 1, pp. 59-60.
- [62] D. A. Humphreys and R. J. King, "Measurement of absorption coefficients of $\text{Ga}_{0.47}\text{In}_{0.53}\text{As}$ over the wavelength range 1.0-1.7 μm ," *Electron. Lett.*, vol. 21, nos. 25/26, pp. 1187-1189, 1985.
- [63] F. R. Bacher, J. S. Blakemore, J. T. Ebner, and J. R. Arthur, "Optical-absorption coefficient of $\text{In}_{1-x}\text{Ga}_x\text{As/InP}$," *Phys. Rev. B*, vol. 37, no. 5, pp. 2551-2557, 1988.
- [64] C. A. Amiento and S. H. Groves, "Impact ionization in (100)-, (110)-, and (111)-oriented InP avalanche photodiodes," *Appl. Phys. Lett.*, vol. 43, no. 2, pp. 333-335, 1983.
- [65] L. W. Cook, G. E. Bulman, and G. E. Stillman, "Electron and hole ionization coefficients in InP determined by photomultiplication measurements," *Appl. Phys. Lett.*, vol. 40, no. 7, pp. 589-591, 1982.
- [66] Y. Kang, M. Zadka, S. Litski, G. Sarid, M. Morse, and M. J. Paniccia, Y.-H. Kuo, J. Bowers, A. Beling, H.-D. Liu, J. Campbell, and A. Pauchard, "Epitaxially-grown Ge/Si avalanche photodiodes for 1.3mm light detection," *Nature Photon.*, vol. 3, pp. 59-63, Jan. 2009.
- [67] Y. Kang, Z. Huang, Y. Saado, J. C. Campbell, A. Pauchard, J. E. Bowers, and M. J. Paniccia, "High performance Ge/Si avalanche photodiodes development in Intel," presented at the Optical Fiber Communications Conf. Expo. Nat. Fiber Optic Engineers Conf., Los Angeles, CA, USA, 2011.
- [68] M. Huang, T. Shi, P. Cai, L. Wang, S. Li, W. Chen, C.-Y. Hong, and D. Pan, "25Gb/s normal incident Ge/Si avalanche photodiode," presented at the Eur. Conf. Optical Communication, Cannes, France, 2014.
- [69] D. Dai, M. Piels, and J. E. Bowers, "Monolithic germanium/silicon photodetectors with decoupled structures: Resonant APDs and UTC photodiodes," *IEEE J. Sel. Topics Quantum Electron.*, vol. 20, no. 6, 3802214 (14 pages), Nov./Dec. 2014.
- [70] K. F. Li, D. S. Ong, J. P. R. David, R. C. Tozer, G. J. Rees, S. A. Plimmer, K. Y. Chang, and J. S. Roberts, "Avalanche noise characteristics of thin GaAs structures with distributed carrier generation," *IEEE Trans. Electron Devices*, vol. 47, no. 5, pp. 910-914, May 2000.
- [71] K. F. Li, D. S. Ong, J. P. R. David, G. J. Rees, R. C. Tozer, P. N. Robson, and R. Grey, "Avalanche multiplication noise characteristics in thin GaAs p^+-i-n^+ diodes," *IEEE Trans. Electron Devices*, vol. 45, no. 10, pp. 2102-2107, Oct. 1998.
- [72] C. Hu, K. A. Anselm, B. G. Streetman, and J. C. Campbell, "Noise characteristics of thin multiplication region GaAs avalanche photodiodes," *Appl. Phys. Lett.*, vol. 69, no. 24, pp. 3734-3736, 1996.
- [73] S. A. Plimmer, J. P. R. David, D. C. Herbert, T.-W. Lee, G. J. Rees, P. A. Houston, R. Grey, P. N. Robson, A. W. Higgs, and D. R. Wight, "Investigation of impact ionization in thin GaAs diodes," *IEEE Trans. Electron Devices*, vol. 43, no. 7, pp. 1066-1072, Jul. 1996.
- [74] C. Lenox, P. Yuan, H. Nie, O. Baklenov, C. Hansing, J. C. Campbell, and B. G. Streetman, "Thin multiplication region InAlAs homojunction avalanche photodiodes," *Appl. Phys. Lett.*, vol. 73, pp. 783-784, 1998.
- [75] C. H. Tan, J. C. Clark, J. P. R. David, G. J. Rees, S. A. Plimmer, R. C. Tozer, D. C. Herbert, D. J. Robbins, W. Y. Leong, and J. Newey, "Avalanche noise measurements in thin Si p^+-i-n^+ diodes," *Appl. Phys. Lett.*, vol. 76, no. 26, pp. 3926-3928, 2000.
- [76] C. H. Tan, J. P. R. David, J. Clark, G. J. Rees, S. A. Plimmer, D. J. Robbins, D. C. Herbert, R. T. Carline, and W. Y. Leong, "Avalanche multiplication and noise in submicron Si $p-i-n$ diodes," *Proc. SPIE, Silicon-Based Optoelectron. II*, vol. 3953, pp. 95-102, 2000.
- [77] S. A. Plimmer, J. P. R. David, G. J. Rees, R. Grey, D. C. Herbert, D. R. Wright, and A. W. Higgs, "Impact ionization in thin $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x = 0.15$ and 0.30) $p-i-n$ diodes," *J. Appl. Phys.*, vol. 82, no. 3, pp. 1231-1235, 1997.
- [78] B. K. Ng, J. P. R. David, G. J. Rees, R. C. Tozer, M. Hopkinson, and R. J. Riley, "Avalanche multiplication and breakdown in $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x < 0.9$)," *IEEE Trans. Electron Devices*, vol. 49, no. 12, pp. 2349-2351, Dec. 2002.
- [79] B. K. Ng, J. P. R. David, R. C. Tozer, M. Hopkinson, G. Hill, and G. H. Rees, "Excess noise characteristics of $\text{Al}_{0.8}\text{Ga}_{0.2}\text{As}$ avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 48, no. 10, pp. 2198-2204, Oct. 2001.
- [80] C. H. Tan, J. P. R. David, S. A. Plimmer, G. J. Rees, R. C. Tozer, and R. Grey, "Low multiplication noise thin $\text{Al}_{0.6}\text{Ga}_{0.4}\text{As}$ avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 48, no. 7, pp. 1310-1317, Jul. 2001.
- [81] B. K. Ng, J. P. R. David, R. C. Tozer, G. J. Rees, Y. Feng, J. H. Zhao, and M. Weiner, "Nonlocal effects in thin 4H-SiC UV avalanche photodiodes," *IEEE Trans. Electron Devices*, vol. 50, no. 8, pp. 1724-1732, Aug. 2003.
- [82] A. L. Beck, B. Yang, S. Wang, C. J. Collins, J. C. Campbell, A. Yulius, A. Chen, and J. M. Woodall, "Quasi-direct UV/blue GaP avalanche photodiodes," *IEEE J. Quantum Electron.*, vol. 40, no. 12, pp. 1695-1699, Dec. 2004.

- [83] C. H. Tan, R. Ghin, J. P. R. David, G. J. Rees, and M. Hopkinson, "The effect of dead space on gain and excess noise in $\text{In}_{0.48}\text{Ga}_{0.52}\text{P}^+\text{In}^+$ diodes," *Semicond. Sci. Technol.*, vol. 18, no. 8, pp. 803–806, 2003.
- [84] N. Li, R. Sidhu, X. Li, F. Ma, X. Zheng, S. Wang, G. Karve, S. Demiguel, A. L. Holmes, Jr., and J. C. Campbell, "InGaAs/InAlAs avalanche photodiode with undepleted absorber," *Appl. Phys. Lett.*, vol. 82, pp. 2175–2177, 2003.
- [85] M. Nada, Y. Muramoto, H. Yokoyama, T. Ishibashi, and H. Matsuzaki, "Triple-mesa avalanche photodiode with inverted P-down structure for reliability and stability," *J. Lightw. Technol.*, vol. 32, no. 81, pp. 1543–1548, Apr. 2014.
- [86] X. Li, S. Demiguel, N. Li, J. C. Campbell, D. L. Tulchinsky, and K. J. Williams, "Backside illuminated high saturation current partially depleted absorber photodetectors," *Electron. Lett.*, vol. 39, pp. 1466–1467, 2003.
- [87] M. Nada, Y. Muramoto, H. Yokoyama, T. Toshimatsu, and H. Matsuzaki, "High-speed avalanche photodiodes for 100-Gb/s systems and beyond," presented at the Eur. Conf. Optical Communication, Cannes, France, 2014.
- [88] F. Capasso, W. T. Tsang, A. L. Hutchinson, and G. F. Williams, "Enhancement of electron impact ionization in a superlattice: a new avalanche photodiode with a large ionization rate ratio," *Appl. Phys. Lett.*, vol. 40, pp. 38–40, 1982.
- [89] R. Chin, N. Holonyak, Jr., G. E. Stillman, J. Y. Tang, and K. Hess, "Impact ionization in multilayered heterojunction structures," *Electron. Lett.*, vol. 16, pp. 467–469, 1980.
- [90] S. Wang, R. Sidhu, X. G. Zheng, X. Li, Sun, A. L. Holmes, Jr., and J. C. Campbell, "Low-noise avalanche photodiodes with graded impact-ionization-engineered multiplication region," *IEEE Photon. Technol. Lett.*, vol. 13, no. 12, pp. 1346–1348, Dec. 2001.
- [91] S. Wang, F. Ma, X. Li, R. Sidhu, X. G. Zheng, X. Sun, A. L. Holmes, Jr., and J. C. Campbell, "Ultra-low noise avalanche photodiodes with a 'centered-well' multiplication region," *IEEE J. Quantum Electron.*, vol. 39, no. 2, pp. 375–378, Feb. 2003.
- [92] F. Ma, S. Wang, and J. C. Campbell, "Shot noise suppression in avalanche photodiodes," *Phys. Rev. Lett.*, vol. 95, no. 17, pp. 176604-1–176604-4, 2005.
- [93] H. R. Burris, M. S. Ferraro, W. T. Freeman, C. I. Moore, J. L. Murphy, W. S. Rabinovich, W. R. Smith, L. L. Summers, L. M. Thomas, M. J. Vilcheck, W. R. Clark, and W. D. Waters, "Development of a large area InGaAs APD receiver based on an impact ionization engineered detector for free-space lasercomm applications," *Proc. SPIE, Int. Soc. Opt. Eng.*, vol. 8380, 838008 (8 pages), 2012.
- [94] J. P. Gordon, R. E. Nahory, M. A. Pollack, and J. M. Worlock, "Low-noise multistage avalanche photodetector," *IEEE Electron. Lett.*, vol. 15, no. 17, pp. 518–519, Aug. 1979.
- [95] S. Rakshit and N. B. Chakraborti, "Multiplication noise in multi-heterostructure avalanche photodiodes," *Solid State Electron.*, vol. 26, no. 10, pp. 999–1003, 1983.
- [96] W. Clark, "Avalanche photodiode multiplication region and avalanche photodiode with low impact ionization rate ratio," U.S. Patent 6 747 296, Jun. 8, 2004.
- [97] G. M. Williams, M. A. Compton, and A. S. Huntington, "High-speed photon counting with linear-mode APD receivers," *Proc. SPIE*, vol. 7320, 732012 (9 pages), 2009.
- [98] W. Sun, X. Zheng, Z. Lu, and J. C. Campbell, "Monte Carlo simulation of InAlAs/InAlGaAs tandem avalanche photodiodes," *J. Quantum Electron.*, vol. 48, no. 4, pp. 528–532, 2012.
- [99] J. C. Dries, M. R. Gokhale, K. J. Thomson, S. R. Forrest, and R. Hull, "Strain compensated In Ga As ($x < 0.47$) quantum well photodiodes for extended wavelength operation," *Appl. Phys. Lett.*, vol. 73, no. 16, pp. 2263–2265, Oct. 1998.
- [100] M. Wada and H. Hosomatsu, "Wide wavelength and low dark current lattice mis-matched InGaAs/InAsP photodiodes grown by metalorganic vapor-phase epitaxy," *Appl. Phys. Lett.*, vol. 64, no. 10, pp. 1265–1267, Mar. 1994.
- [101] R. Sidhu, L. Zhang, N. Tan, N. Duan, J. C. Campbell, A. L. Holmes, D.-F. Hsu, and M. A. Itzler, "2.4 μm cutoff wavelength avalanche photodiode on InP substrate," *Electron. Lett.*, vol. 42, no. 3, pp. 181–182, 2006.
- [102] D. S. G. Ong, J. Shien Ng, Y. Ling Goh, C. H. Tan, Z. Zhang, and J. P. R. David, "InAlAs avalanche photodiode with Type-II superlattice absorber for detection beyond 2 μm ," *IEEE Trans. Electron Devices*, vol. 58, no. 2, pp. 486–489, Feb. 2011.

Author biography not available at the time of publication.