ID	Task Mode	Task Name	Duration	CW	Start	Finish	% Complete	Predecessors	Resource Names	Notes
1		**HKMC 2020 proto**	**106,3 days**		**Thu 13.02.20**	**Wed 15.07.20**	**7%**			
2		HKMC R0 SDB	0 days	4.W07 - 4.W07	Thu 13.02.20	Thu 13.02.20	0%			Starting MileStone
3		**SW Release R0**	**48,3 days**		**Thu 13.02.20**	**Thu 23.04.20**	**14%**			**FLC Variant : ...**
4		**Systems**	**16,8 days**		**Thu 13.02.20**	**Fri 06.03.20**	**80%**			
5		Creation of SDB	16,8 days	4.W07 - 5.W10	Thu 13.02.20	Fri 06.03.20	80%	2	Soo-Jin Ahn;Philippe Lavole	This is a FO Task, but at present s
6		SDB Release	0 days	5.W10 - 5.W10	Fri 06.03.20	Fri 06.03.20	0%	5	Soo-Jin Ahn	
7		**Application SW**	**40,3 days**		**Tue 25.02.20**	**Thu 23.04.20**	**0%**		**Srikanth Mukku**	
8		**SW Architecture**	**19 days**		**Fri 06.03.20**	**Fri 03.04.20**	**0%**		**Soo-Jin Ahn**	
9		SW Adaptors Strategy.	2 days	5.W10 - 2.W11	Fri 06.03.20	Tue 10.03.20	0%	6	Soo-Jin Ahn	This is not yet reflected in the wor
10		SW Architecture in EA Development	4 days	2.W13 - 1.W14	Tue 24.03.20	Mon 30.03.20	0%	9;26;47	Soo-Jin Ahn	SW Architecture for Adaptors
11		SW EA Architecture Review	3 days	1.W14 - 5.W14	Mon 30.03.20	Fri 03.04.20	0%	10	Soo-Jin Ahn;Choong-Yup Sl	
12		SW EA Architecture Release	0 days	5.W14 - 5.W14	Fri 03.04.20	Fri 03.04.20	0%	11	Soo-Jin Ahn	
13		**SW Architecture ARXML 's Development**	**7 days**		**Thu 12.03.20**	**Mon 23.03.20**	**0%**		**Soo-Jin Ahn**	
14		Adaptors Design in Davinci Developer.	4 days	4.W11 - 3.W12	Thu 12.03.20	Wed 18.03.20	0%	24;44;6	Soo-Jin Ahn	
15		Adaptors ARXML Generation for simulink import.	1 day	3.W12 - 4.W12	Wed 18.03.20	Thu 19.03.20	0%	14	Soo-Jin Ahn	
16		Scheduler for ASP SW Integration	1 day	4.W12 - 5.W12	Thu 19.03.20	Fri 20.03.20	0%	15	Soo-Jin Ahn	
17		Scheduler for SW Adaptors.	1 day	5.W12 - 1.W13	Fri 20.03.20	Mon 23.03.20	0%	16	Soo-Jin Ahn	
18		ARXML's Review for Basic SW & Adaptors Development	1 day	1.W13 - 2.W13	Mon 23.03.20	Tue 24.03.20	0%	17	Soo-Jin Ahn;Choong-Yup Sl	
19		ARXML's Release	0 days	2.W13 - 2.W13	Tue 24.03.20	Tue 24.03.20	0%	18	Soo-Jin Ahn	
20		**Adaptor 1 : FCA-Car/Ped Adaptor Development**	**17,5 days**		**Fri 06.03.20**	**Thu 02.04.20**	**0%**		**Hoon Bae**	
21		**Feature_SignalInterfaceMatrix**	**2 days**		**Fri 06.03.20**	**Tue 10.03.20**	**0%**		**Hoon Bae**	**This is the signal interface matr**
22		Creation of SignalInterfaceMatrix	12 hrs	5.W10 - 2.W11	Fri 06.03.20	Tue 10.03.20	0%	6	Hoon Bae	
23		Review of SignalInterface Matrix	4 hrs	2.W11 - 2.W11	Tue 10.03.20	Tue 10.03.20	0%	22	Hoon Bae;Soo-Jin Ahn;Your	
24		Release of FCA Car7Ped Adaptor SignalInterface Matrix	0 days	2.W11 - 2.W11	Tue 10.03.20	Tue 10.03.20	0%	23	Hoon Bae	
25		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	21;64	Hoon Bae	not applicable for R0
26		Adaptor Detailed Design Review	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	25	Hoon Bae;Soo-Jin Ahn;Your	not applicable for R0
27		Release of FCA Car/Ped Adaptor Detailed Design	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	26	Hoon Bae	not applicable for R0
28		**Adaptor Simulink Development**	**11,5 days**		**Thu 12.03.20**	**Mon 30.03.20**	**0%**		**Hoon Bae**	
29		Simulink Design Development	4 days	4.W11 - 3.W12	Thu 12.03.20	Wed 18.03.20	0%	27	Hoon Bae	
30		Simulink Import of ARXML & linking to Development Model	4 hrs	4.W13 - 5.W13	Thu 26.03.20	Fri 27.03.20	0%	19;69	Hoon Bae	
31		Simulink Design Release & Review	4 hrs	5.W13 - 5.W13	Fri 27.03.20	Fri 27.03.20	0%	30	Hoon Bae;Soo-Jin Ahn;Your	
32		Adaptor Code Generation from Simulink.	4 hrs	5.W13 - 1.W14	Fri 27.03.20	Mon 30.03.20	0%	31	Hoon Bae	
33		Release of FCA-Car/Ped Adaptor Simulink Design	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	32	Hoon Bae	
34		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Mon 30.03.20**	**Mon 30.03.20**	**0%**		**Hoon Bae**	**not applicable for R0**
35		Adaptor Unit Test Specification.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	33	Hoon Bae	not applicable for R0
36		Adaptor Unit Test Cases Implementation.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	35	Hoon Bae	not applicable for R0
37		Adaptor Unit Test Execution and its report generation.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	36	Hoon Bae	not applicable for R0
38		SW Source Code & Unit Test Report Review	2 days	1.W14 - 4.W14	Mon 30.03.20	Thu 02.04.20	0%	37	Hoon Bae;Soo-Jin Ahn;Your	
39		Release of FCA-Car/Ped SW Source Code & SW Unit Test Repc	0 days	4.W14 - 4.W14	Thu 02.04.20	Thu 02.04.20	0%	38	Hoon Bae	
40		**Adaptor 2 : LKA-L Adaptor Development**	**16,5 days**		**Fri 06.03.20**	**Tue 31.03.20**	**0%**		**Choong-Yup Shin**	
41		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Fri 06.03.20**	**Tue 10.03.20**	**0%**		**Choong-Yup Shin**	
42		Creation of SignalInterfaceMatrix	12 hrs	5.W10 - 2.W11	Fri 06.03.20	Tue 10.03.20	0%	6	Choong-Yup Shin	
43		Review & Release of SignalInterface Matrix	4 hrs	2.W11 - 2.W11	Tue 10.03.20	Tue 10.03.20	0%	42	Soo-Jin Ahn;Choong-Yup Sl	
44		Release of LKA-L Adaptor SignalInterface Matrix	0 days	2.W11 - 2.W11	Tue 10.03.20	Tue 10.03.20	0%	43	Choong-Yup Shin	
45		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	44;84	Choong-Yup Shin	not applicable for R0
46		Adaptor Detailed Design Review	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	45	Soo-Jin Ahn;Choong-Yup Sl	not applicable for R0
47		Release of LKA-L Adaptor Detailed Design	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	46	Choong-Yup Shin	not applicable for R0
48		**Adaptor Simulink Development**	**11,5 days**		**Thu 12.03.20**	**Mon 30.03.20**	**0%**		**Choong-Yup Shin**	
49		Simulink Design Development	4 days	4.W11 - 3.W12	Thu 12.03.20	Wed 18.03.20	0%	47	Choong-Yup Shin	
50		Simulink Import of ARXML & linking to Development Model	4 hrs	4.W13 - 5.W13	Thu 26.03.20	Fri 27.03.20	0%	19;89	Choong-Yup Shin	
51		Simulink Design Release & Review	4 hrs	5.W13 - 5.W13	Fri 27.03.20	Fri 27.03.20	0%	50	Choong-Yup Shin;Soo-Jin A	
52		Adaptor Code Generation from Simulink.	4 hrs	5.W13 - 1.W14	Fri 27.03.20	Mon 30.03.20	0%	51	Choong-Yup Shin	
53		Release of LKA-L Adaptor Simulink Design	0 hrs	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	52	Choong-Yup Shin	
54		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Mon 30.03.20**	**Mon 30.03.20**	**0%**		**Choong-Yup Shin**	**not applicable for R0**
55		Adaptor Unit Test Specification.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	53	Choong-Yup Shin	not applicable for R0
56		Adaptor Unit Test Cases Implementation.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	55	Choong-Yup Shin	not applicable for R0
57		Adaptor Unit Test Execution and its report generation.	0 days	1.W14 - 1.W14	Mon 30.03.20	Mon 30.03.20	0%	56	Choong-Yup Shin	not applicable for R0
58		SW Source Code & Unit Test Report Review	1 day	1.W14 - 2.W14	Mon 30.03.20	Tue 31.03.20	0%	57	Choong-Yup Shin;Soo-Jin A	
59		Release of LKA_L SW Source Code & Unit Test Report	0 days	2.W14 - 2.W14	Tue 31.03.20	Tue 31.03.20	0%	58	Choong-Yup Shin	
60		**Vehicle Input Adaptor**	**19,5 days**		**Tue 10.03.20**	**Wed 08.04.20**	**0%**		**Hoon Bae**	
61		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Tue 10.03.20**	**Thu 12.03.20**	**0%**			
62		Creation of SignalInterfaceMatrix	12 hrs	2.W11 - 4.W11	Tue 10.03.20	Thu 12.03.20	0%	6;24	Hoon Bae	

Project: HKMCApplicationSWPI
Date: Tue 03.03.20

Legend:
Task · Summary · Inactive Milestone · Duration-only · Start-only · External Milestone · Manual Progress
Split · Project Summary · Inactive Summary · Manual Summary Rollup · Finish-only · Deadline
Milestone · Inactive Task · Manual Task · Manual Summary · External Tasks · Progress

ID	Task Mode	Task Name	Duration	CW	Start	Finish	% Complete	Predecessors	Resource Names	Notes
63		Review & Release of SignalInterface Matrix	4 hrs	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	62	Soo-Jin Ahn;Hoon Bae;Your	
64		Release of Veh_Inp Adaptor SignalInterface Matrix	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	63	Hoon Bae	
65		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	61;29	Hoon Bae	not applicable for R0
66		Adaptor Detailed Design Review	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	65	Soo-Jin Ahn;Hoon Bae;Your	not applicable for R0
67		Release of Veh_Inp Adaptor Detailed Design	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	66	Hoon Bae	not applicable for R0
68		**Adaptor Simulink Development**	**12,5 days**		**Wed 18.03.20**	**Tue 07.04.20**	**0%**		**Hoon Bae**	
69		Simulink Design Development	6 days	3.W12 - 4.W13	Wed 18.03.20	Thu 26.03.20	0%	67	Hoon Bae	
70		Simulink Import of ARXML & linking to Development Model	4 hrs	4.W14 - 4.W14	Thu 02.04.20	Thu 02.04.20	0%	19;39	Hoon Bae	
71		Simulink Design Release & Review	4 hrs	4.W14 - 5.W14	Thu 02.04.20	Fri 03.04.20	0%	70	Hoon Bae;Soo-Jin Ahn;Your	
72		Adaptor Code Generation from Simulink.	2 days	5.W14 - 2.W15	Fri 03.04.20	Tue 07.04.20	0%	71	Hoon Bae	
73		Release of Veh_Inp Adaptor Simulink Design	0 days	2.W15 - 2.W15	Tue 07.04.20	Tue 07.04.20	0%	72	Hoon Bae	
74		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Tue 07.04.20**	**Tue 07.04.20**	**0%**		**Hoon Bae**	**not applicable for R0**
75		Adaptor Unit Test Specification.	0 days	2.W15 - 2.W15	Tue 07.04.20	Tue 07.04.20	0%	73	Hoon Bae	not applicable for R0
76		Adaptor Unit Test Cases Implementation.	0 days	2.W15 - 2.W15	Tue 07.04.20	Tue 07.04.20	0%	75	Hoon Bae	not applicable for R0
77		Adaptor Unit Test Execution and its report generation.	0 days	2.W15 - 2.W15	Tue 07.04.20	Tue 07.04.20	0%	76	Hoon Bae	not applicable for R0
78		SW Source Code & Unit Test Report Review	1 day	2.W15 - 3.W15	Tue 07.04.20	Wed 08.04.20	0%	77	Soo-Jin Ahn;Hoon Bae;Your	
79		Release of Vhe_In Adaptor SW Source Code & Unit Test Report	0 days	3.W15 - 3.W15	Wed 08.04.20	Wed 08.04.20	0%	78	Hoon Bae	
80		**Vehicle Output Adaptor**	**18,5 days**		**Tue 10.03.20**	**Tue 07.04.20**	**0%**		**Choong-Yup Shin**	
81		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Tue 10.03.20**	**Thu 12.03.20**	**0%**			
82		Creation of SignalInterfaceMatrix	12 hrs	2.W11 - 4.W11	Tue 10.03.20	Thu 12.03.20	0%	6;44	Choong-Yup Shin	
83		Review & Release of SignalInterface Matrix	4 hrs	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	82	Soo-Jin Ahn;Choong-Yup Sh	
84		Release of Veh_Out Adaptor SignalInterface Matrix	0 days	4.W11 - 4.W11	Thu 12.03.20	Thu 12.03.20	0%	83	Choong-Yup Shin	
85		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	81;49	Choong-Yup Shin	not applicable for R0
86		Adaptor Detailed Design Review	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	85	Soo-Jin Ahn;Choong-Yup Sh	not applicable for R0
87		Release of Veh_Out Adaptor Detailed Design	0 days	3.W12 - 3.W12	Wed 18.03.20	Wed 18.03.20	0%	85	Choong-Yup Shin	not applicable for R0
88		**Adaptor Simulink Development**	**11,5 days**		**Wed 18.03.20**	**Mon 06.04.20**	**0%**		**Choong-Yup Shin**	
89		Simulink Design Development	6 days	3.W12 - 4.W13	Wed 18.03.20	Thu 26.03.20	0%	87	Choong-Yup Shin	
90		Simulink Import of ARXML & linking to Development Model	4 hrs	2.W14 - 2.W14	Tue 31.03.20	Tue 31.03.20	0%	19;59	Choong-Yup Shin	
91		Simulink Design Release & Review	4 hrs	2.W14 - 4.W14	Tue 31.03.20	Thu 02.04.20	0%	90	Choong-Yup Shin;Soo-Jin A	
92		Adaptor Code Generation from Simulink.	2 days	4.W14 - 1.W15	Thu 02.04.20	Mon 06.04.20	0%	91	Choong-Yup Shin	
93		Release of Veh_Out Adaptor Simulink Design	0 days	1.W15 - 1.W15	Mon 06.04.20	Mon 06.04.20	0%	92	Choong-Yup Shin	
94		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Mon 06.04.20**	**Mon 06.04.20**	**0%**		**Choong-Yup Shin**	**not applicable for R0**
95		Adaptor Unit Test Specification.	0 days	1.W15 - 1.W15	Mon 06.04.20	Mon 06.04.20	0%	93	Choong-Yup Shin	not applicable for R0
96		Adaptor Unit Test Cases Implementation.	0 days	1.W15 - 1.W15	Mon 06.04.20	Mon 06.04.20	0%	95	Choong-Yup Shin	not applicable for R0
97		Adaptor Unit Test Execution and its report generation.	0 days	1.W15 - 1.W15	Mon 06.04.20	Mon 06.04.20	0%	96	Choong-Yup Shin	not applicable for R0
98		SW Source Code & Unit Test Report Review	1 day	1.W15 - 2.W15	Mon 06.04.20	Tue 07.04.20	0%	97	Choong-Yup Shin;Soo-Jin A	
99		Release of Veh_Out Adaptor SW Source Code & Unit Test Repo	0 days	2.W15 - 2.W15	Tue 07.04.20	Tue 07.04.20	0%	98	Choong-Yup Shin	
100		**SW Integration**	**40,3 days**		**Tue 25.02.20**	**Thu 23.04.20**	**0%**			
101		SW Integration Test Specification.	4 days	2.W09 - 5.W09	Tue 25.02.20	Fri 28.02.20	0%			
102		SW Integration Test Cases Implementation.	2 days	1.W10 - 2.W10	Mon 02.03.20	Tue 03.03.20	0%	101		
103		SW Integration.	4 days	3.W15 - 2.W16	Wed 08.04.20	Tue 14.04.20	0%	38;59;7		
104		SW Integration Test execution & Its Rerport Generation.	2 days	2.W16 - 5.W16	Tue 14.04.20	Fri 17.04.20	0%	103		
105		SW Version Management.	3 days	2.W16 - 1.W17	Tue 14.04.20	Mon 20.04.20	0%	103		
106		SW Release Notes.	1 day	1.W17 - 2.W17	Mon 20.04.20	Tue 21.04.20	0%	105		
107		HKMC SW Integration	1 day	5.W16 - 1.W17	Fri 17.04.20	Mon 20.04.20	0%	104		(Buildable Application SW on GIT repo together with HKMC Basic SW
108		HKMC Application SW Release	0 days	1.W17 - 1.W17	Mon 20.04.20	Mon 20.04.20	0%	107		
109		App SW Integration Testing on MCU	2 days	2.W17 - 4.W17	Tue 21.04.20	Thu 23.04.20	0%	112		Need to Follow Basic SW Integration Release
110		HKMC Application SW Release for Testing	0 days	4.W17 - 4.W17	Thu 23.04.20	Thu 23.04.20	0%	109		
111		**Basic SW**	**1 day**		**Mon 20.04.20**	**Tue 21.04.20**	**0%**			
112		SW Integration Review	1 day	1.W17 - 2.W17	Mon 20.04.20	Tue 21.04.20	0%	108	Young-Jung Huh	
113		HKMC R2 SDB	0 days	1.W15 - 1.W15	Mon 06.04.20	Mon 06.04.20	0%			Starting MileStone
114		**SW Release R2**	**70,3 days**		**Mon 06.04.20**	**Wed 15.07.20**	**0%**			**FLC Variant : ...**
115		**Systems**	**38,8 days**		**Mon 06.04.20**	**Mon 01.06.20**	**0%**			
116		Creation of SDB	16,8 days	1.W15 - 1.W23	Mon 06.04.20	Mon 01.06.20	0%	113	Soo-Jin Ahn;Philippe Lavole	This is a FO Task, but at present s
117		SDB Release	0 days	1.W23 - 1.W23	Mon 01.06.20	Mon 01.06.20	0%	116	Soo-Jin Ahn	
118		**Application SW**	**70,3 days**		**Mon 06.04.20**	**Wed 15.07.20**	**0%**		**Srikanth Mukku**	
119		**SW Architecture**	**19 days**		**Mon 01.06.20**	**Fri 26.06.20**	**0%**		**Soo-Jin Ahn**	
120		SW Adaptors Strategy.	2 days	1.W23 - 3.W23	Mon 01.06.20	Wed 03.06.20	0%	117	Soo-Jin Ahn	This is not yet reflected in the wor
121		SW Architecture in EA Development	4 days	3.W25 - 2.W26	Wed 17.06.20	Tue 23.06.20	0%	120;13	Soo-Jin Ahn	SW Architecture for Adaptors
122		SW EA Architecture Review	3 days	2.W26 - 5.W26	Tue 23.06.20	Fri 26.06.20	0%	121	Soo-Jin Ahn;Choong-Yup Sh	

Legend:
Task — Summary — Inactive Milestone — Duration-only — Start-only — External Milestone — Manual Progress
Split — Project Summary — Inactive Summary — Manual Summary Rollup — Finish-only — Deadline
Milestone — Inactive Task — Manual Task — Manual Summary — External Tasks — Progress

ID	Task Mode	Task Name	Duration	CW	Start	Finish	% Complete	Predecessors	Resource Names	Notes
123		SW EA Architecture Release	0 days	5.W26 - 5.W26	Fri 26.06.20	Fri 26.06.20	0%	122	Soo-Jin Ahn	
124		**SW Architecture ARXML 's Development**	**7 days**		**Fri 05.06.20**	**Tue 16.06.20**	**0%**		**Soo-Jin Ahn**	
125		Adaptors Design in Davinci Developer.	4 days	5.W23 - 4.W24	Fri 05.06.20	Thu 11.06.20	0%	117;13	Soo-Jin Ahn	
126		Adaptors ARXML Generation for simulink import.	1 day	4.W24 - 5.W24	Thu 11.06.20	Fri 12.06.20	0%	125	Soo-Jin Ahn	
127		Scheduler for ASP SW Integration	1 day	5.W24 - 1.W25	Fri 12.06.20	Mon 15.06.20	0%	126	Soo-Jin Ahn	
128		Scheduler for SW Adaptors.	1 day	1.W25 - 2.W25	Mon 15.06.20	Tue 16.06.20	0%	127	Soo-Jin Ahn	
129		ARXML's Review for Basic SW & Adaptors Development	1 day	2.W25 - 3.W25	Tue 16.06.20	Wed 17.06.20	0%	128	Soo-Jin Ahn;Choong-Yup SI	
130		ARXML's Release	0 days	3.W25 - 3.W25	Wed 17.06.20	Wed 17.06.20	0%	129	Soo-Jin Ahn	
131		**Adaptor 1 : FCA-Car/Ped Adaptor Development**	**17,5 days**		**Mon 01.06.20**	**Thu 25.06.20**	**0%**		**Hoon Bae**	
132		**Feature_SignalInterfaceMatrix**	**2 days**		**Mon 01.06.20**	**Wed 03.06.20**	**0%**		**Hoon Bae**	**This is the signal interface matr**
133		Creation of SignalInterfaceMatrix	12 hrs	1.W23 - 3.W23	Mon 01.06.20	Wed 03.06.20	0%	117	Hoon Bae	
134		Review of SignalInterface Matrix	4 hrs	3.W23 - 3.W23	Wed 03.06.20	Wed 03.06.20	0%	133	Hoon Bae;Soo-Jin Ahn	
135		Release of FCA Car7Ped Adaptor SignalInterface Matrix	0 days	3.W23 - 3.W23	Wed 03.06.20	Wed 03.06.20	0%	134	Hoon Bae	
136		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	132;17	Hoon Bae	not applicable for R0
137		Adaptor Detailed Design Review	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	136	Hoon Bae;Soo-Jin Ahn	not applicable for R0
138		Release of FCA Car/Ped Adaptor Detailed Design	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	137	Hoon Bae	not applicable for R0
139		**Adaptor Simulink Development**	**11,5 days**		**Fri 05.06.20**	**Tue 23.06.20**	**0%**		**Hoon Bae**	
140		Simulink Design Development	4 days	5.W23 - 4.W24	Fri 05.06.20	Thu 11.06.20	0%	138	Hoon Bae	
141		Simulink Import of ARXML & linking to Development Model	4 hrs	5.W25 - 1.W26	Fri 19.06.20	Mon 22.06.20	10%	130;18	Hoon Bae	
142		Simulink Design Release & Review	4 hrs	1.W26 - 1.W26	Mon 22.06.20	Mon 22.06.20	0%	141	Hoon Bae;Soo-Jin Ahn	
143		Adaptor Code Generation from Simulink.	4 hrs	1.W26 - 2.W26	Mon 22.06.20	Tue 23.06.20	0%	142	Hoon Bae	
144		Release of FCA-Car/Ped Adaptor Simulink Design	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	143	Hoon Bae	
145		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Tue 23.06.20**	**Tue 23.06.20**	**0%**		**Hoon Bae**	**not applicable for R0**
146		Adaptor Unit Test Specification.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	144	Hoon Bae	not applicable for R0
147		Adaptor Unit Test Cases Implementation.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	146	Hoon Bae	not applicable for R0
148		Adaptor Unit Test Execution and its report generation.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	147	Hoon Bae	not applicable for R0
149		SW Source Code & Unit Test Report Review	2 days	2.W26 - 4.W26	Tue 23.06.20	Thu 25.06.20	0%	148	Hoon Bae;Soo-Jin Ahn	
150		Release of FCA-Car/Ped SW Source Code & SW Unit Test Repo	0 days	4.W26 - 4.W26	Thu 25.06.20	Thu 25.06.20	0%	149	Hoon Bae	
151		**Adaptor 2 : LKA-L Adaptor Development**	**16,5 days**		**Mon 01.06.20**	**Wed 24.06.20**	**0%**		**Choong-Yup Shin**	
152		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Mon 01.06.20**	**Wed 03.06.20**	**0%**		**Choong-Yup Shin**	
153		Creation of SignalInterfaceMatrix	12 hrs	1.W23 - 3.W23	Mon 01.06.20	Wed 03.06.20	0%	117	Choong-Yup Shin	
154		Review & Release of SignalInterface Matrix	4 hrs	3.W23 - 3.W23	Wed 03.06.20	Wed 03.06.20	0%	153	Soo-Jin Ahn;Choong-Yup SI	
155		Release of LKA-L Adaptor SignalInterface Matrix	0 days	3.W23 - 3.W23	Wed 03.06.20	Wed 03.06.20	0%	154	Choong-Yup Shin	
156		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	155;19	Choong-Yup Shin	not applicable for R0
157		Adaptor Detailed Design Review	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	156	Soo-Jin Ahn;Choong-Yup SI	not applicable for R0
158		Release of LKA-L Adaptor Detailed Design	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	157	Choong-Yup Shin	not applicable for R0
159		**Adaptor Simulink Development**	**11,5 days**		**Fri 05.06.20**	**Tue 23.06.20**	**0%**		**Choong-Yup Shin**	
160		Simulink Design Development	4 days	5.W23 - 4.W24	Fri 05.06.20	Thu 11.06.20	0%	158	Choong-Yup Shin	
161		Simulink Import of ARXML & linking to Development Model	4 hrs	5.W25 - 1.W26	Fri 19.06.20	Mon 22.06.20	0%	130;20	Choong-Yup Shin	
162		Simulink Design Release & Review	4 hrs	1.W26 - 1.W26	Mon 22.06.20	Mon 22.06.20	0%	161	Choong-Yup Shin;Soo-Jin A	
163		Adaptor Code Generation from Simulink.	4 hrs	1.W26 - 2.W26	Mon 22.06.20	Tue 23.06.20	0%	162	Choong-Yup Shin	
164		Release of LKA-L Adaptor Simulink Design	0 hrs	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	163	Choong-Yup Shin	
165		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Tue 23.06.20**	**Tue 23.06.20**	**0%**		**Choong-Yup Shin**	**not applicable for R0**
166		Adaptor Unit Test Specification.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	164	Choong-Yup Shin	not applicable for R0
167		Adaptor Unit Test Cases Implementation.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	166	Choong-Yup Shin	not applicable for R0
168		Adaptor Unit Test Execution and its report generation.	0 days	2.W26 - 2.W26	Tue 23.06.20	Tue 23.06.20	0%	167	Choong-Yup Shin	not applicable for R0
169		SW Source Code & Unit Test Report Review	1 day	2.W26 - 3.W26	Tue 23.06.20	Wed 24.06.20	0%	168	Choong-Yup Shin;Soo-Jin A	
170		Release of LKA_L SW Source Code & Unit Test Report	0 days	3.W26 - 3.W26	Wed 24.06.20	Wed 24.06.20	0%	169	Choong-Yup Shin	
171		**Vehicle Input Adaptor**	**19,5 days**		**Wed 03.06.20**	**Wed 01.07.20**	**0%**		**Hoon Bae**	
172		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Wed 03.06.20**	**Fri 05.06.20**	**0%**			
173		Creation of SignalInterfaceMatrix	12 hrs	3.W23 - 5.W23	Wed 03.06.20	Fri 05.06.20	0%	117;13	Hoon Bae	
174		Review & Release of SignalInterface Matrix	4 hrs	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	173	Soo-Jin Ahn;Hoon Bae	
175		Release of Veh_Inp Adaptor SignalInterface Matrix	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	174	Hoon Bae	
176		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	140;17	Hoon Bae	not applicable for R0
177		Adaptor Detailed Design Review	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	176	Soo-Jin Ahn;Hoon Bae	not applicable for R0
178		Release of Veh_Inp Adaptor Detailed Design	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	177	Hoon Bae	not applicable for R0
179		**Adaptor Simulink Development**	**12,5 days**		**Thu 11.06.20**	**Tue 30.06.20**	**0%**		**Hoon Bae**	
180		Simulink Design Development	6 days	4.W24 - 4.W25	Thu 11.06.20	Fri 19.06.20	0%	178	Hoon Bae	
181		Simulink Import of ARXML & linking to Development Model	4 hrs	4.W26 - 4.W26	Thu 25.06.20	Thu 25.06.20	0%	130;15	Hoon Bae	
182		Simulink Design Release & Review	4 hrs	4.W26 - 5.W26	Thu 25.06.20	Fri 26.06.20	0%	181	Hoon Bae;Soo-Jin Ahn	
183		Adaptor Code Generation from Simulink.	2 days	5.W26 - 2.W27	Fri 26.06.20	Tue 30.06.20	0%	182	Hoon Bae	

Task | Summary | Inactive Milestone | Duration-only | Start-only | External Milestone | Manual Progress
Split | Project Summary | Inactive Summary | Manual Summary Rollup | Finish-only | Deadline
Milestone | Inactive Task | Manual Task | Manual Summary | External Tasks | Progress

ID	Task Mode	Task Name	Duration	CW	Start	Finish	% Comple	Predeces	Resource Names	Notes
184		Release of Veh_Inp Adaptor Simulink Design	0 days	2.W27 - 2.W27	Tue 30.06.20	Tue 30.06.20	0%	183	Hoon Bae	
185		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Tue 30.06.20**	**Tue 30.06.20**	**0%**		**Hoon Bae**	**not applicable for R0**
186		Adaptor Unit Test Specification.	0 days	2.W27 - 2.W27	Tue 30.06.20	Tue 30.06.20	0%	184	Hoon Bae	not applicable for R0
187		Adaptor Unit Test Cases Implementation.	0 days	2.W27 - 2.W27	Tue 30.06.20	Tue 30.06.20	0%	186	Hoon Bae	not applicable for R0
188		Adaptor Unit Test Execution and its report generation.	0 days	2.W27 - 2.W27	Tue 30.06.20	Tue 30.06.20	0%	187	Hoon Bae	not applicable for R0
189		SW Source Code & Unit Test Report Review	1 day	2.W27 - 3.W27	Tue 30.06.20	Wed 01.07.20	0%	188	Soo-Jin Ahn;Hoon Bae	
190		Release of Vhe_In Adaptor SW Source Code & Unit Test Repo	0 days	3.W27 - 3.W27	Wed 01.07.20	Wed 01.07.20	0%	189	Hoon Bae	
191		**Vehicle Output Adaptor**	**18,5 days**		**Wed 03.06.20**	**Tue 30.06.20**	**0%**		**Choong-Yup Shin**	
192		**Signal Matrix preparation for Adaptor for architecture supp**	**2 days**		**Wed 03.06.20**	**Fri 05.06.20**	**0%**			
193		Creation of SignalInterfaceMatrix	12 hrs	3.W23 - 5.W23	Wed 03.06.20	Fri 05.06.20	0%	117;15	Choong-Yup Shin	
194		Review & Release of SignalInterface Matrix	4 hrs	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	193	Soo-Jin Ahn;Choong-Yup Sl	
195		Release of Veh_Out Adaptor SignalInterface Matrix	0 days	5.W23 - 5.W23	Fri 05.06.20	Fri 05.06.20	0%	194	Choong-Yup Shin	
196		Adaptor Detailed Design(if it is a small gap FD has to do that, if it is a big gap FO has to do that).	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	160;19	Choong-Yup Shin	not applicable for R0
197		Adaptor Detailed Design Review	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	196	Soo-Jin Ahn;Choong-Yup Sl	not applicable for R0
198		Release of Veh_Out Adaptor Detailed Design	0 days	4.W24 - 4.W24	Thu 11.06.20	Thu 11.06.20	0%	196	Choong-Yup Shin	not applicable for R0
199		**Adaptor Simulink Development**	**11,5 days**		**Thu 11.06.20**	**Mon 29.06.20**	**0%**		**Choong-Yup Shin**	
200		Simulink Design Development	6 days	4.W24 - 5.W25	Thu 11.06.20	Fri 19.06.20	0%	198	Choong-Yup Shin	
201		Simulink Import of ARXML & linking to Development Model	4 hrs	3.W26 - 3.W26	Wed 24.06.20	Wed 24.06.20	0%	130;170	Choong-Yup Shin	
202		Simulink Design Release & Review	4 hrs	3.W26 - 4.W26	Thu 25.06.20	Thu 25.06.20	0%	201	Choong-Yup Shin	
203		Adaptor Code Generation from Simulink.	2 days	4.W26 - 1.W27	Thu 25.06.20	Mon 29.06.20	0%	202	Choong-Yup Shin	
204		Release of Veh_Out Adaptor Simulink Design	0 days	1.W27 - 1.W27	Mon 29.06.20	Mon 29.06.20	0%	203	Choong-Yup Shin	
205		**SW Unit Test Specification & Test Cases Development**	**0 days**		**Mon 29.06.20**	**Mon 29.06.20**	**0%**		**Choong-Yup Shin**	**not applicable for R0**
206		Adaptor Unit Test Specification.	0 days	1.W27 - 1.W27	Mon 29.06.20	Mon 29.06.20	0%	204	Choong-Yup Shin	not applicable for R0
207		Adaptor Unit Test Cases Implementation.	0 days	1.W27 - 1.W27	Mon 29.06.20	Mon 29.06.20	0%	206	Choong-Yup Shin	not applicable for R0
208		Adaptor Unit Test Execution and its report generation.	0 days	1.W27 - 1.W27	Mon 29.06.20	Mon 29.06.20	0%	207	Choong-Yup Shin	not applicable for R0
209		SW Source Code & Unit Test Report Review	1 day	1.W27 - 2.W27	Mon 29.06.20	Tue 30.06.20	0%	208	Choong-Yup Shin;Soo-Jin A	
210		Release of Veh_Out Adaptor SW Source Code & Unit Test Repo	0 days	2.W27 - 2.W27	Tue 30.06.20	Tue 30.06.20	0%	209	Choong-Yup Shin	
211		**SW Integration**	**70,3 days**		**Mon 06.04.20**	**Wed 15.07.20**	**0%**			
212		SW Integration Test Specification.	4 days	1.W15 - 4.W15	Mon 06.04.20	Thu 09.04.20	0%	113		
213		SW Integration Test Cases Implementation.	2 days	5.W15 - 1.W16	Fri 10.04.20	Mon 13.04.20	0%	212		
214		SW Integration.	4 days	3.W27 - 2.W28	Wed 01.07.20	Tue 07.07.20	0%	149;170		
215		SW Integration Test execution & Its Rerport Generation.	2 days	2.W28 - 4.W28	Tue 07.07.20	Thu 09.07.20	0%	214		
216		SW Version Management.	3 days	2.W28 - 5.W28	Tue 07.07.20	Fri 10.07.20	0%	214		
217		SW Release Notes.	1 day	5.W28 - 1.W29	Fri 10.07.20	Mon 13.07.20	0%	216		
218		HKMC SW Integration	1 day	4.W28 - 5.W28	Thu 09.07.20	Fri 10.07.20	0%	215		(Buildable Application SW on GIT repo together with HKMC Basic SW
219		HKMC Application SW Release	0 days	5.W28 - 5.W28	Fri 10.07.20	Fri 10.07.20	0%	218		
220		App SW Integration Testing on MCU	2 days	1.W29 - 3.W29	Mon 13.07.20	Wed 15.07.20	0%	223		Need to Follow Basic SW Integration Release
221		HKMC Application SW Release for Testing	0 days	3.W29 - 3.W29	Wed 15.07.20	Wed 15.07.20	0%	220		
222		**Basic SW**	**1 day**		**Fri 10.07.20**	**Mon 13.07.20**	**0%**			
223		SW Integration Review	1 day	5.W28 - 1.W29	Fri 10.07.20	Mon 13.07.20	0%	219		

Project: HKMCApplicationSWPl
Date: Tue 03.03.20

Task		Summary		Inactive Milestone		Duration-only		Start-only		External Milestone	Manual Progress
Split		Project Summary		Inactive Summary		Manual Summary Rollup		Finish-only		Deadline	
Milestone		Inactive Task		Manual Task		Manual Summary		External Tasks		Progress	

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